

B1L001S, U24-ch0, adc0

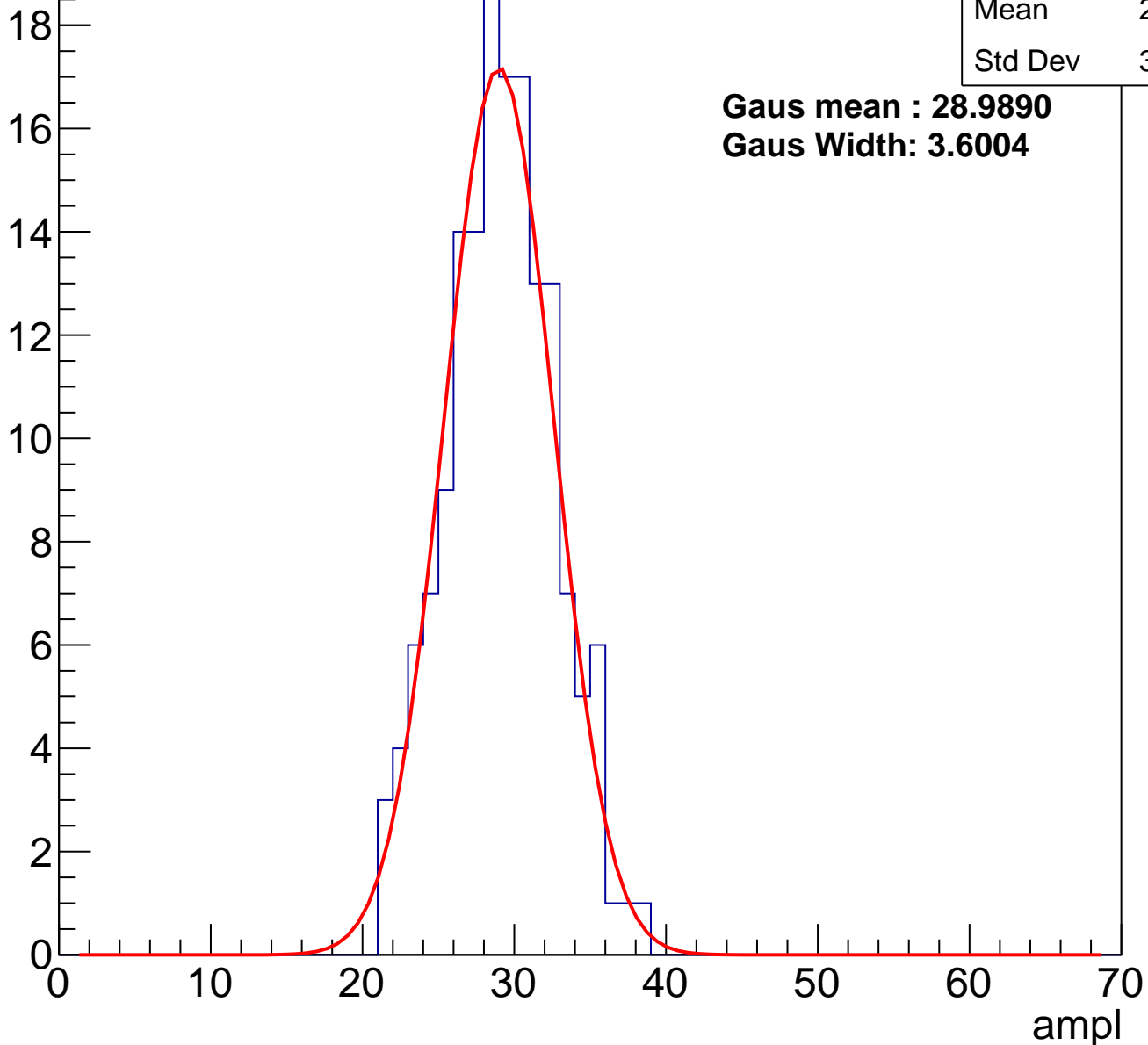
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	28.66
Std Dev	3.514

Gaus mean : 28.9890

Gaus Width: 3.6004

Entry



B1L001S, U24-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.03
Std Dev	3.525

Gaus mean : 36.4838

Gaus Width: 3.6592

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

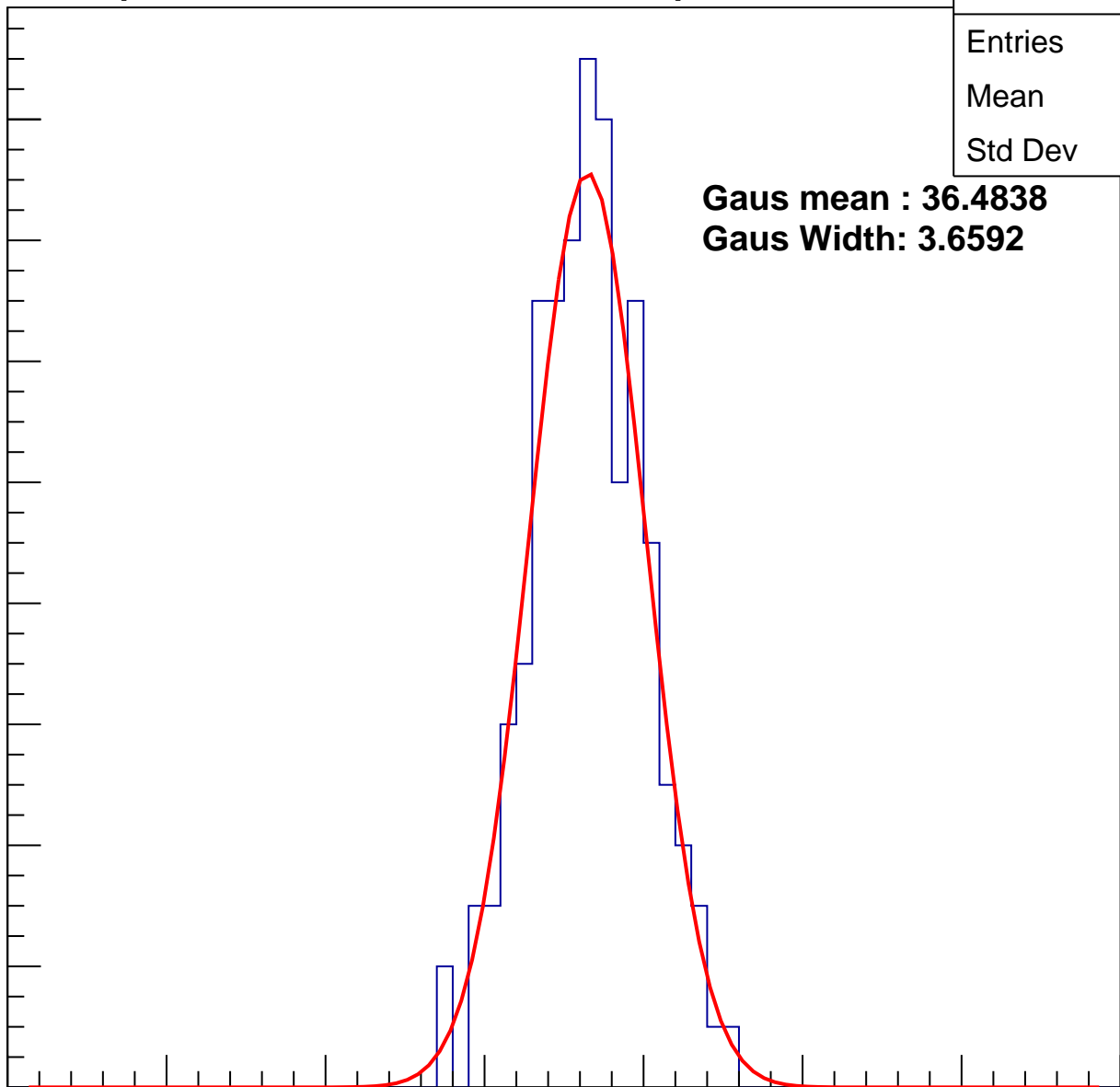
40

50

60

70

ampl



B1L001S, U24-ch0, adc2

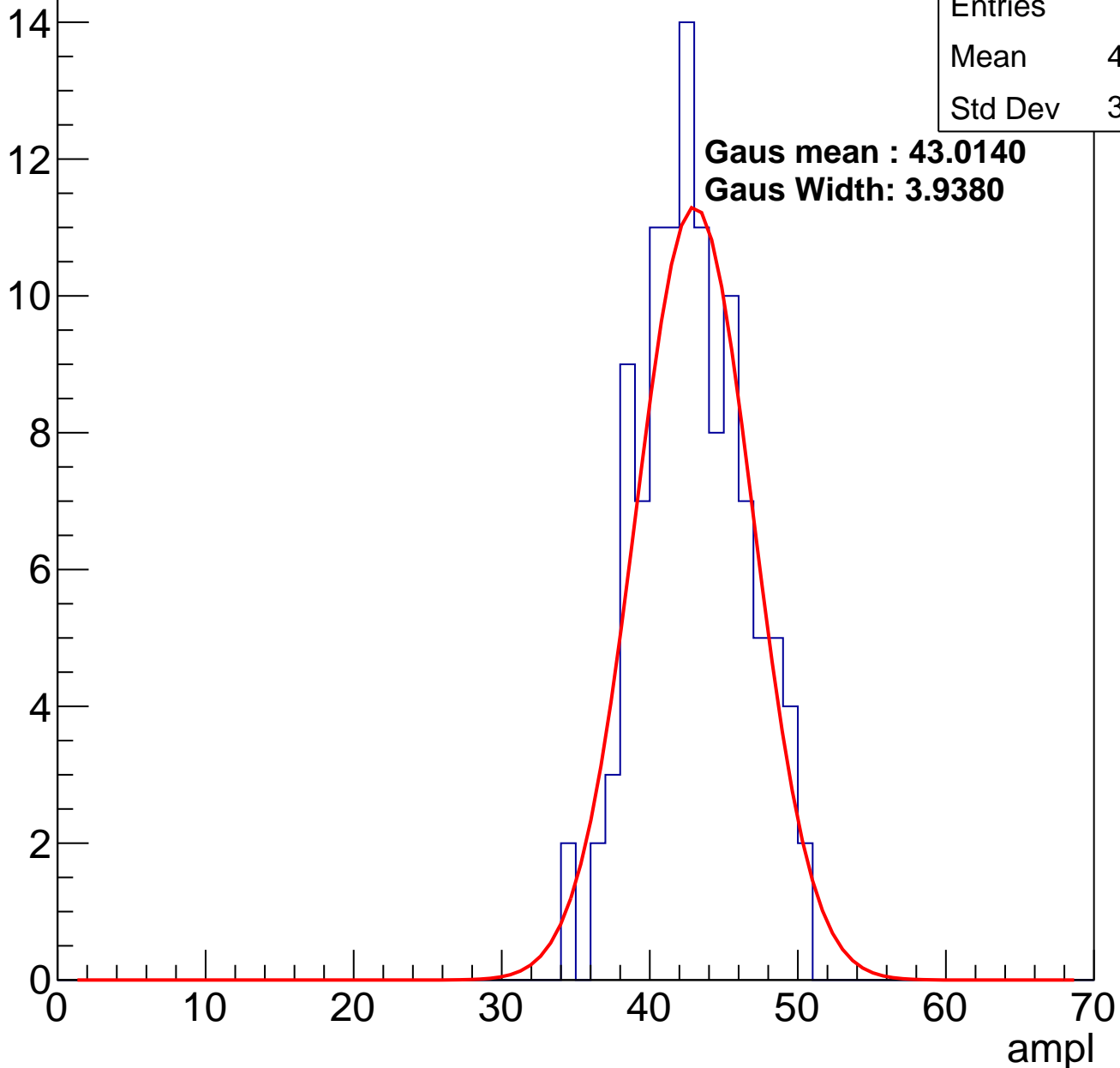
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	42.46
Std Dev	3.536

Gaus mean : 43.0140

Gaus Width: 3.9380

Entry



B1L001S, U24-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

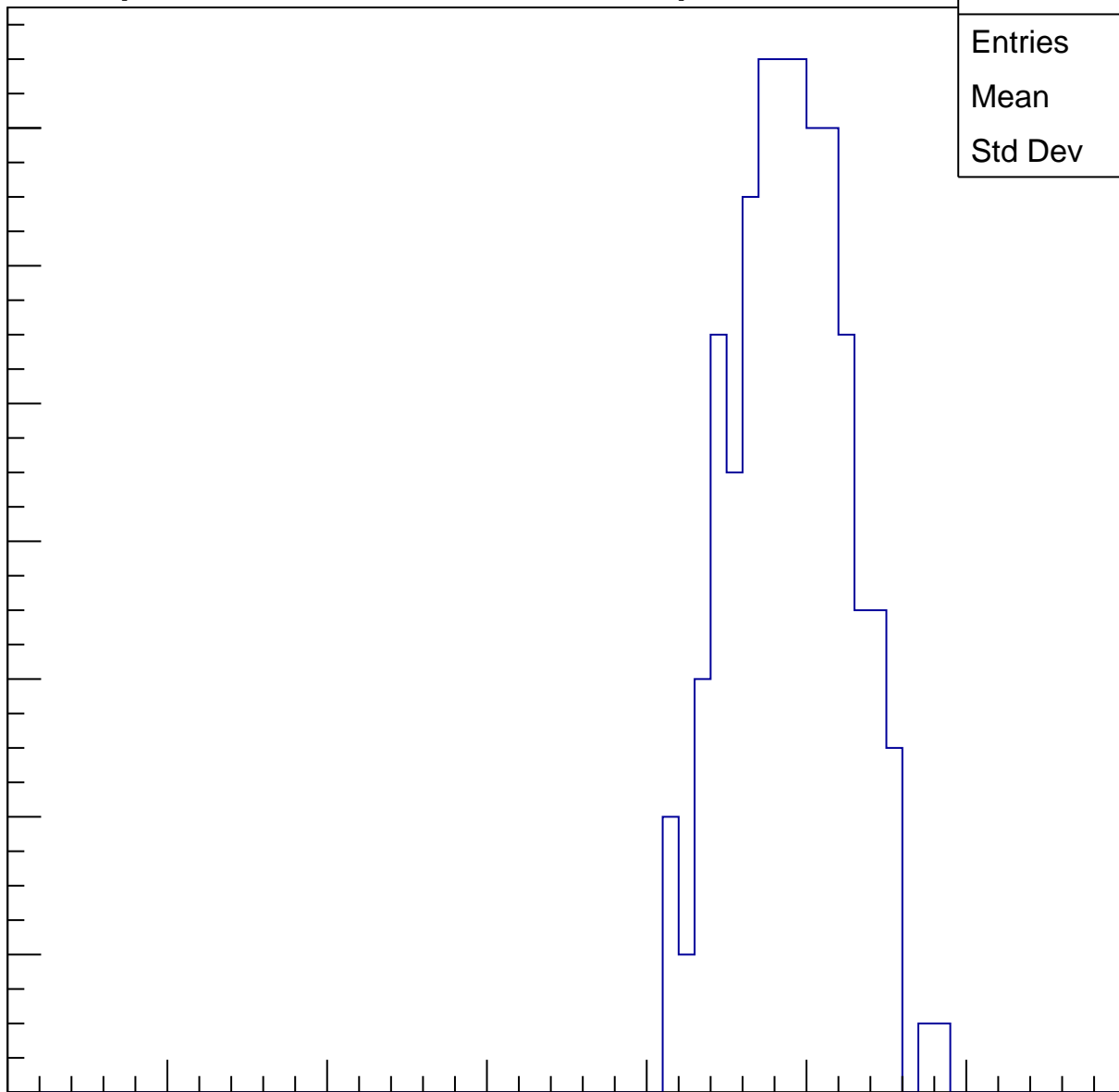
Entries	150
Mean	48.52
Std Dev	3.585

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

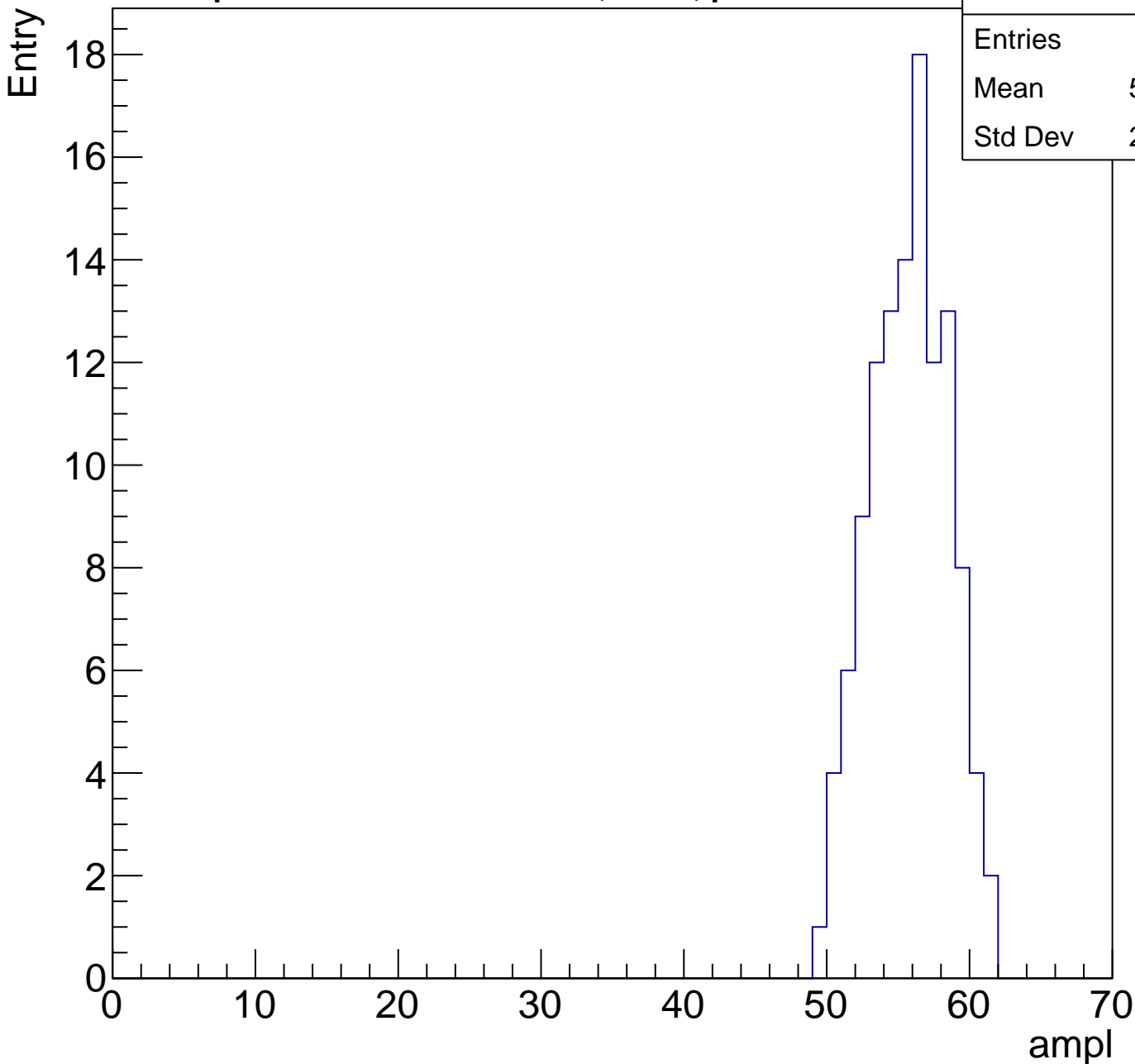
ampl



B1L001S, U24-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	55.27
Std Dev	2.705



B1L001S, U24-ch0, adc5

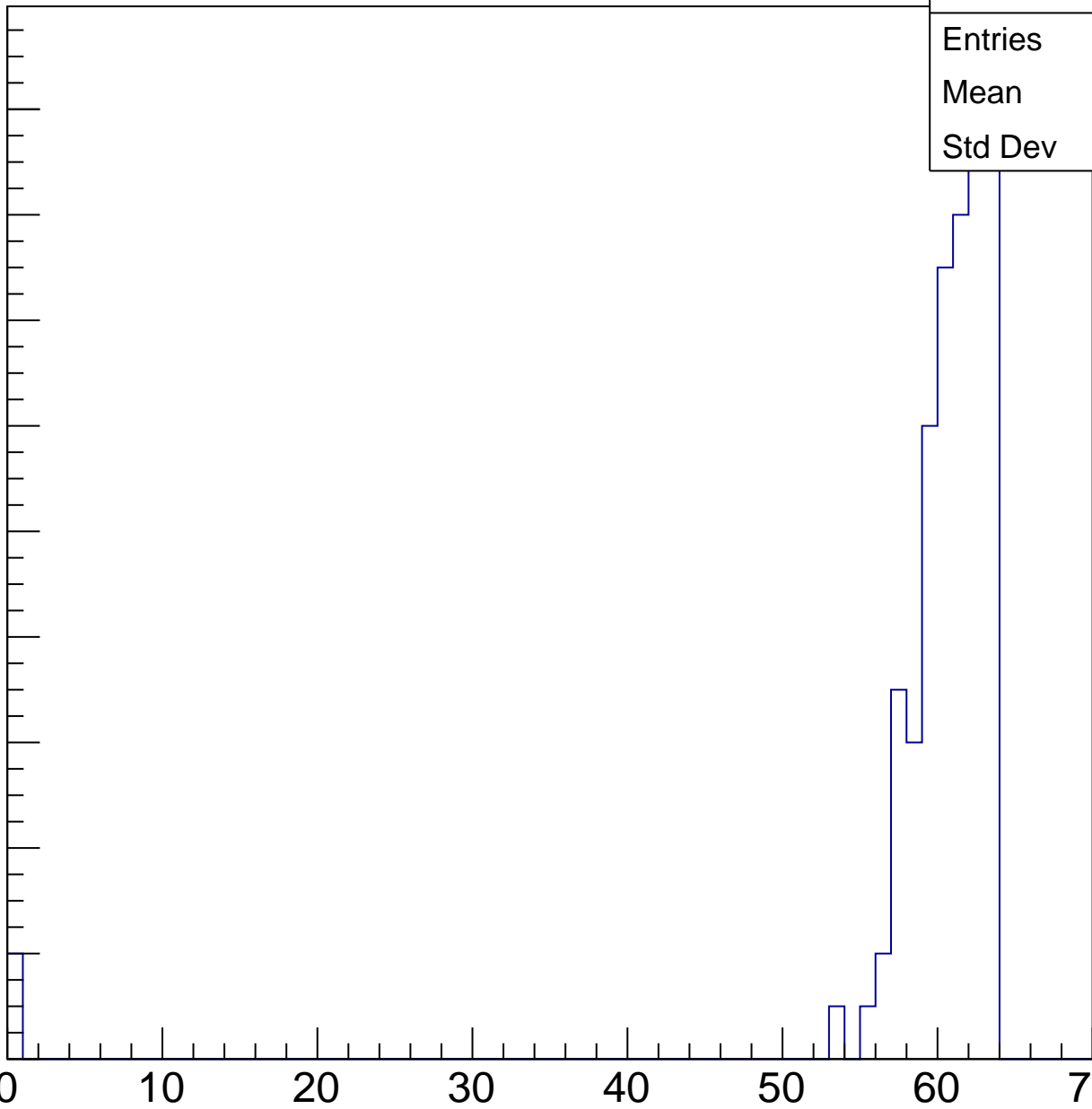
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.22
Std Dev	8.761

ampl



B1L001S, U24-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

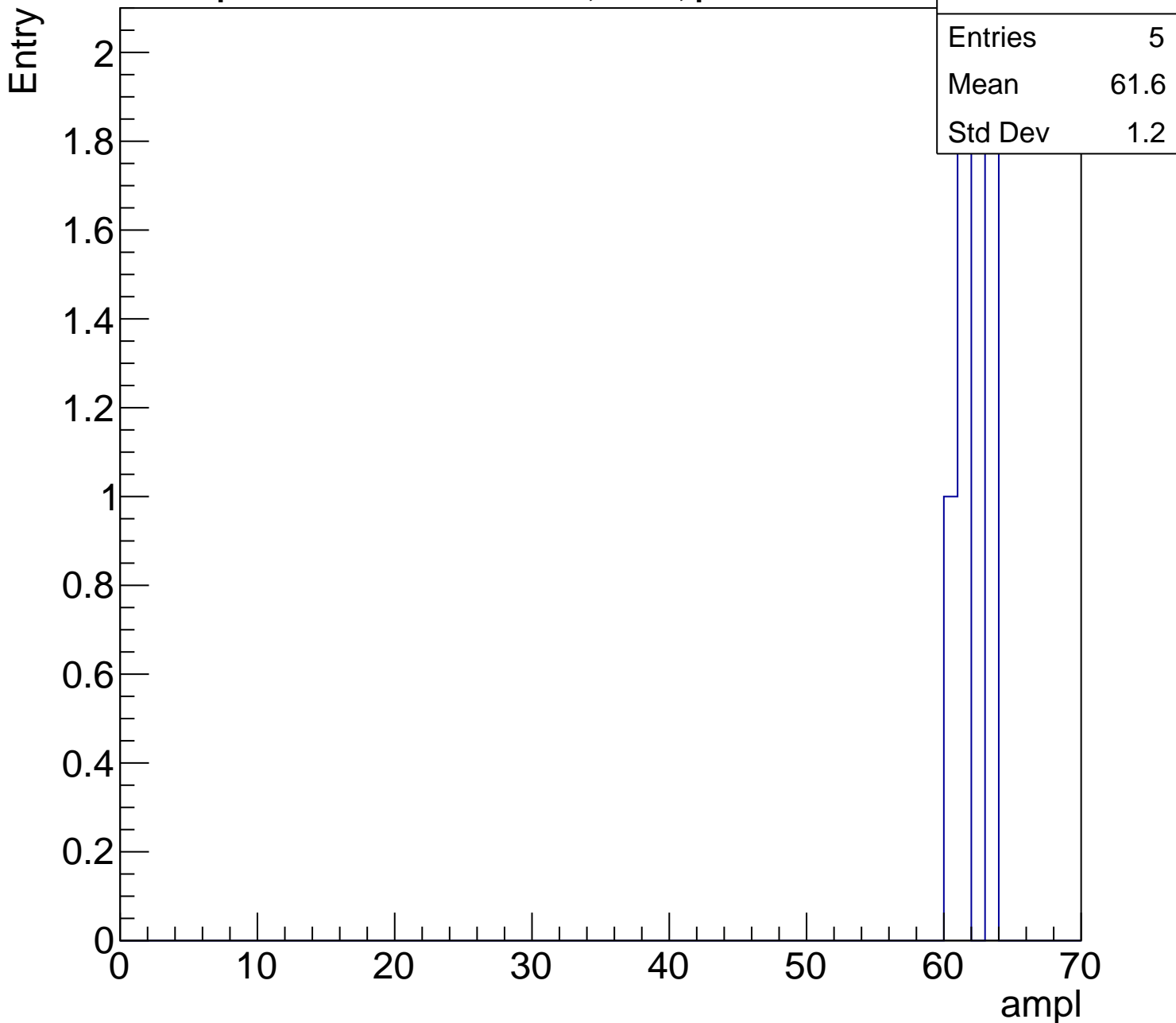
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.2

0 10 20 30 40 50 60 70

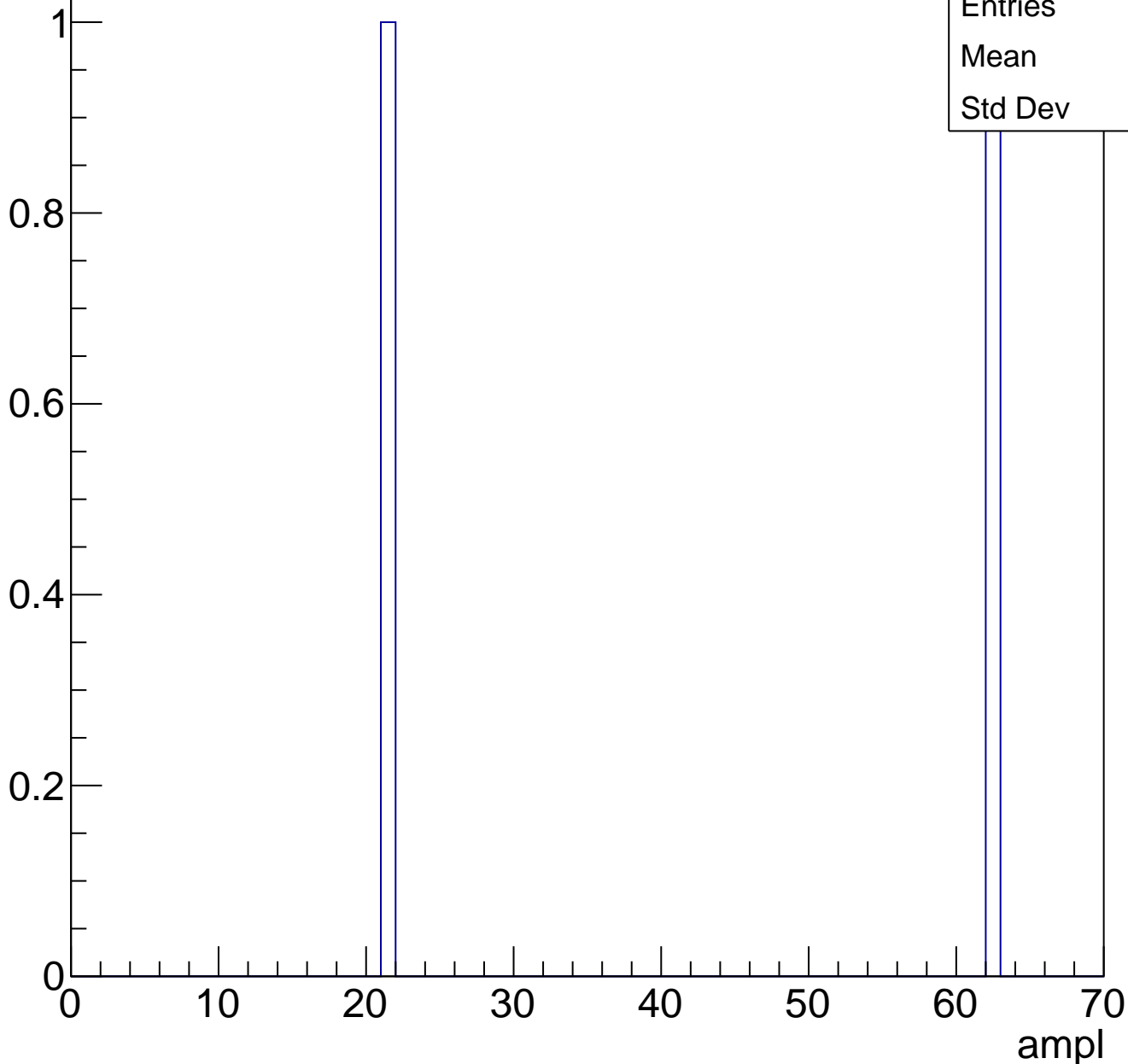
ampl



B1L001S, U24-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



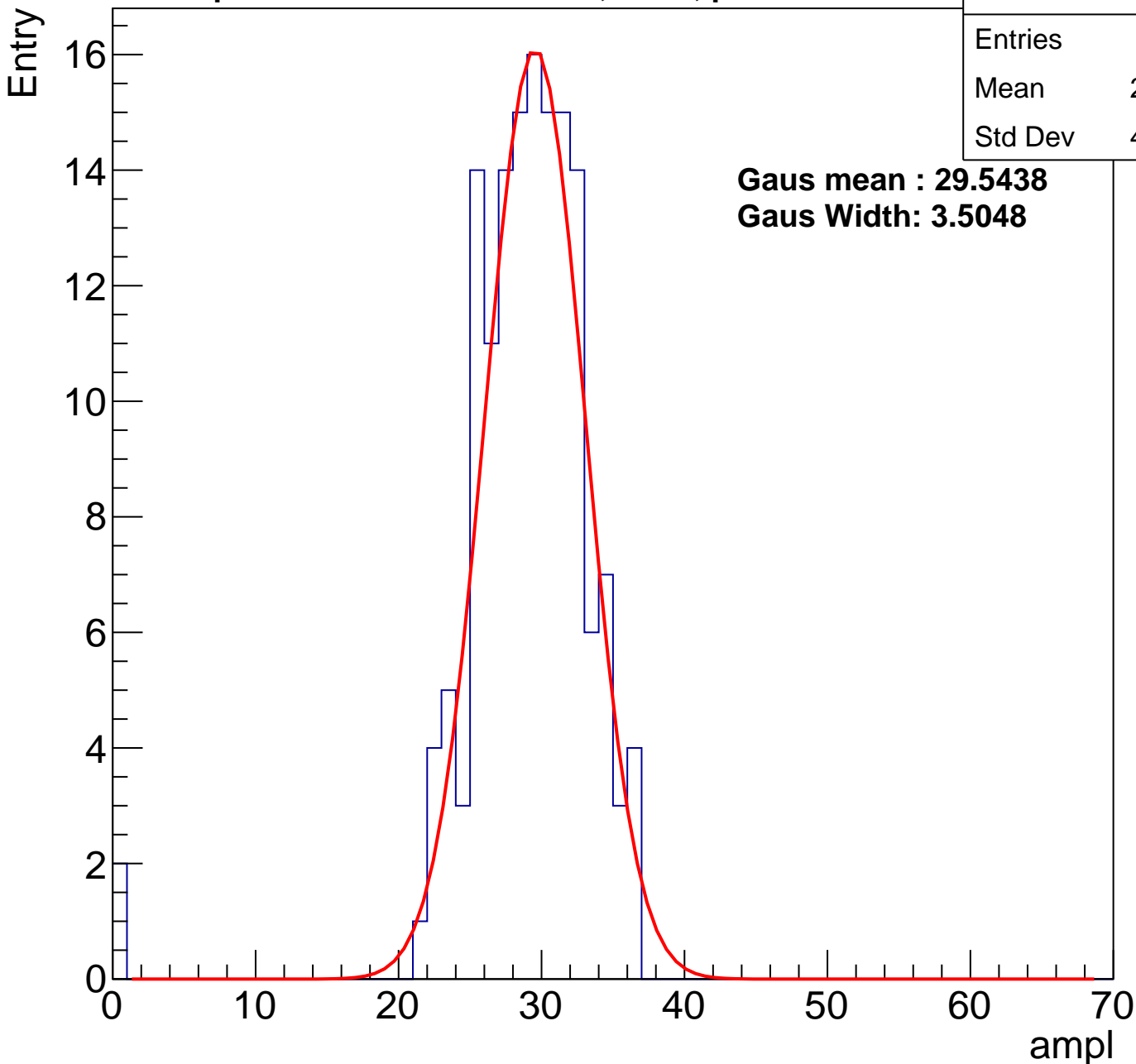
B1L001S, U24-ch1, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	28.47
Std Dev	4.724

Gaus mean : 29.5438

Gaus Width: 3.5048



B1L001S, U24-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	36.59
Std Dev	3.865

Gaus mean : 37.3167

Gaus Width: 3.7681

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

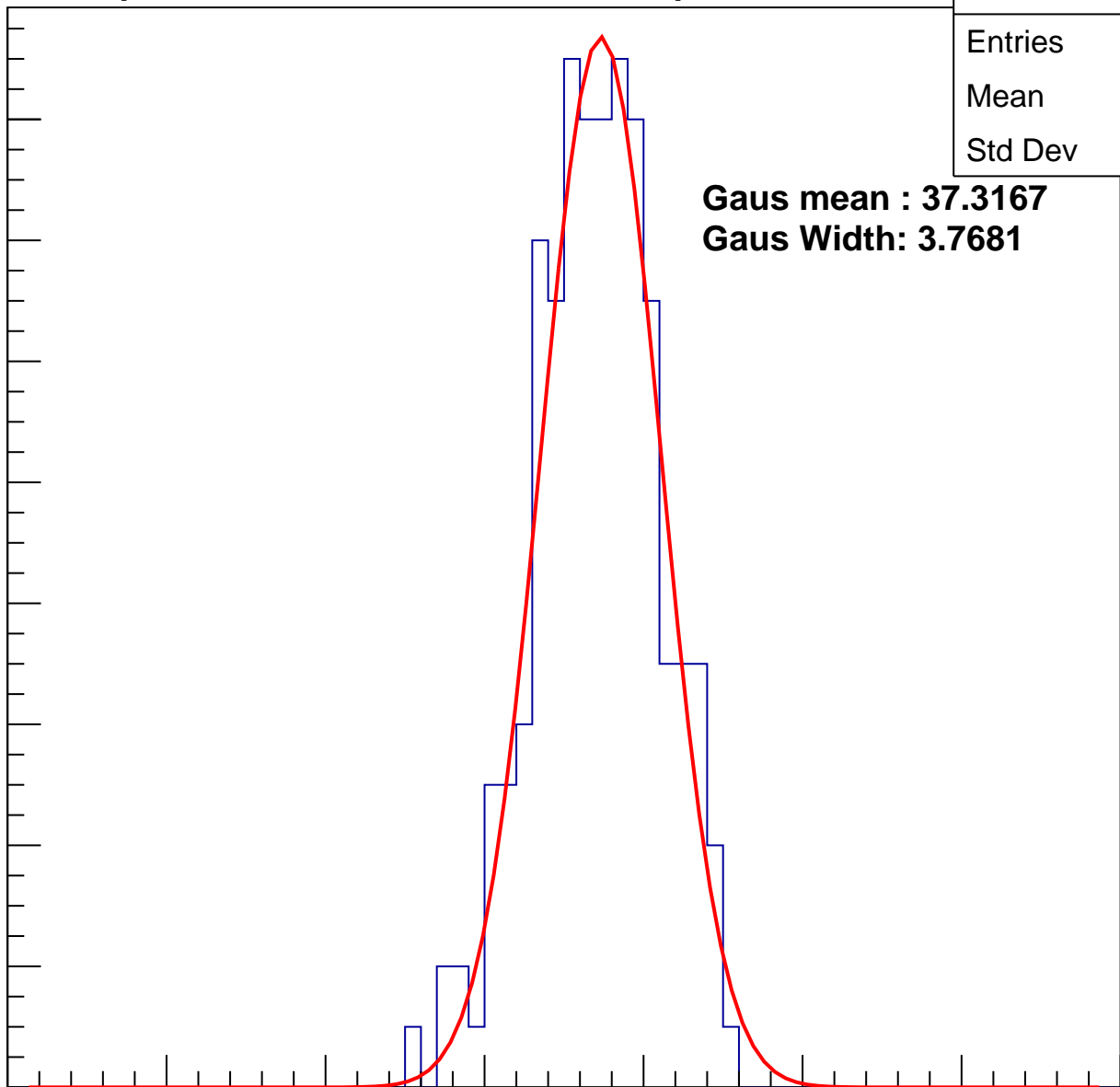
40

50

60

70

ampl



B1L001S, U24-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

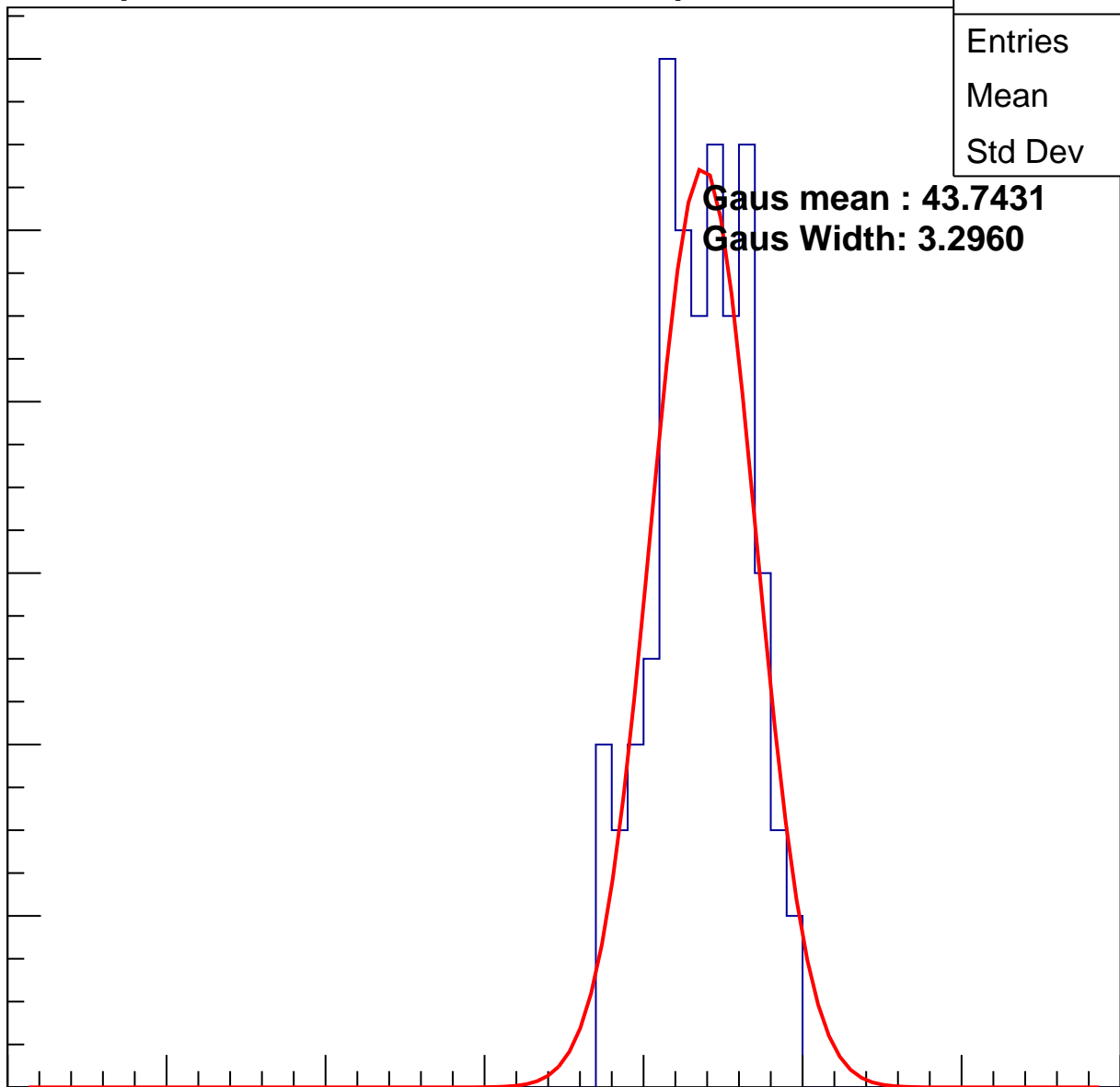
Entries	89
Mean	43.1
Std Dev	2.949

Gaus mean : 43.7431

Gaus Width: 3.2960

0 10 20 30 40 50 60 70

ampl

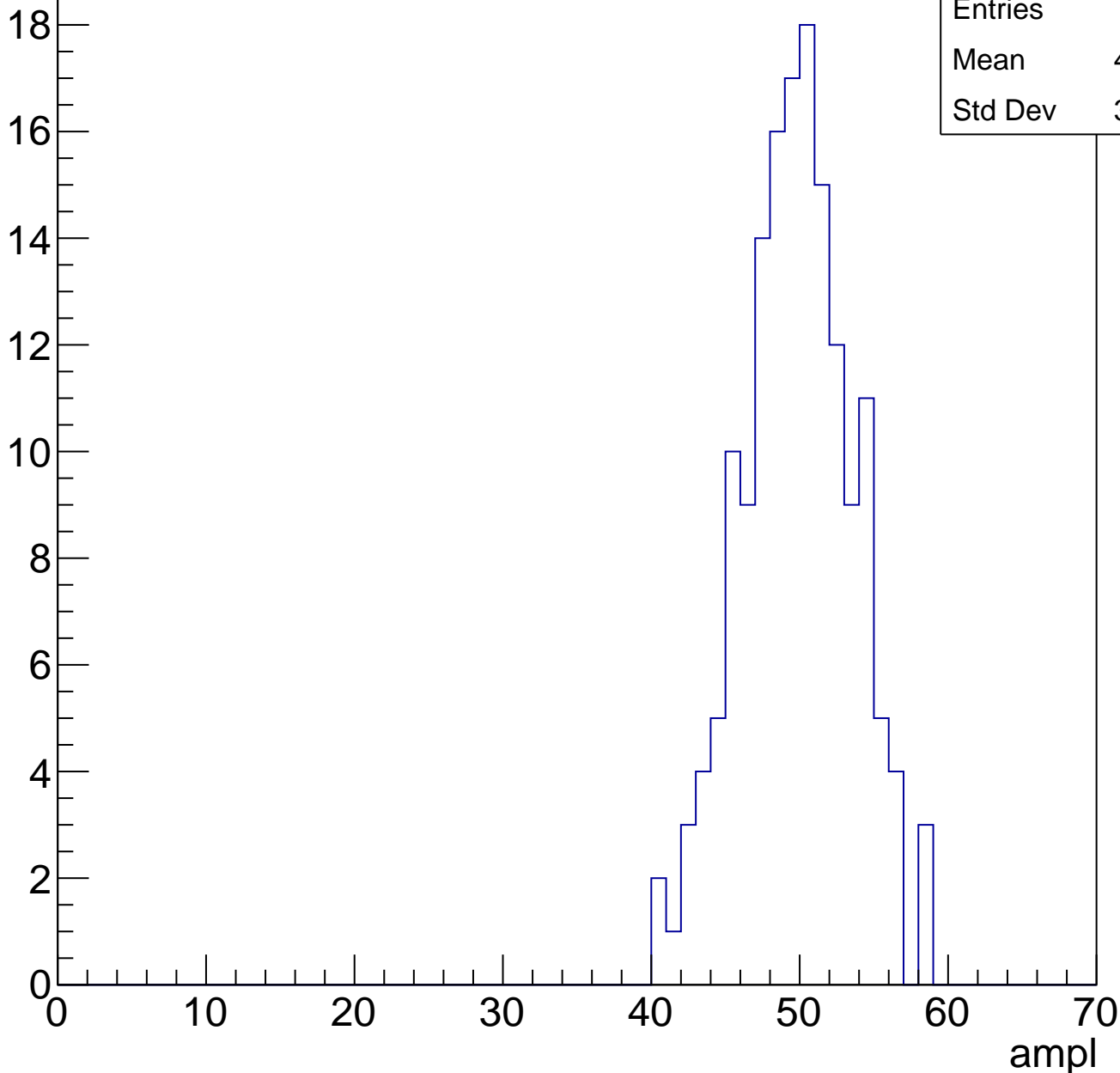


B1L001S, U24-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	49.34
Std Dev	3.698

Entry



B1L001S, U24-ch1, adc4

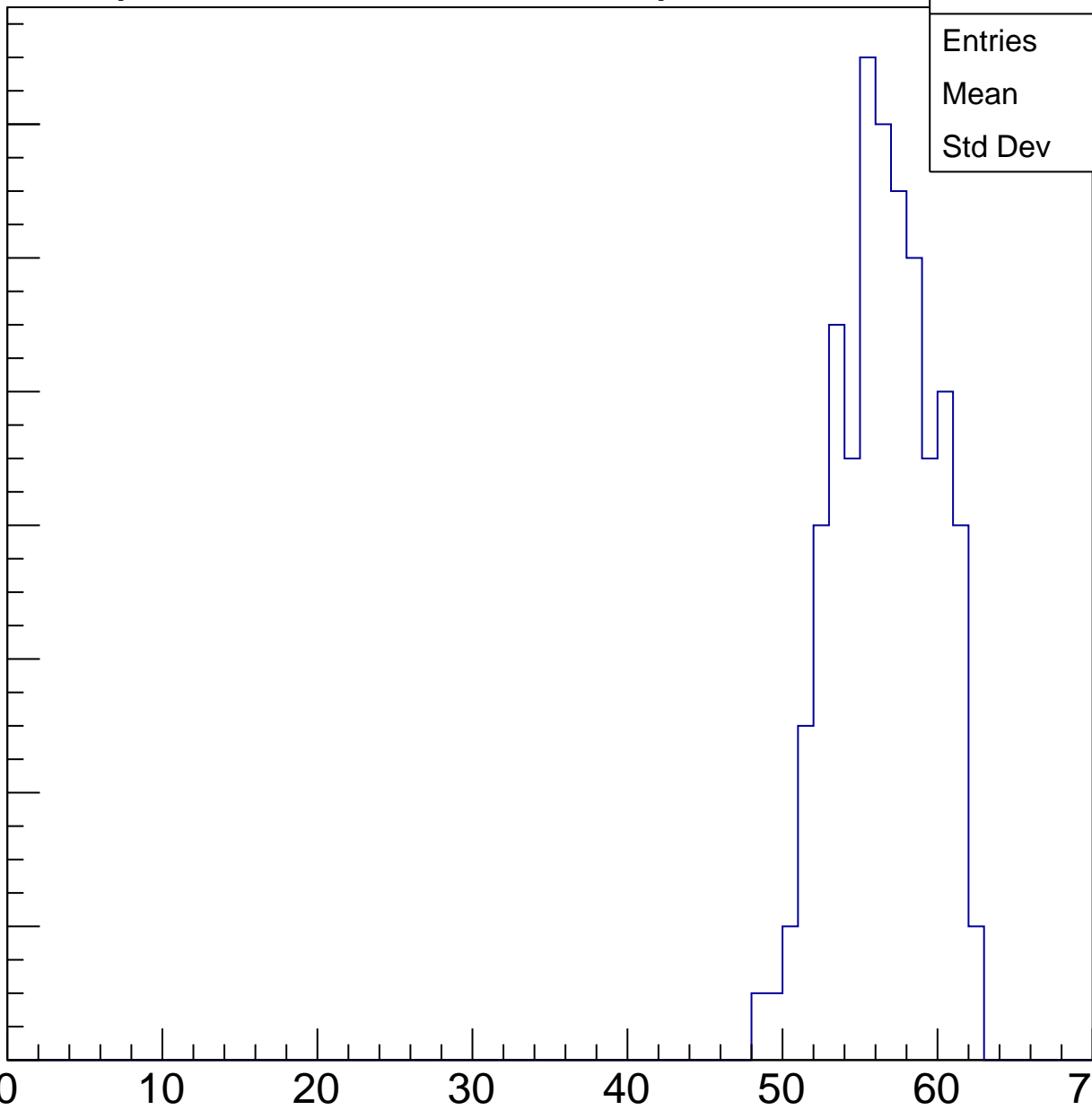
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	56.05
Std Dev	3.111

ampl



B1L001S, U24-ch1, adc5

calib_packv5_042523_0143.root, FC#2, port C2

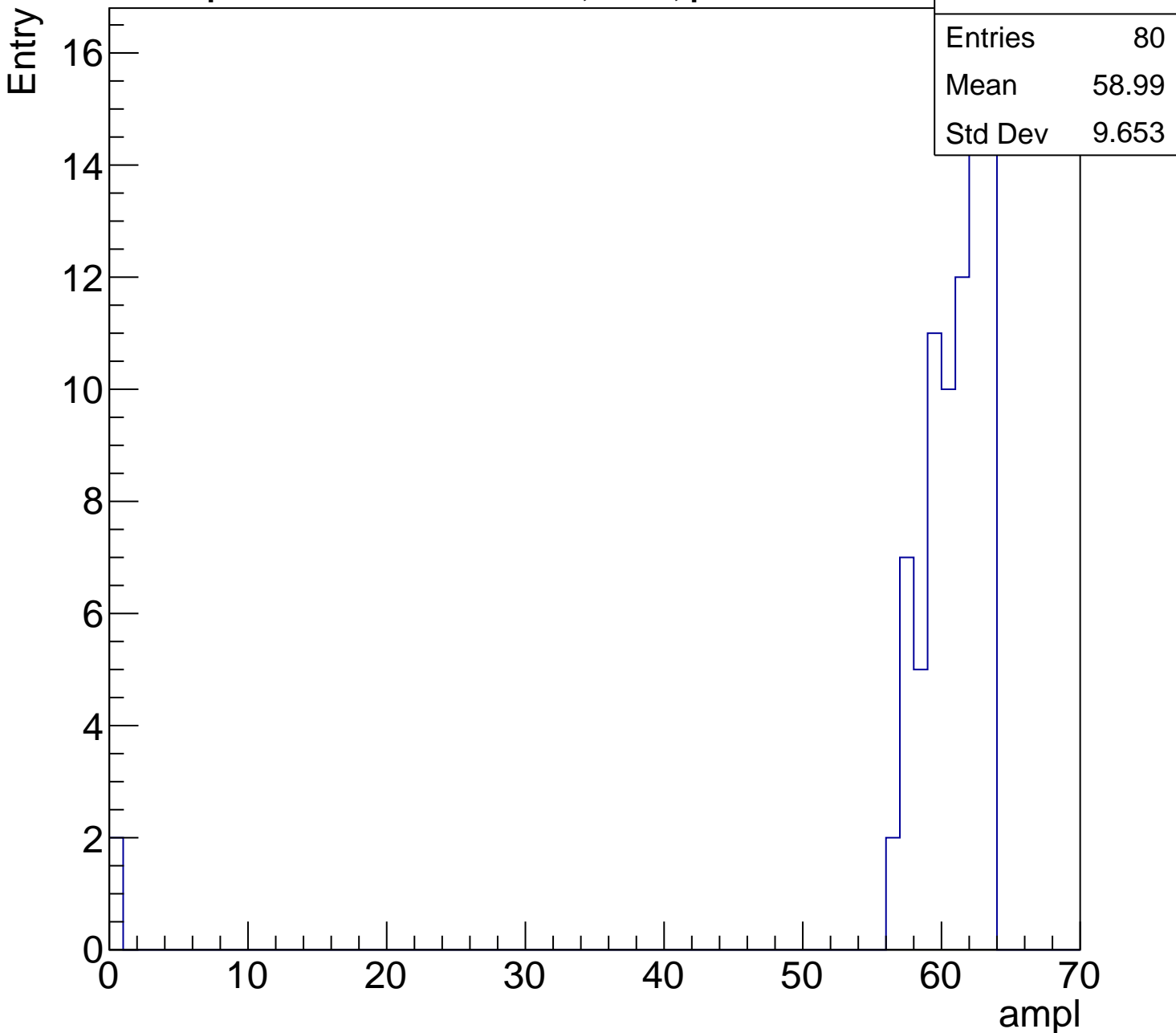
Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	58.99
Std Dev	9.653

ampl

0 10 20 30 40 50 60 70

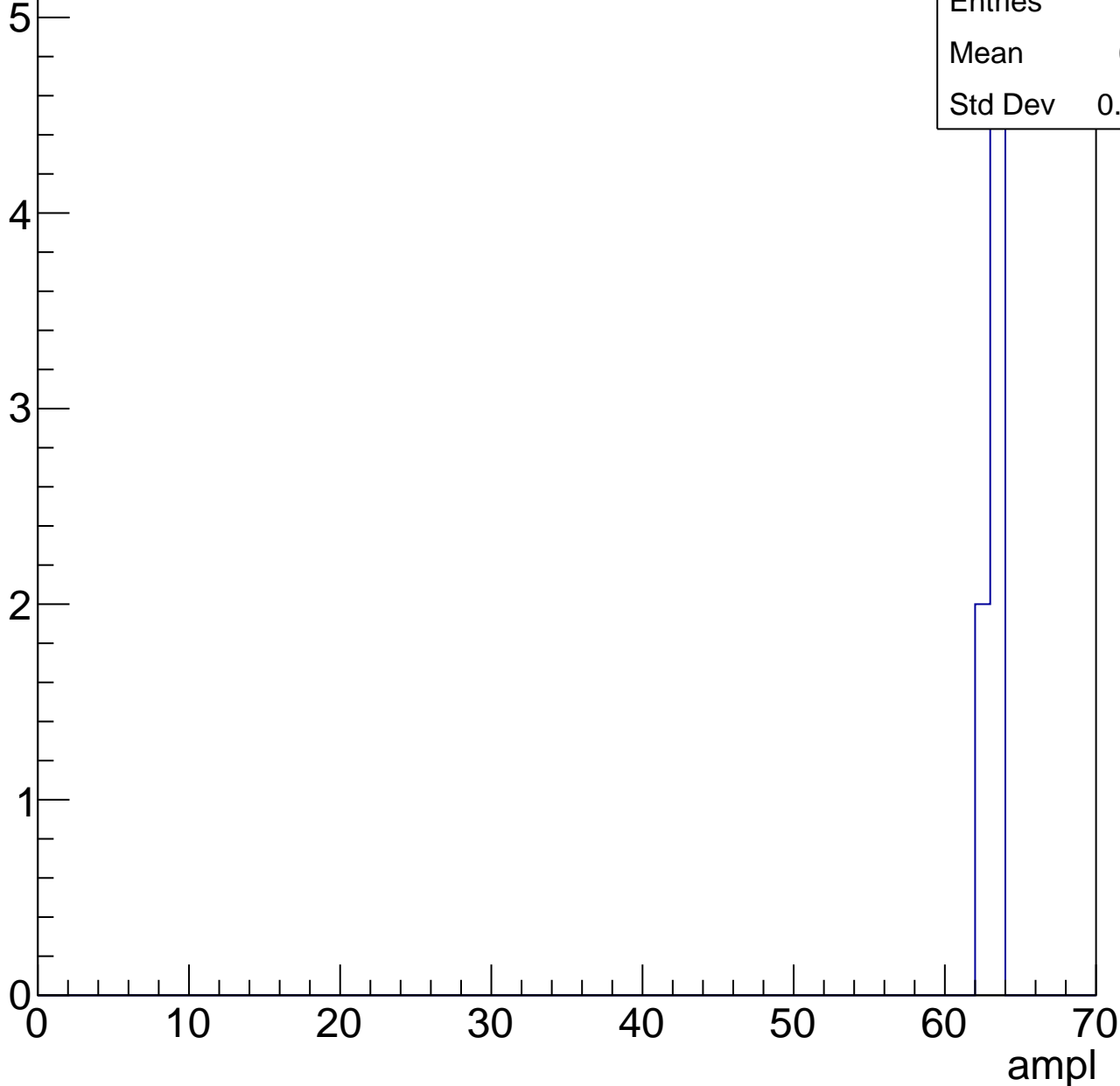


B1L001S, U24-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L001S, U24-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch2, adc0

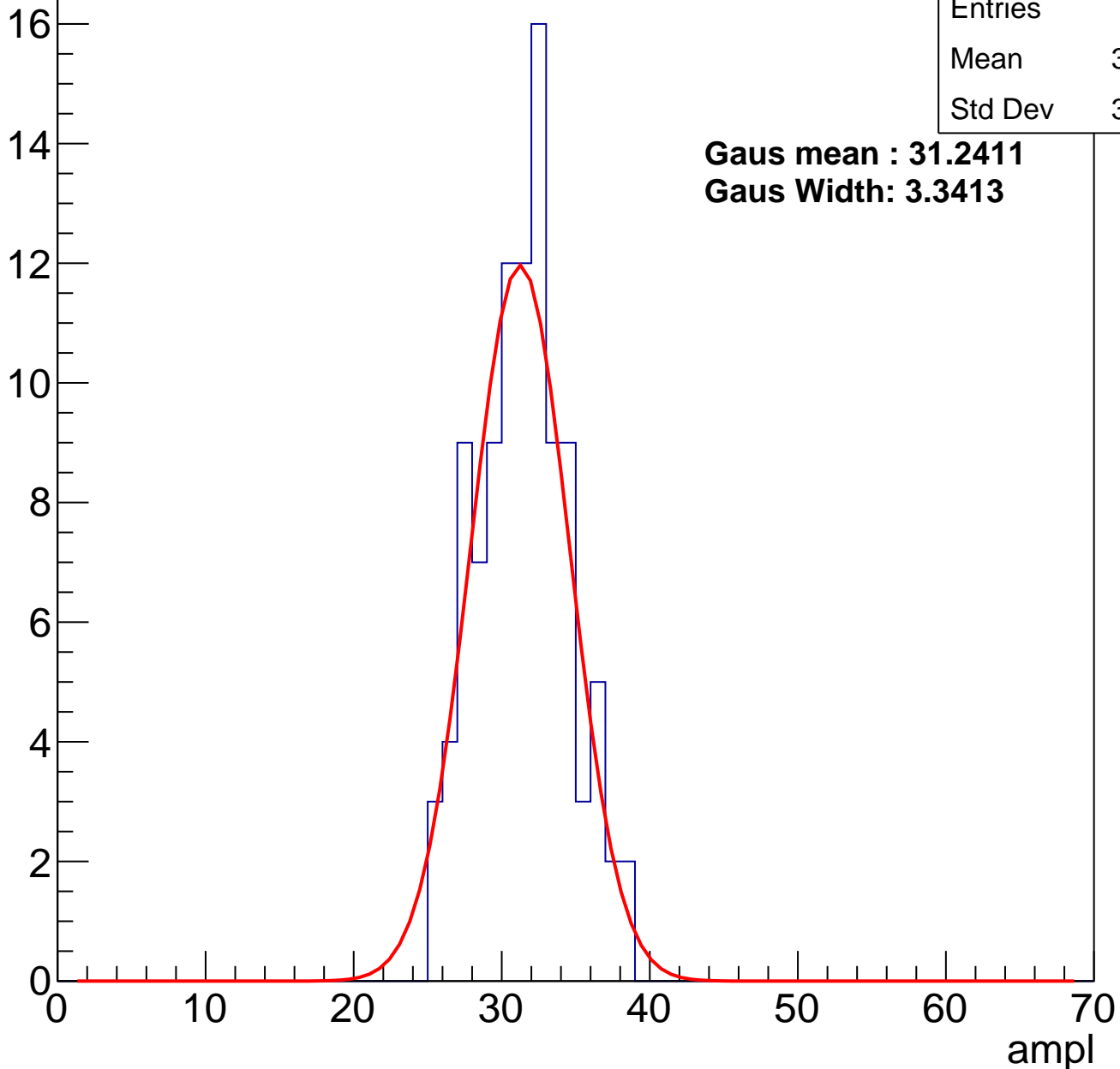
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	30.99
Std Dev	3.034

Gaus mean : 31.2411

Gaus Width: 3.3413

Entry



B1L001S, U24-ch2, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	37.77
Std Dev	3.446

Gaus mean : 38.3269

Gaus Width: 3.3872

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

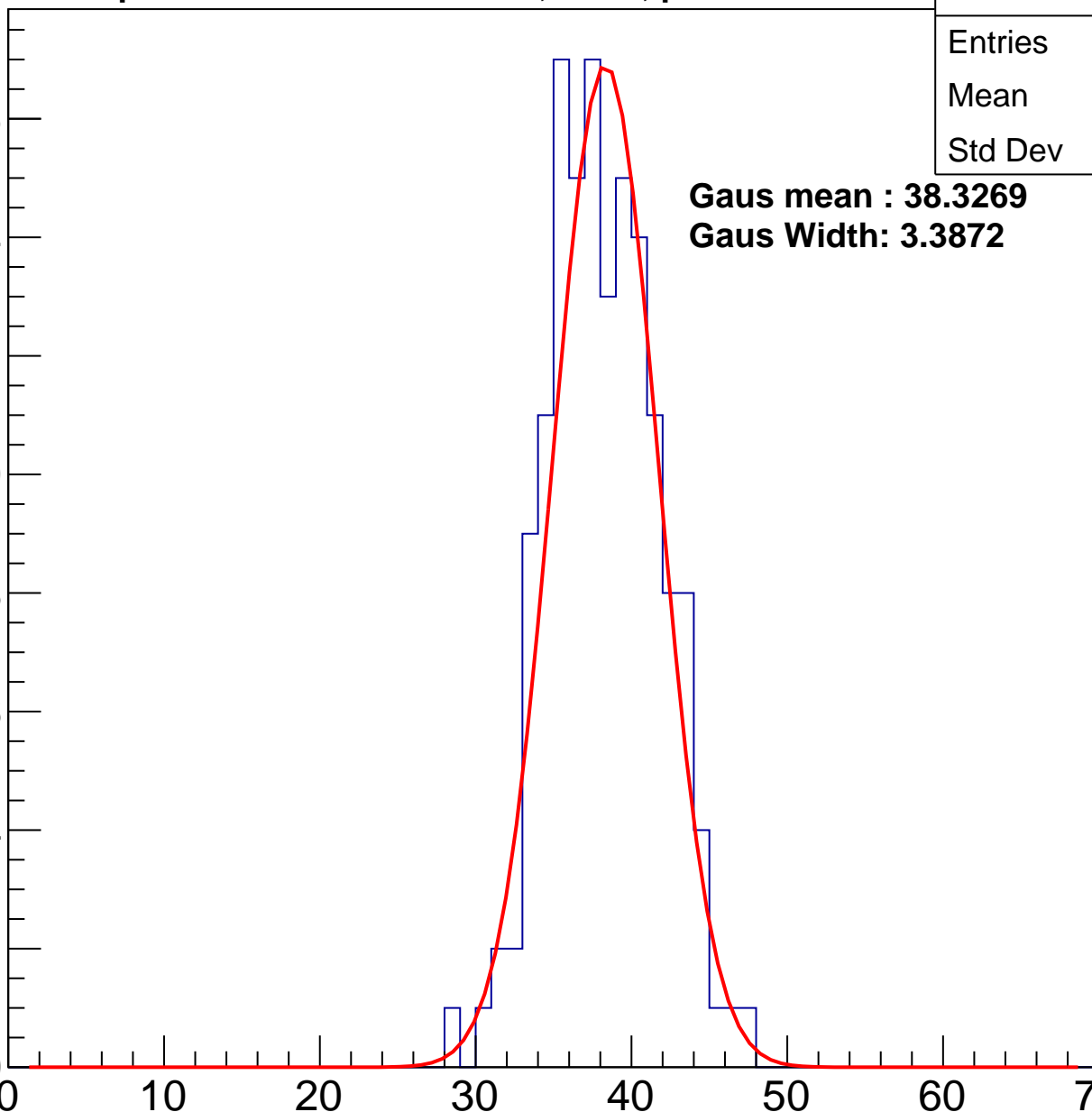
40

50

60

70

ampl



B1L001S, U24-ch2, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

118

Mean

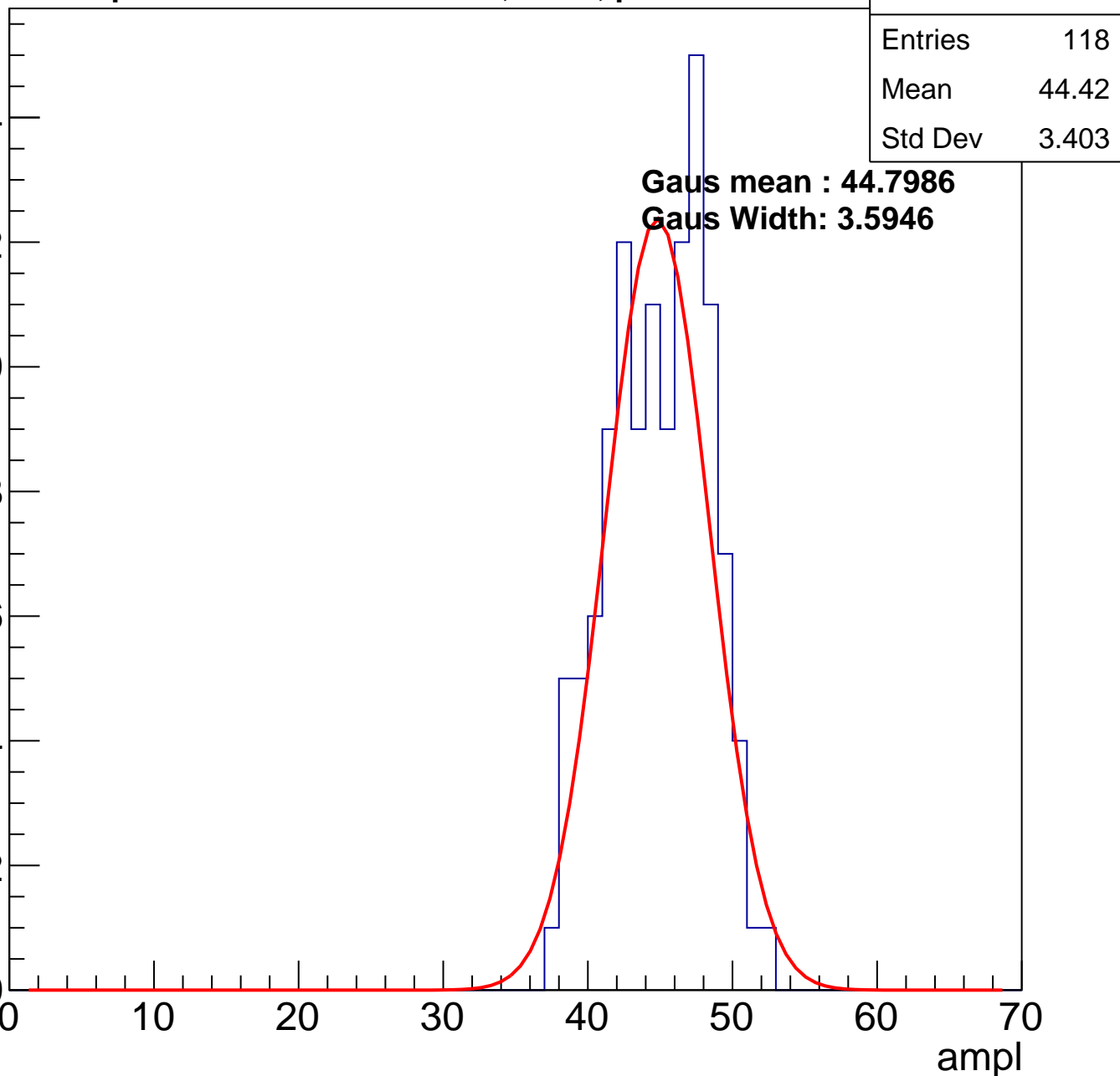
44.42

Std Dev

3.403

Gaus mean : 44.7986

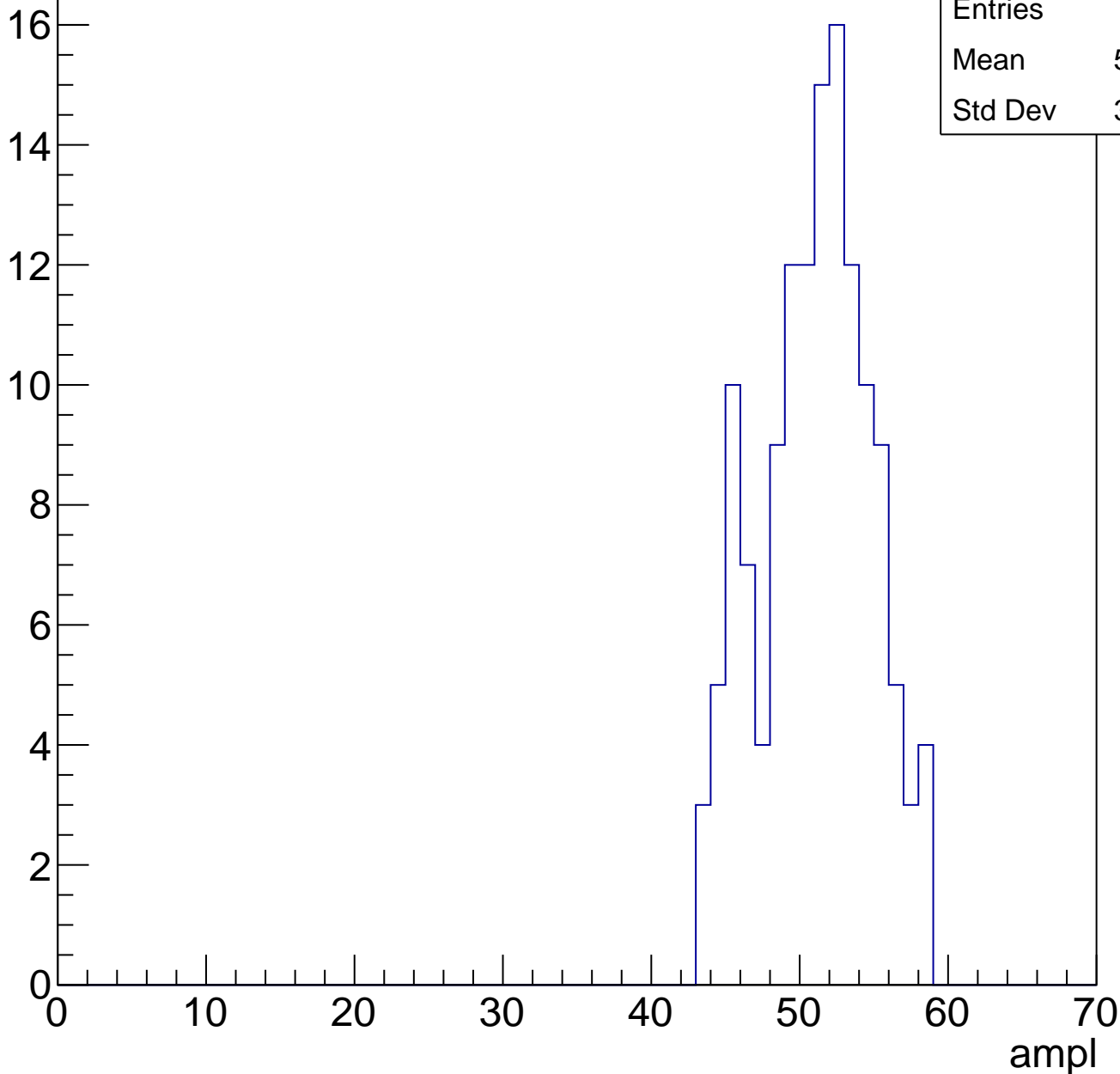
Gaus Width: 3.5946



B1L001S, U24-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

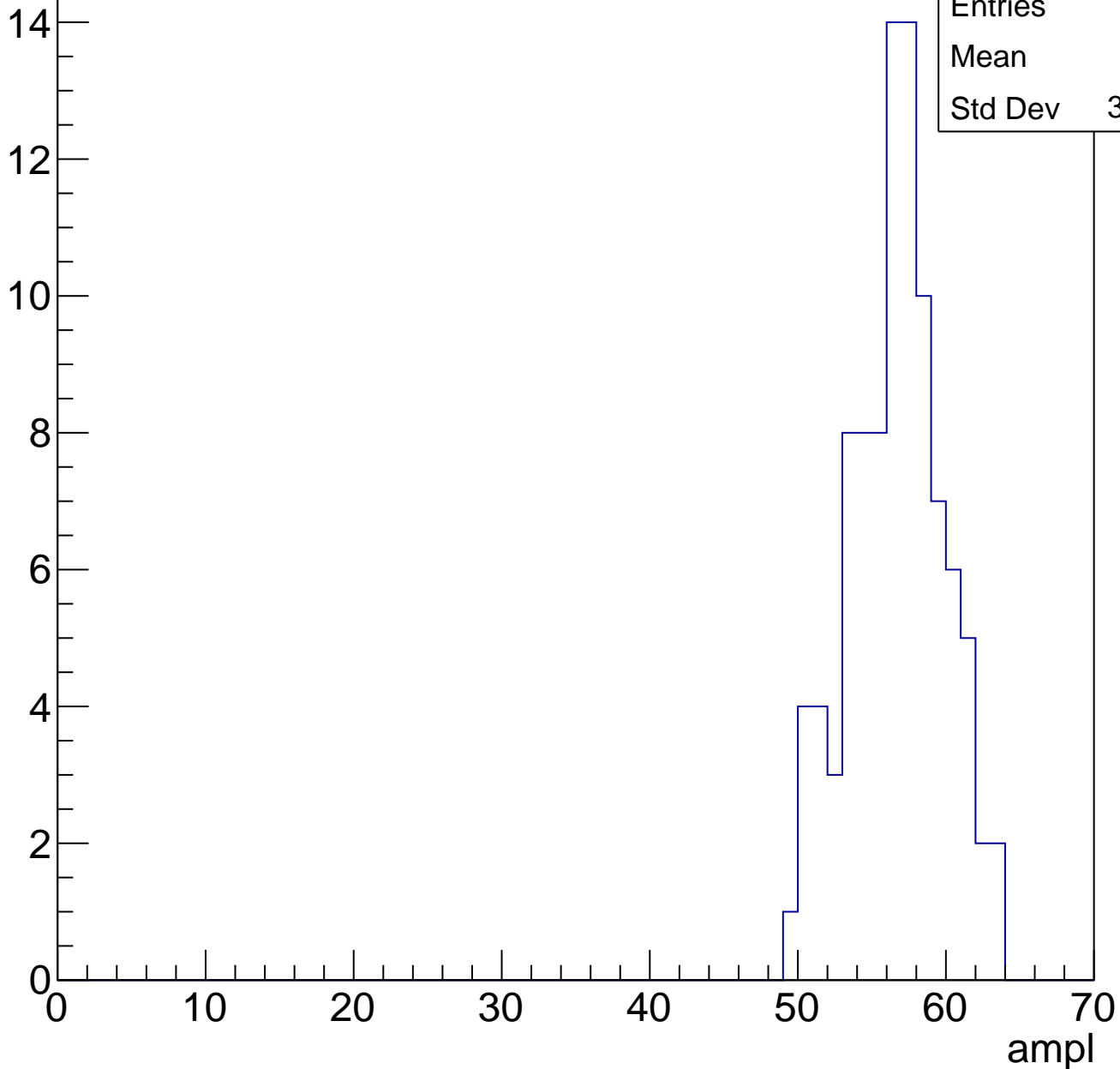
Entry



B1L001S, U24-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	96
Mean	56.2
Std Dev	3.158

B1L001S, U24-ch2, adc5

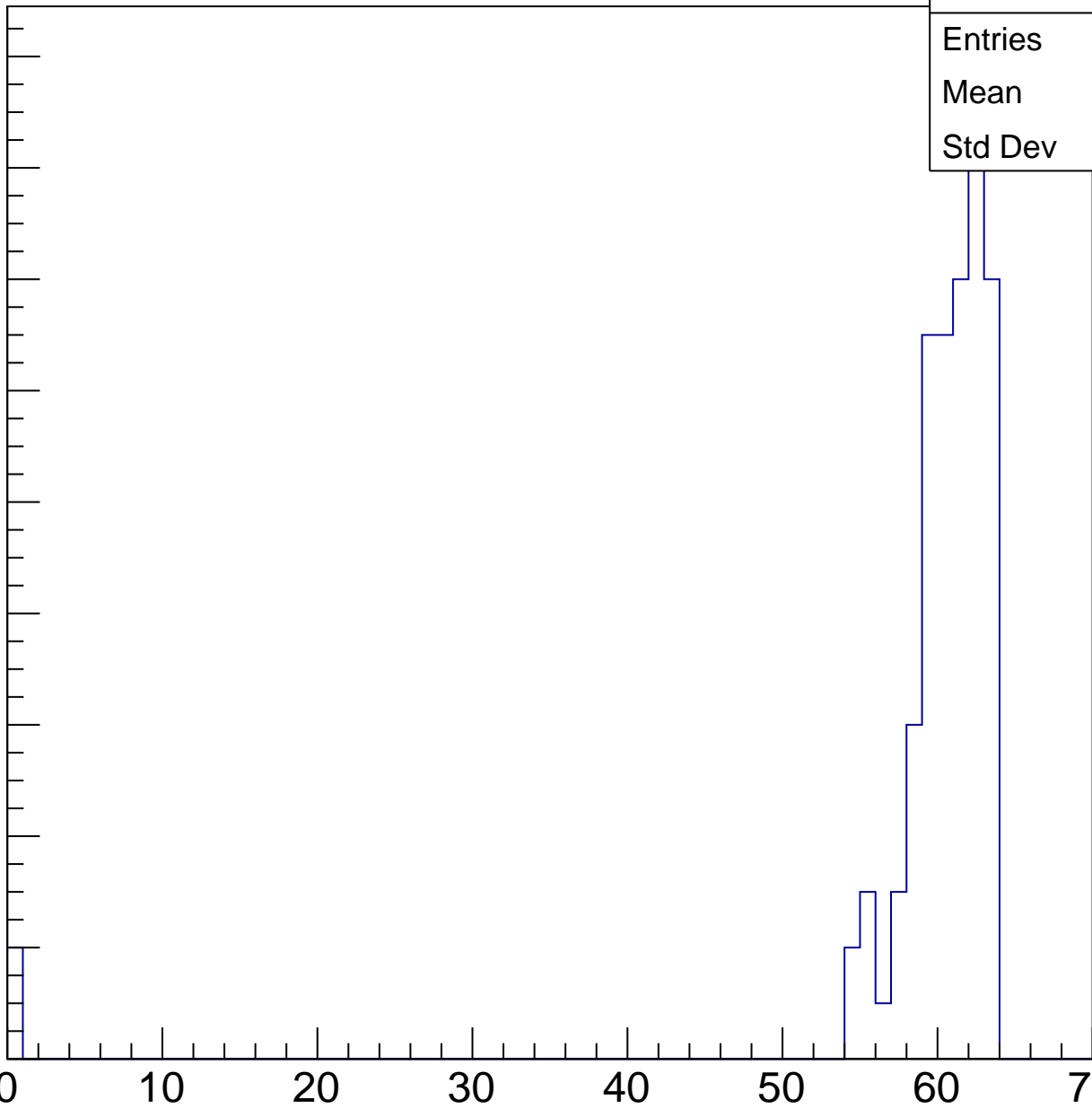
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.96
Std Dev	9.205

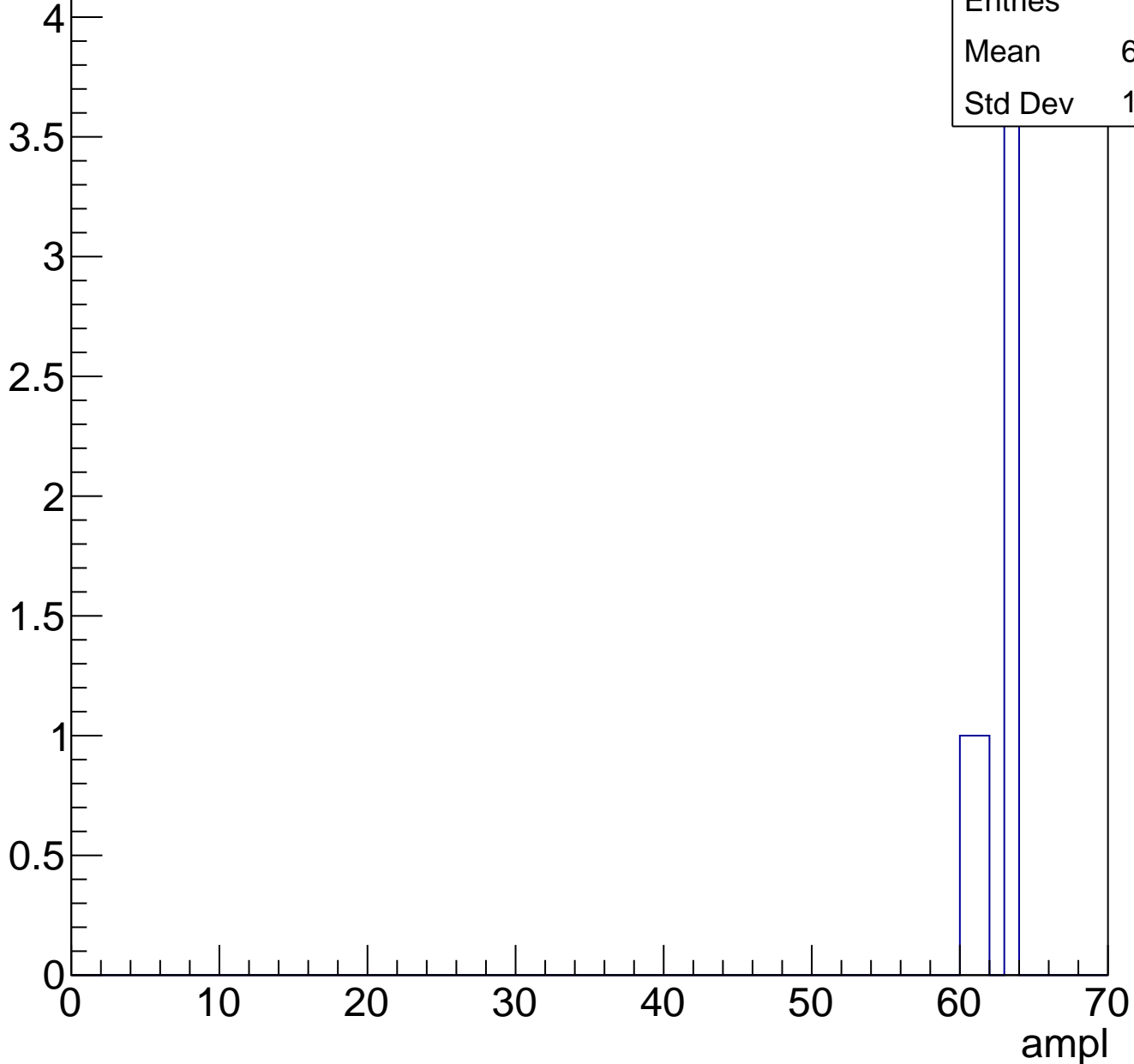
ampl



B1L001S, U24-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U24-ch3, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.76
Std Dev	3.955

Gaus mean : 30.3031

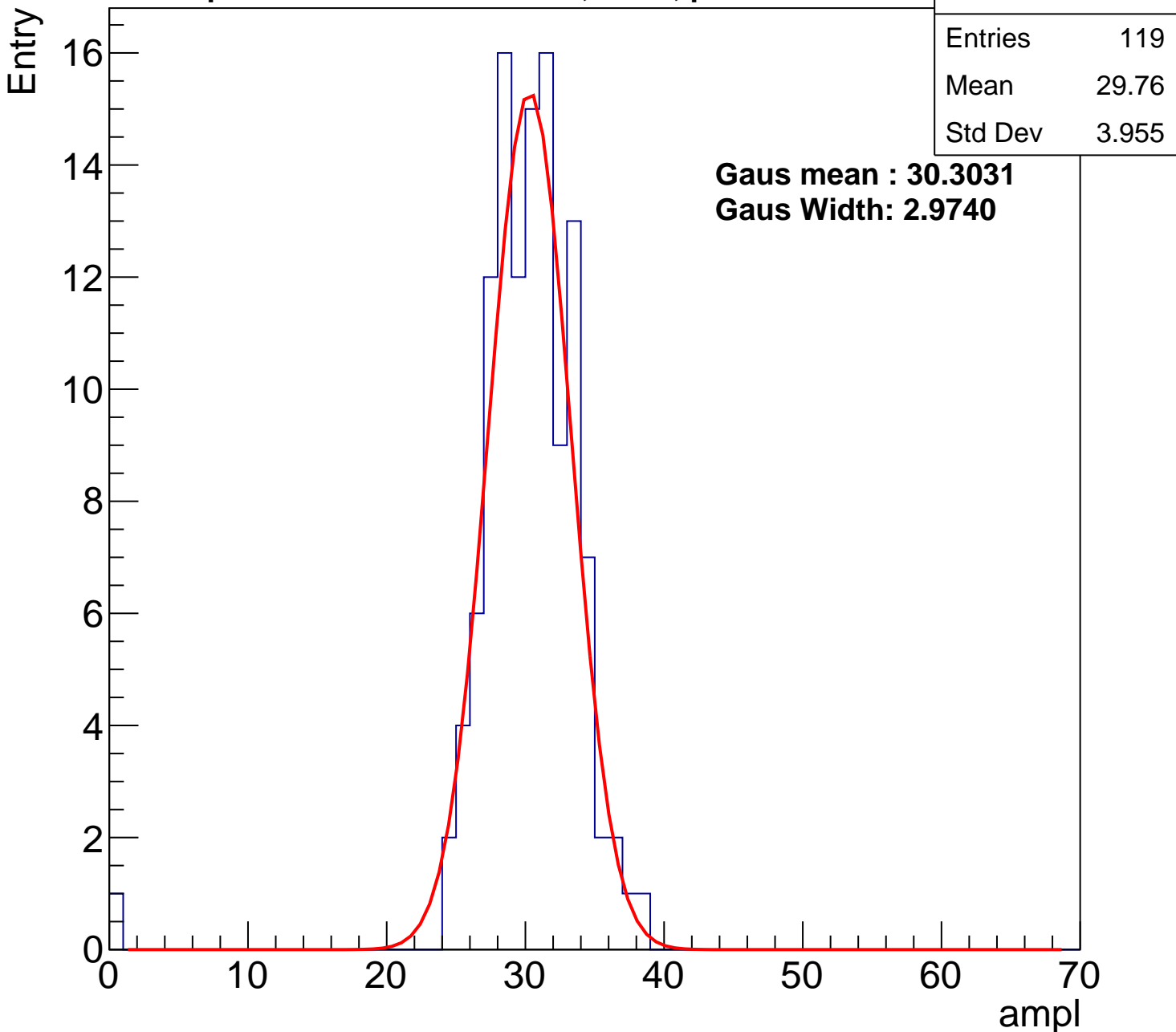
Gaus Width: 2.9740

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch3, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	36.62
Std Dev	3.406

Gaus mean : 37.0966

Gaus Width: 3.4426

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

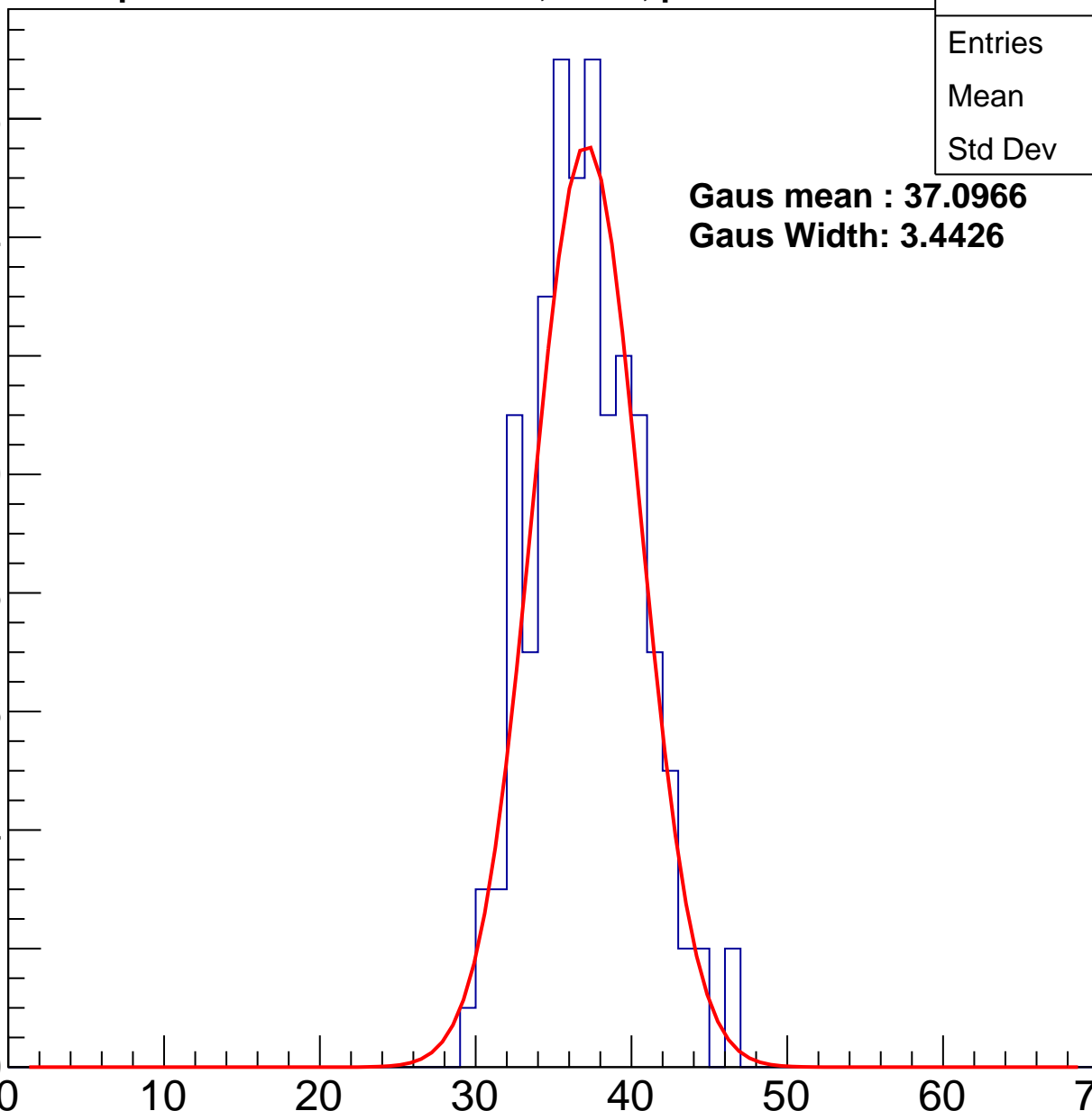
40

50

60

70

ampl



B1L001S, U24-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

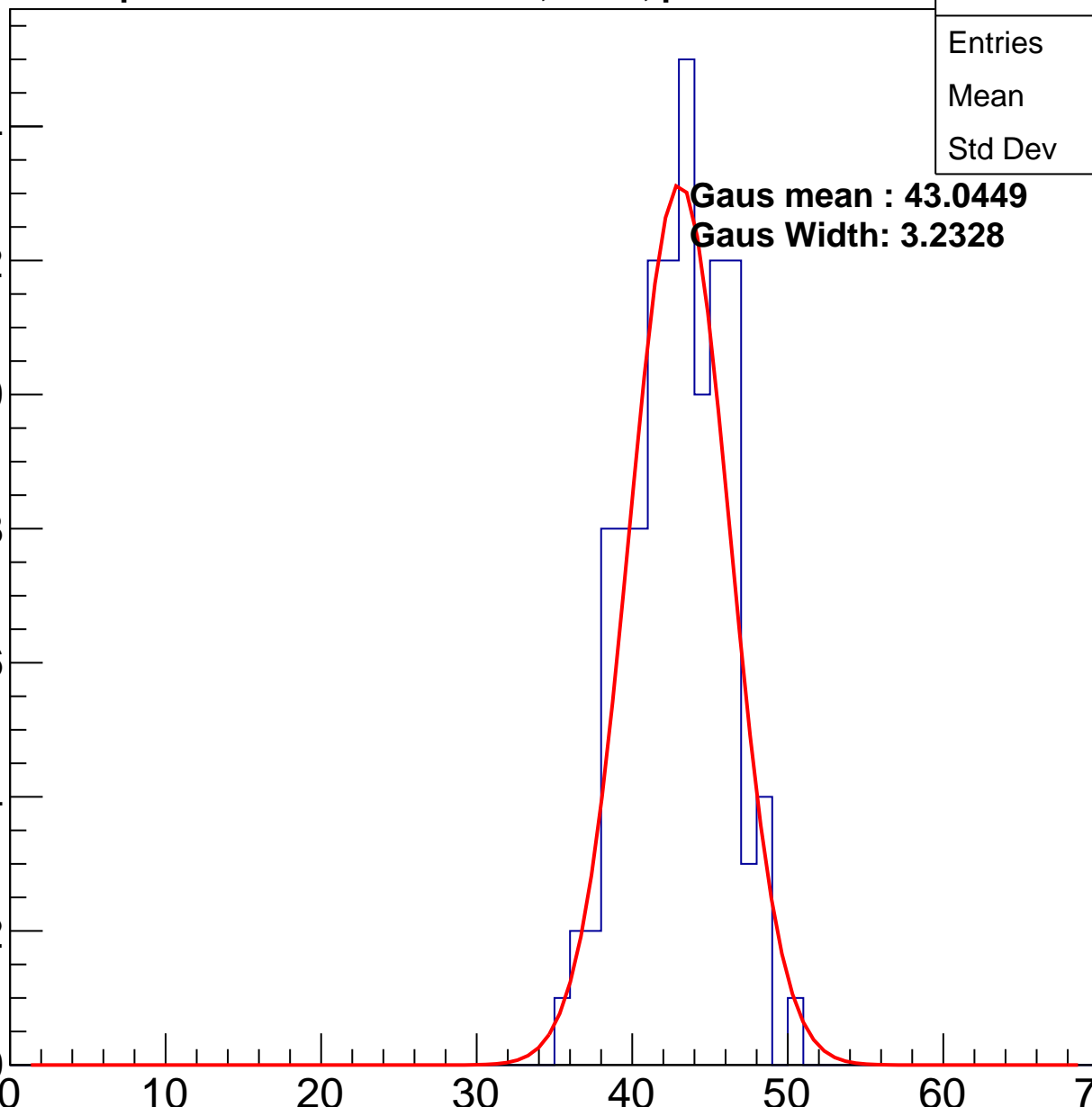
70

ampl

Entries	110
Mean	42.48
Std Dev	3.056

Gaus mean : 43.0449

Gaus Width: 3.2328

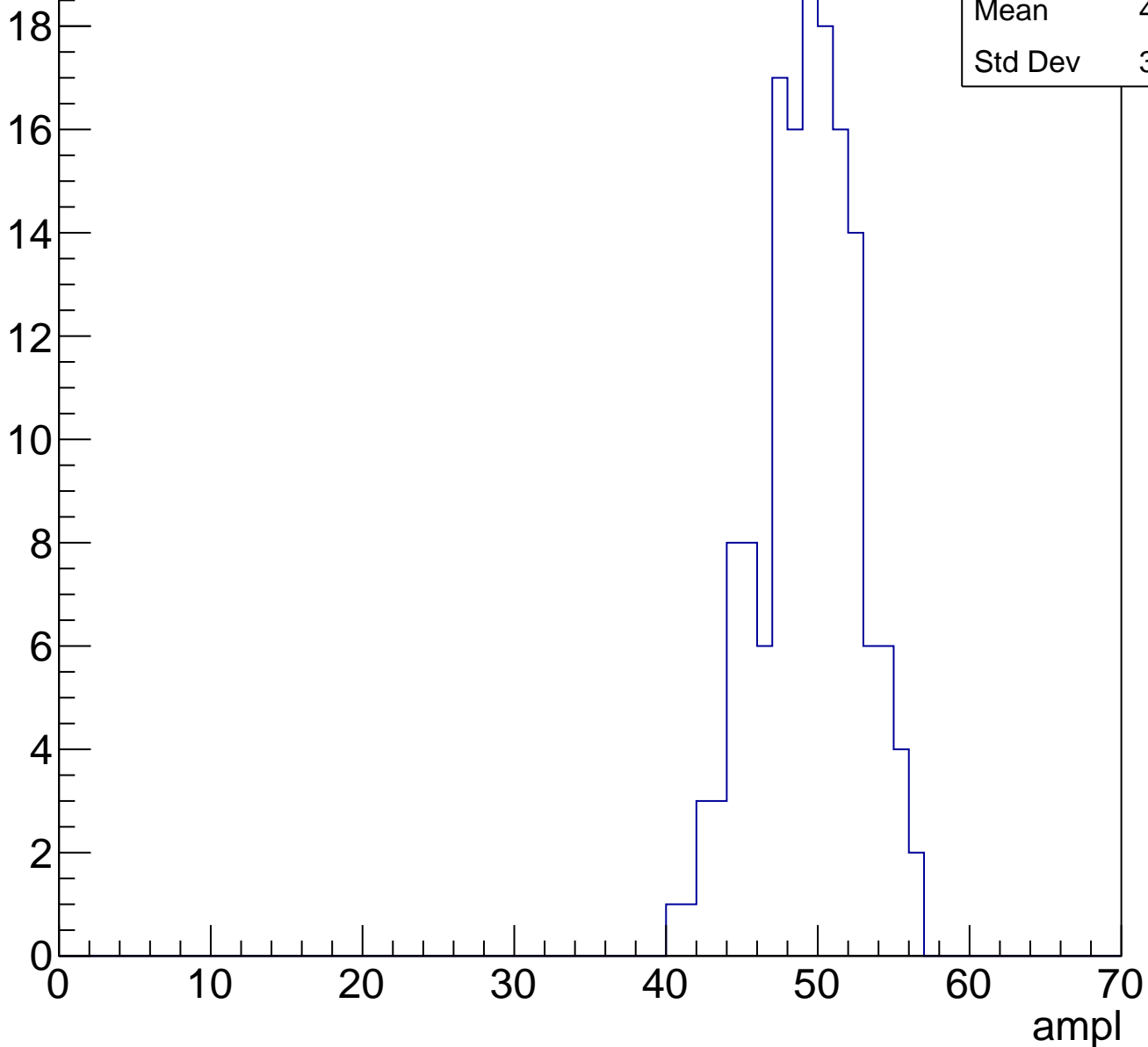


B1L001S, U24-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	48.92
Std Dev	3.248

Entry



B1L001S, U24-ch3, adc4

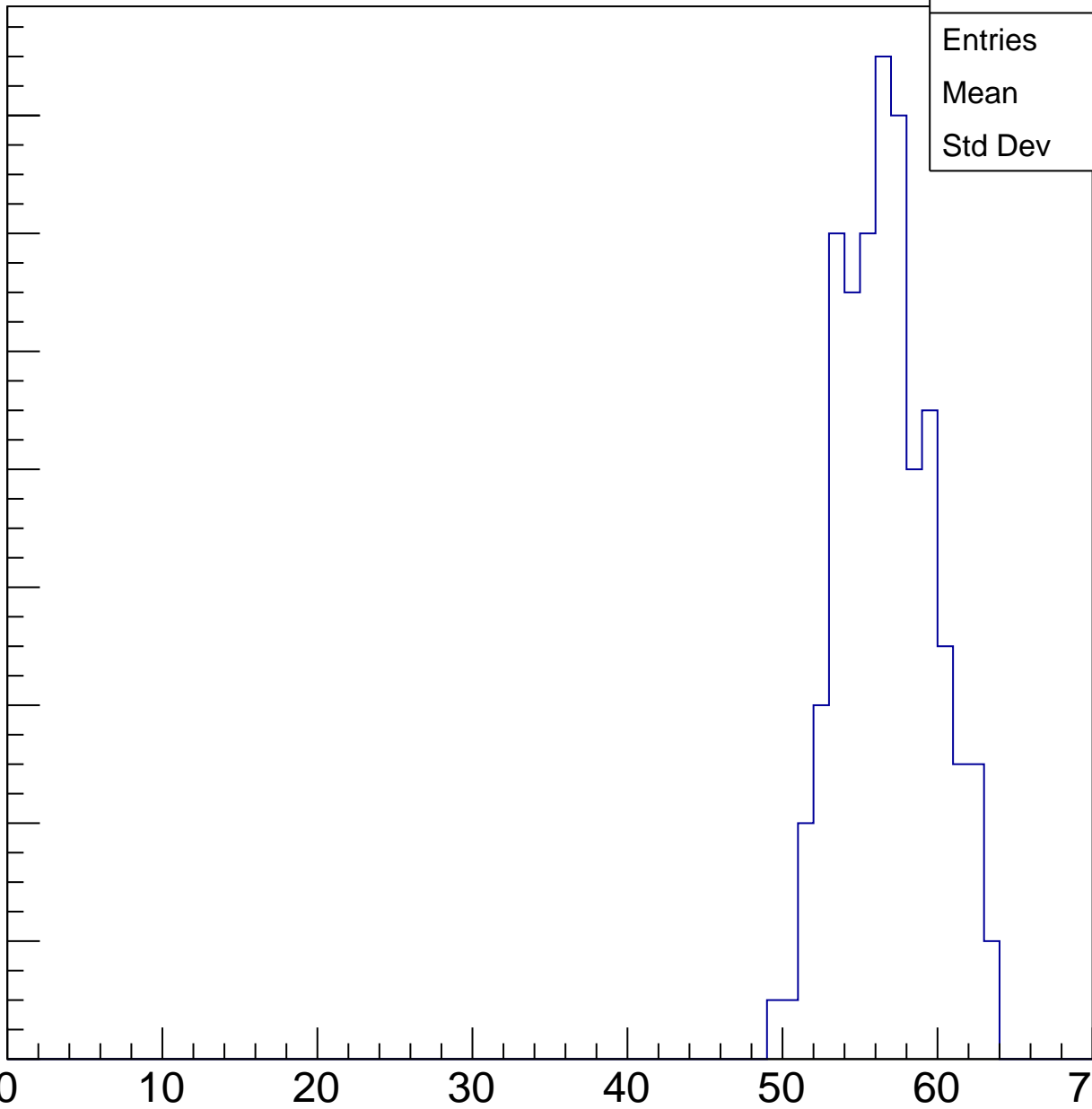
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.21
Std Dev	3.004

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch3, adc5

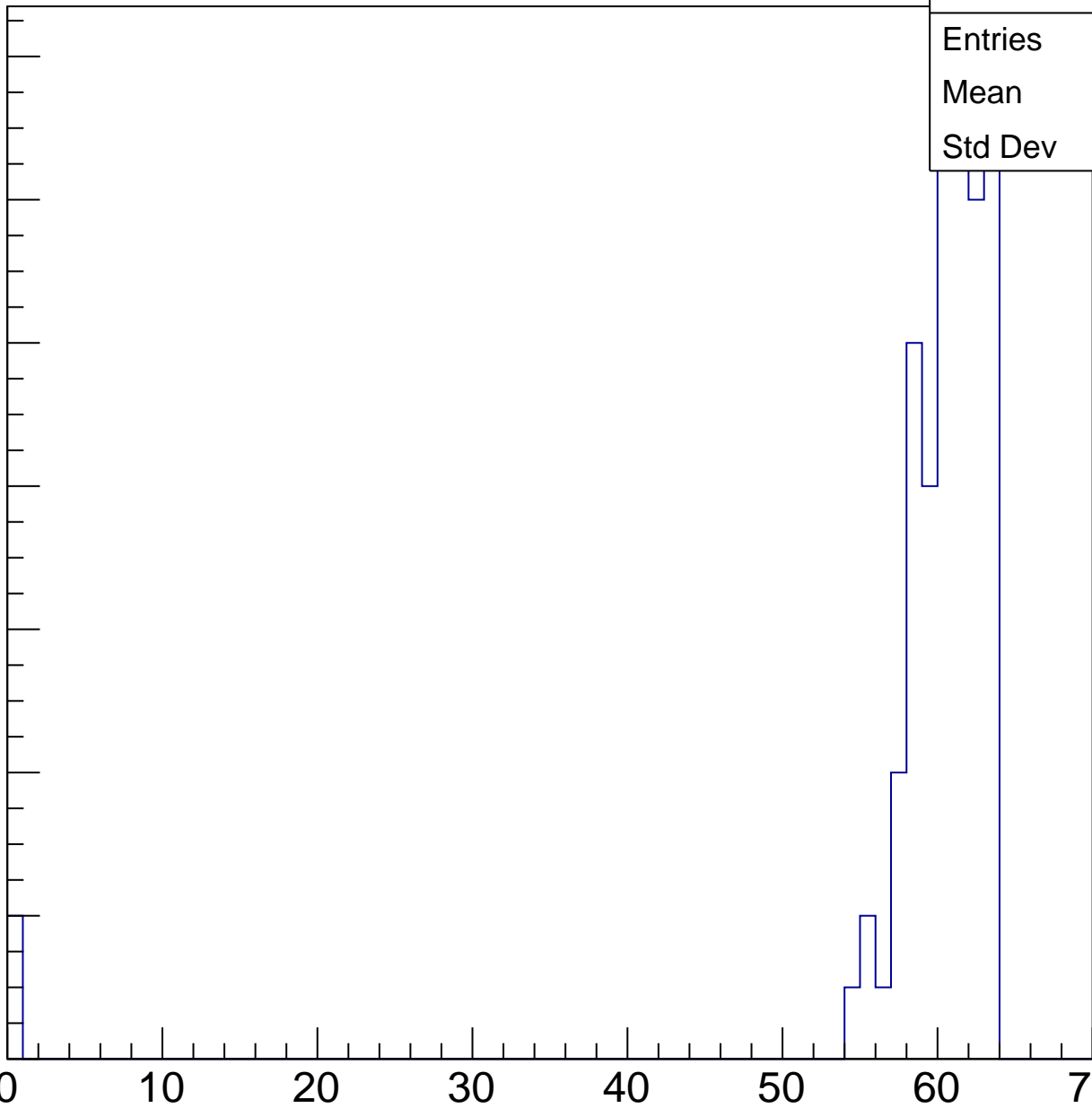
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.71
Std Dev	9.639

ampl

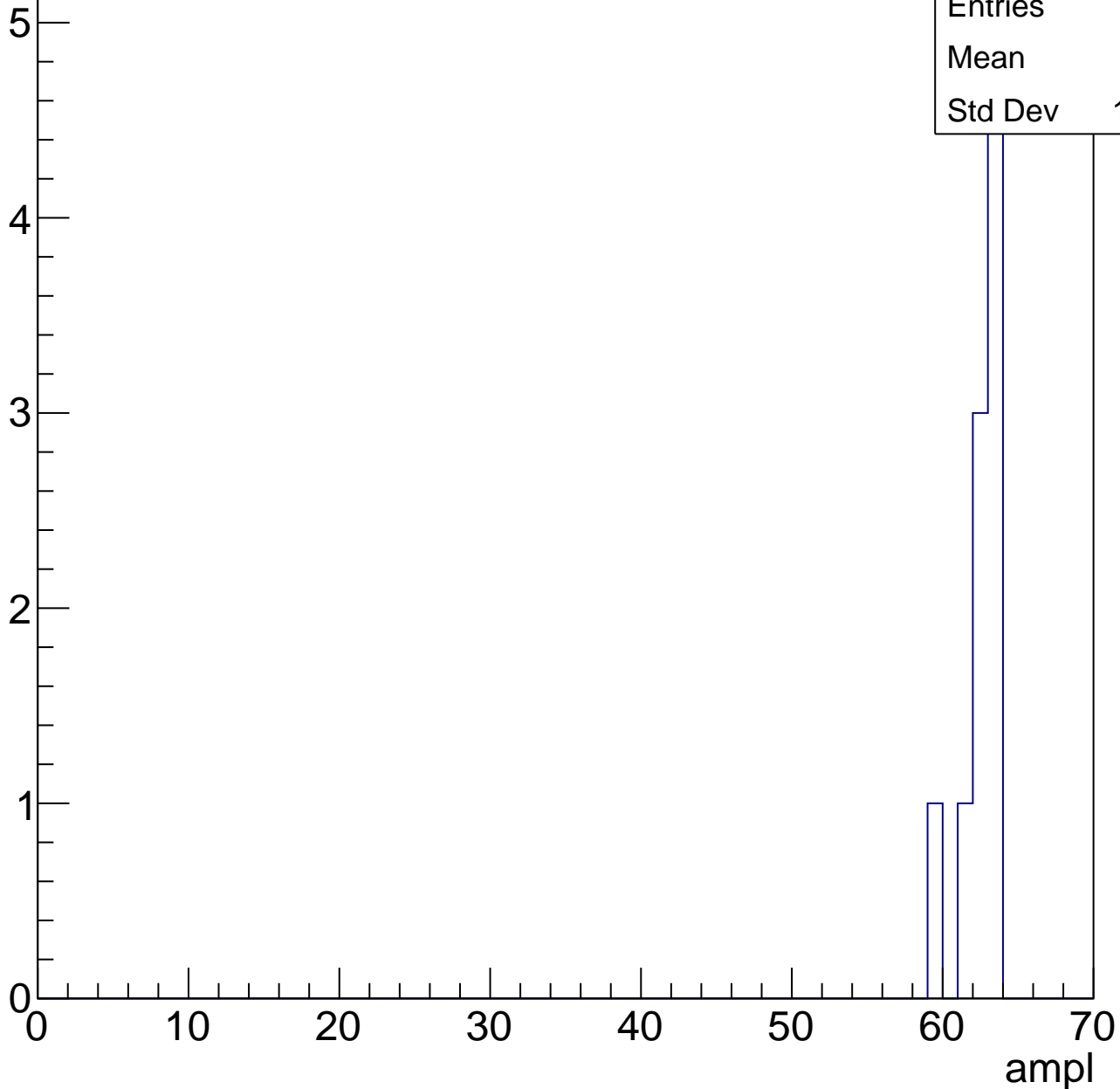


B1L001S, U24-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62.1
Std Dev	1.221



B1L001S, U24-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



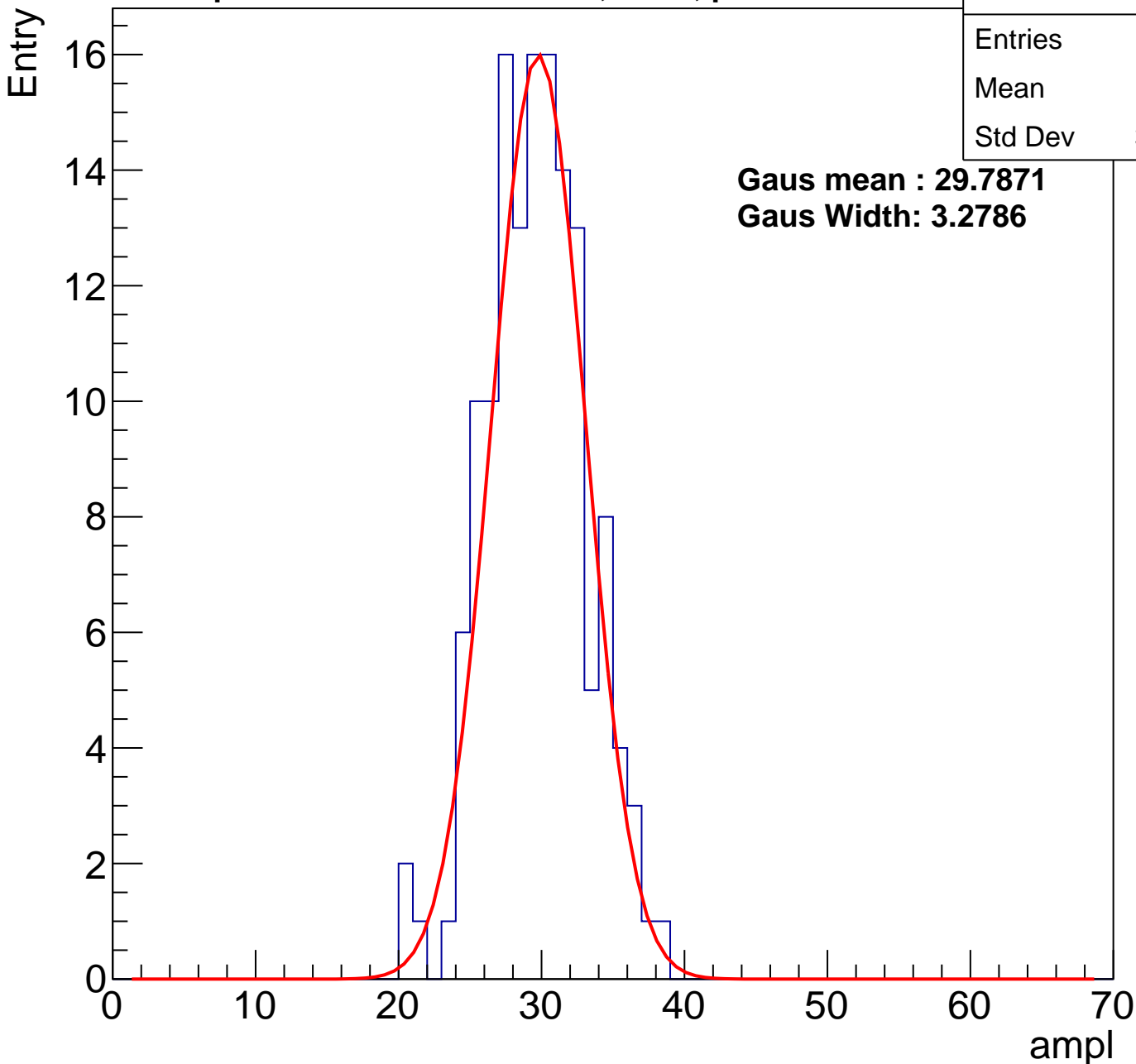
B1L001S, U24-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.2
Std Dev	3.421

Gaus mean : 29.7871

Gaus Width: 3.2786



B1L001S, U24-ch4, adc1

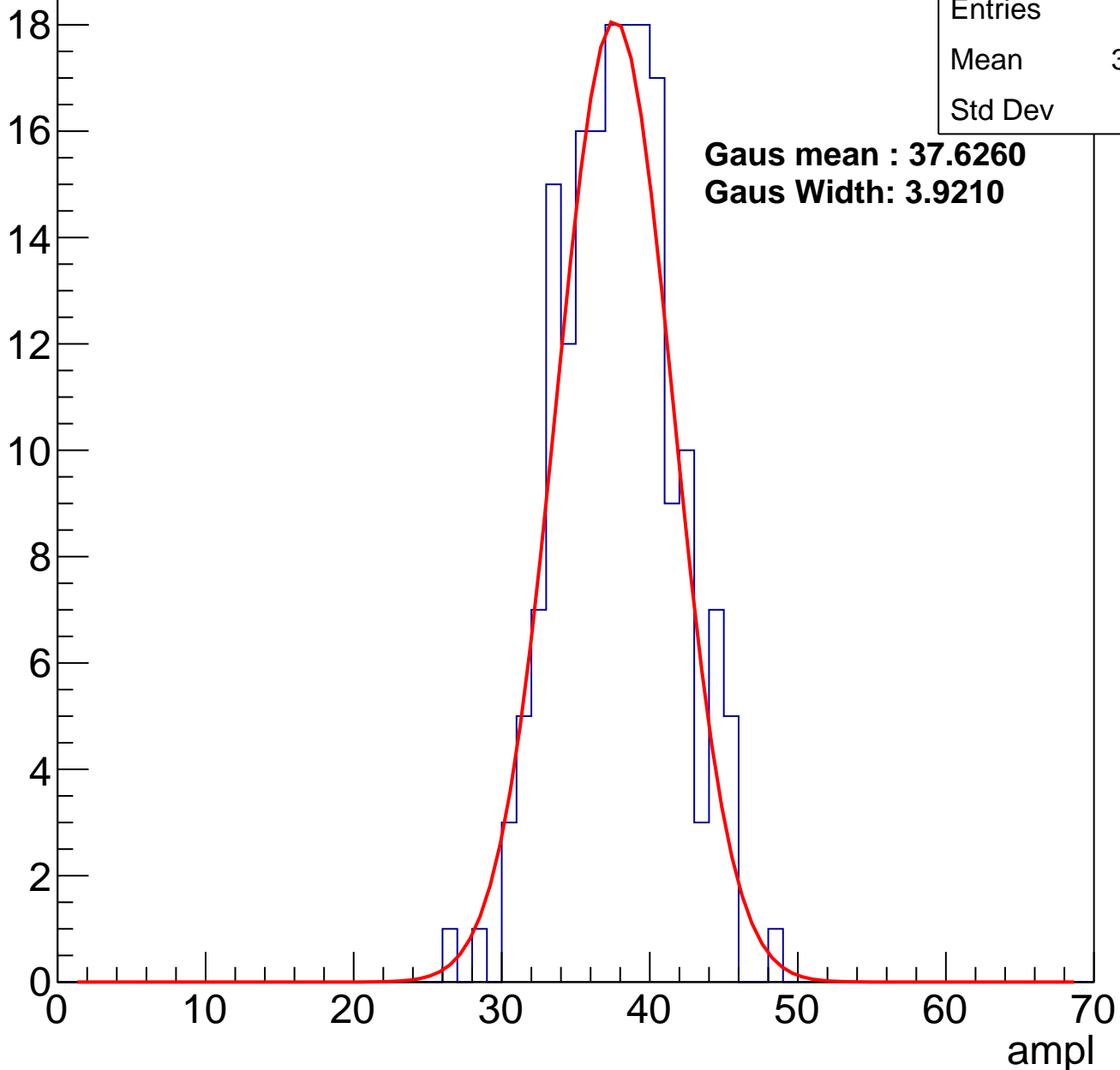
calib_packv5_042523_0143.root, FC#2, port C2

Entries	182
Mean	37.32
Std Dev	3.81

Gaus mean : 37.6260

Gaus Width: 3.9210

Entry



B1L001S, U24-ch4, adc2

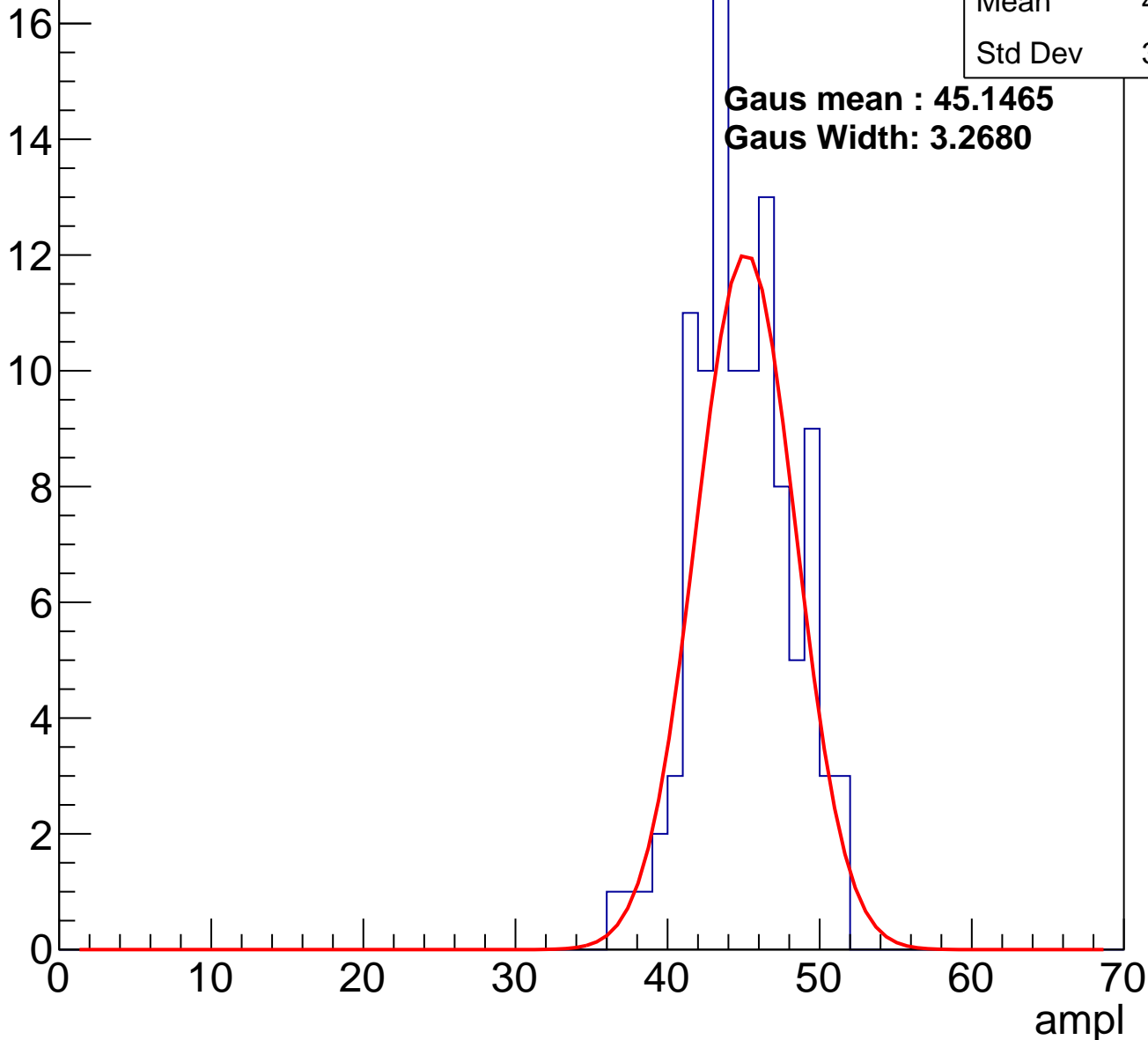
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	107
Mean	44.48
Std Dev	3.166

Gaus mean : 45.1465

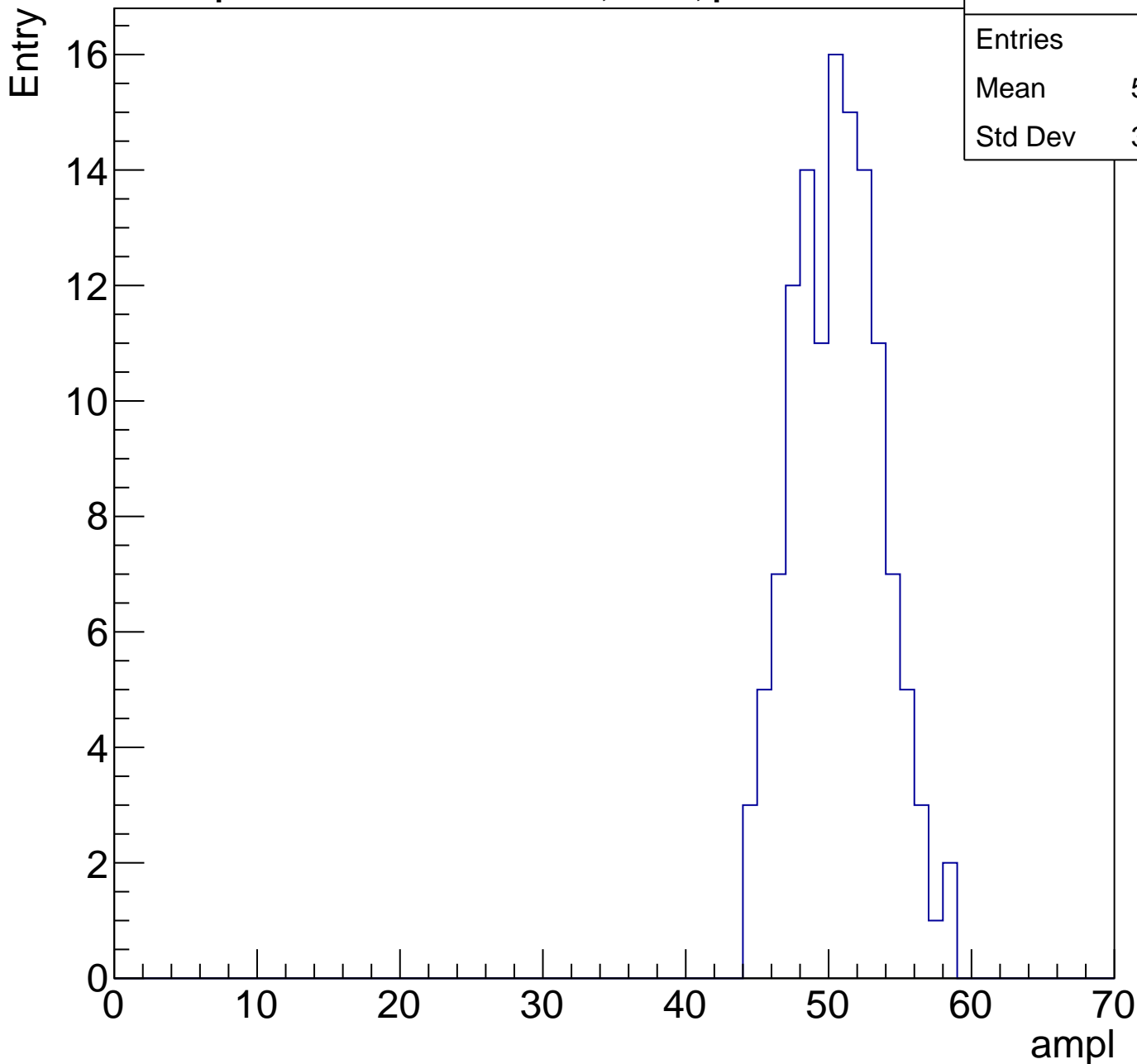
Gaus Width: 3.2680



B1L001S, U24-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

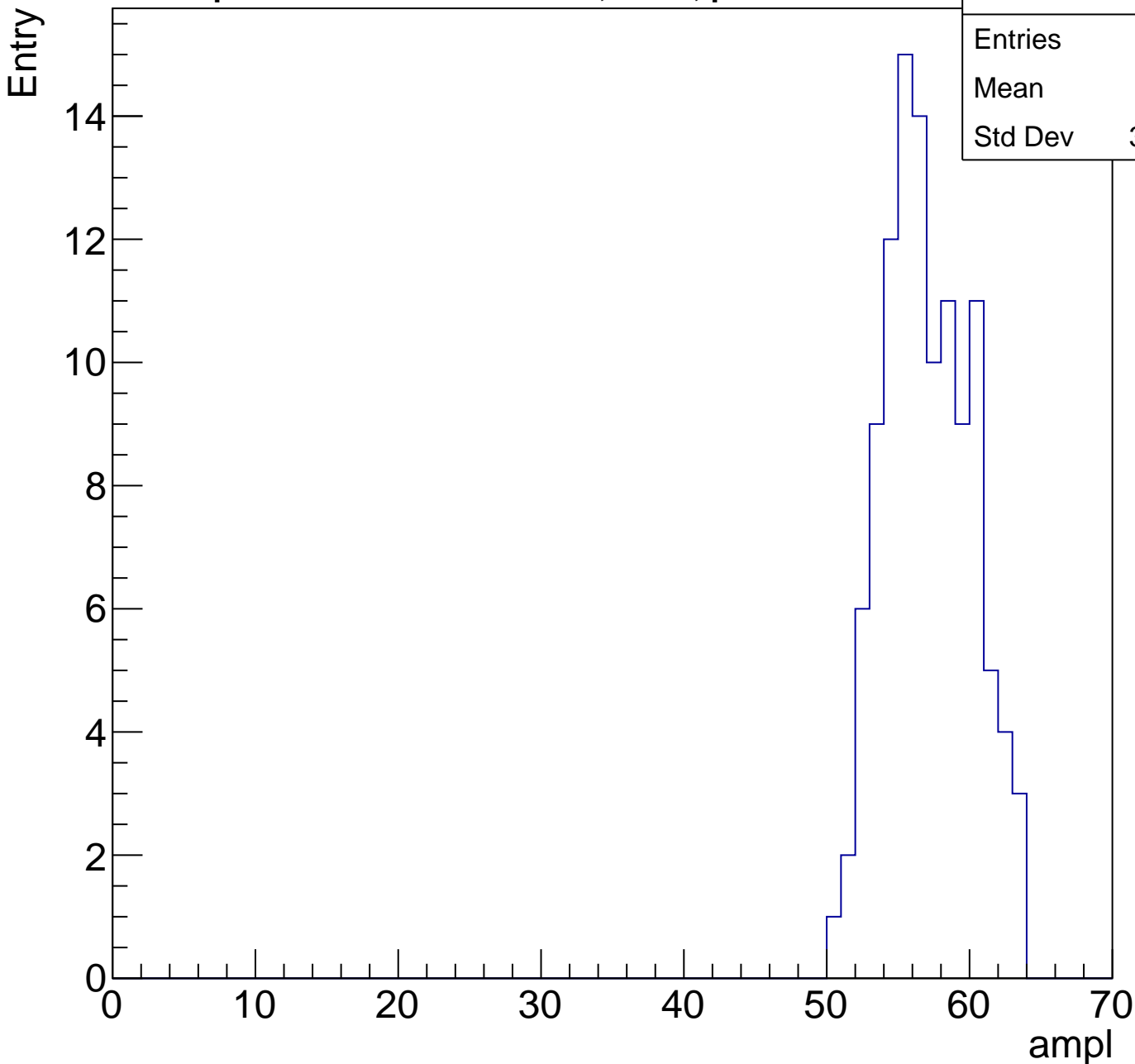
Entries	126
Mean	50.19
Std Dev	3.096



B1L001S, U24-ch4, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	56.6
Std Dev	3.007



B1L001S, U24-ch4, adc5

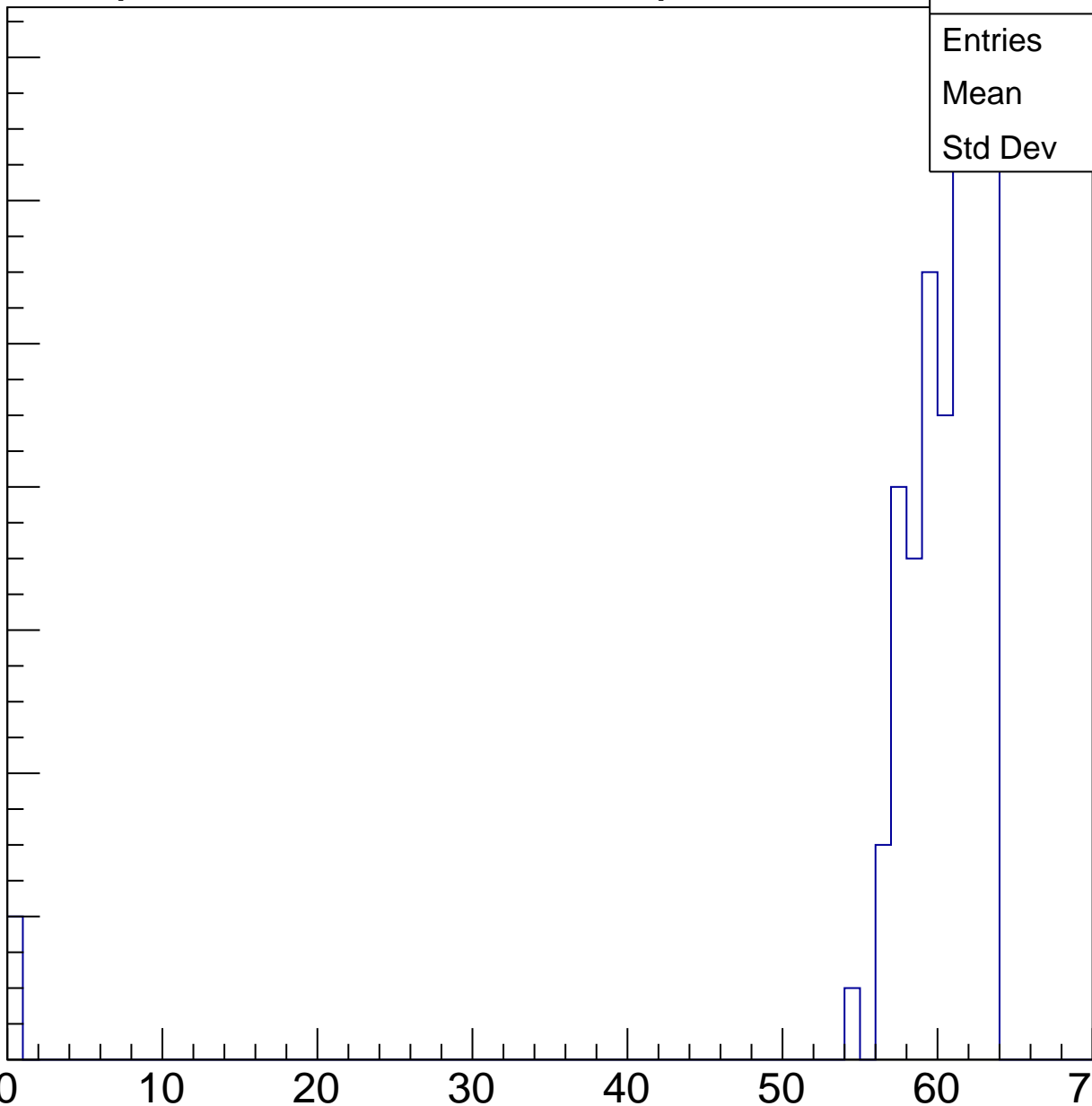
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	58.71
Std Dev	9.529

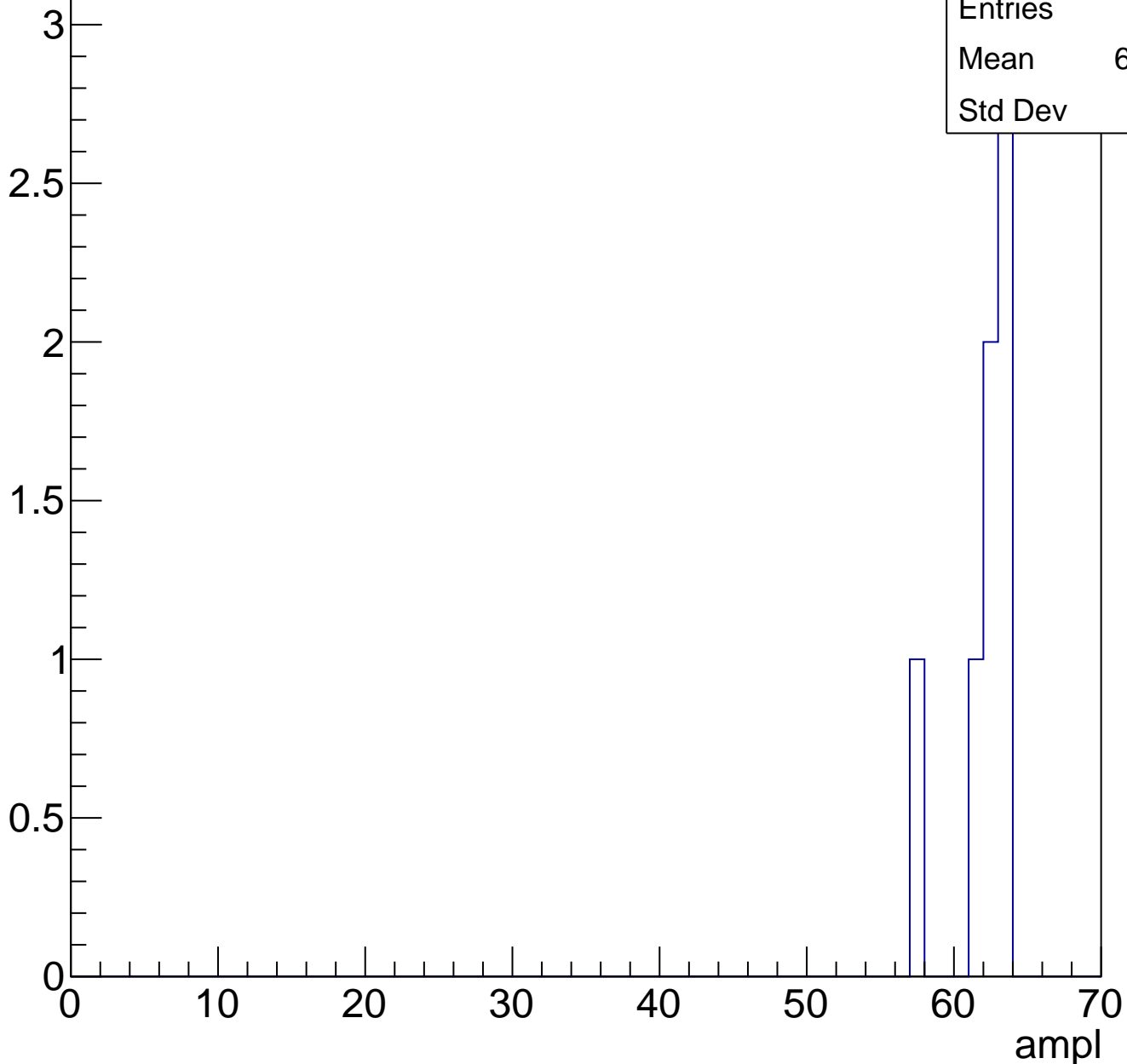
ampl



B1L001S, U24-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

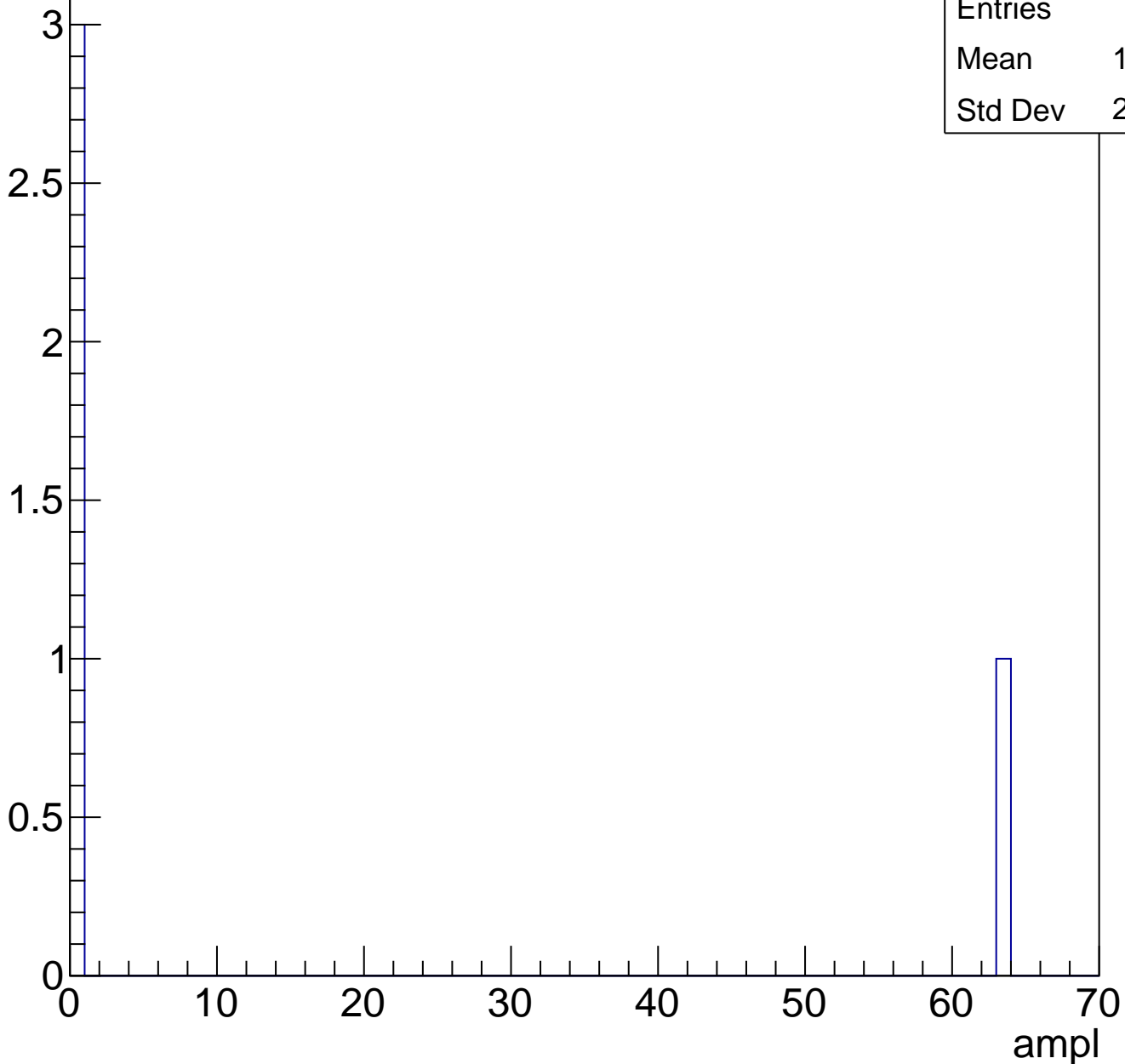
Entry



B1L001S, U24-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch5, adc0

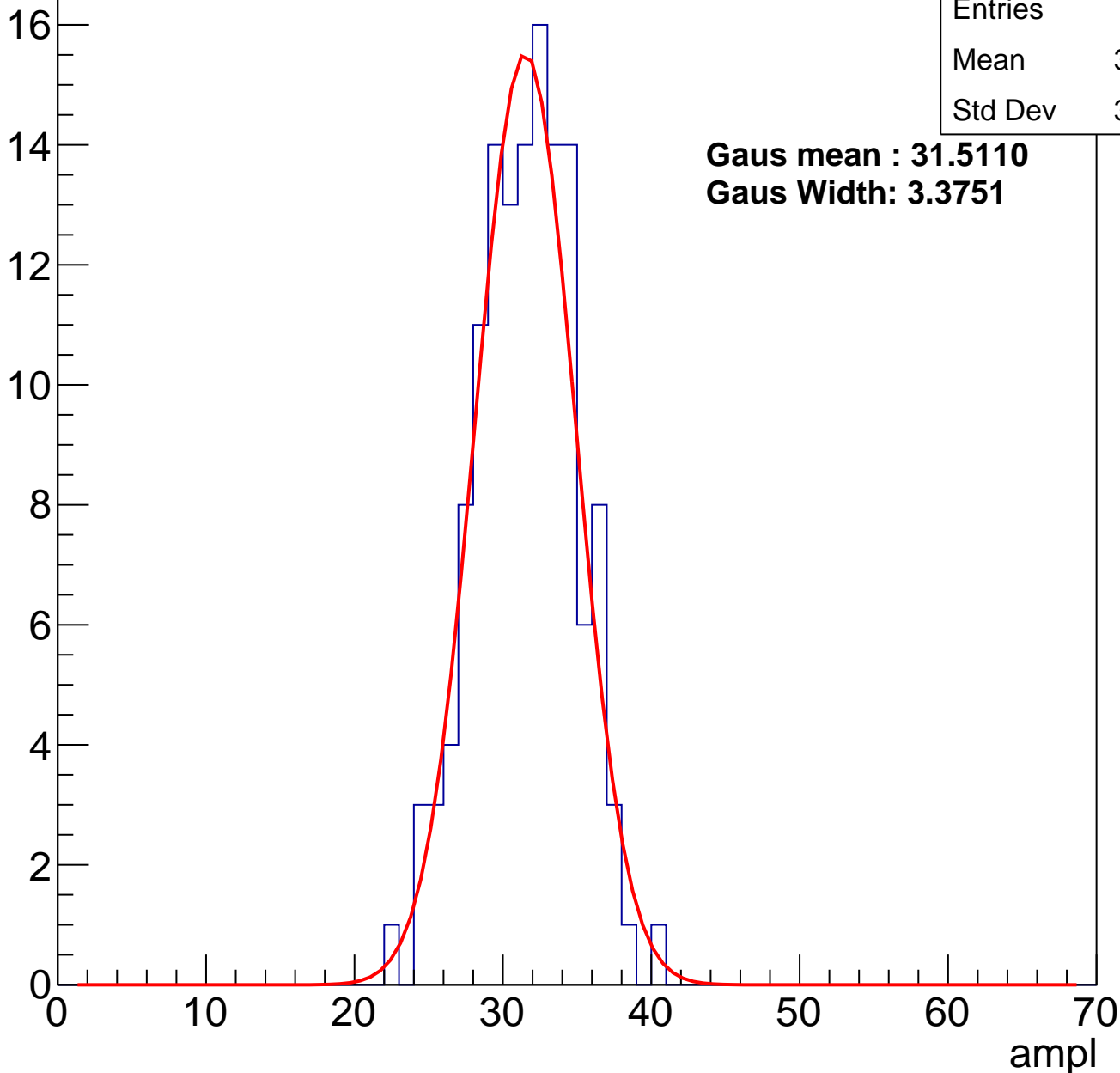
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	31.07
Std Dev	3.305

Gaus mean : 31.5110

Gaus Width: 3.3751

Entry



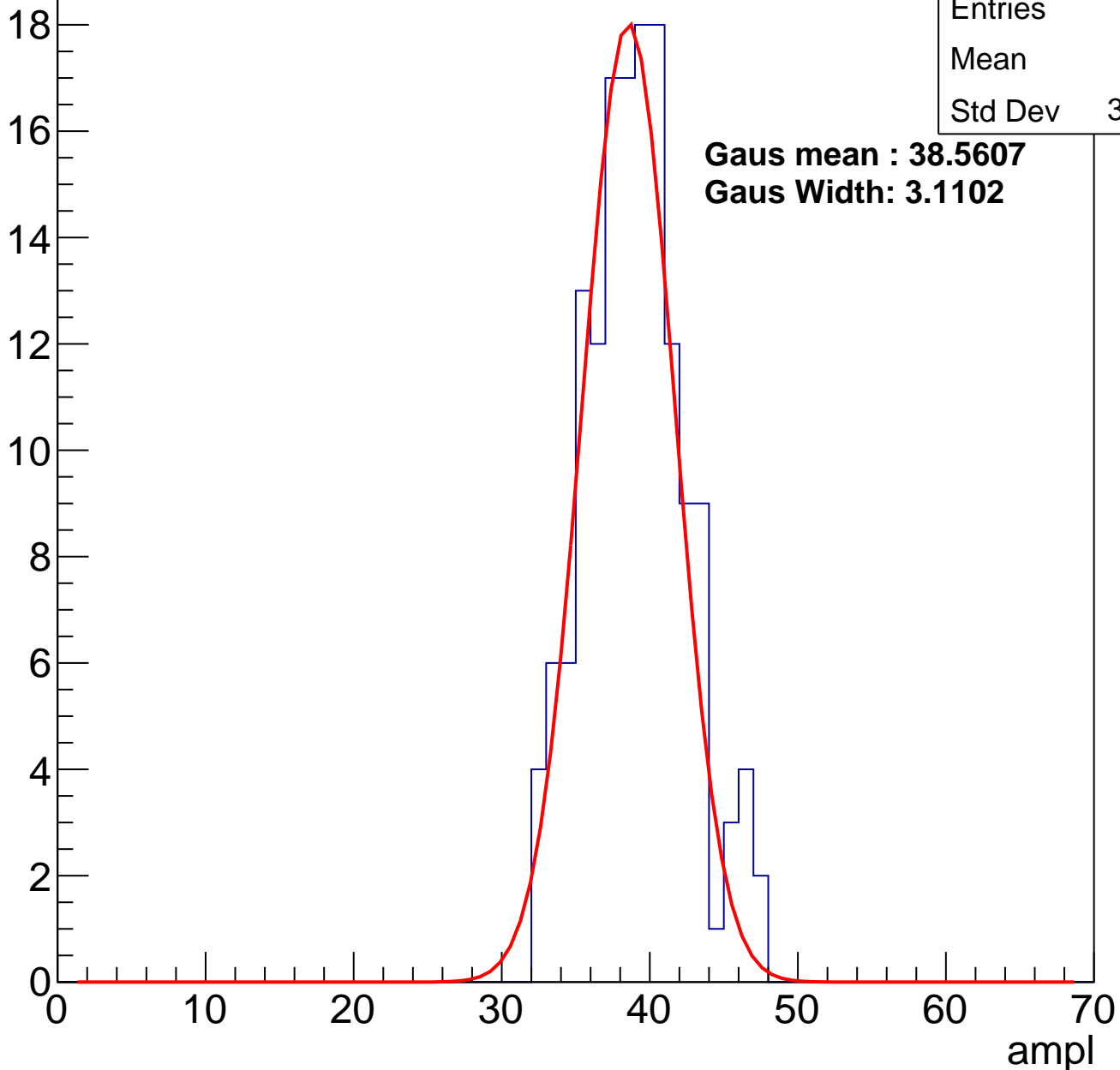
B1L001S, U24-ch5, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	38.6
Std Dev	3.342

Gaus mean : 38.5607
Gaus Width: 3.1102

Entry



B1L001S, U24-ch5, adc2

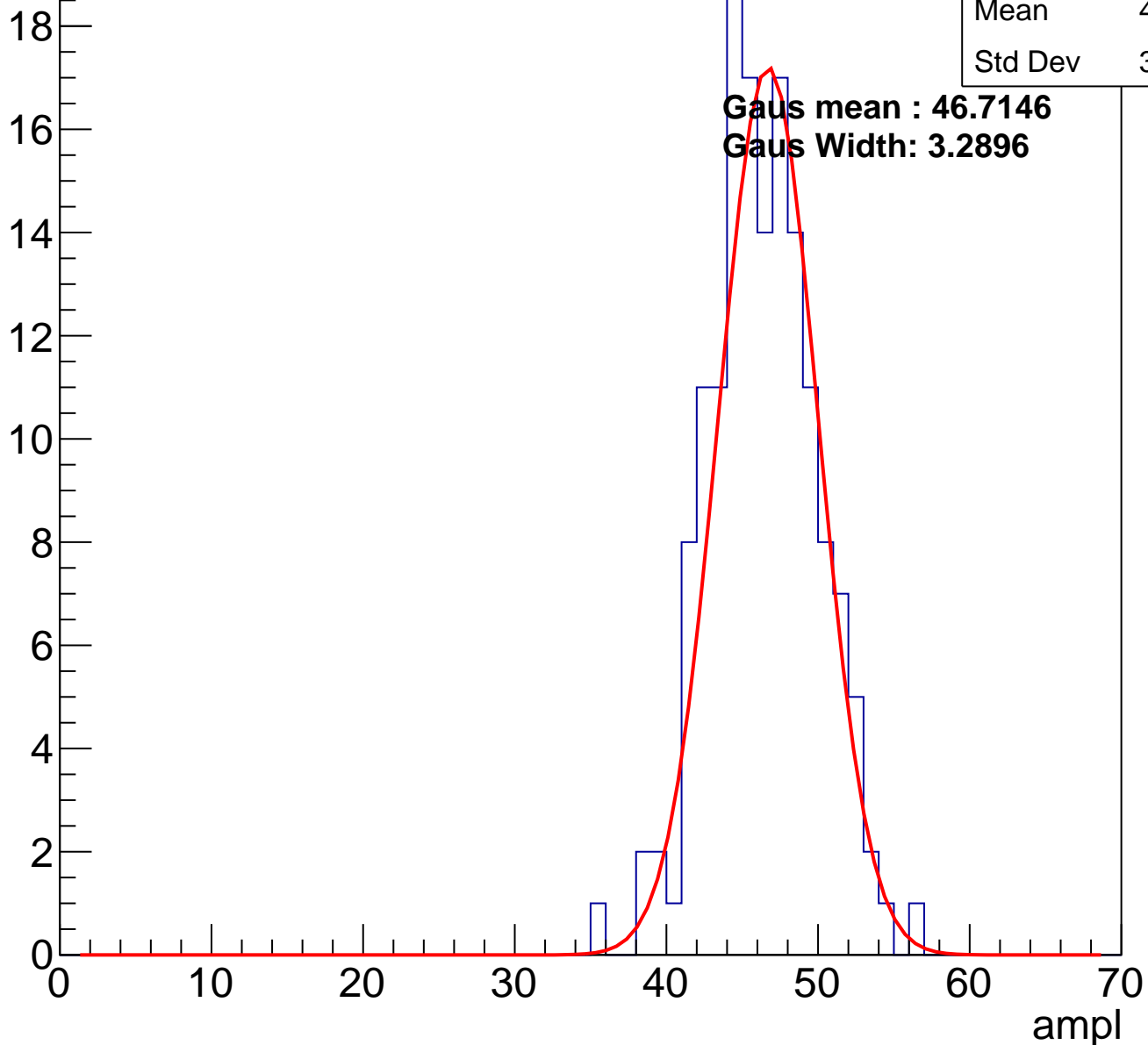
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	45.92
Std Dev	3.505

Gaus mean : 46.7146

Gaus Width: 3.2896

Entry



B1L001S, U24-ch5, adc3

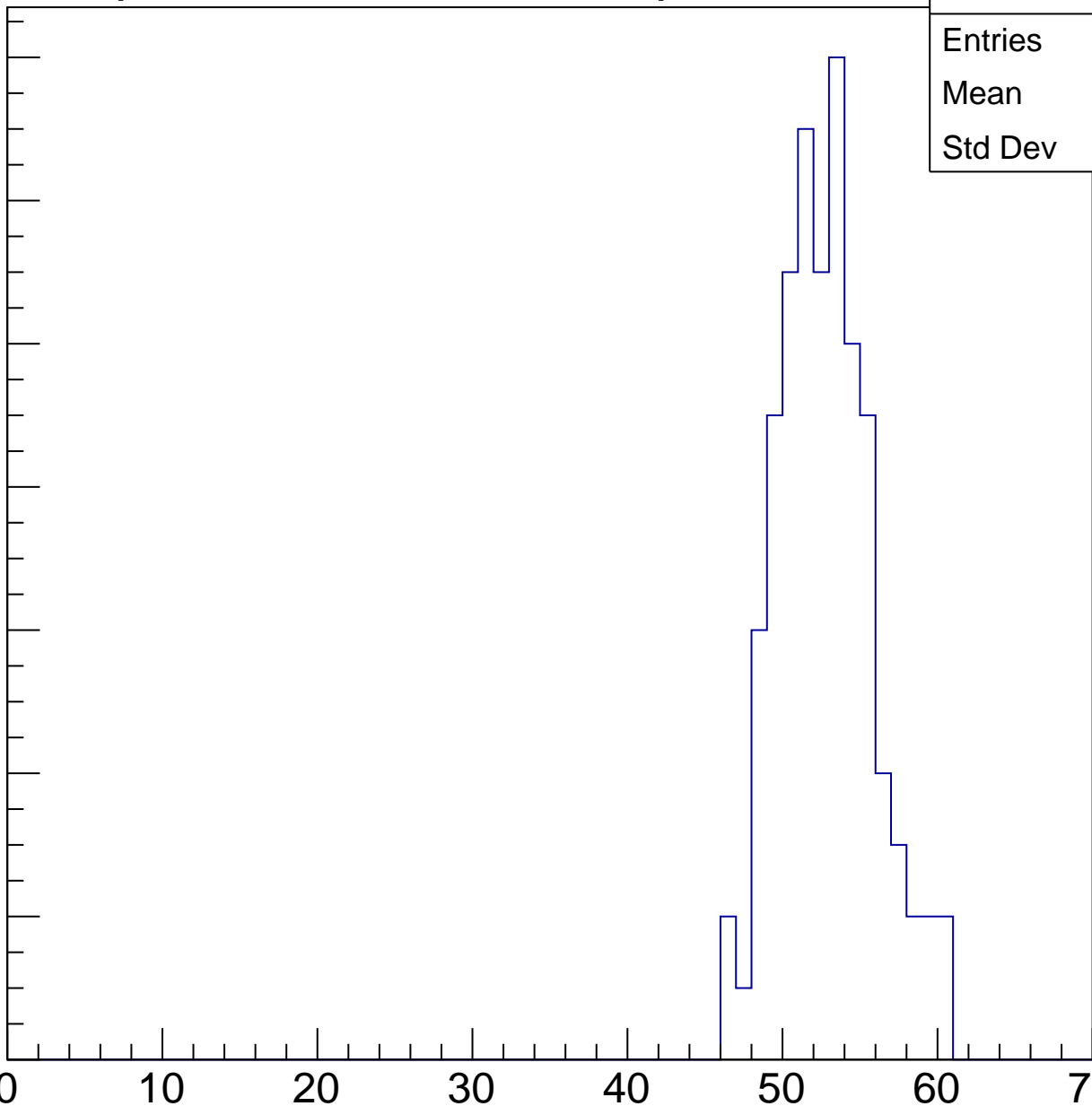
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	52.31
Std Dev	3.004

ampl



B1L001S, U24-ch5, adc4

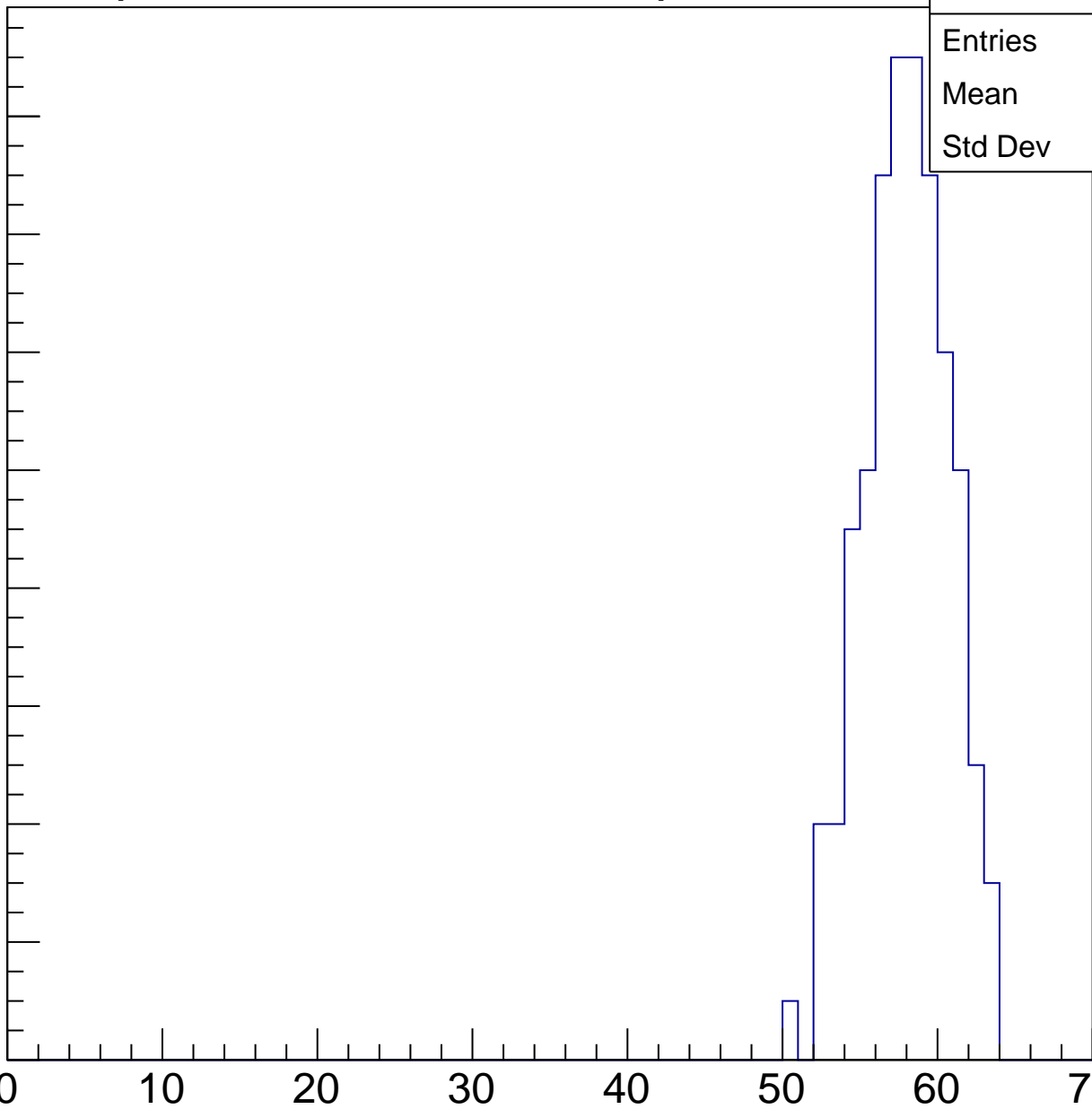
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	57.5
Std Dev	2.711

ampl



B1L001S, U24-ch5, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	55
Mean	59.29
Std Dev	11.62

Entry

16
14
12
10
8
6
4
2
0

0

10

20

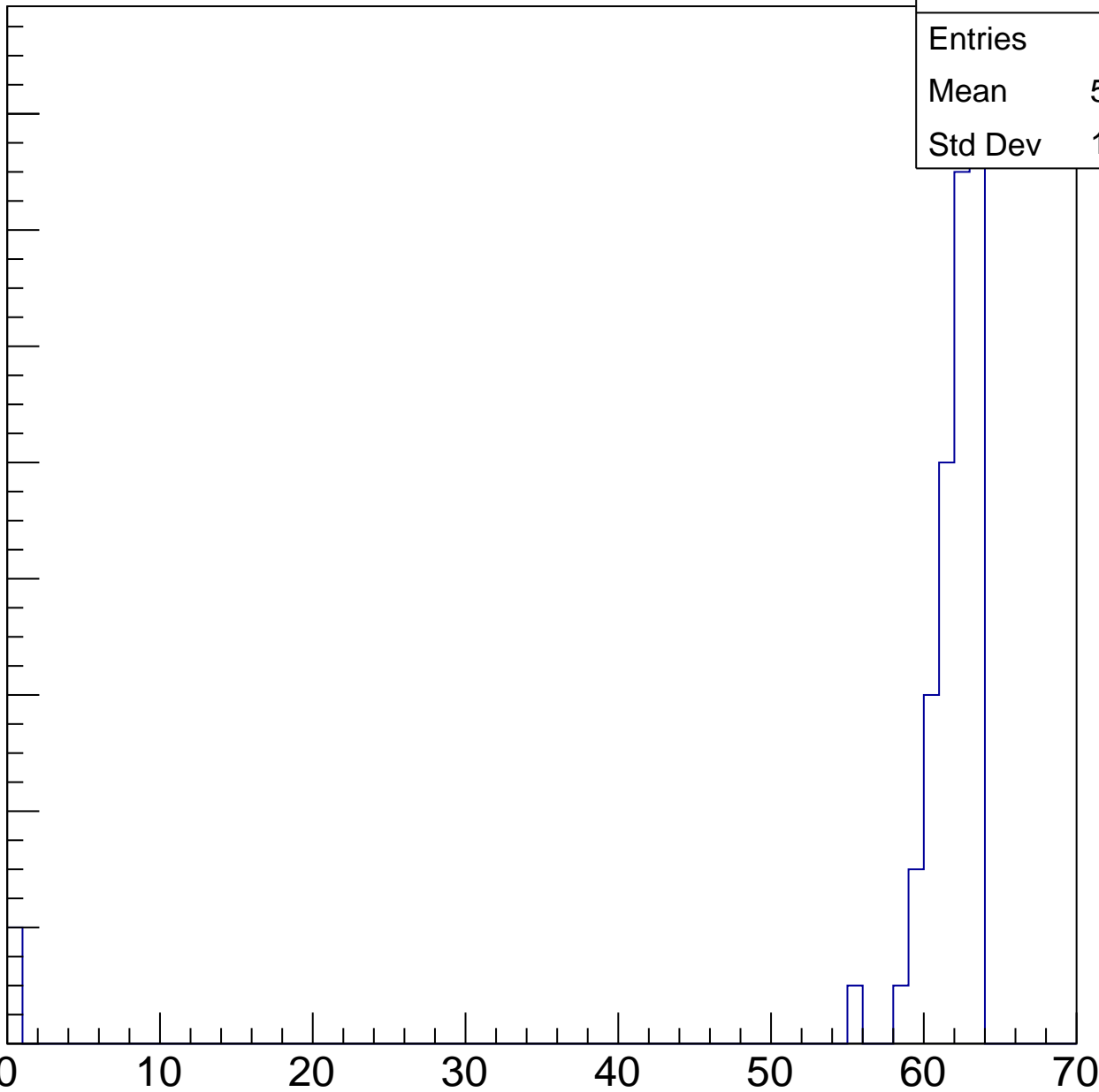
30

40

50

60

ampl



B1L001S, U24-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

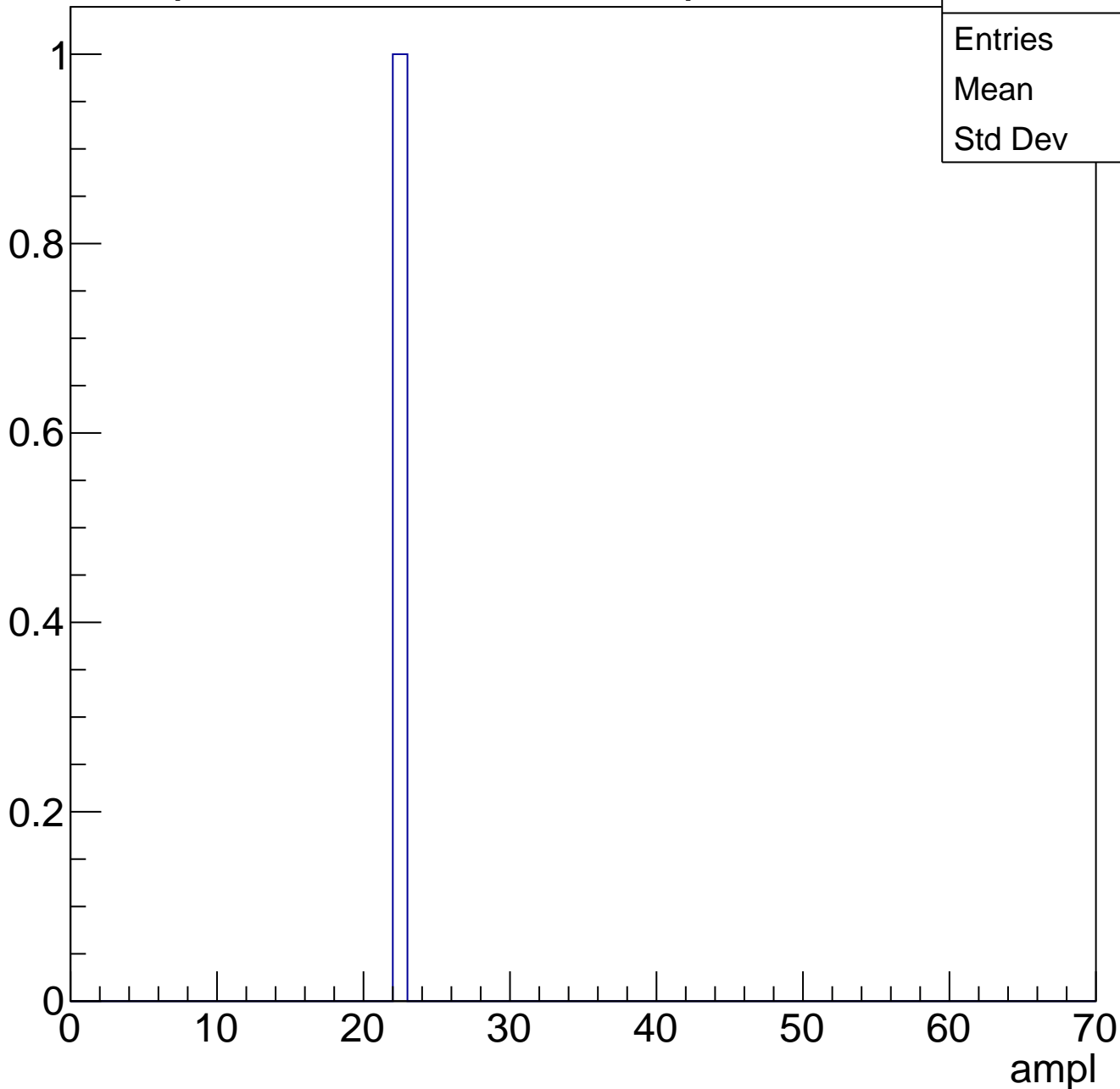


Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch6, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.09
Std Dev	3.096

Gaus mean : 29.6387

Gaus Width: 3.2118

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

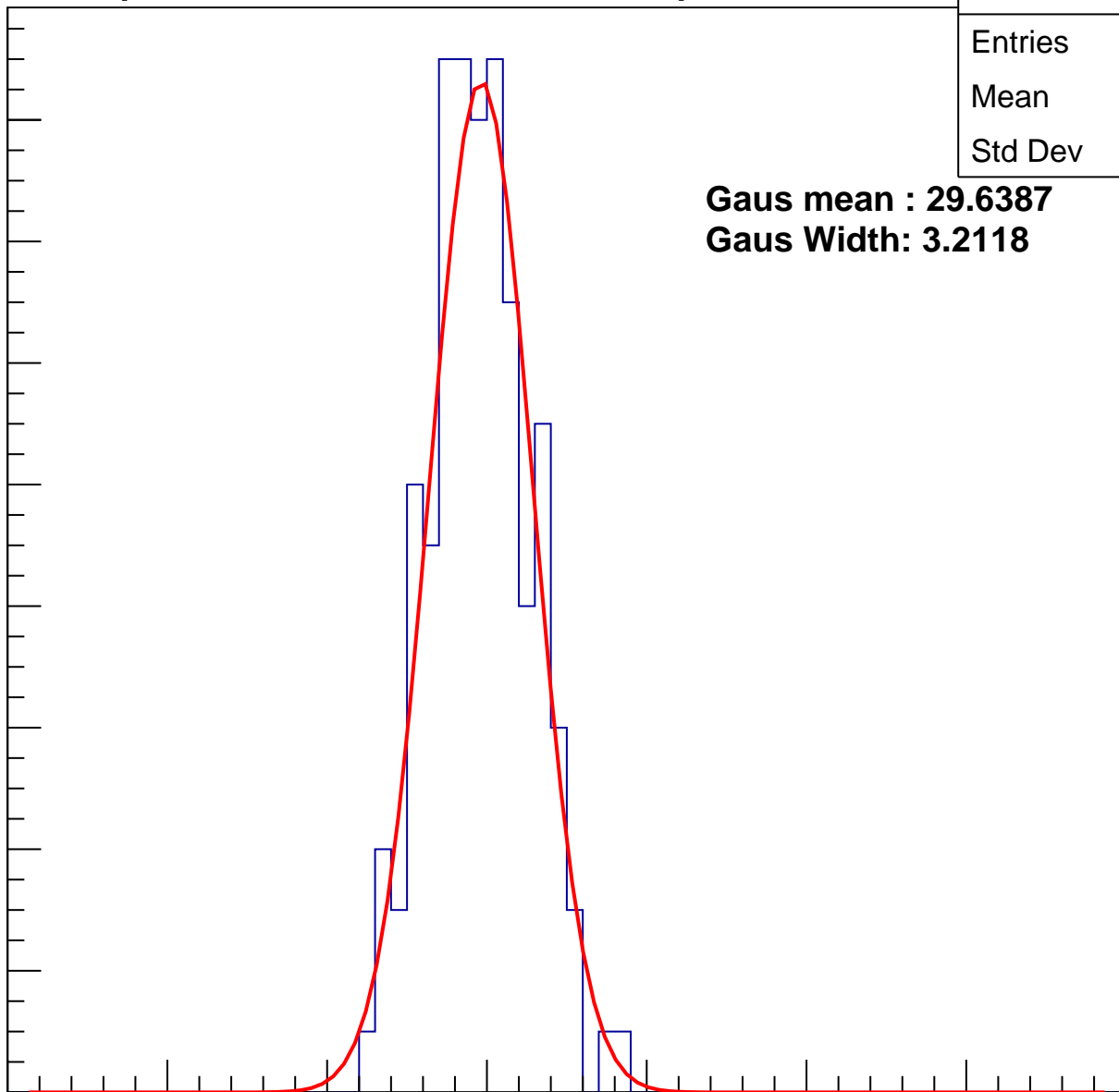
40

50

60

70

ampl



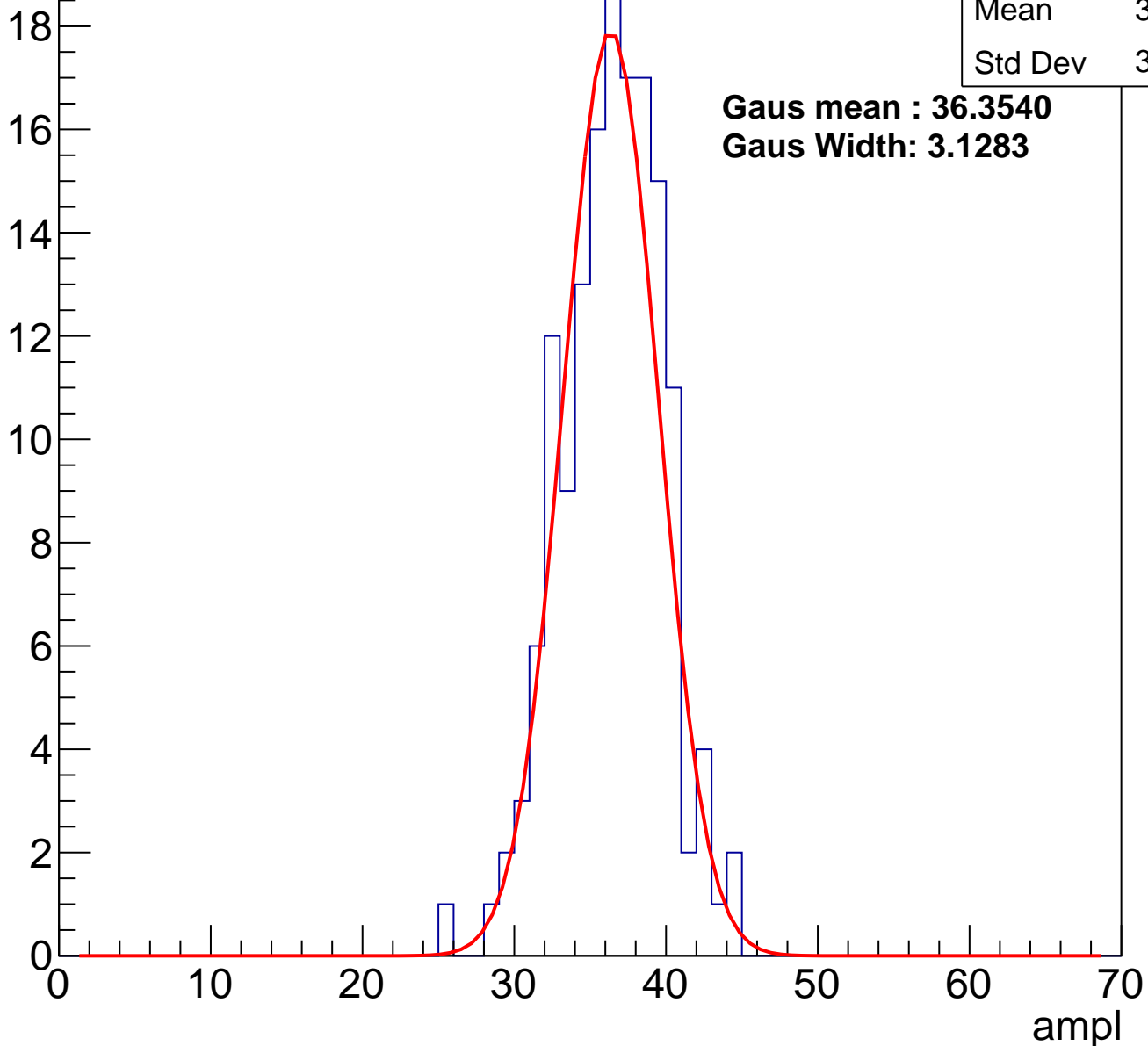
B1L001S, U24-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.99
Std Dev	3.305

Gaus mean : 36.3540
Gaus Width: 3.1283

Entry



B1L001S, U24-ch6, adc2

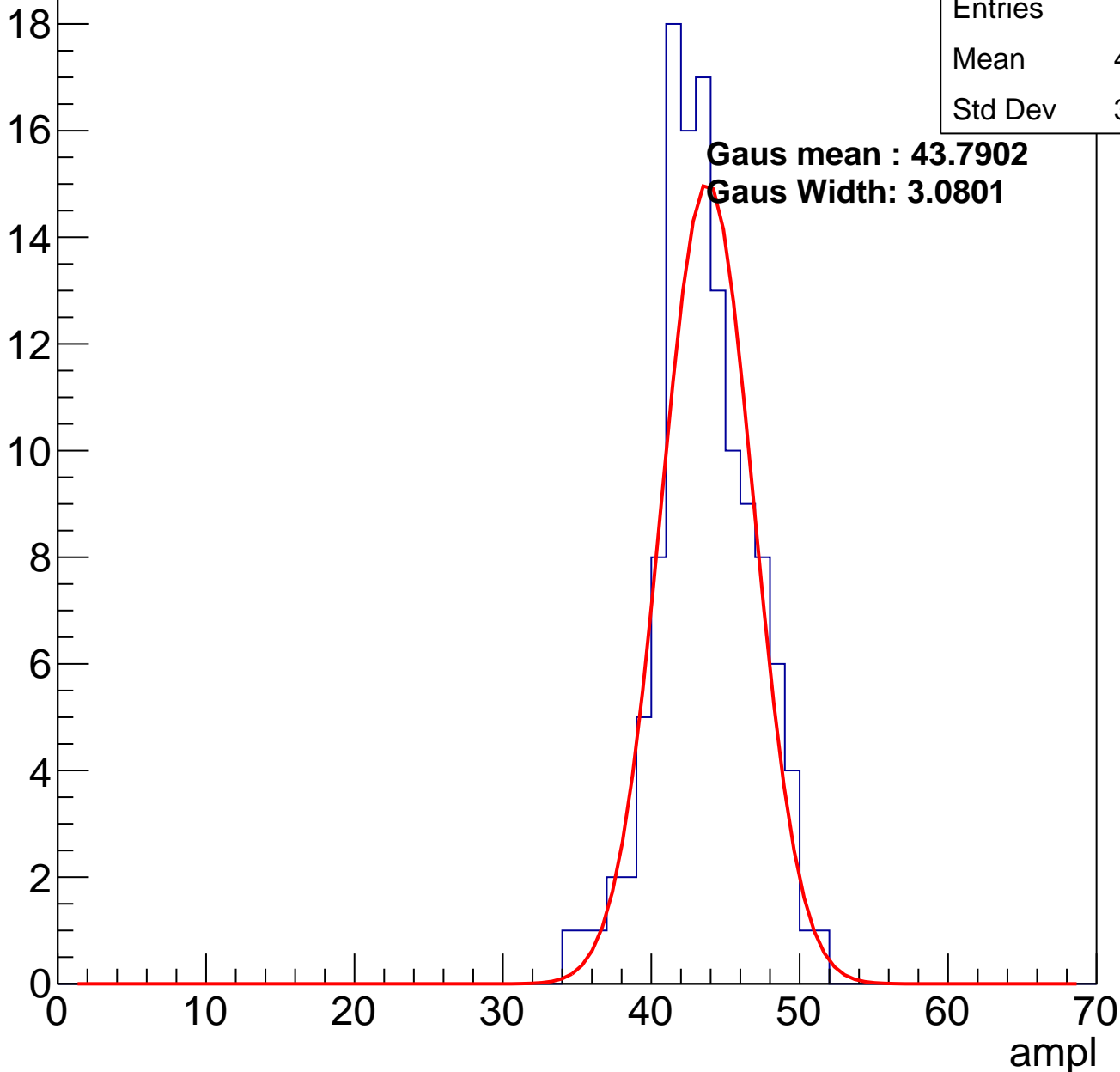
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	43.15
Std Dev	3.152

Gaus mean : 43.7902

Gaus Width: 3.0801

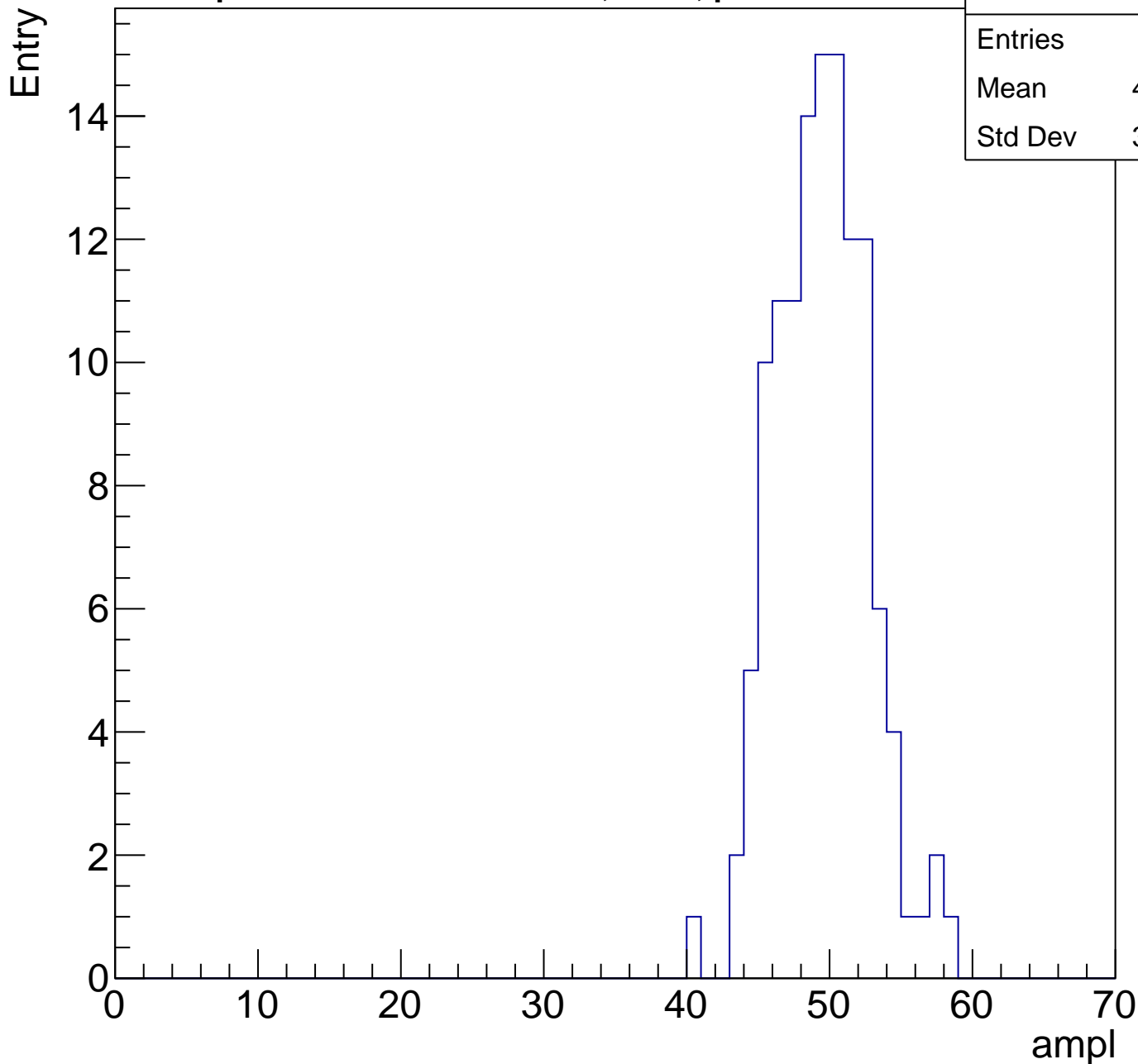
Entry



B1L001S, U24-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	49.02
Std Dev	3.195



B1L001S, U24-ch6, adc4

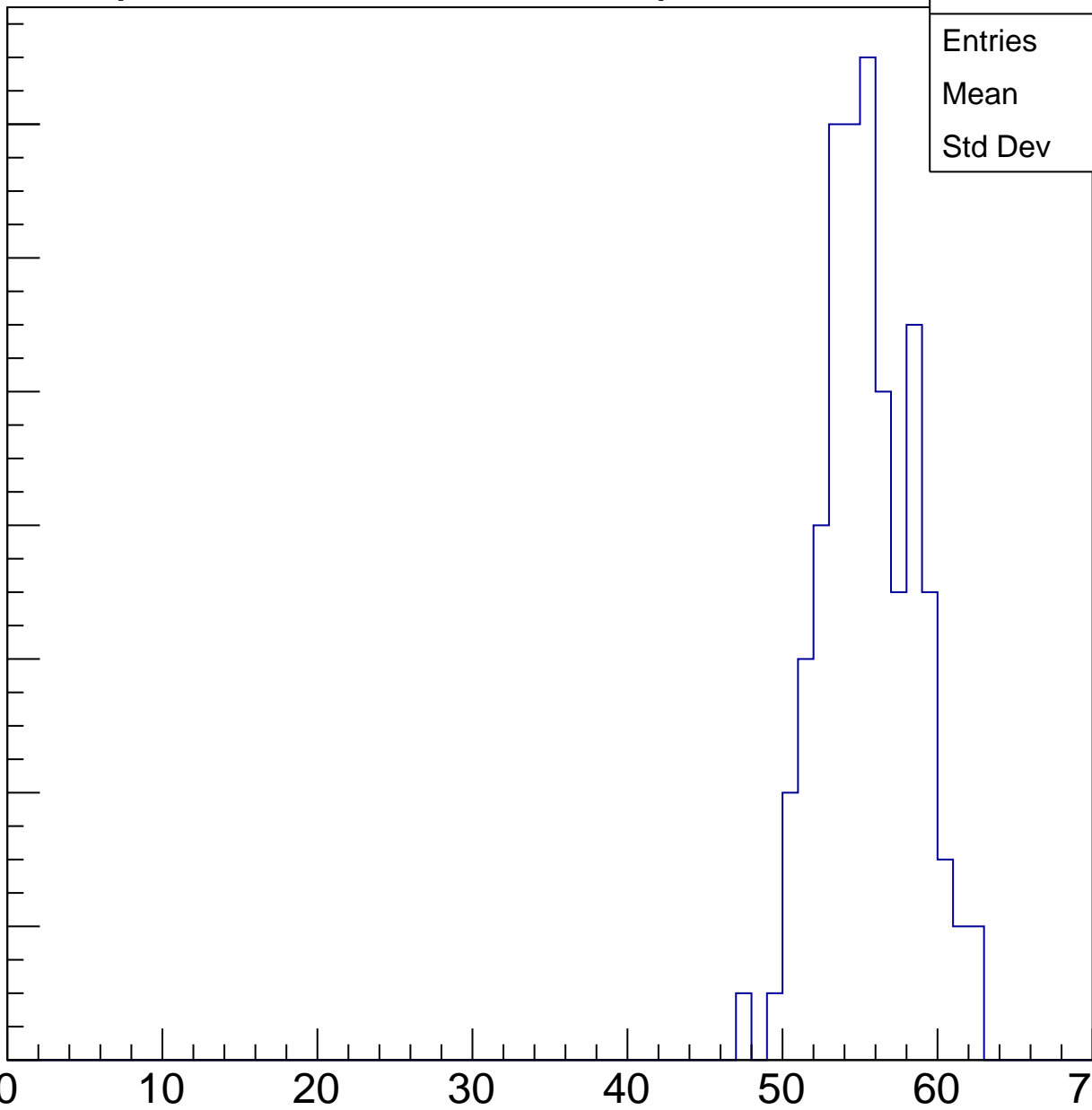
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	105
Mean	55.02
Std Dev	2.979

ampl



B1L001S, U24-ch6, adc5

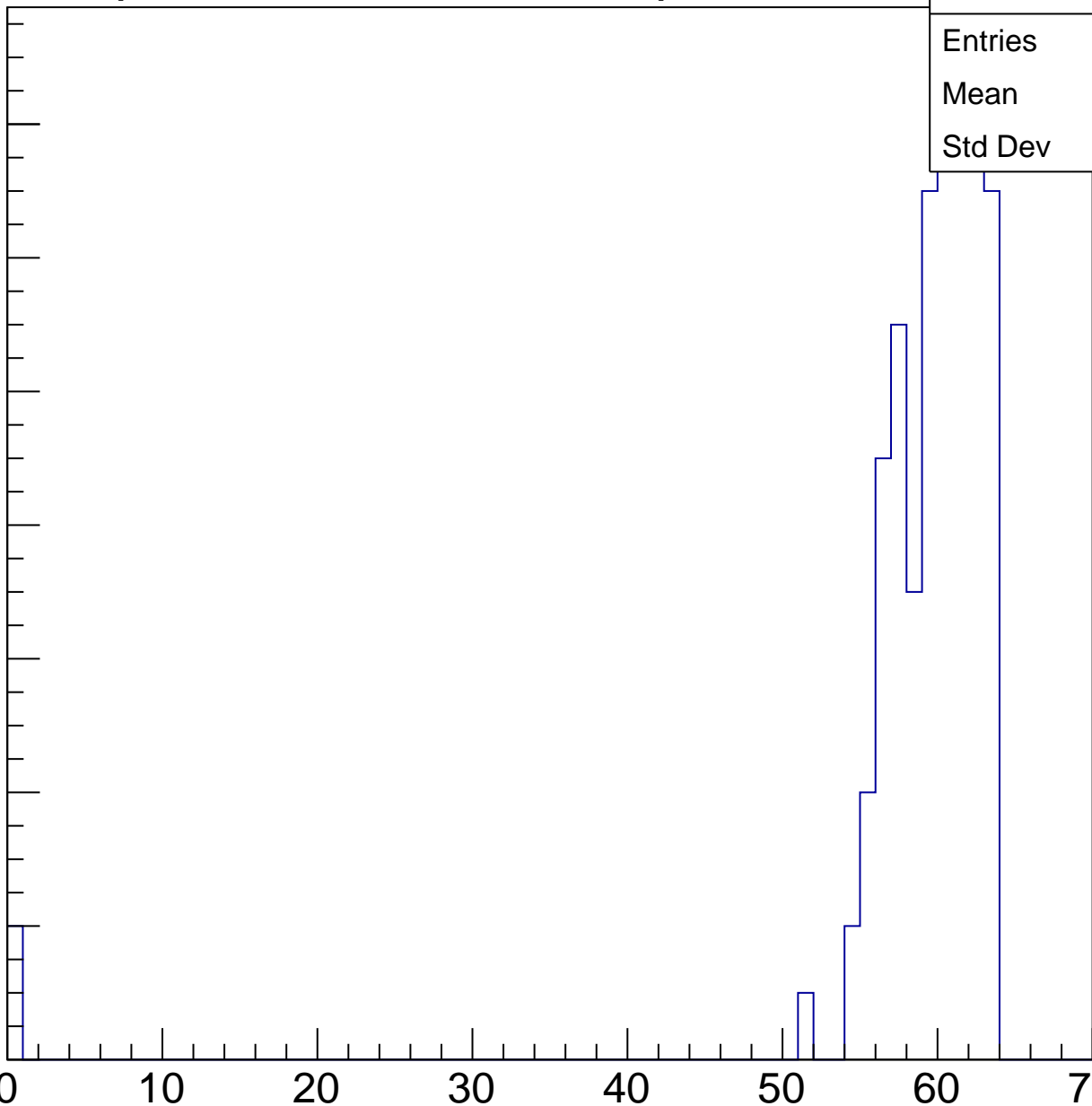
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	58.38
Std Dev	8.455

ampl

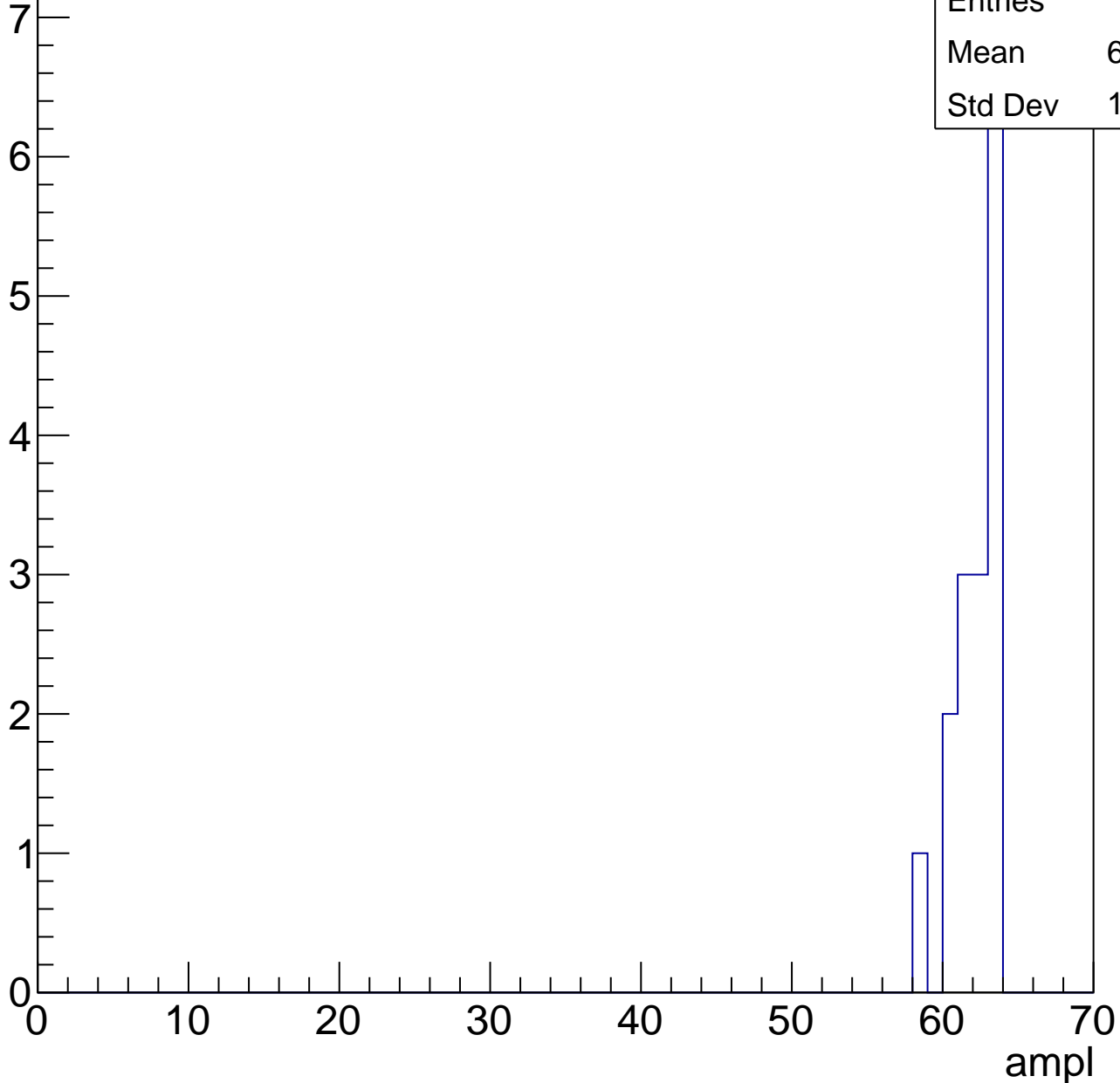


B1L001S, U24-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

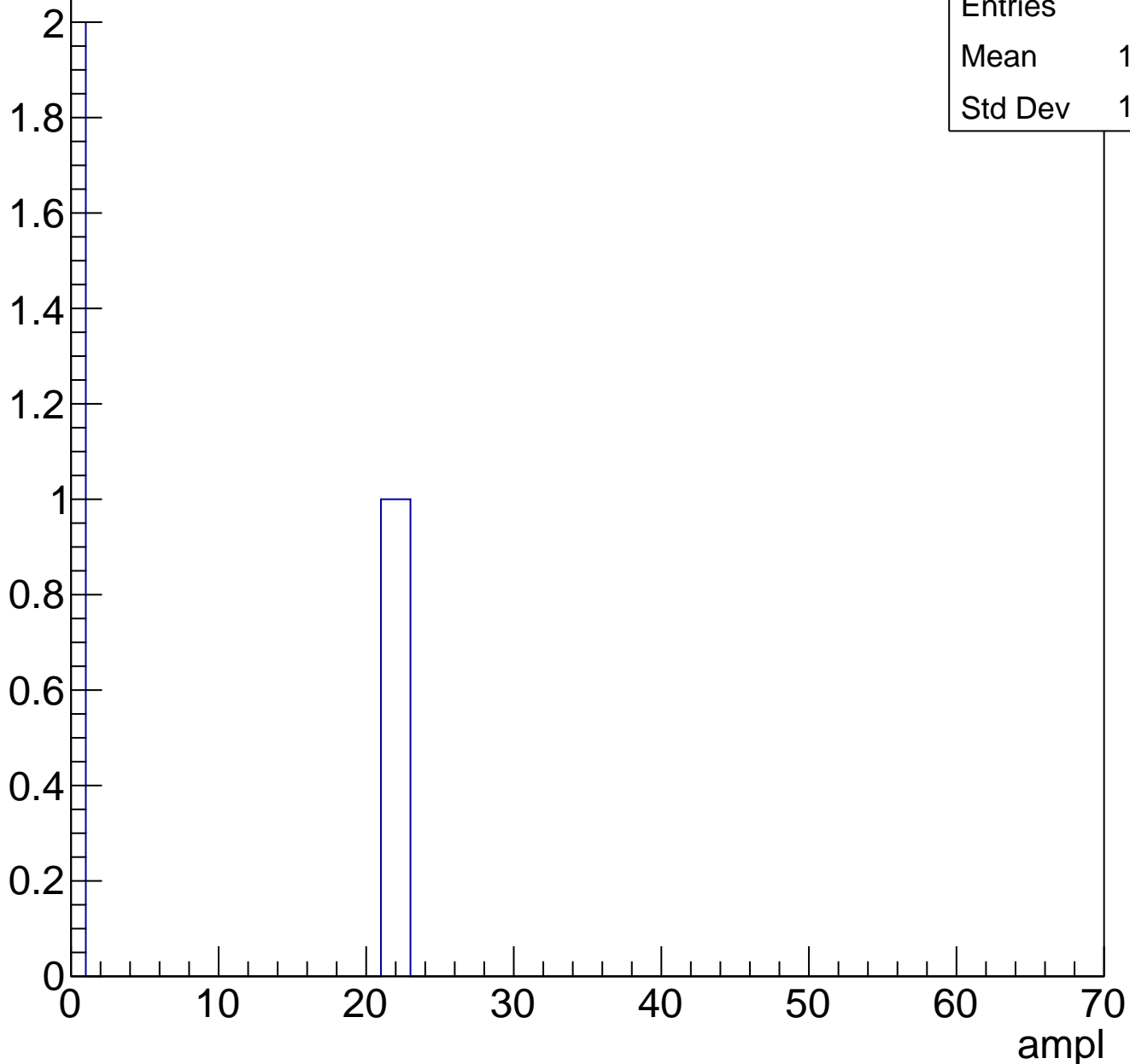
Entries	16
Mean	61.75
Std Dev	1.436



B1L001S, U24-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	10.75
Std Dev	10.76

B1L001S, U24-ch7, adc0

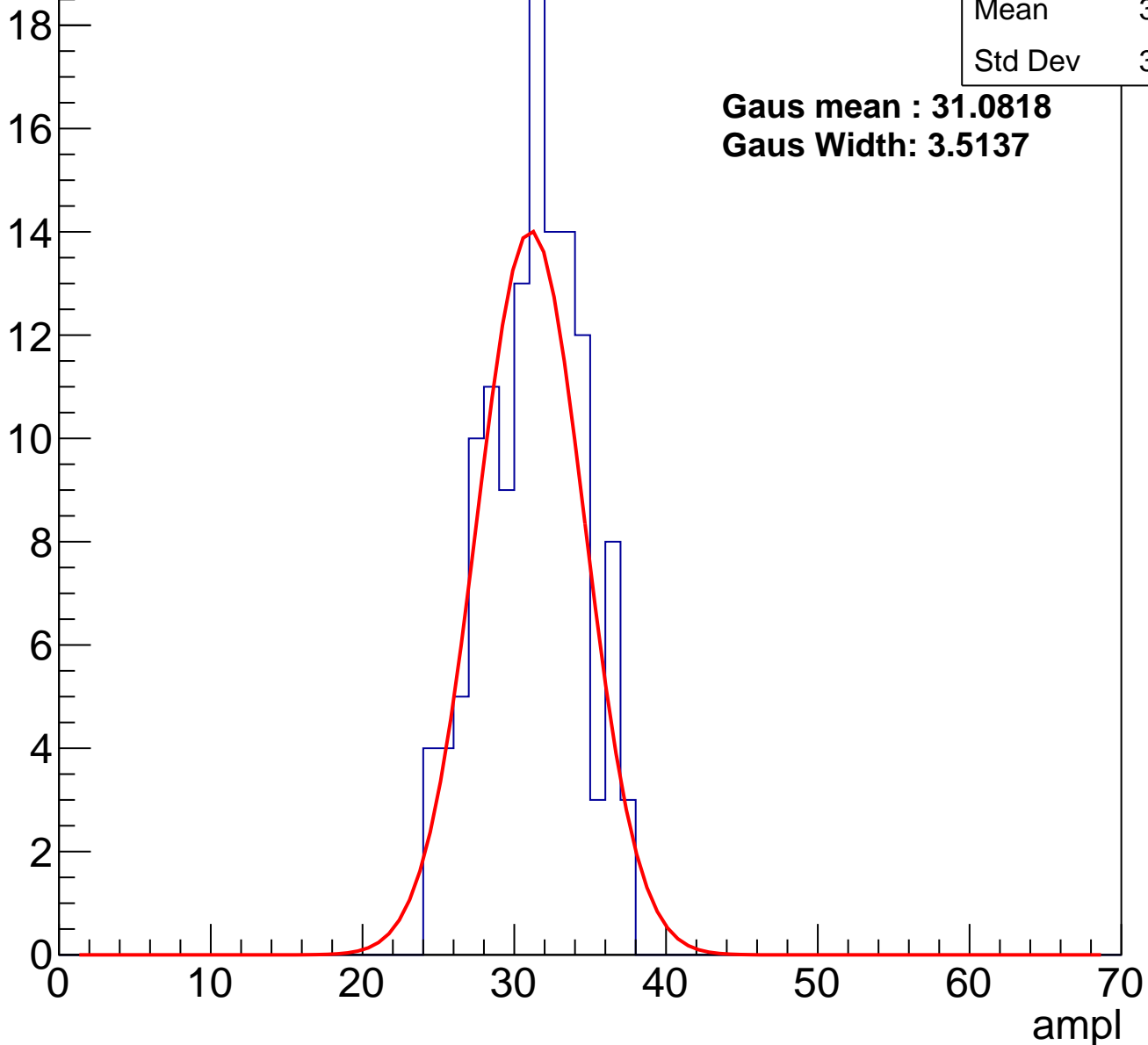
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	30.74
Std Dev	3.173

Gaus mean : 31.0818

Gaus Width: 3.5137

Entry



B1L001S, U24-ch7, adc1

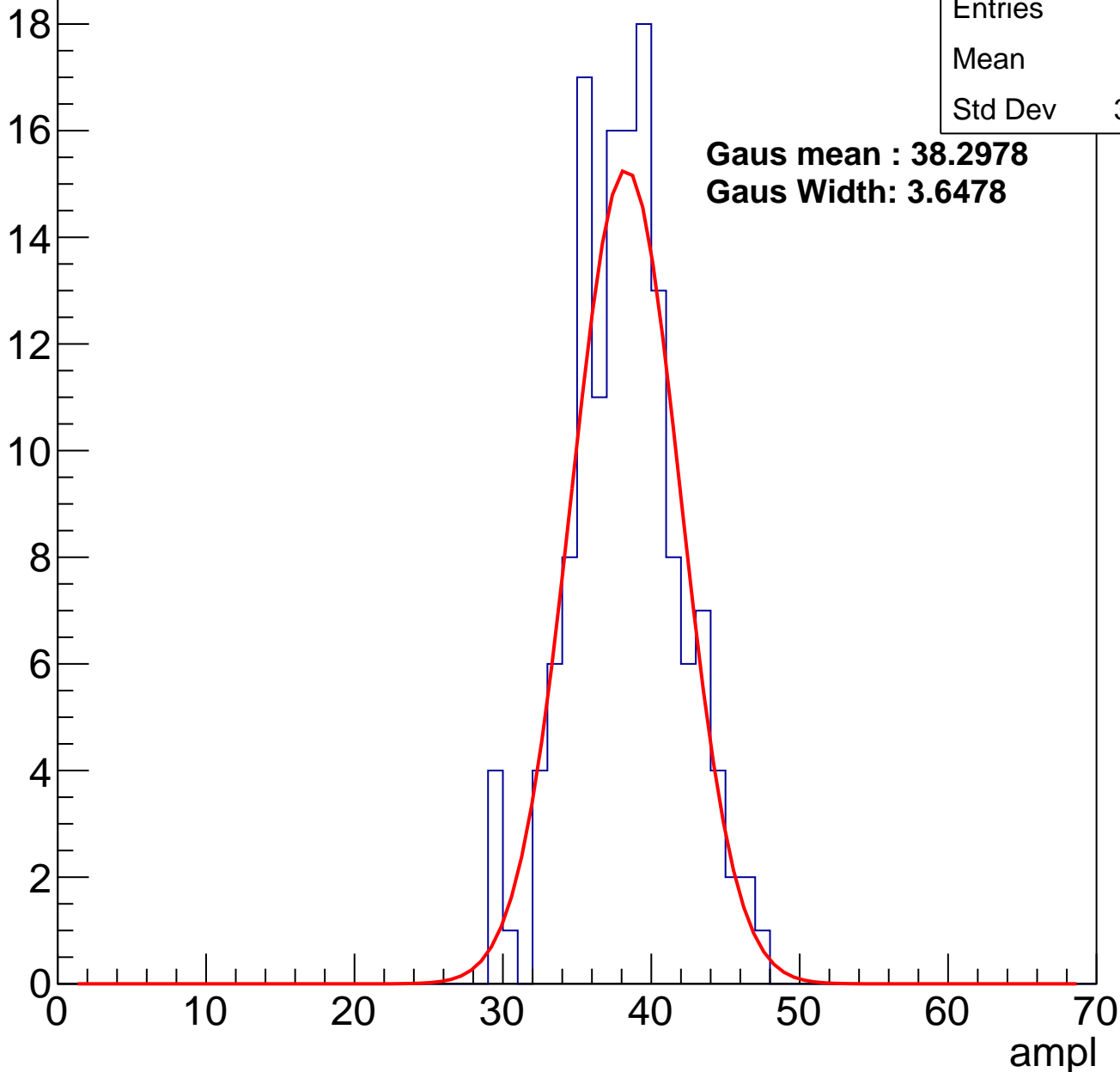
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	37.8
Std Dev	3.614

Gaus mean : 38.2978

Gaus Width: 3.6478

Entry



B1L001S, U24-ch7, adc2

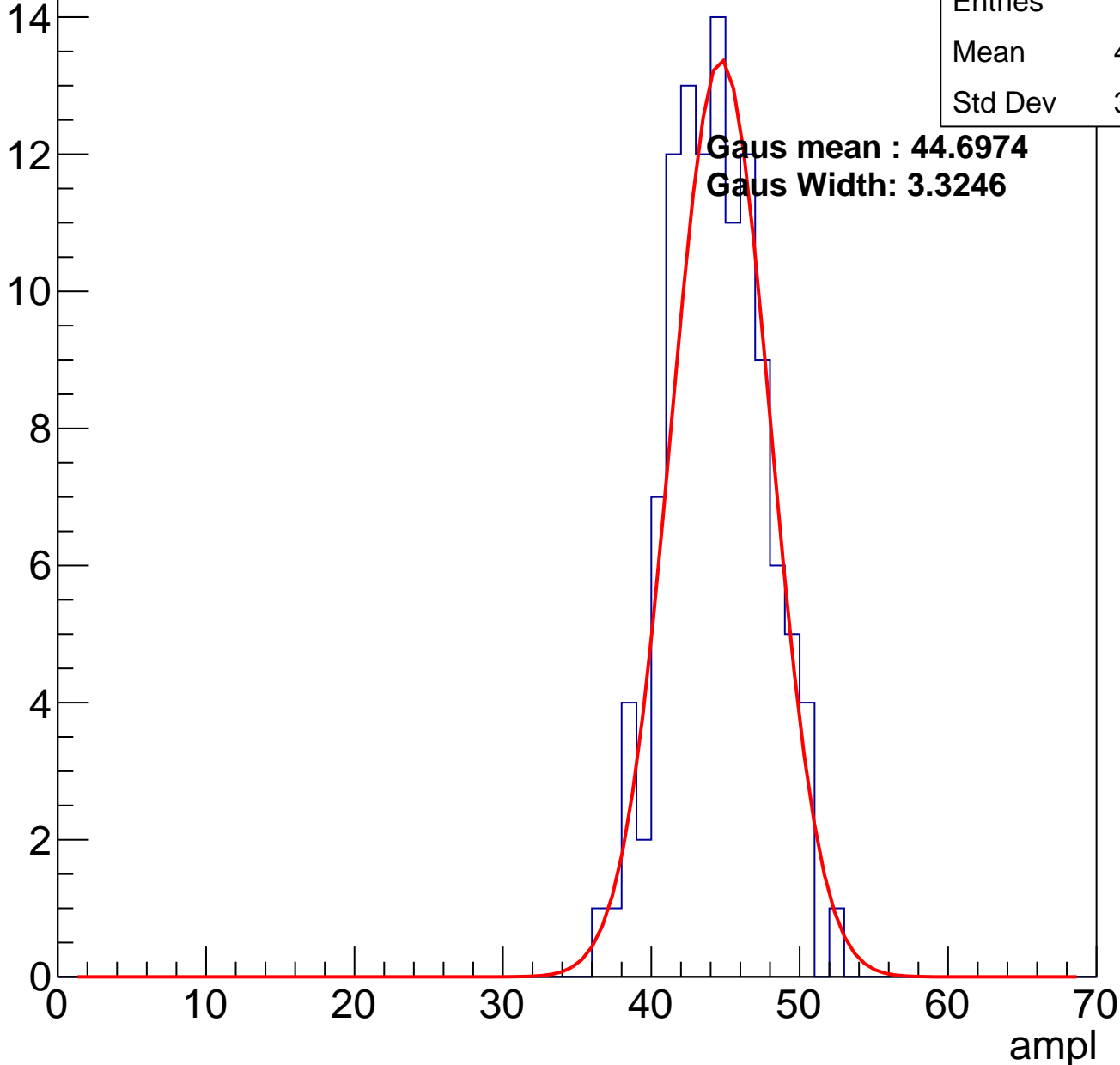
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	43.93
Std Dev	3.189

Gaus mean : 44.6974

Gaus Width: 3.3246

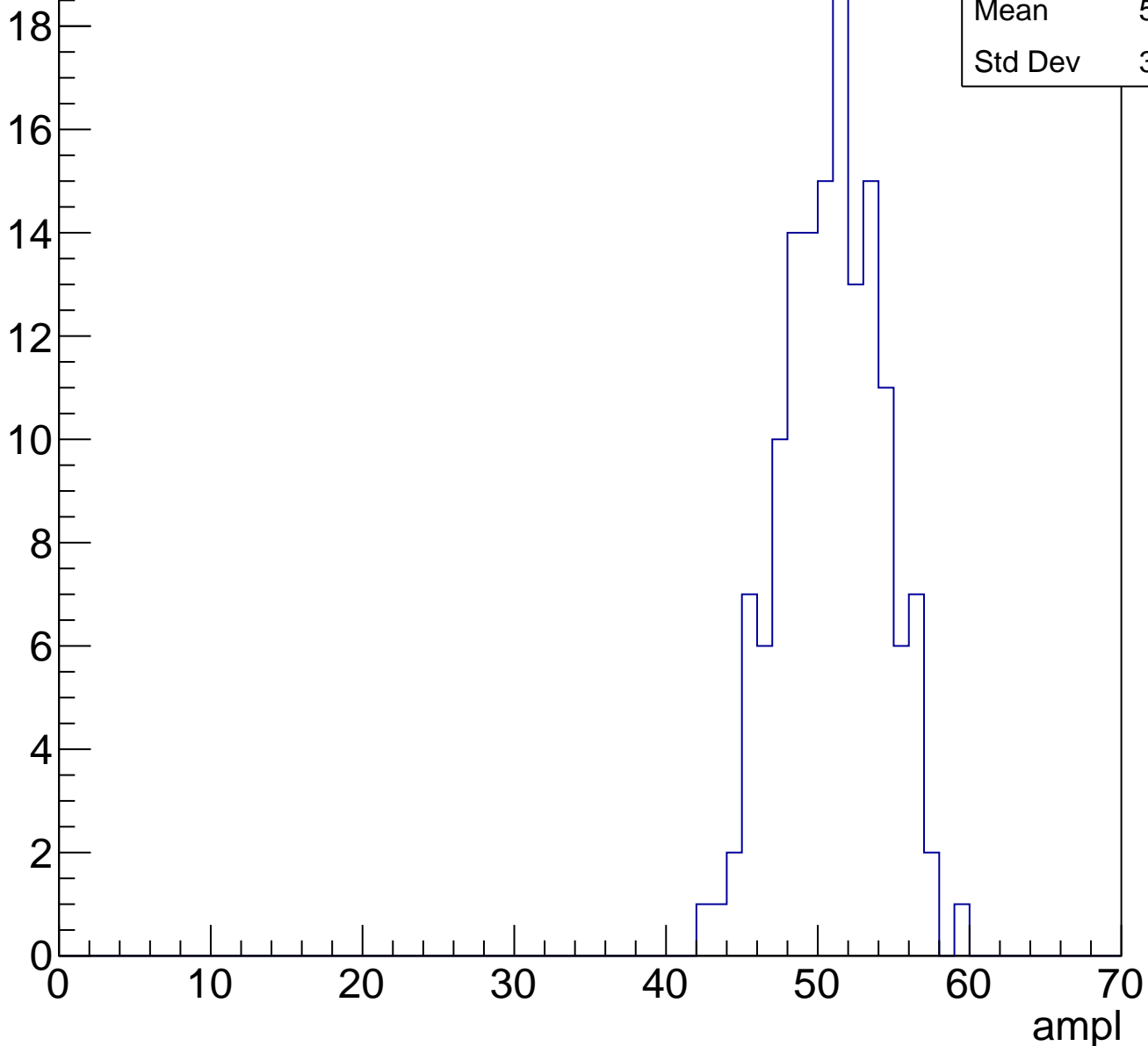
Entry



B1L001S, U24-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch7, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

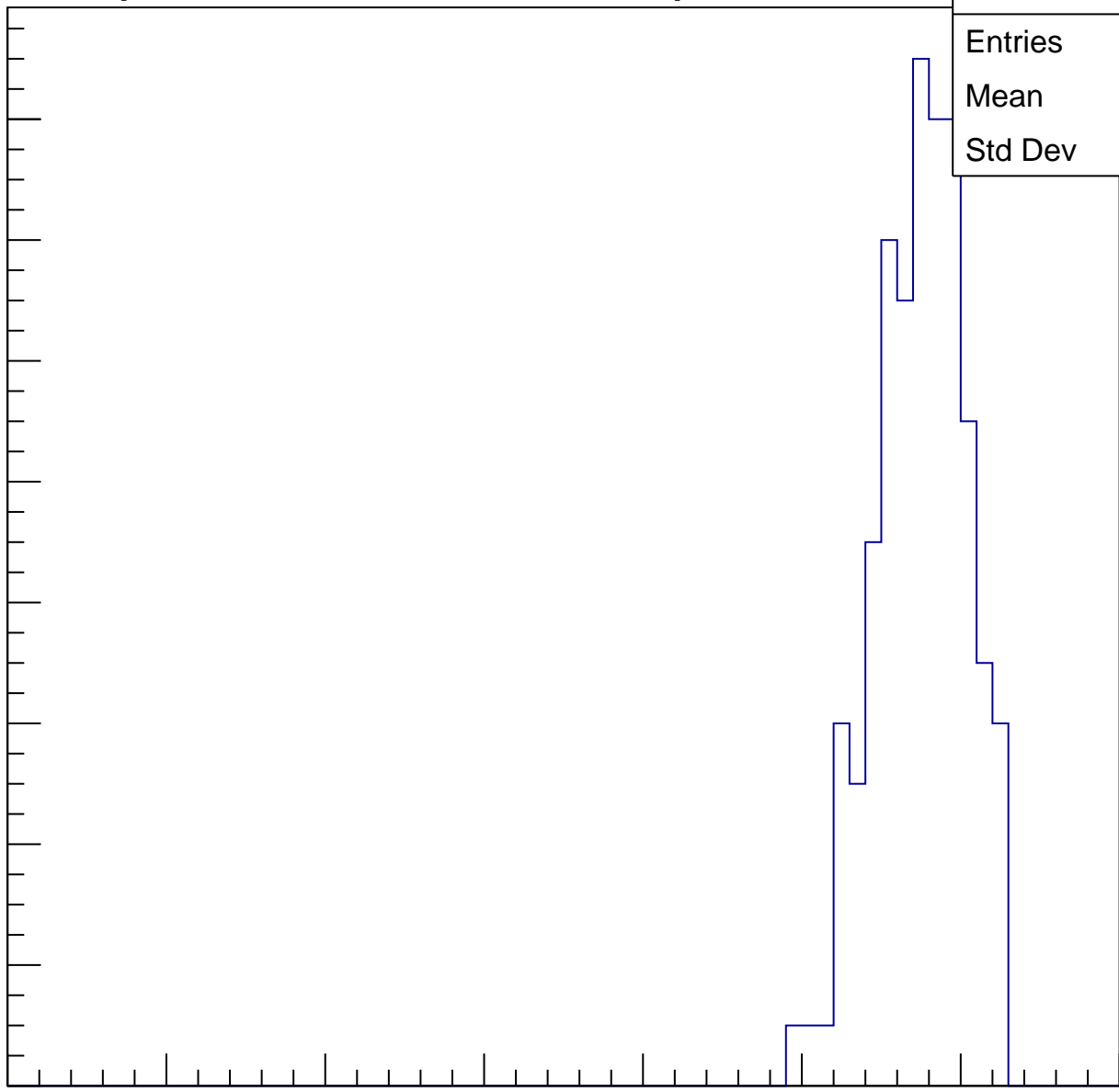
50

60

70

ampl

Entries	123
Mean	57
Std Dev	2.808



B1L001S, U24-ch7, adc5

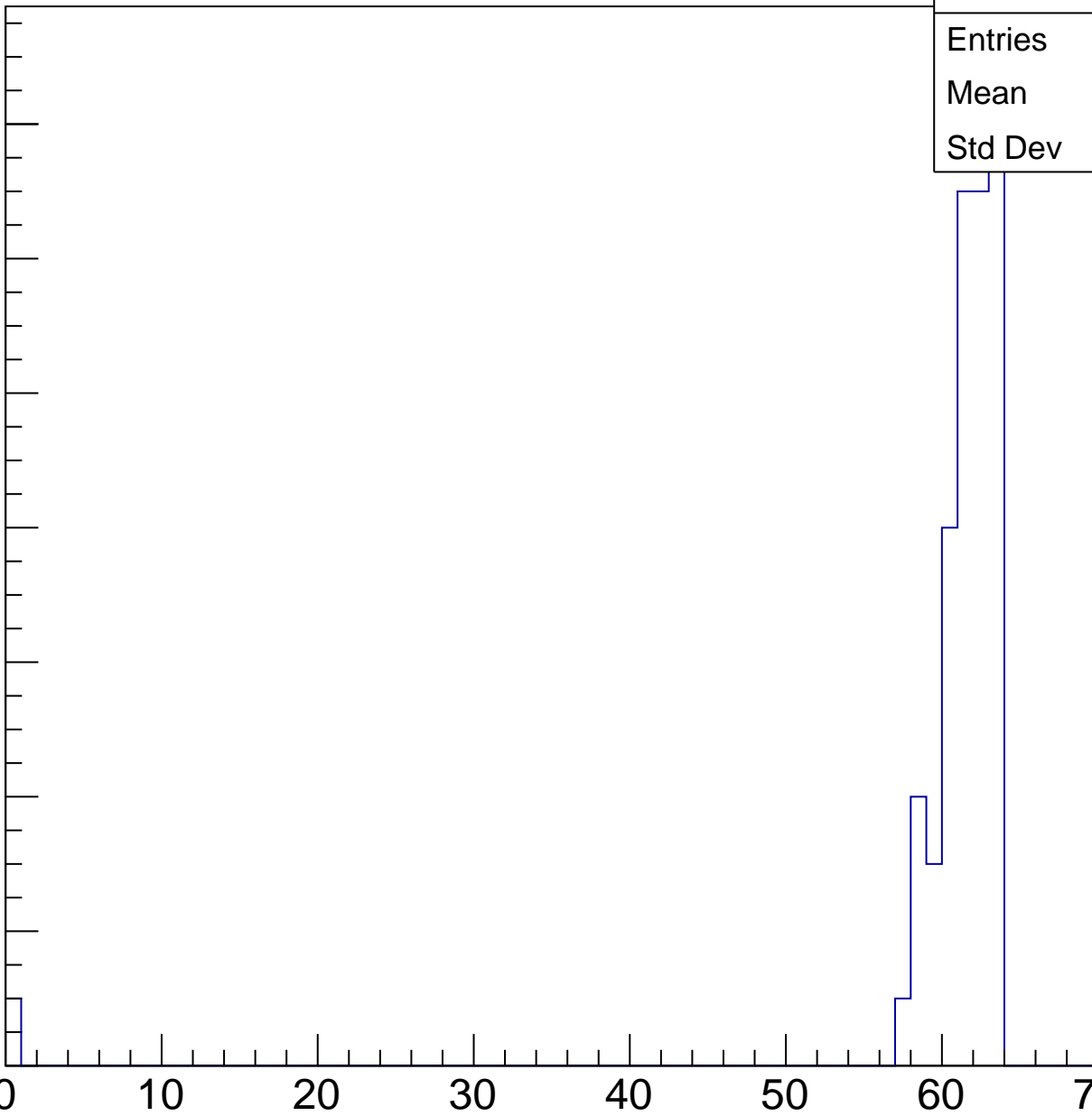
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	60.17
Std Dev	8.122

ampl

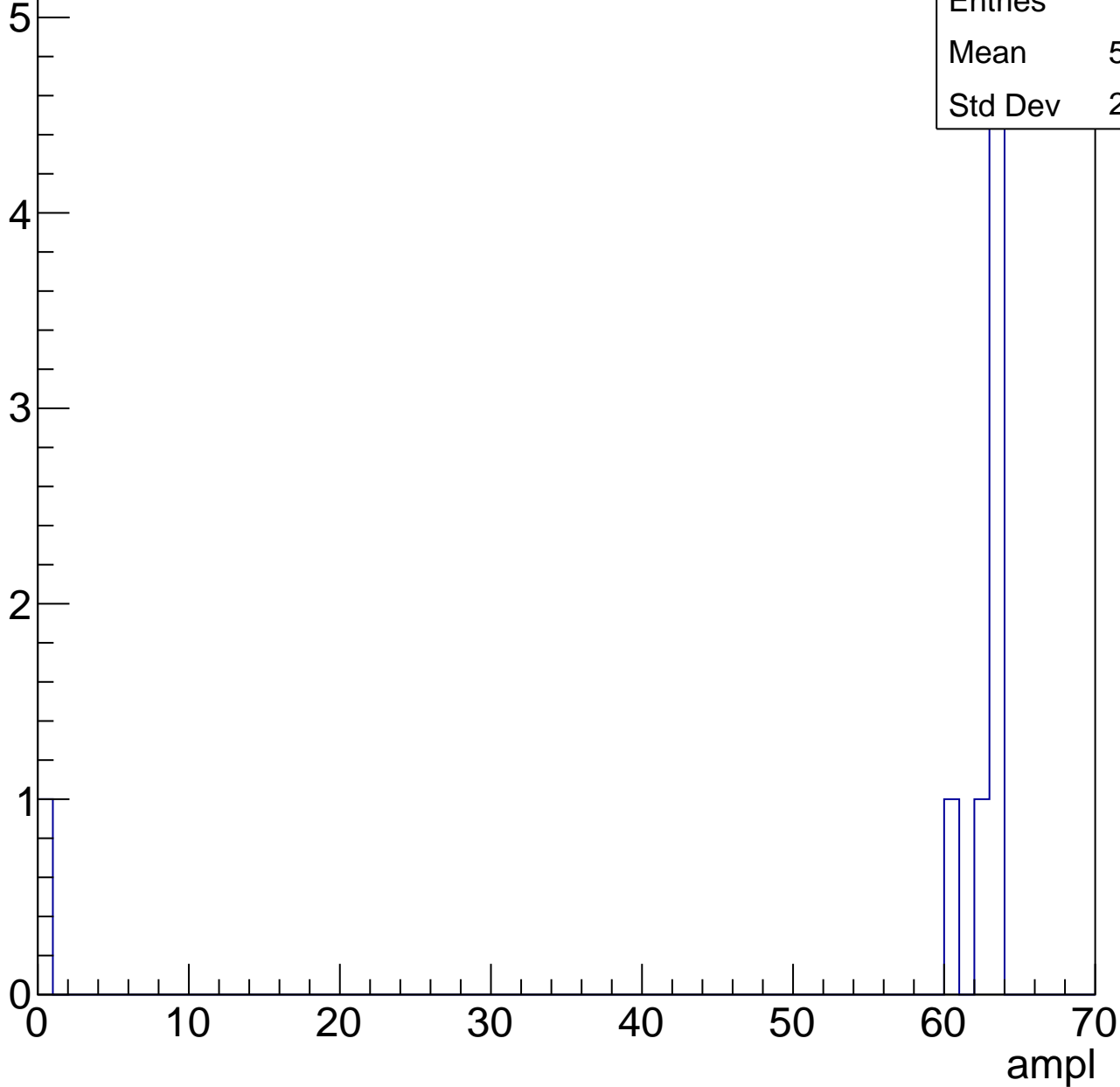


B1L001S, U24-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	54.62
Std Dev	20.67



B1L001S, U24-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch8, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	30.38
Std Dev	2.526

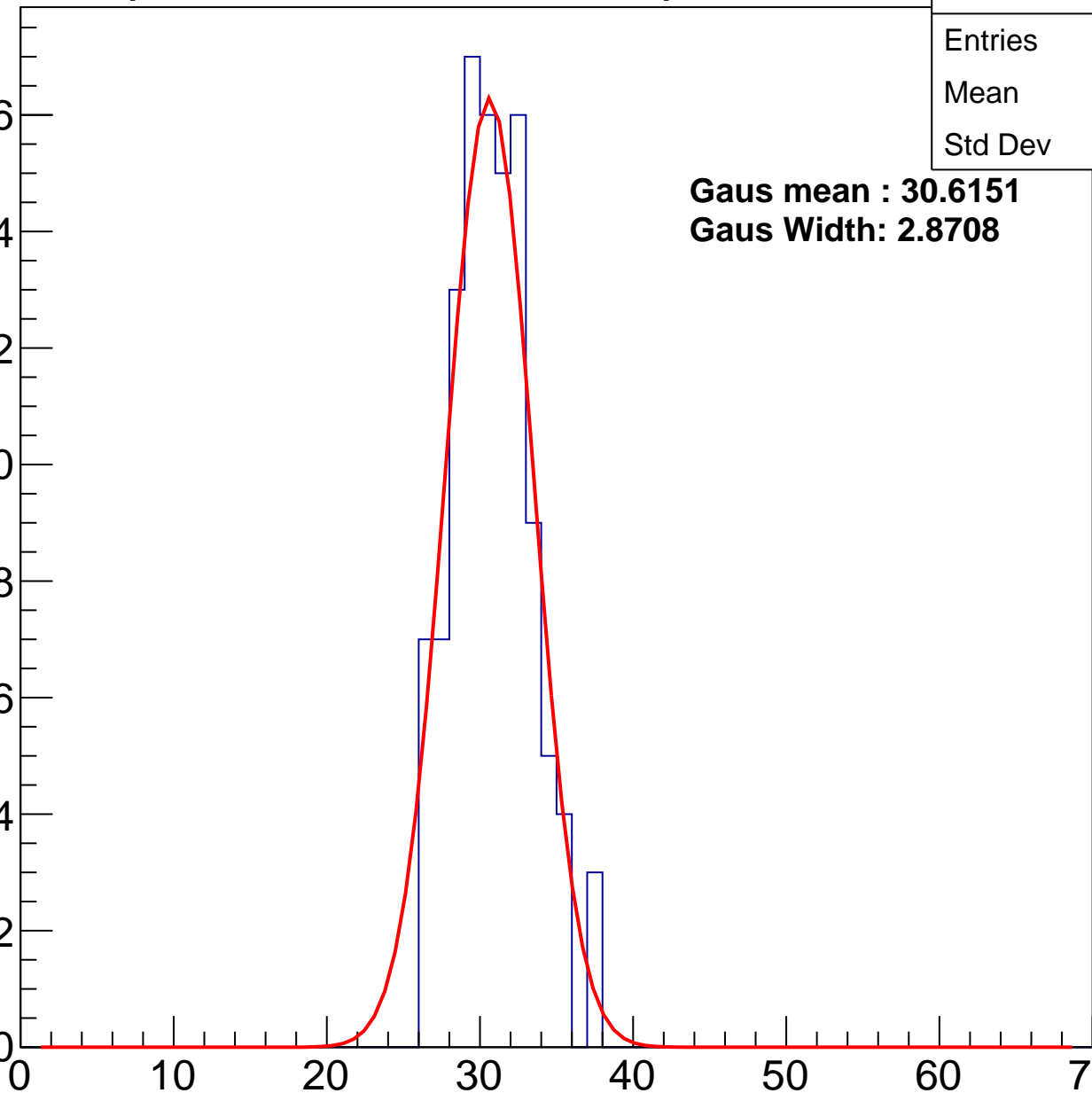
Gaus mean : 30.6151

Gaus Width: 2.8708

Entry

16
14
12
10
8
6
4
2
0

ampl



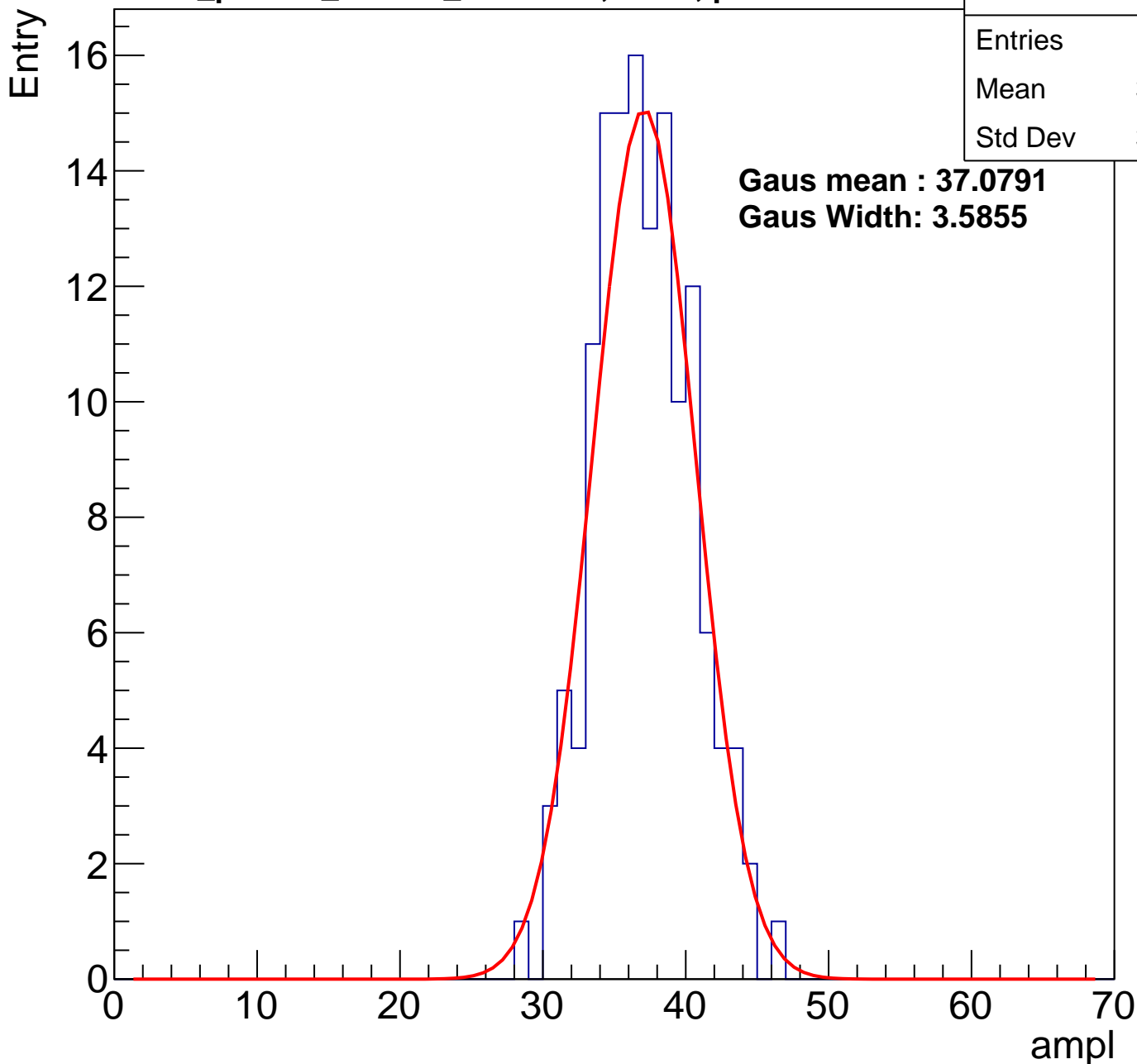
B1L001S, U24-ch8, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	36.61
Std Dev	3.371

Gaus mean : 37.0791

Gaus Width: 3.5855



B1L001S, U24-ch8, adc2

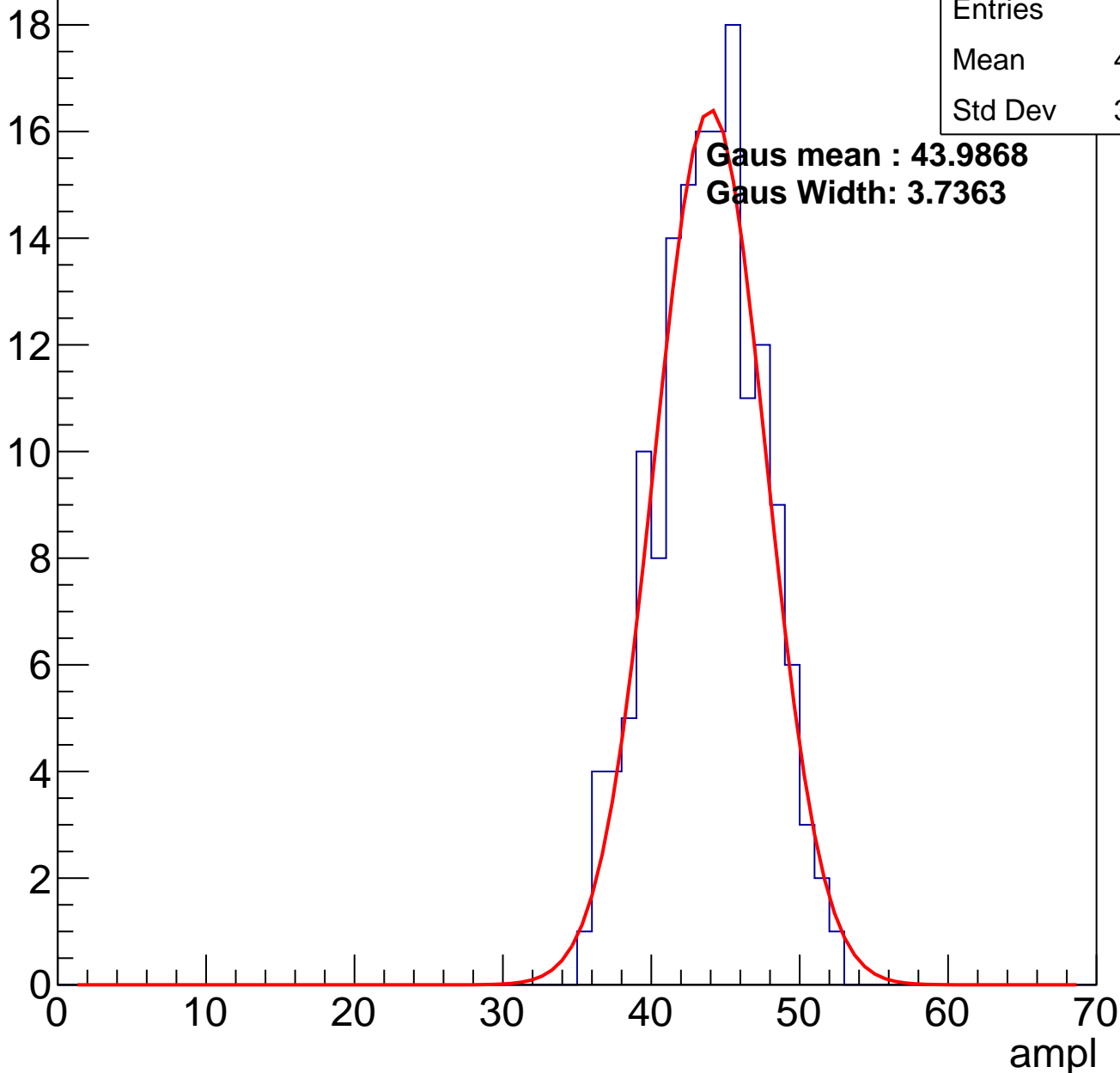
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	43.44
Std Dev	3.572

Gaus mean : 43.9868

Gaus Width: 3.7363

Entry



B1L001S, U24-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

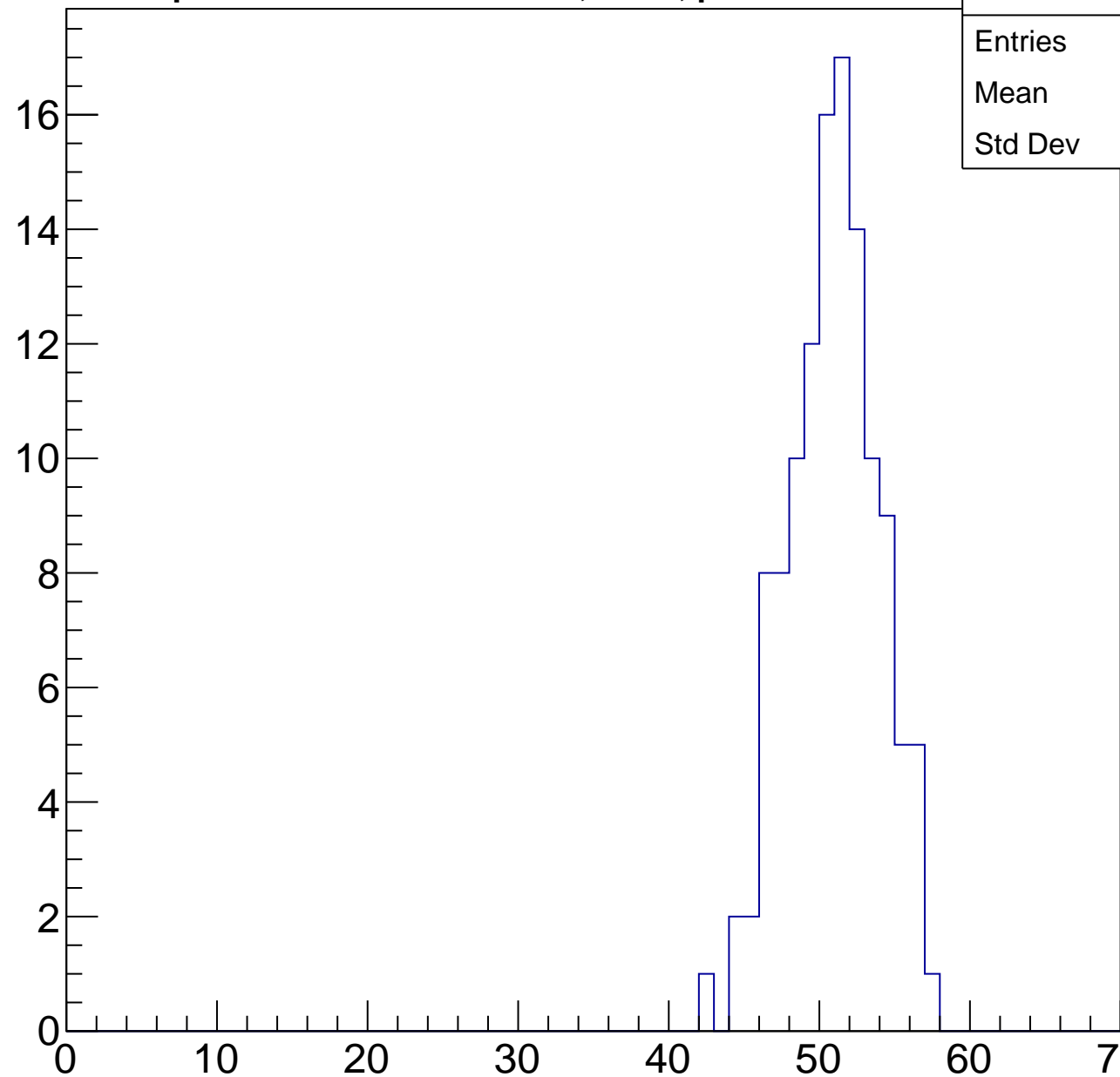
Entries	120
Mean	50.46
Std Dev	2.991

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch8, adc4

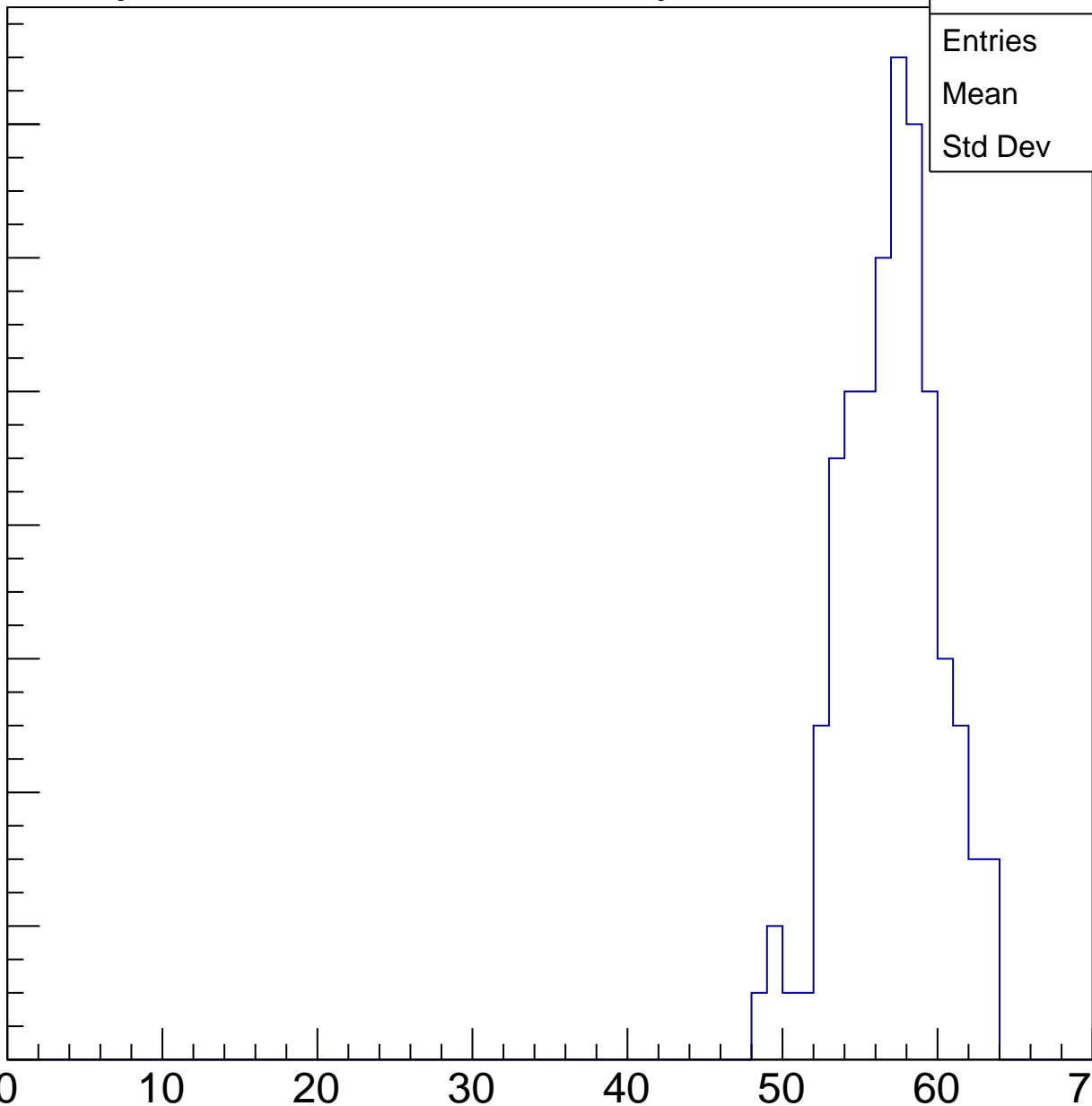
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	56.48
Std Dev	3.128

ampl



B1L001S, U24-ch8, adc5

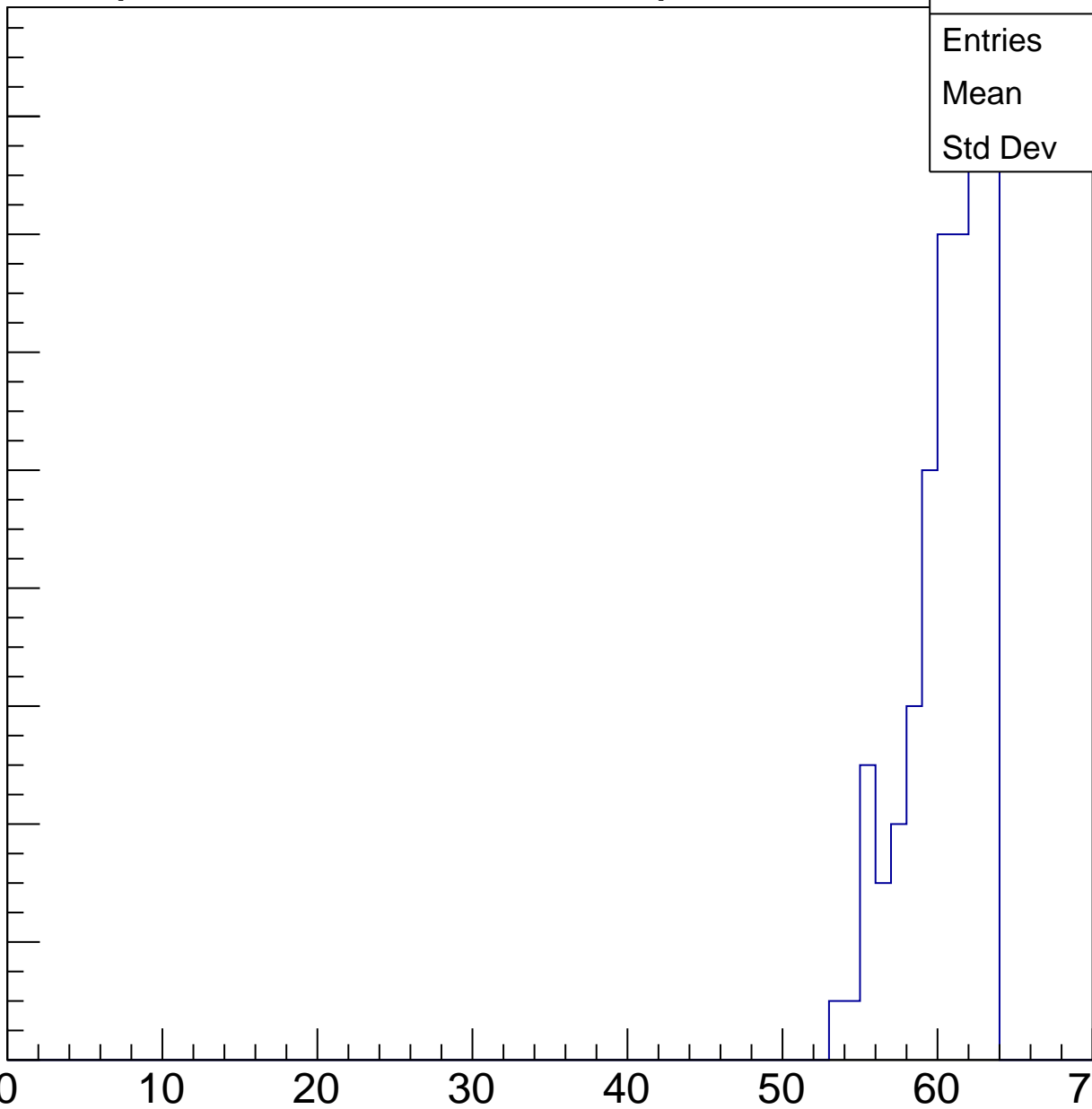
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	60.14
Std Dev	2.474

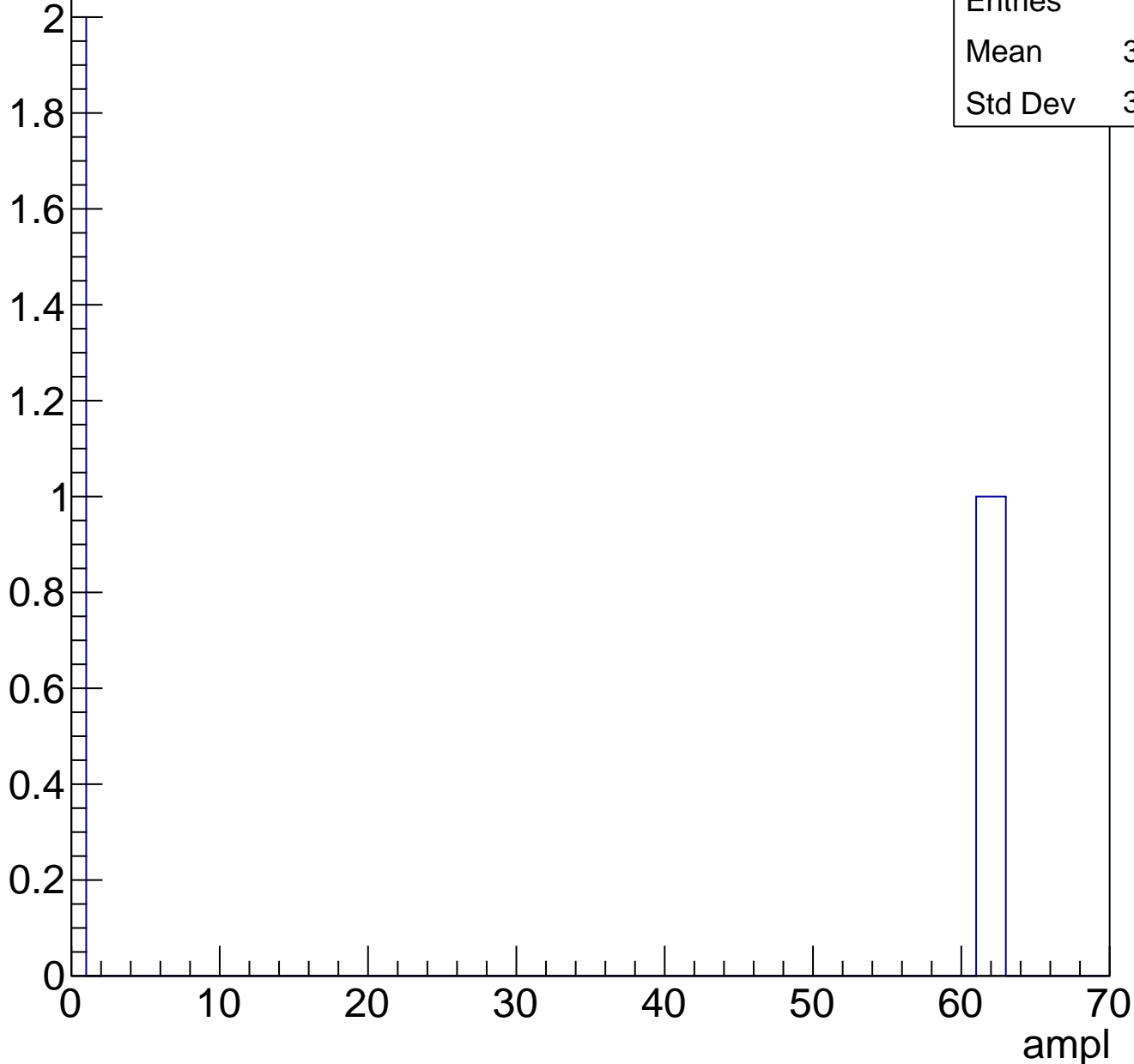
ampl



B1L001S, U24-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	30.75
Std Dev	30.75

B1L001S, U24-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch9, adc0

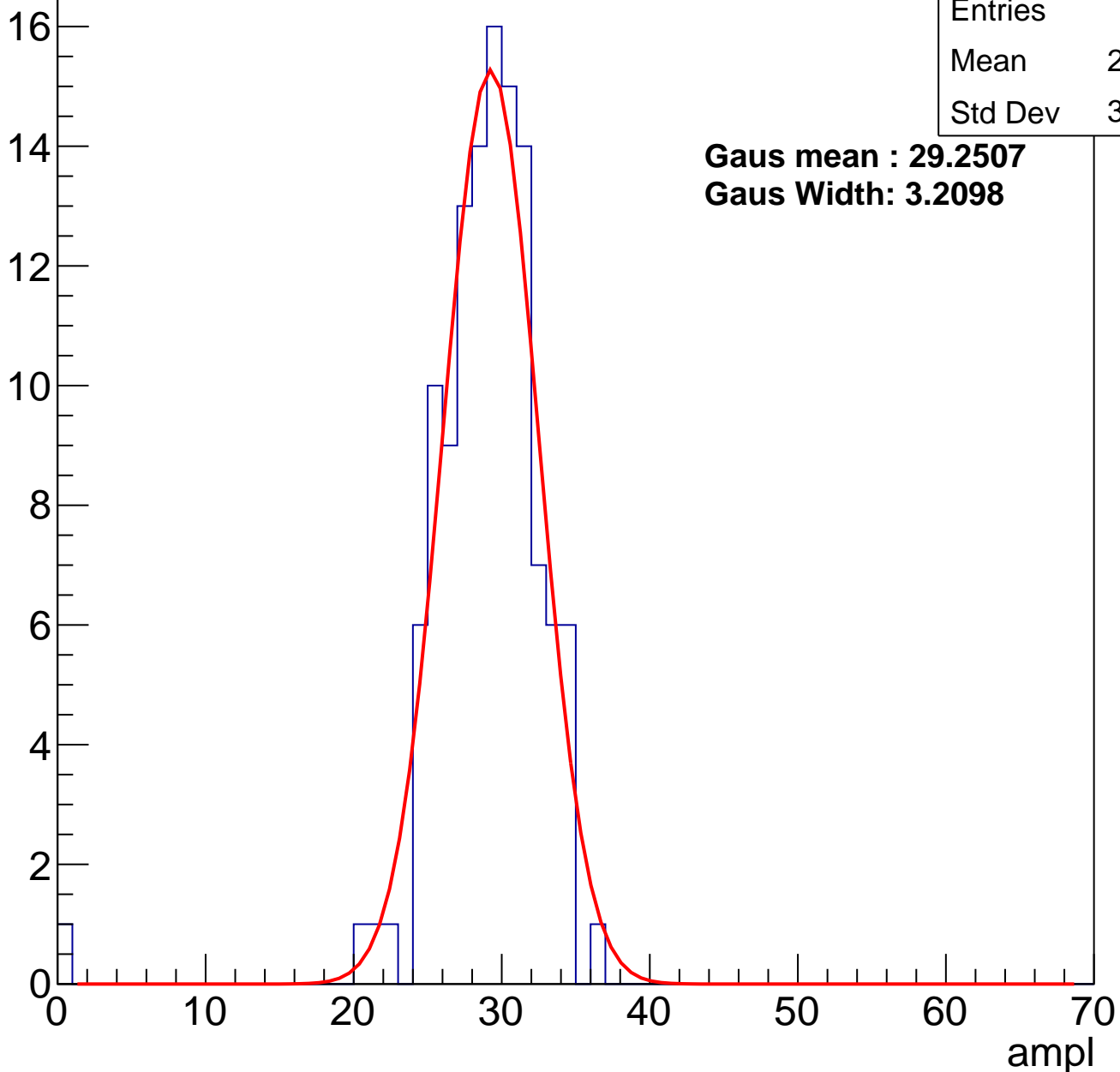
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	28.46
Std Dev	3.948

Gaus mean : 29.2507

Gaus Width: 3.2098

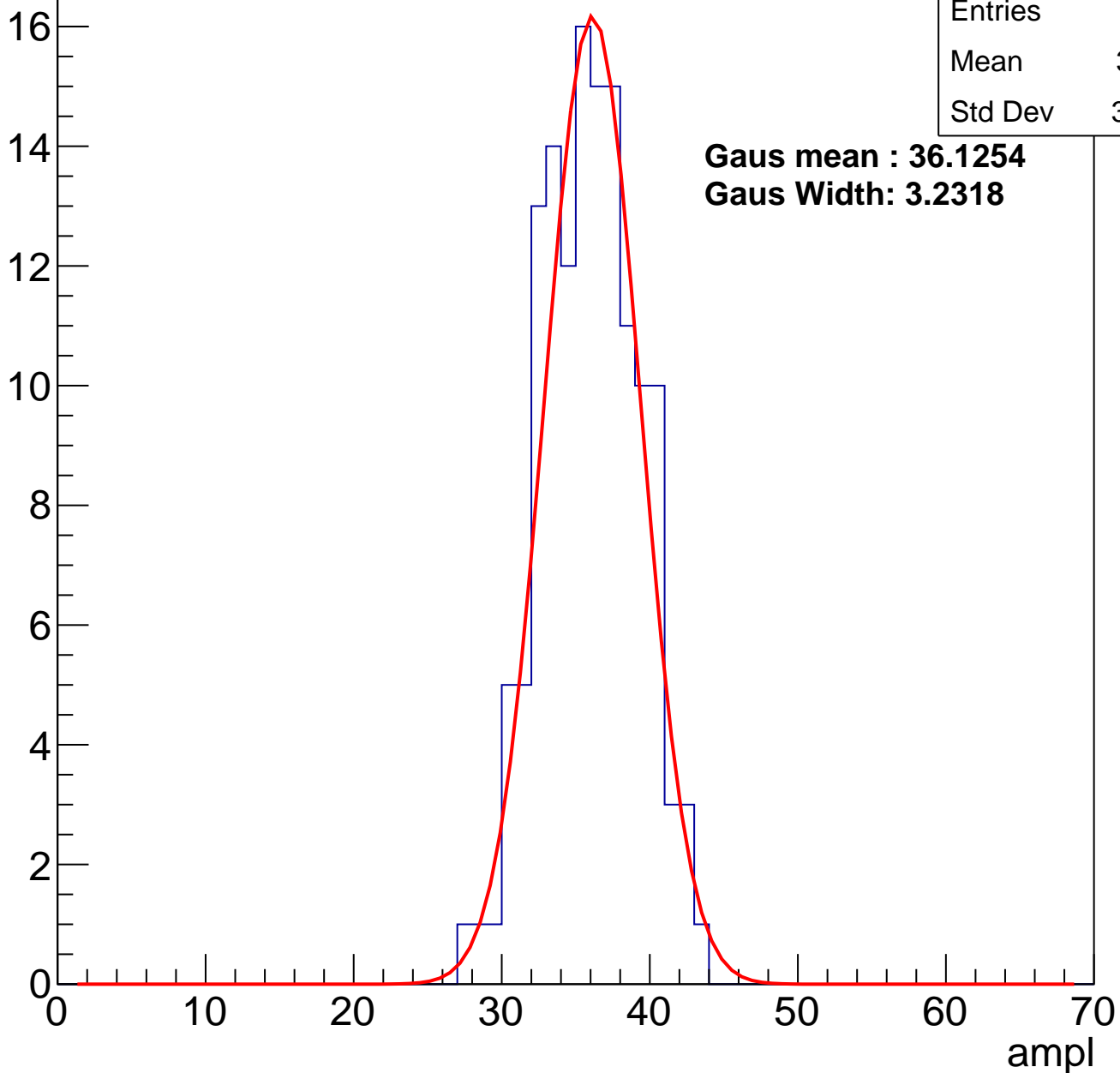
Entry



B1L001S, U24-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

42.15

Std Dev

3.729

Gaus mean : 42.4444

Gaus Width: 3.8647

0

10

20

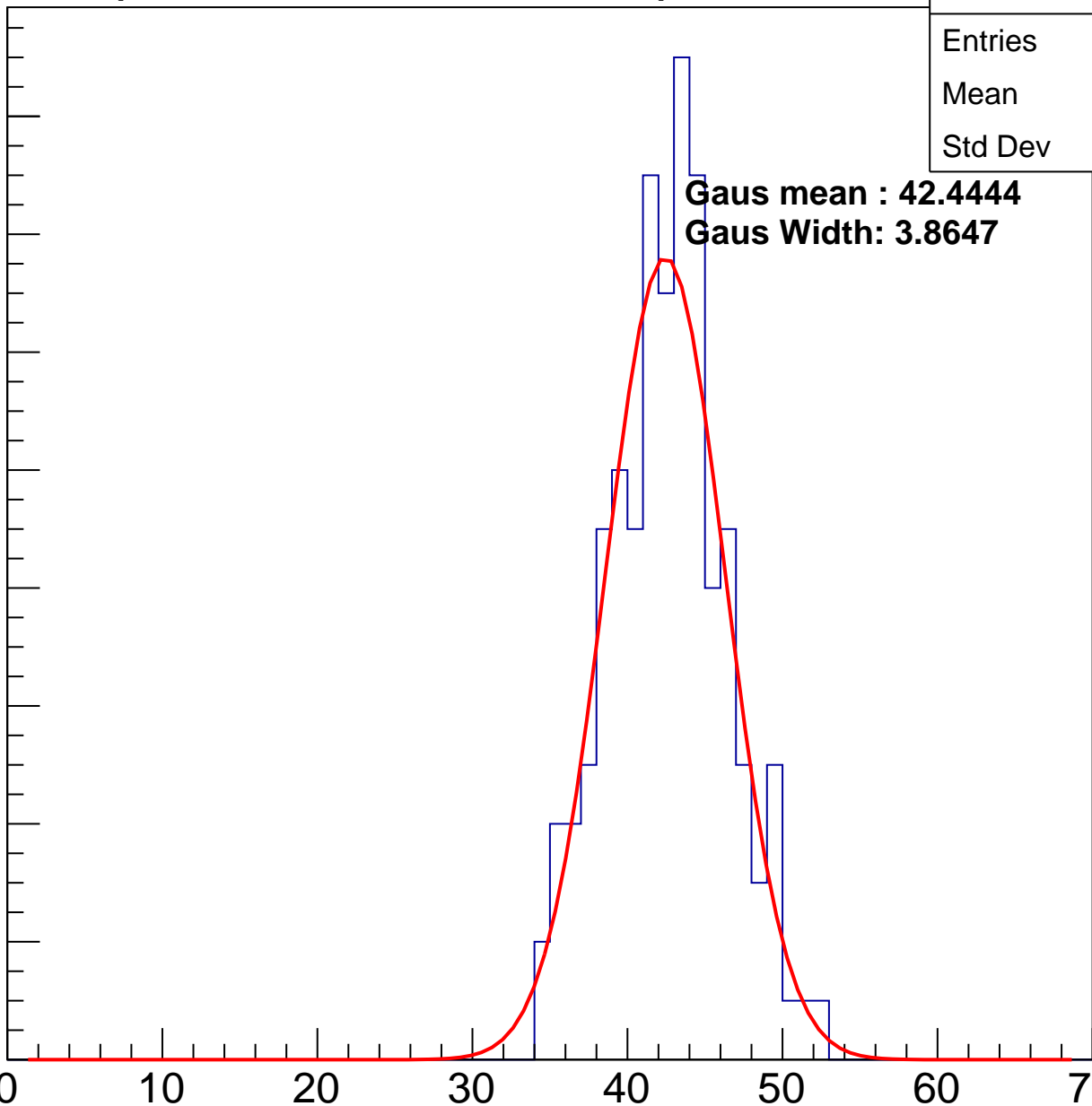
30

40

50

60

ampl



B1L001S, U24-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

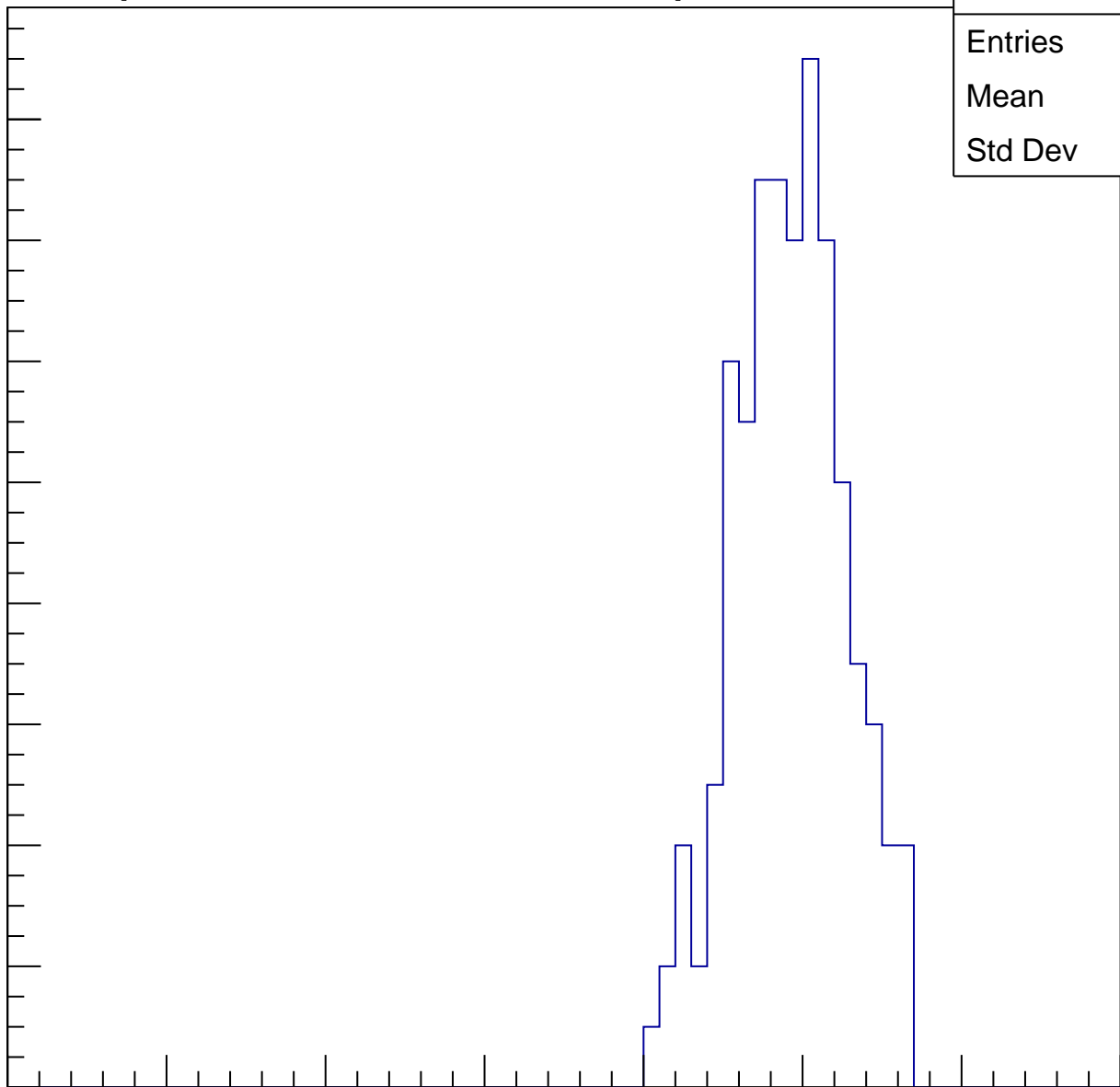
Entries	143
Mean	48.78
Std Dev	3.474

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

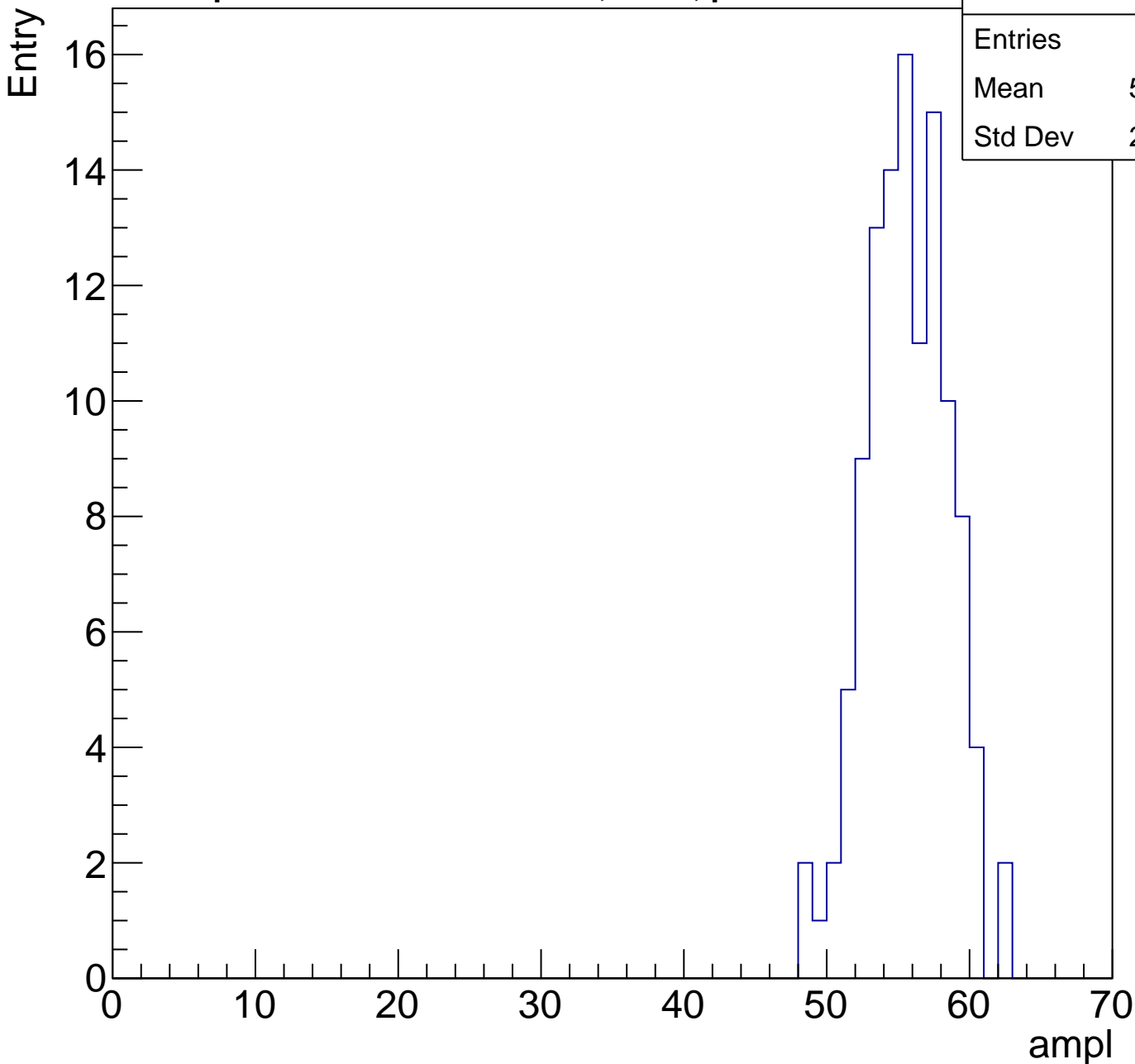
ampl



B1L001S, U24-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	55.18
Std Dev	2.826



B1L001S, U24-ch9, adc5

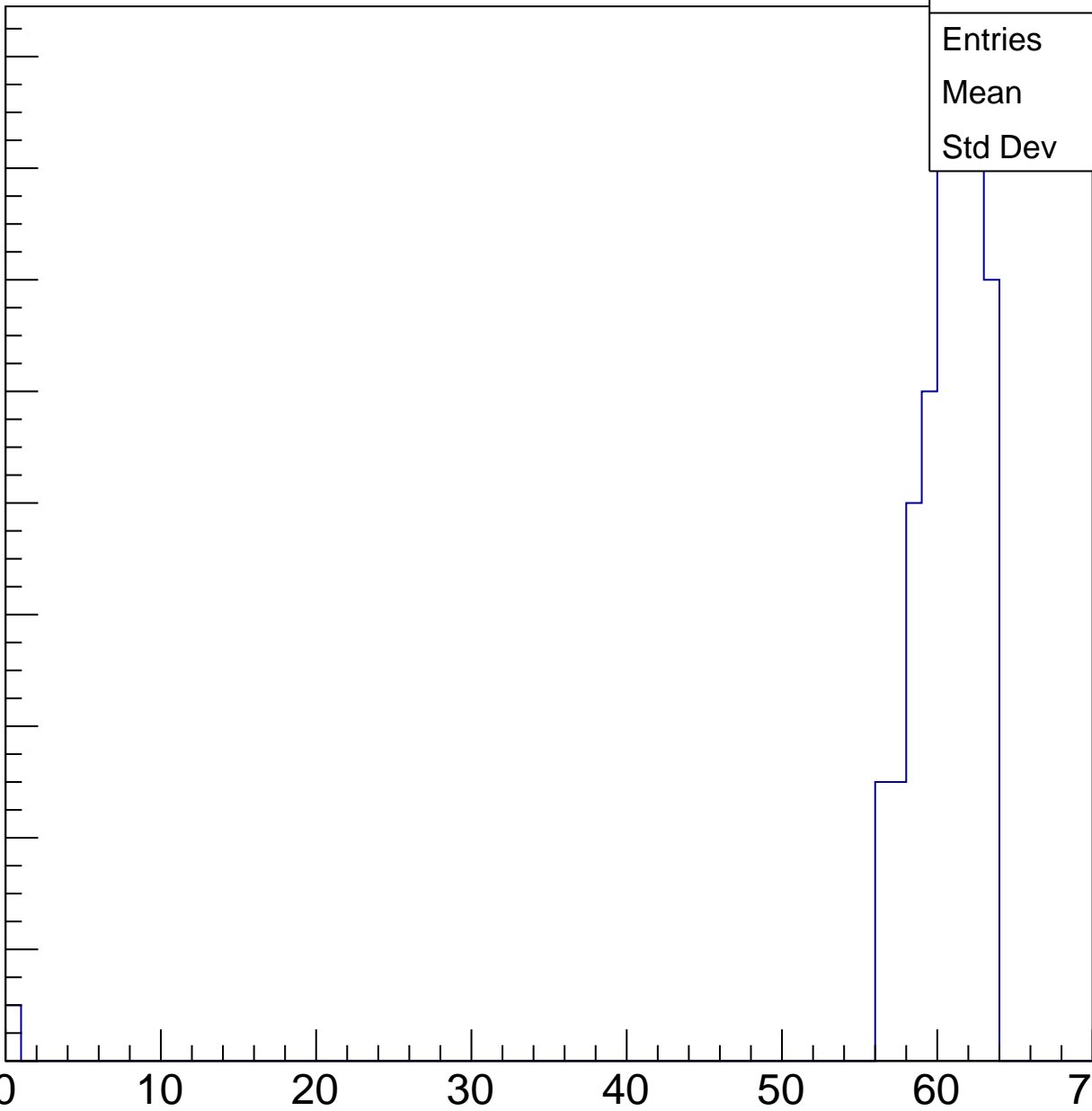
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.64
Std Dev	6.399

ampl

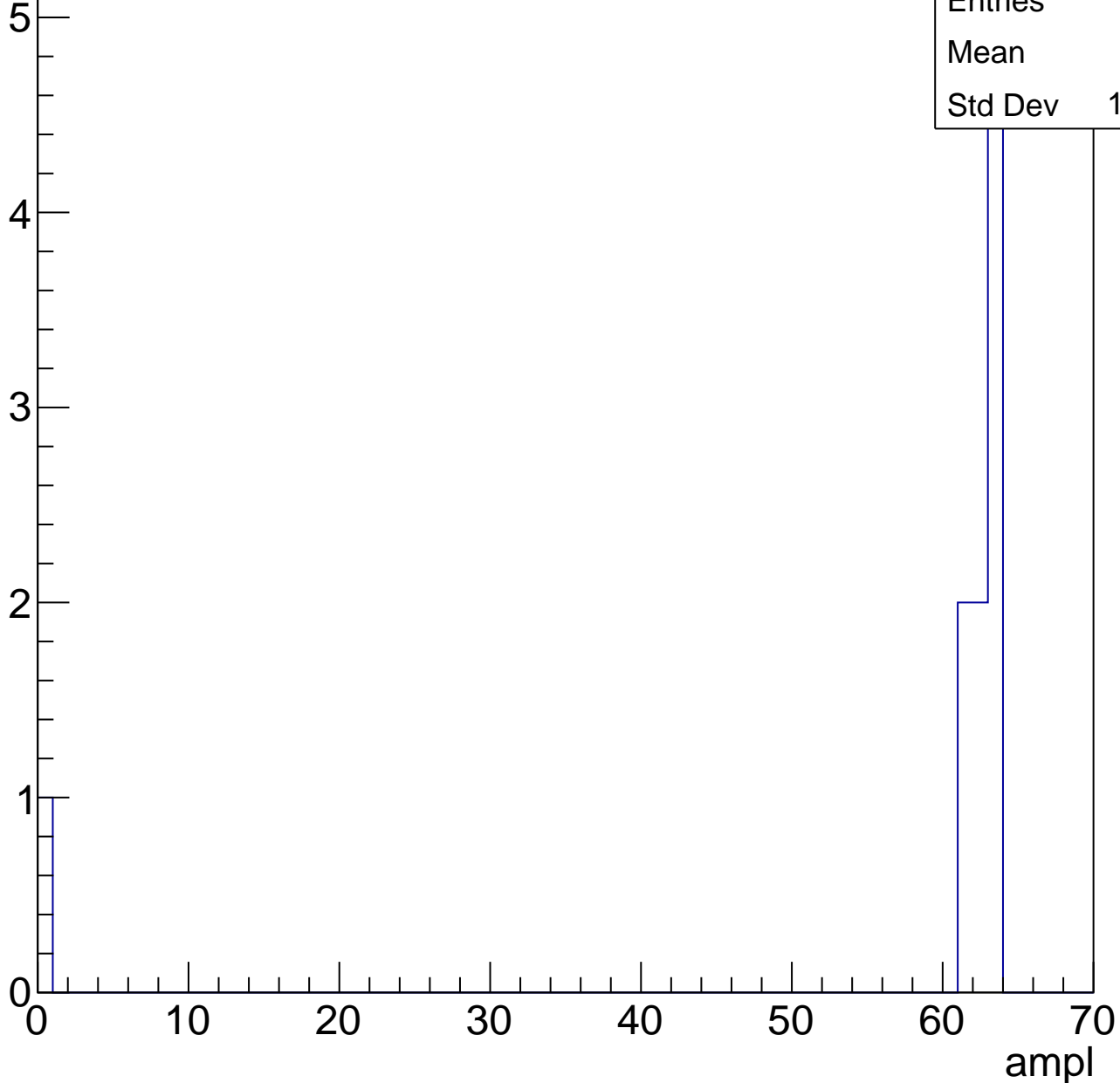


B1L001S, U24-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

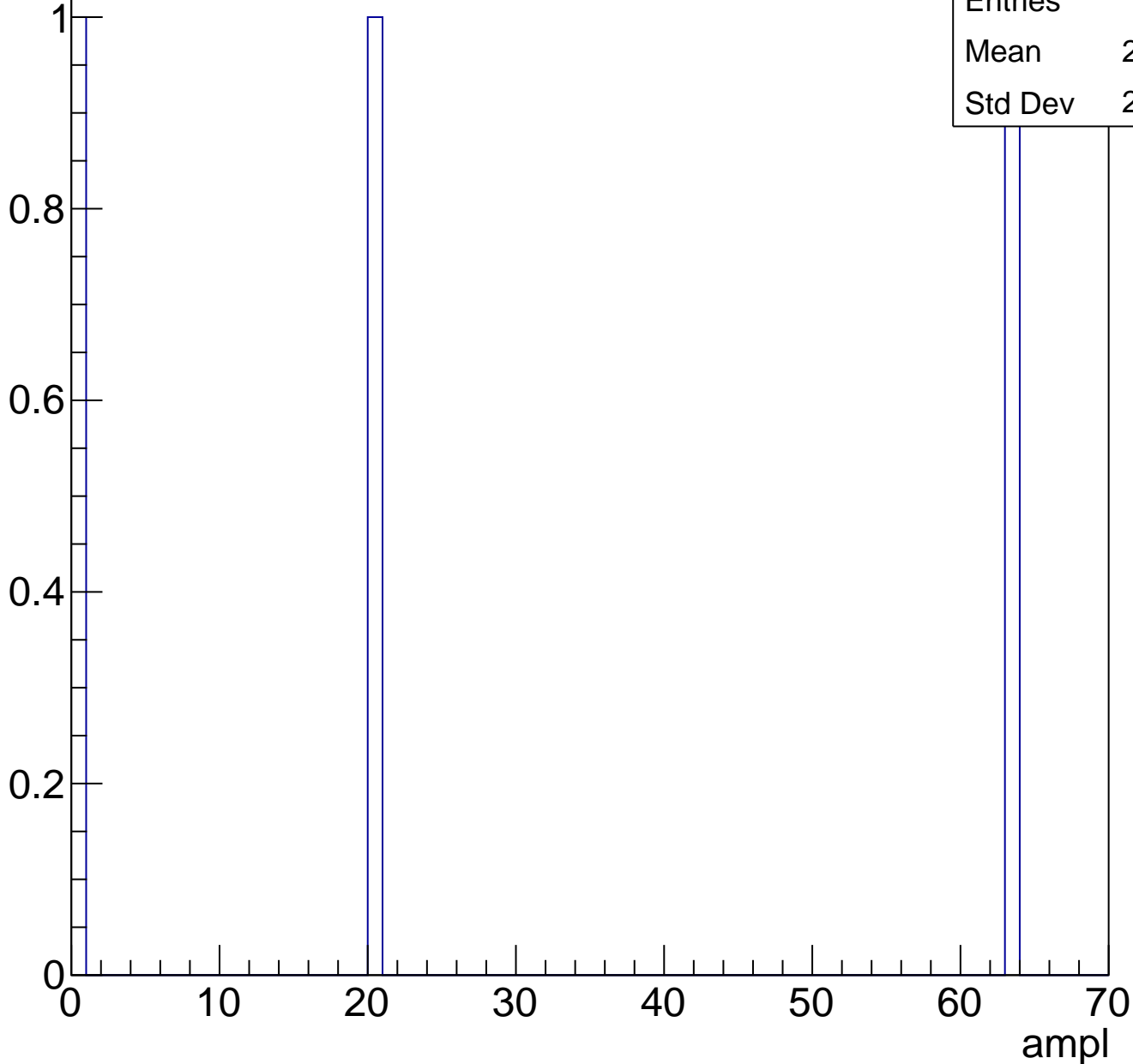
Entries	10
Mean	56.1
Std Dev	18.72



B1L001S, U24-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B1L001S, U24-ch10, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.2
Std Dev	4.092

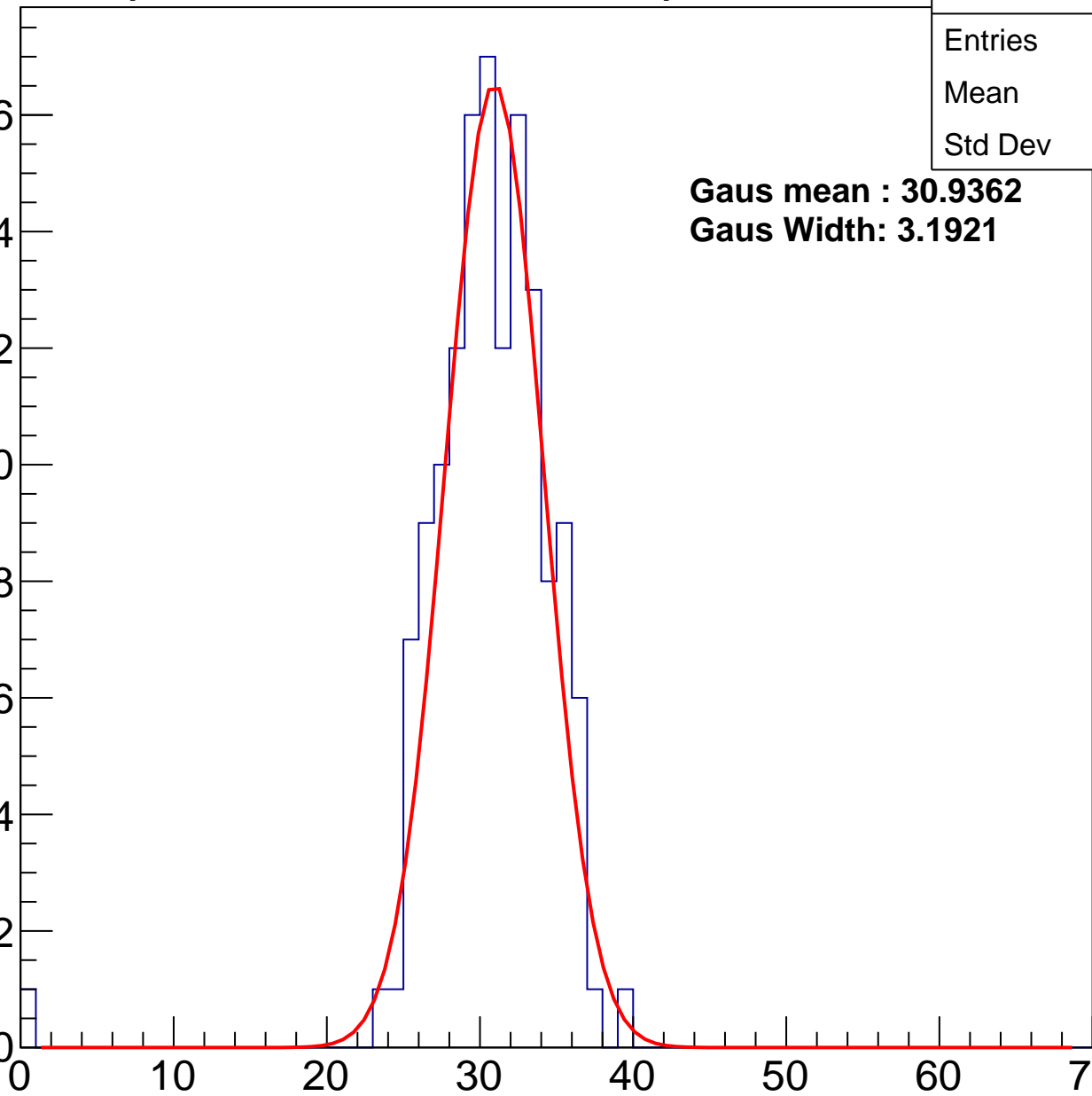
Gaus mean : 30.9362

Gaus Width: 3.1921

Entry

16
14
12
10
8
6
4
2
0

ampl



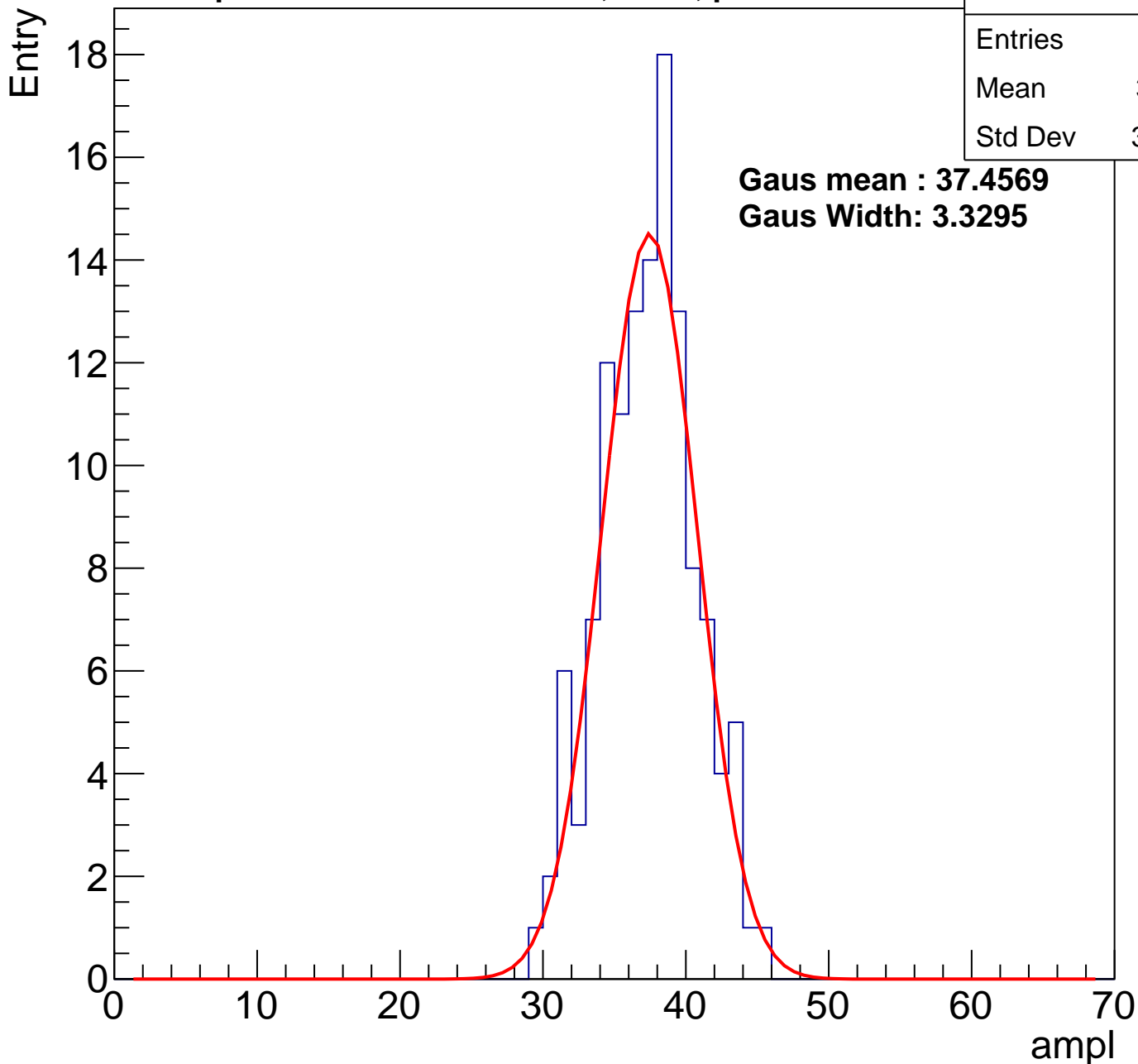
B1L001S, U24-ch10, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	36.91
Std Dev	3.305

Gaus mean : 37.4569

Gaus Width: 3.3295



B1L001S, U24-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.5
Std Dev	3.452

Gaus mean : 44.2333

Gaus Width: 3.3548

Entry

16

14

12

10

8

6

4

2

0

0

10

20

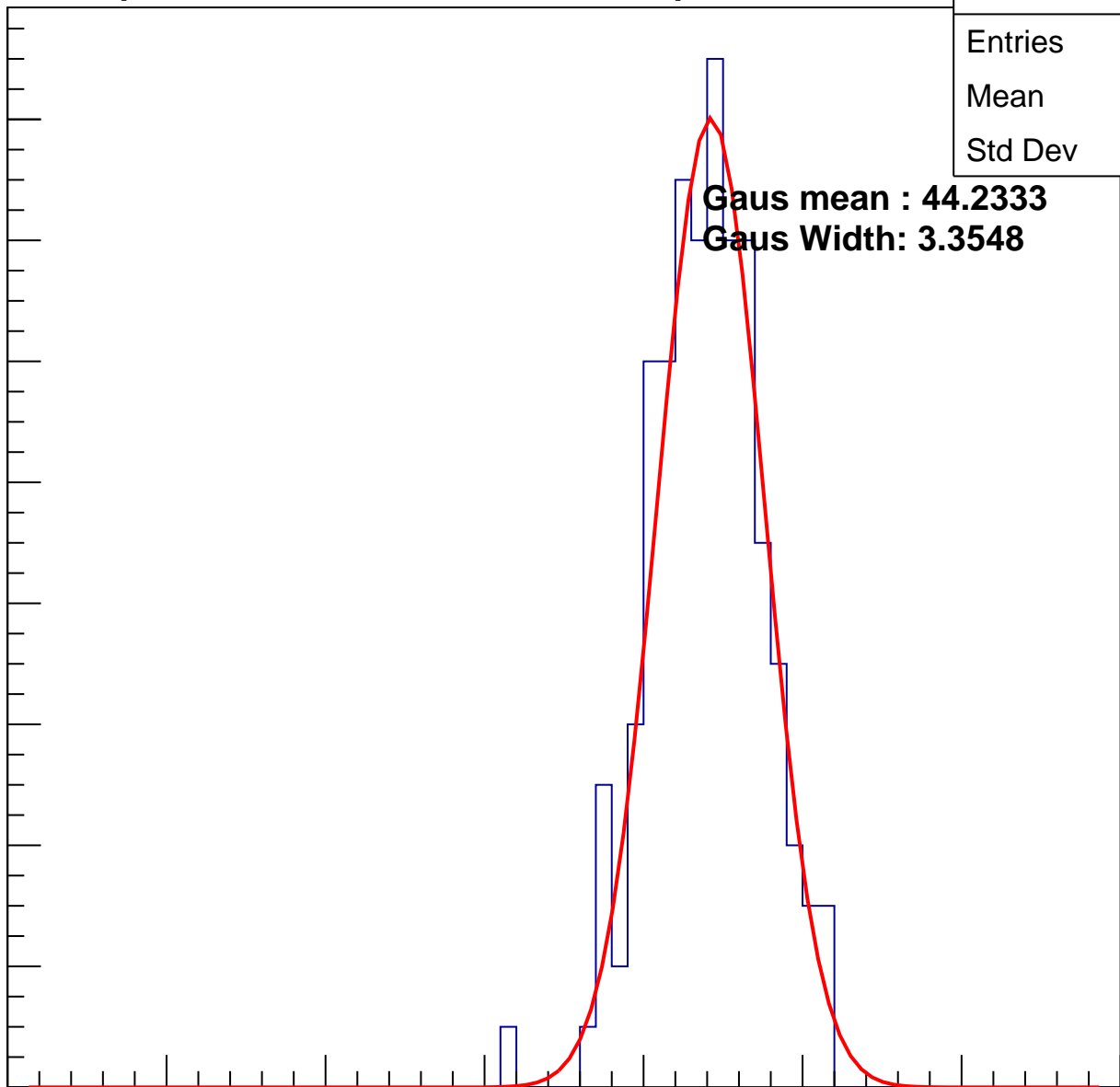
30

40

50

60

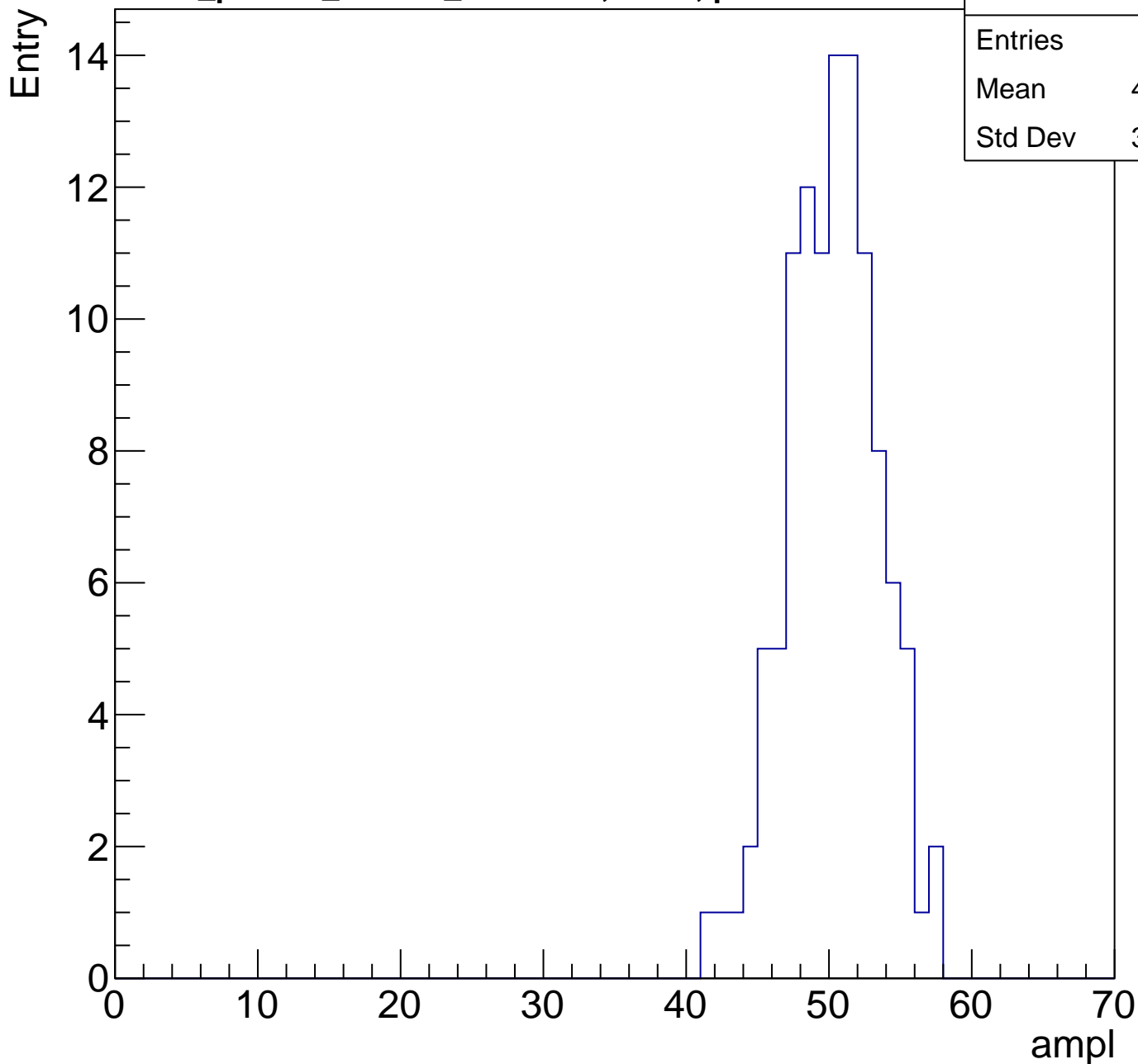
ampl



B1L001S, U24-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	49.82
Std Dev	3.177



B1L001S, U24-ch10, adc4

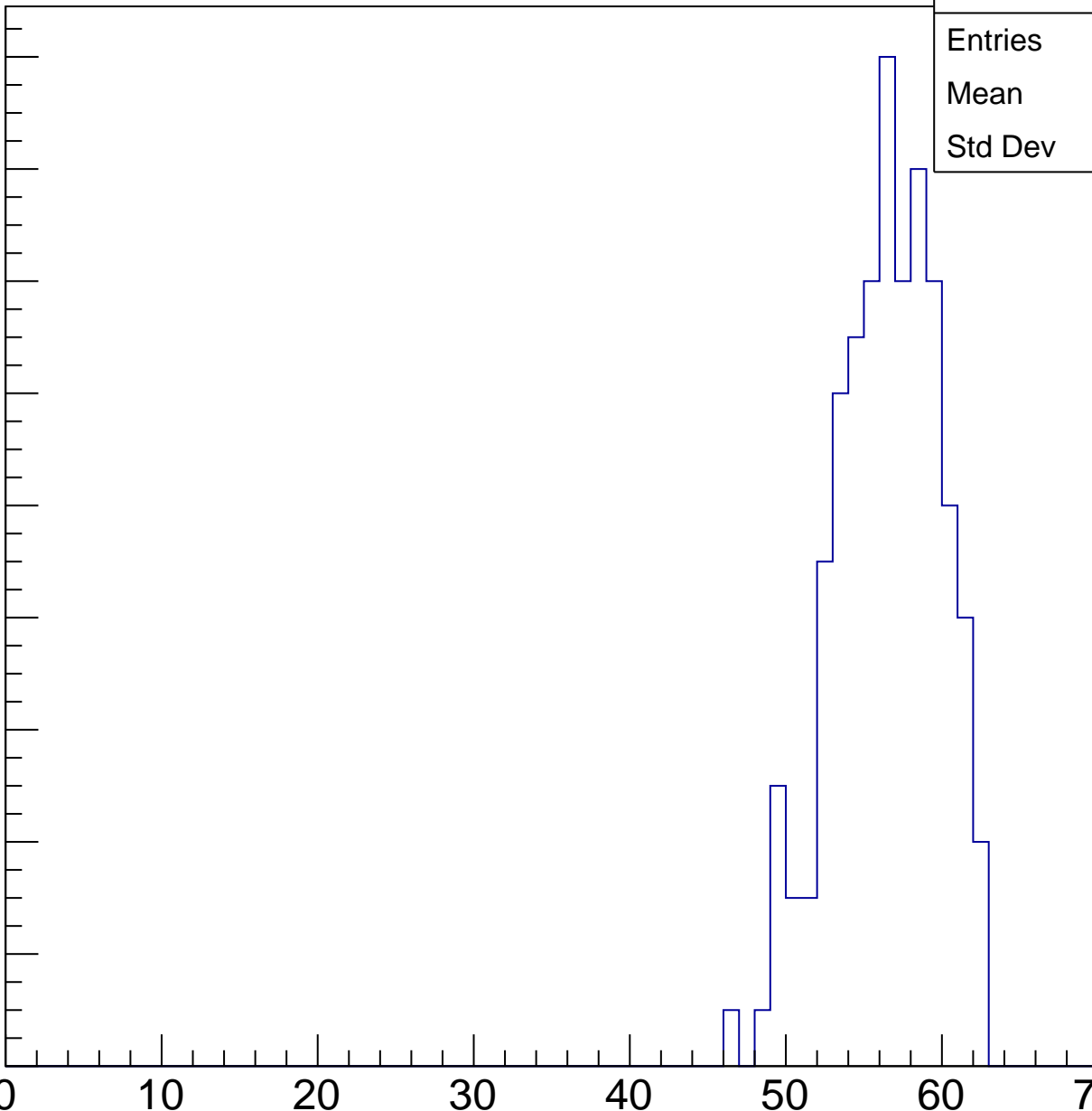
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	145
Mean	55.96
Std Dev	3.34

ampl



B1L001S, U24-ch10, adc5

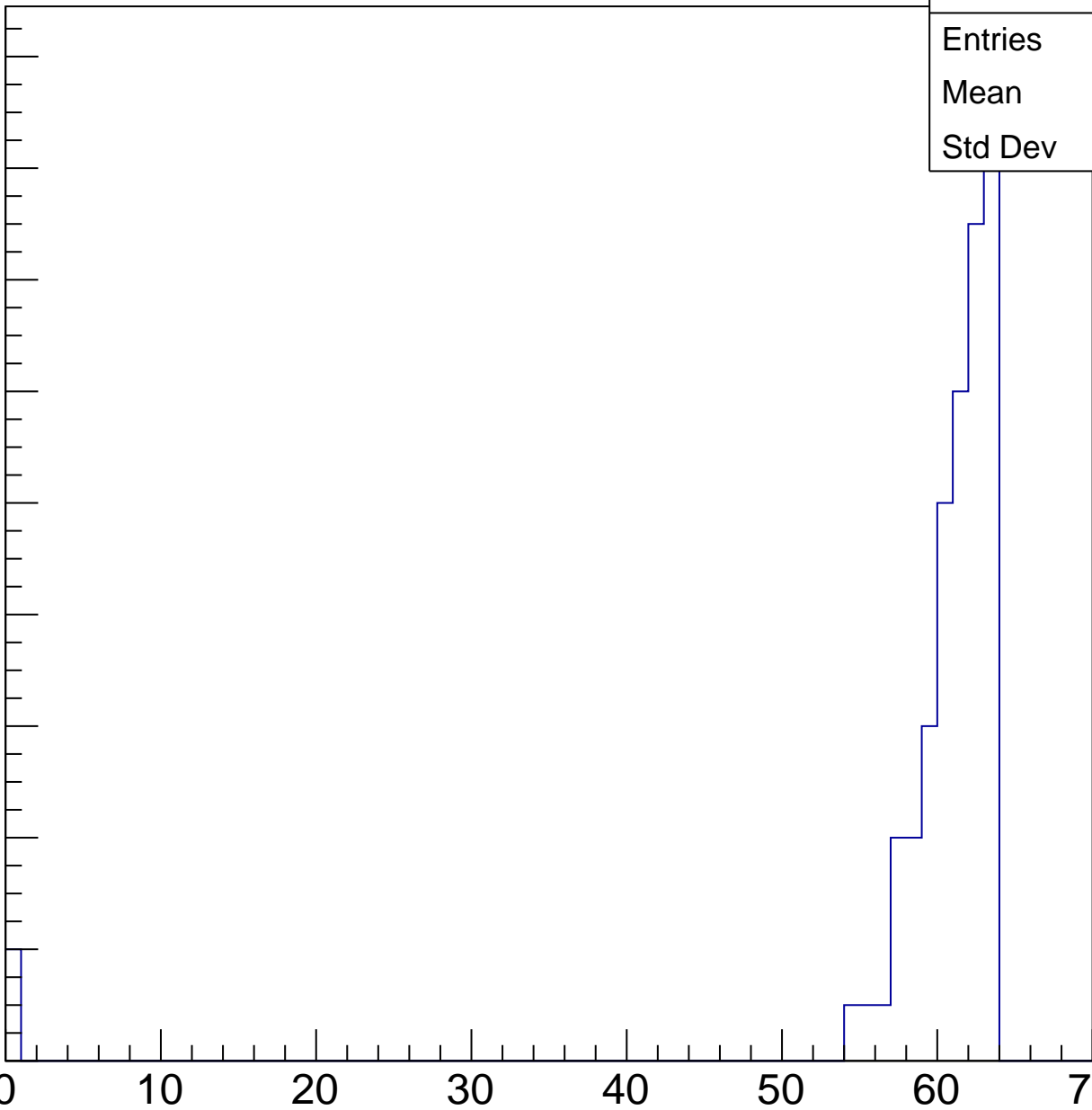
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.12
Std Dev	10.08

ampl



B1L001S, U24-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



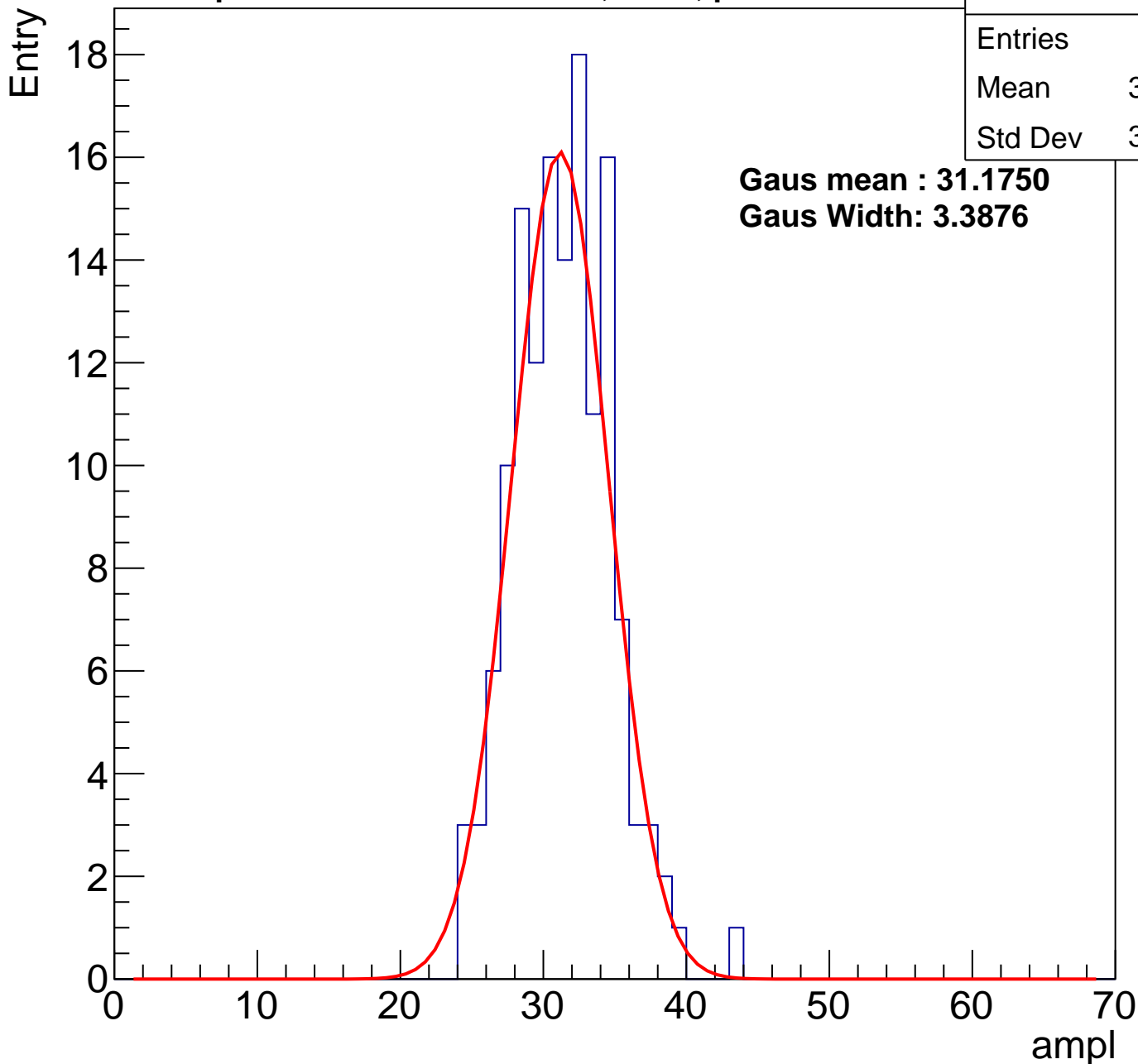
B1L001S, U24-ch11, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.92
Std Dev	3.337

Gaus mean : 31.1750

Gaus Width: 3.3876



B1L001S, U24-ch11, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	38.01
Std Dev	3.801

Gaus mean : 38.8806

Gaus Width: 3.6666

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

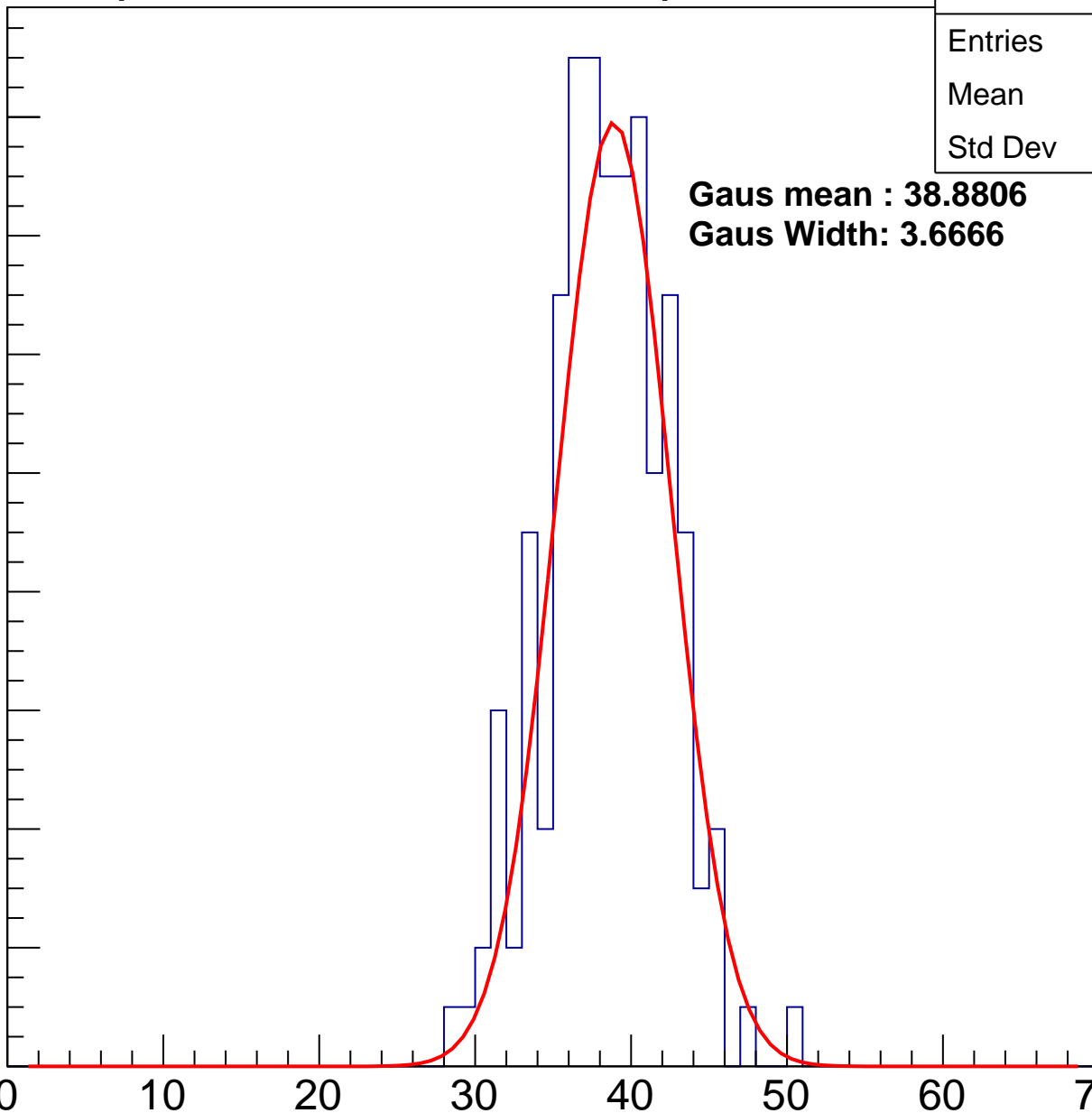
40

50

60

70

ampl



B1L001S, U24-ch11, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

45.19

Std Dev

3.261

Gaus mean : 45.7054

Gaus Width: 3.4334

0

10

20

30

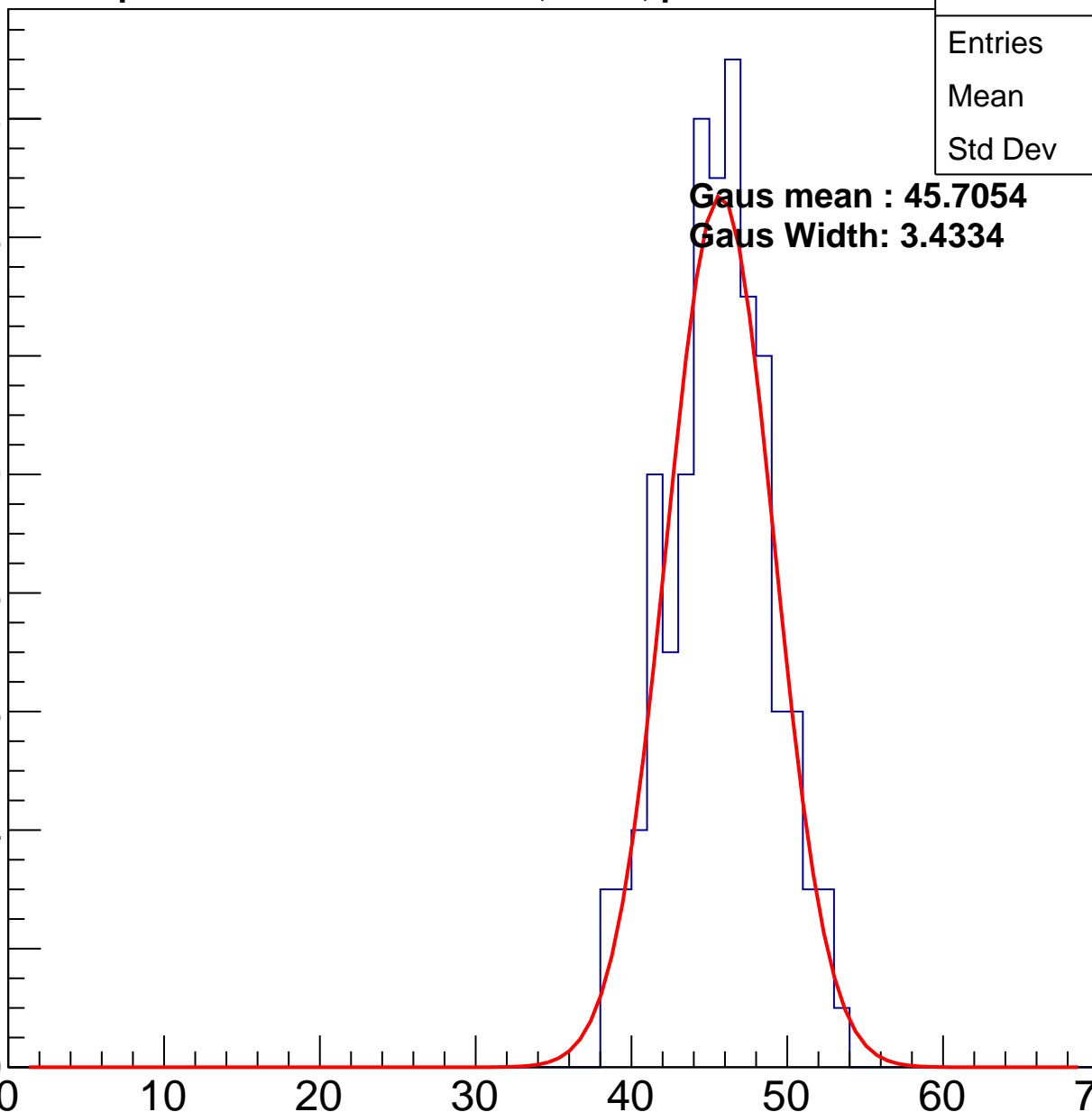
40

50

60

70

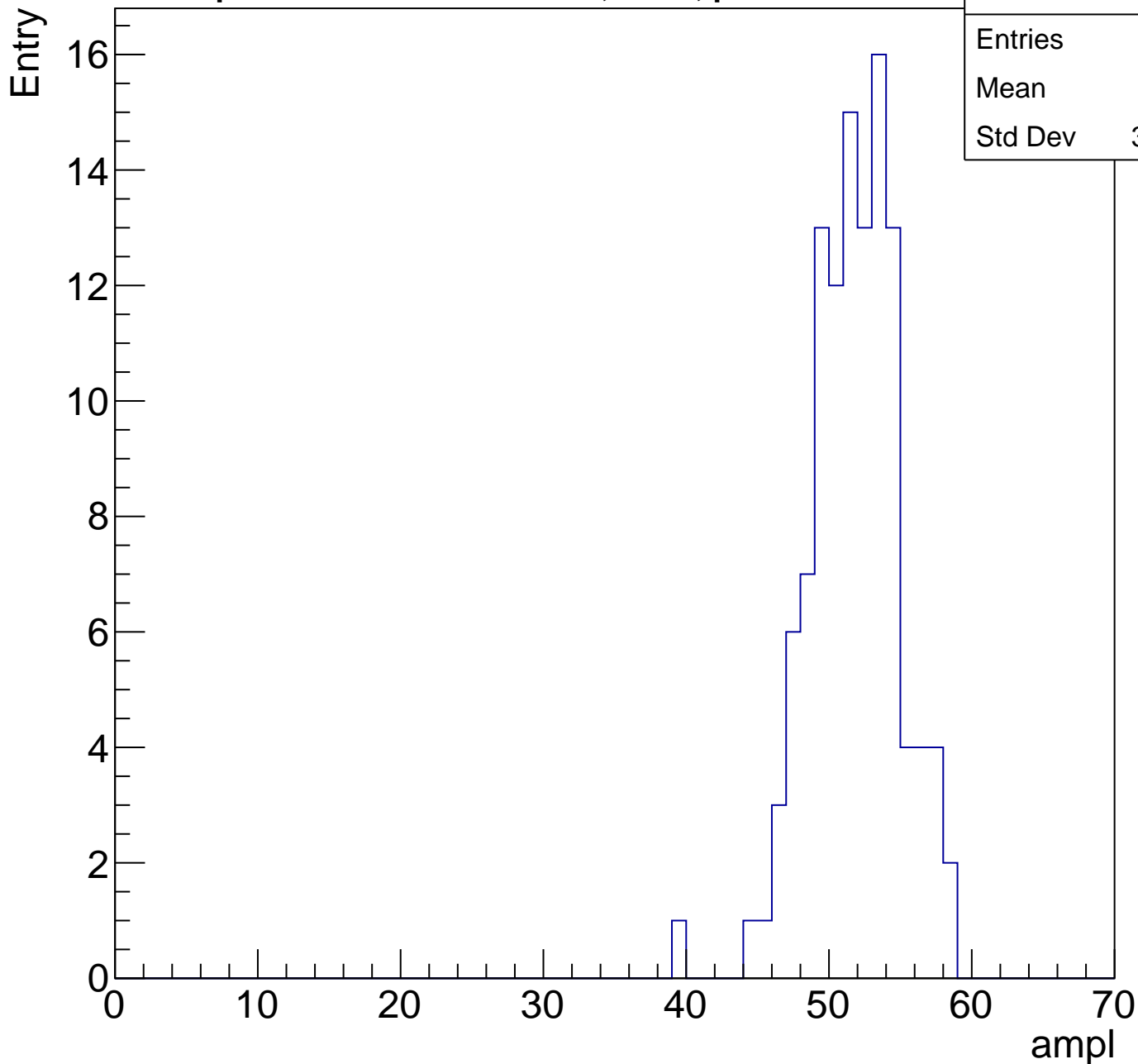
ampl



B1L001S, U24-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	51.3
Std Dev	3.116



B1L001S, U24-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

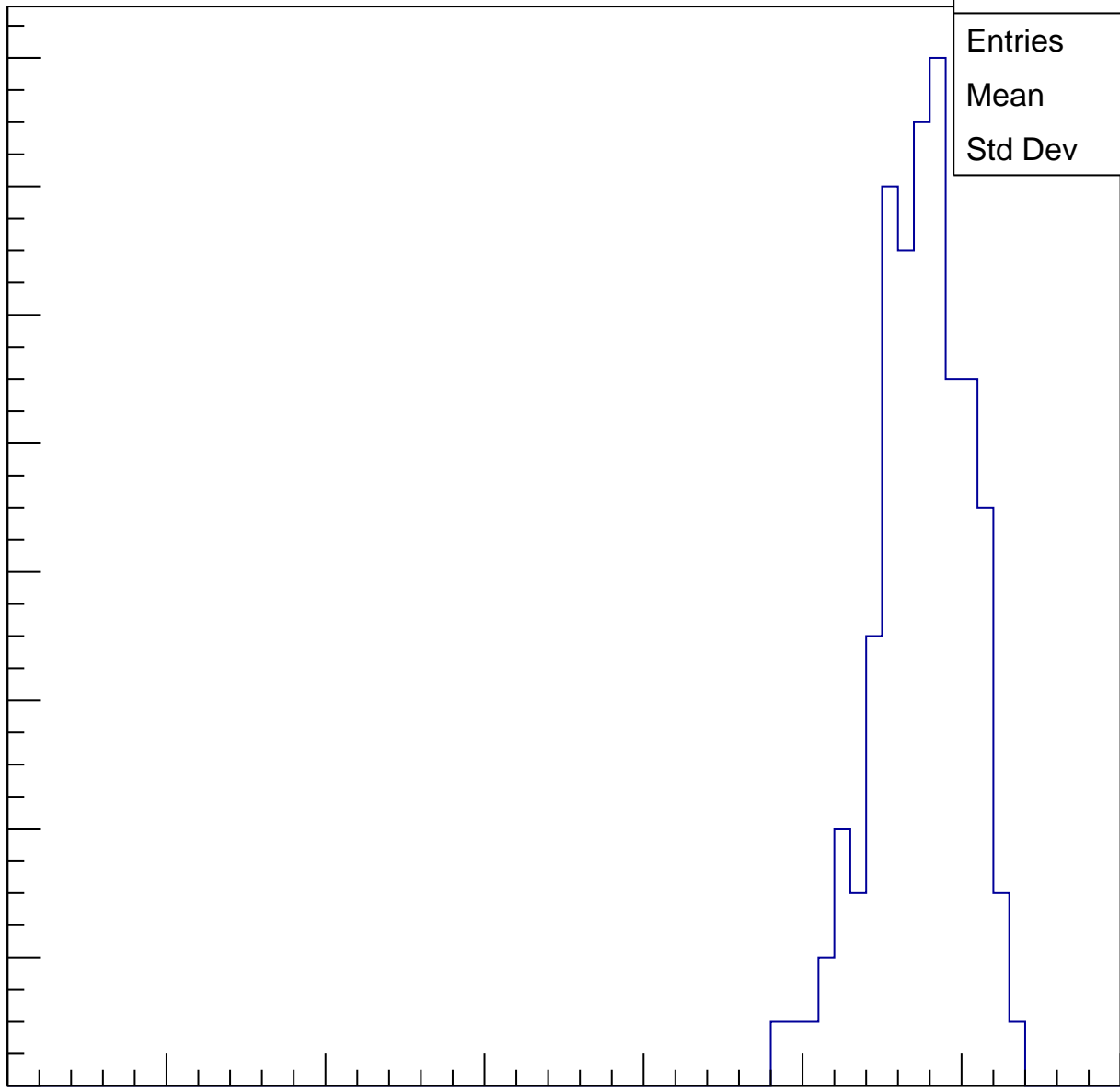
Entries	112
Mean	56.98
Std Dev	2.928

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch11, adc5

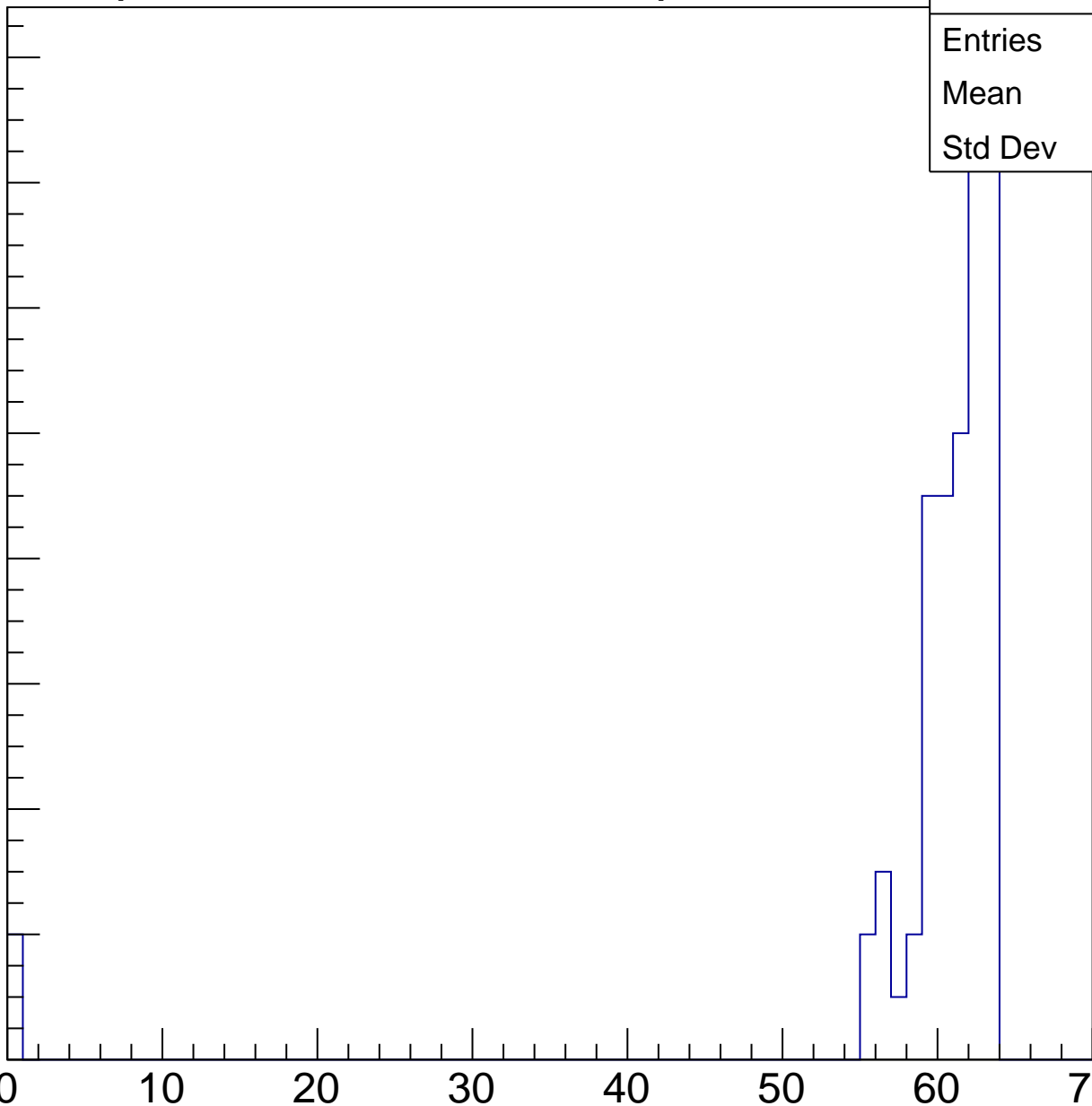
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.99
Std Dev	10.4

ampl



B1L001S, U24-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U24-ch12, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	30.01
Std Dev	3.209

Gaus mean : 30.5215

Gaus Width: 3.2554

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

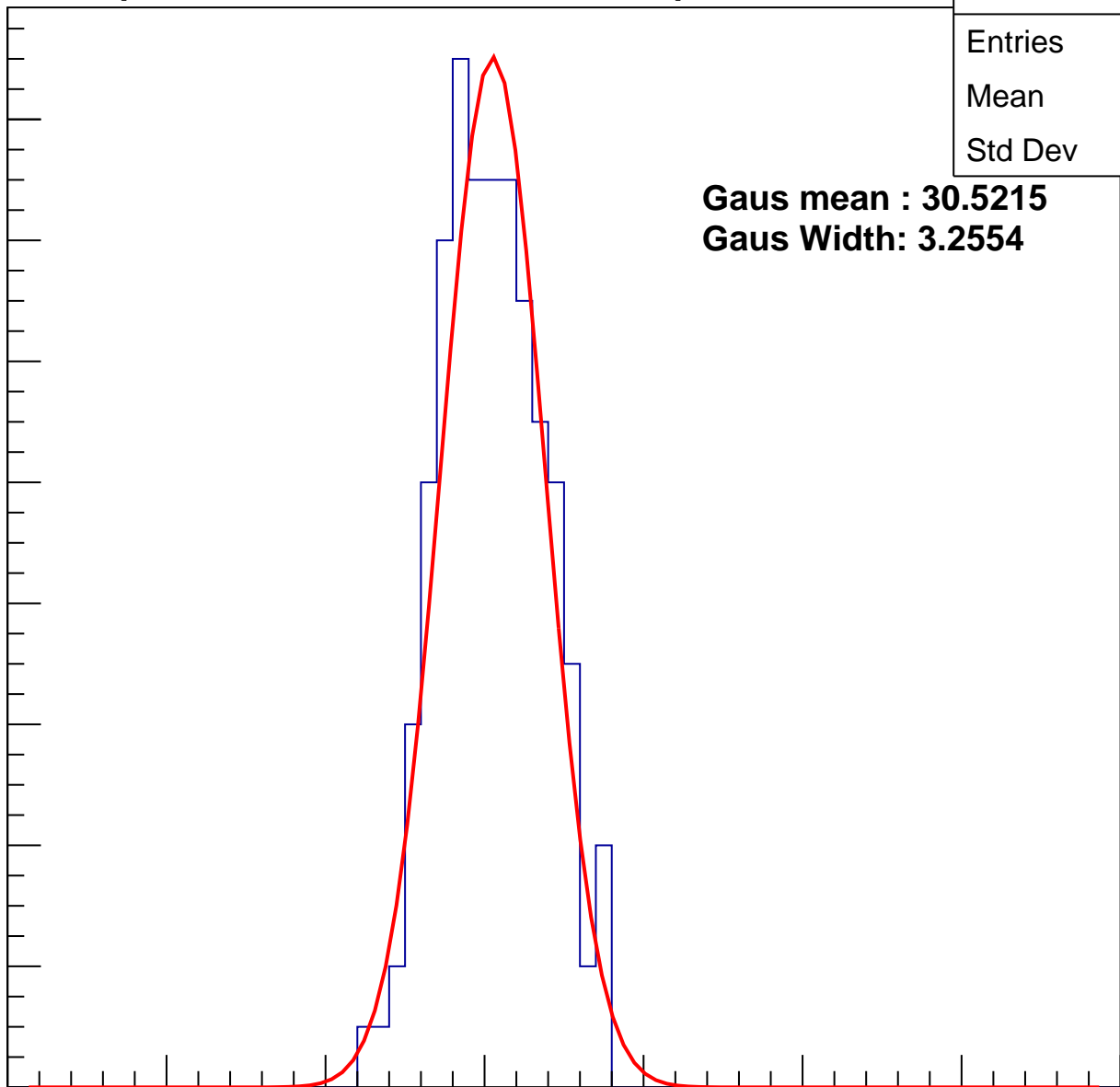
40

50

60

70

ampl



B1L001S, U24-ch12, adc1

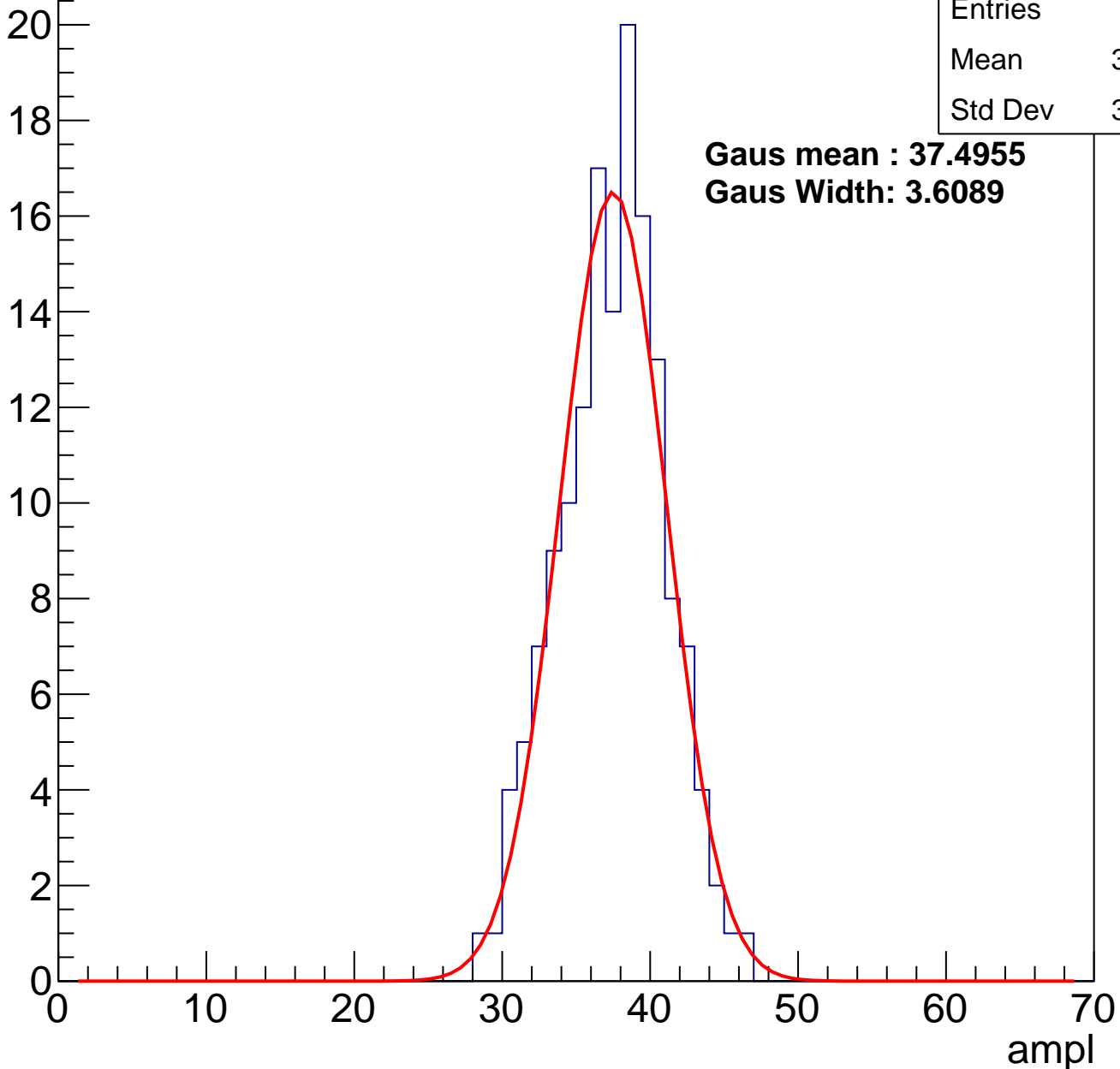
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	36.97
Std Dev	3.522

Gaus mean : 37.4955

Gaus Width: 3.6089

Entry



B1L001S, U24-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

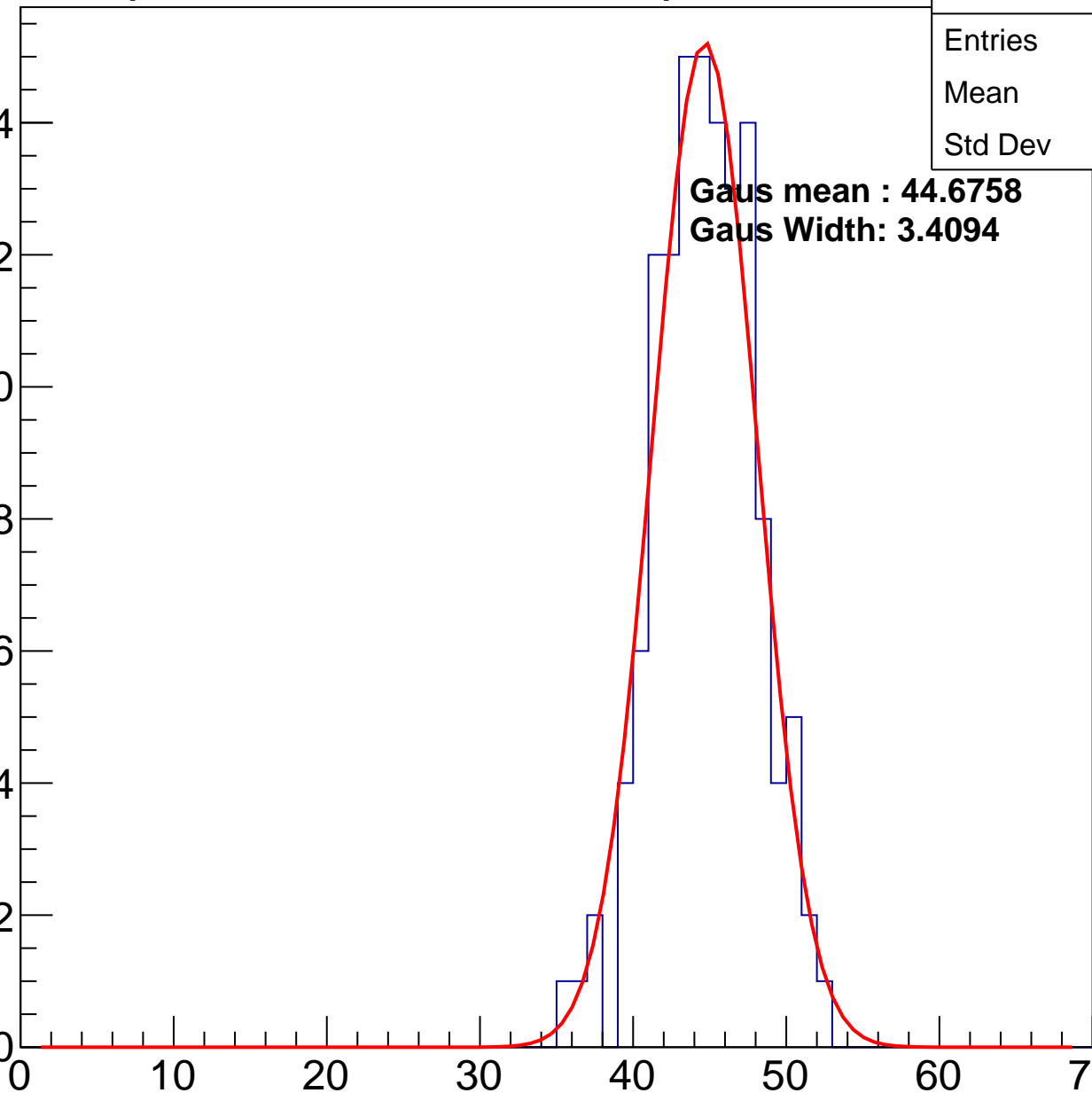
14
12
10
8
6
4
2
0

Entries	129
Mean	44.28
Std Dev	3.249

Gaus mean : 44.6758

Gaus Width: 3.4094

ampl



B1L001S, U24-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

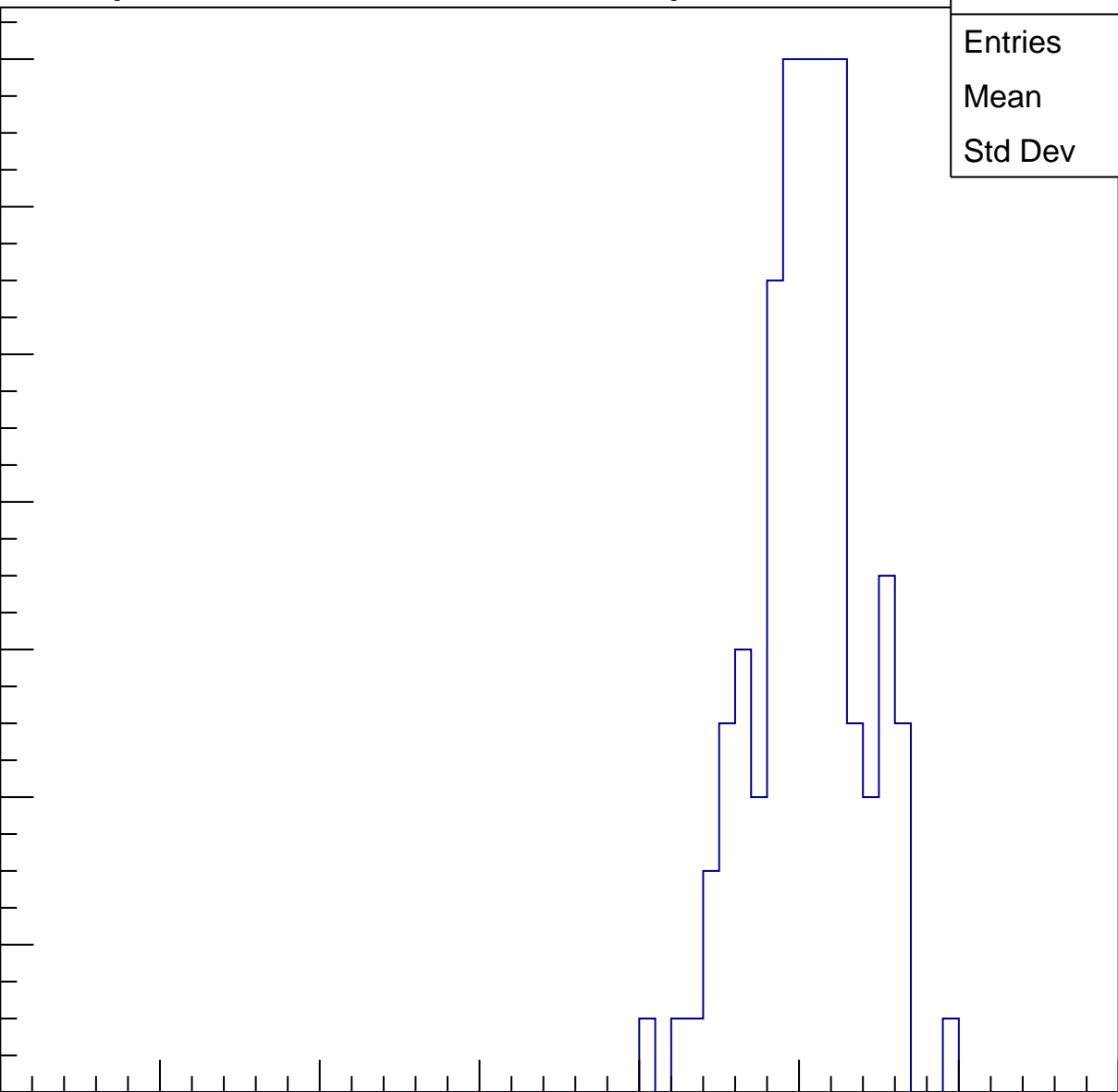
Entries	110
Mean	50.06
Std Dev	3.372

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

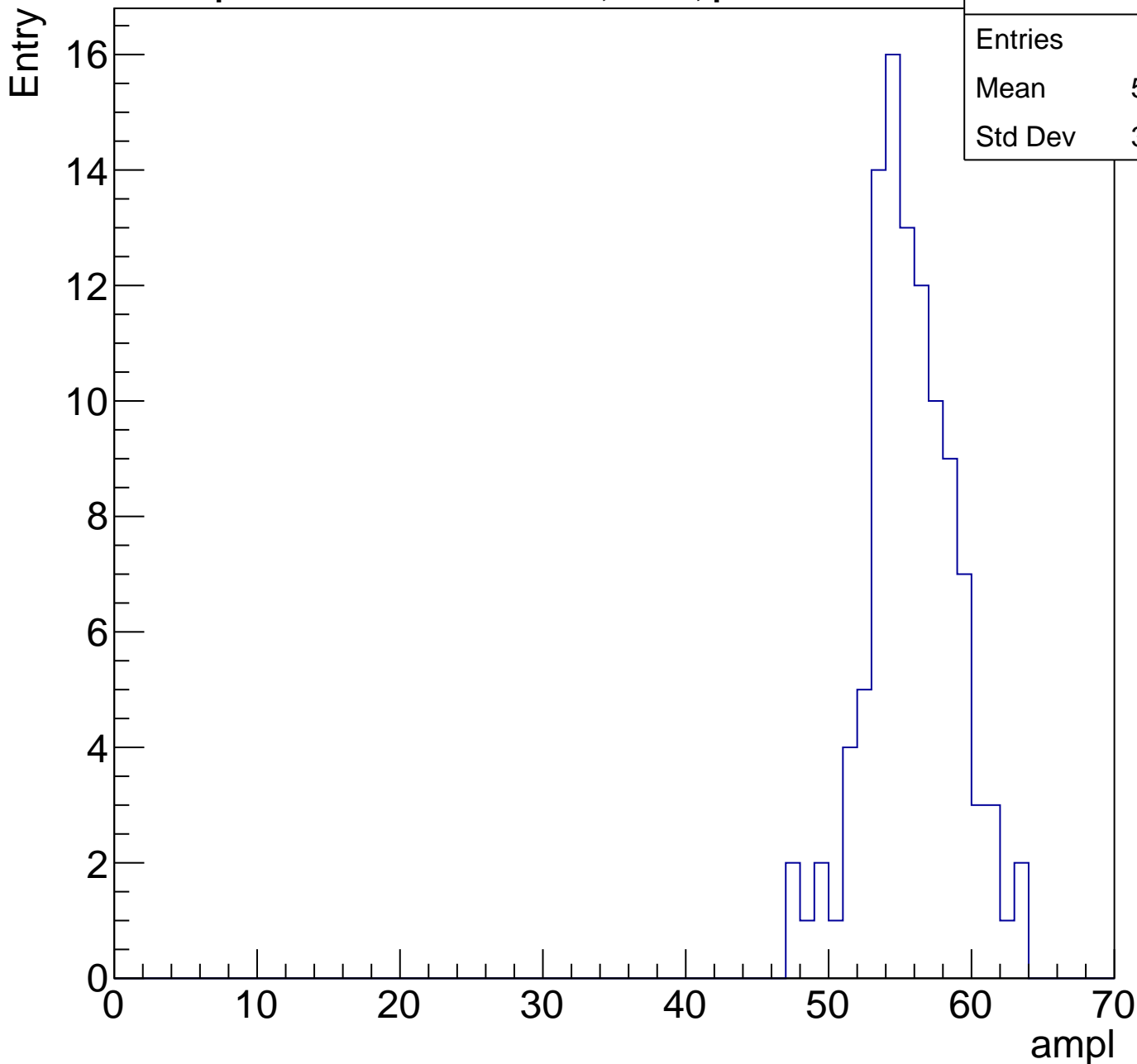
ampl



B1L001S, U24-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

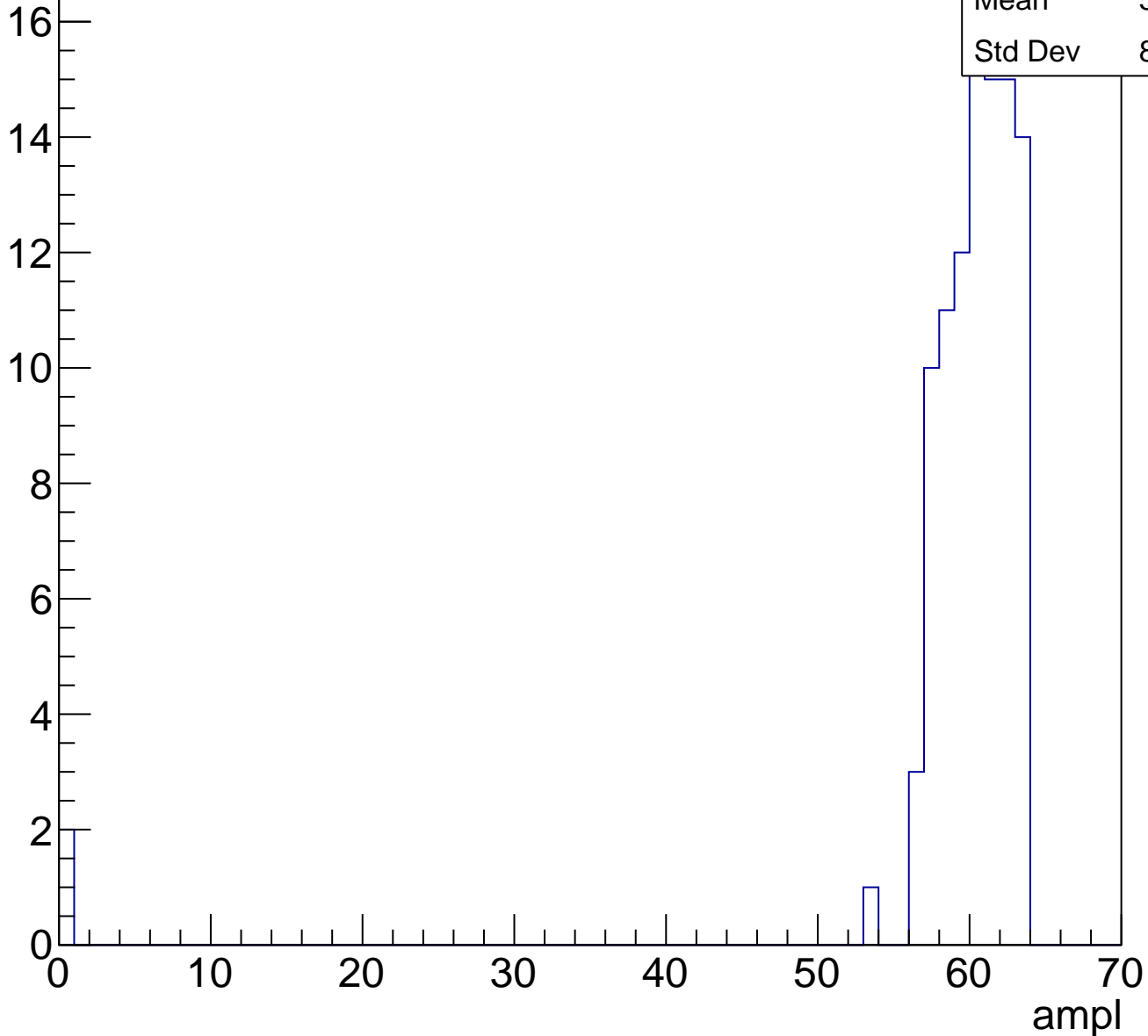
Entries	105
Mean	55.27
Std Dev	3.166



B1L001S, U24-ch12, adc5

calib_packv5_042523_0143.root, FC#2, port C2

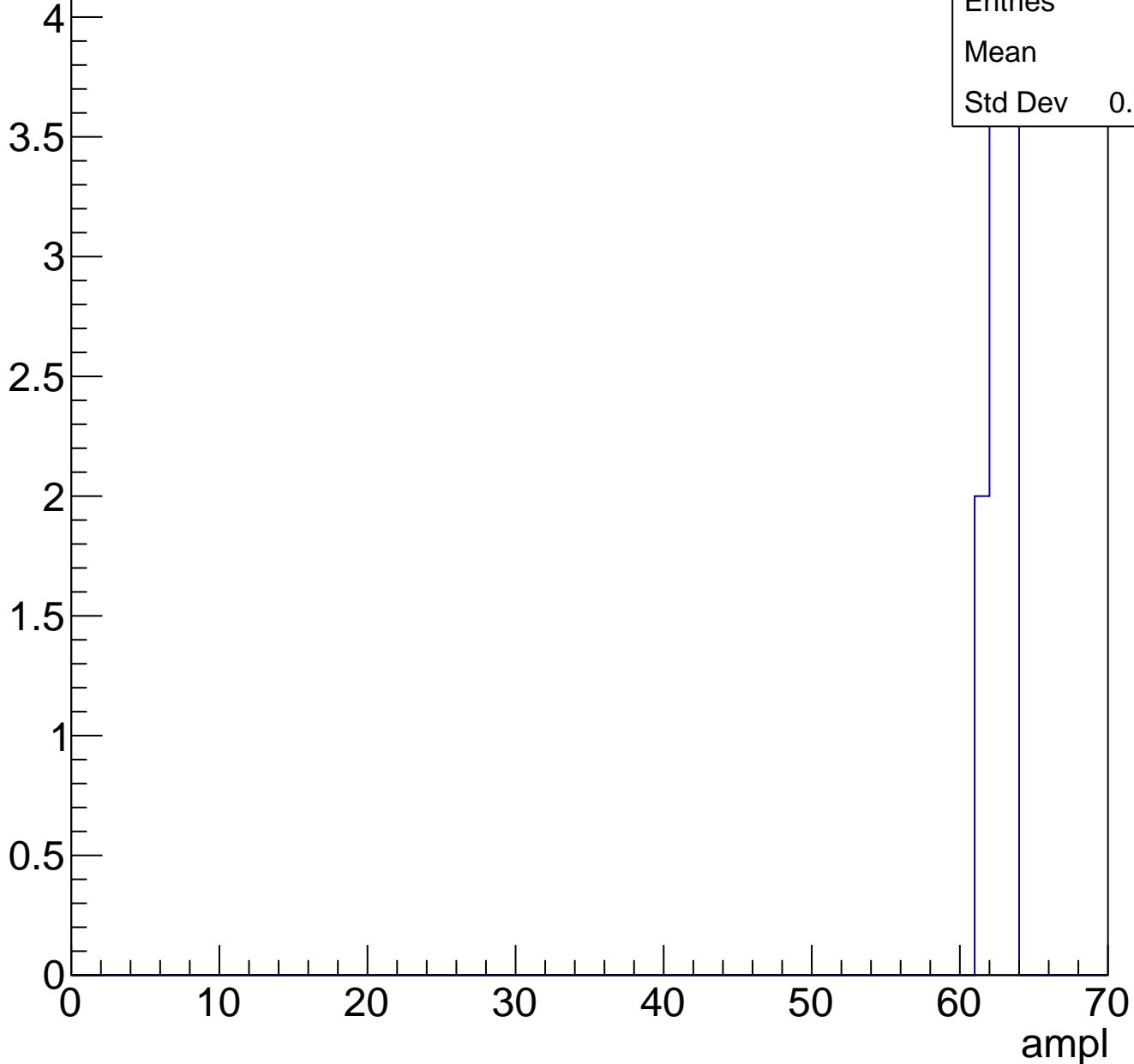
Entry



B1L001S, U24-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

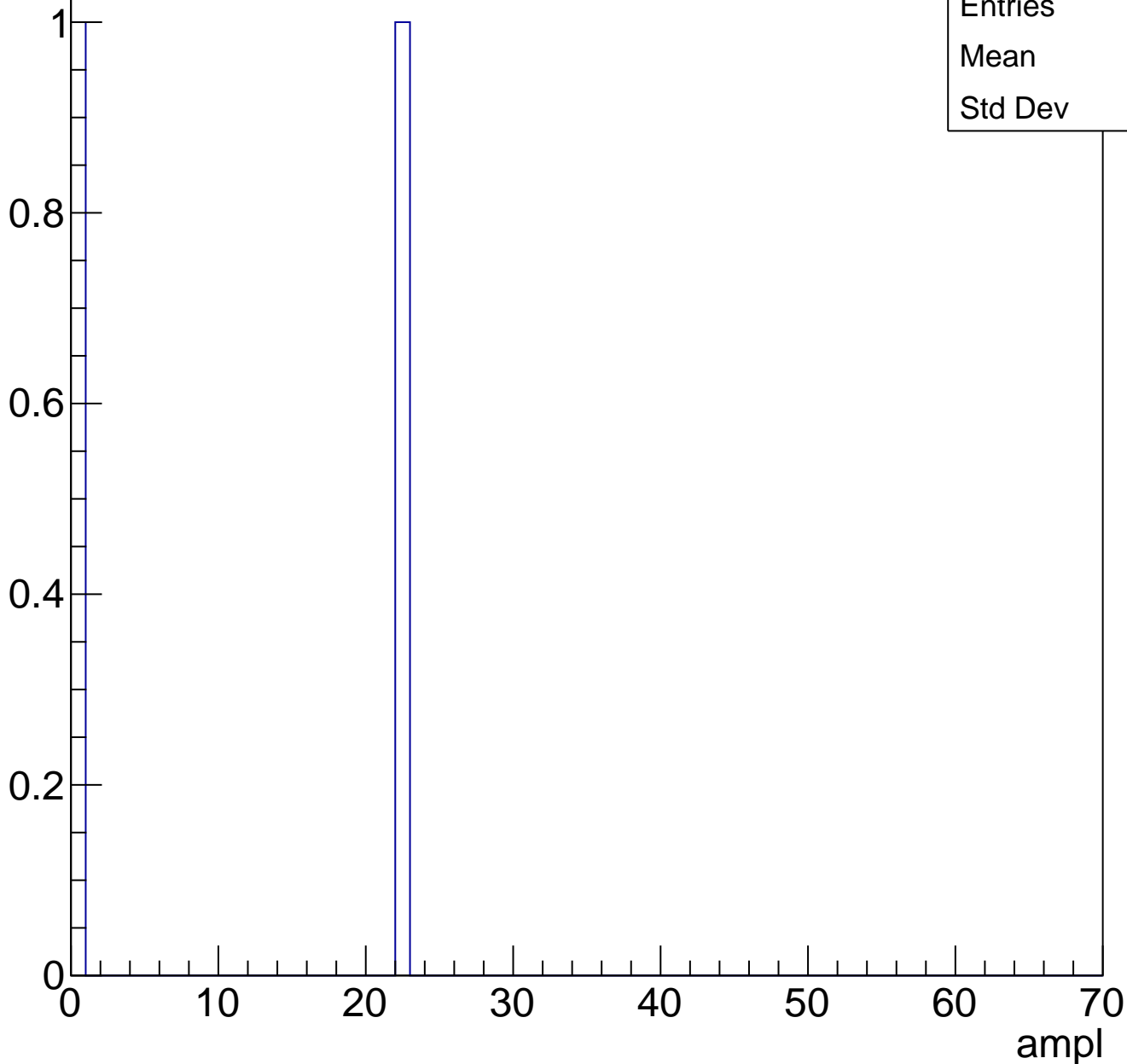
Entry



B1L001S, U24-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U24-ch13, adc0

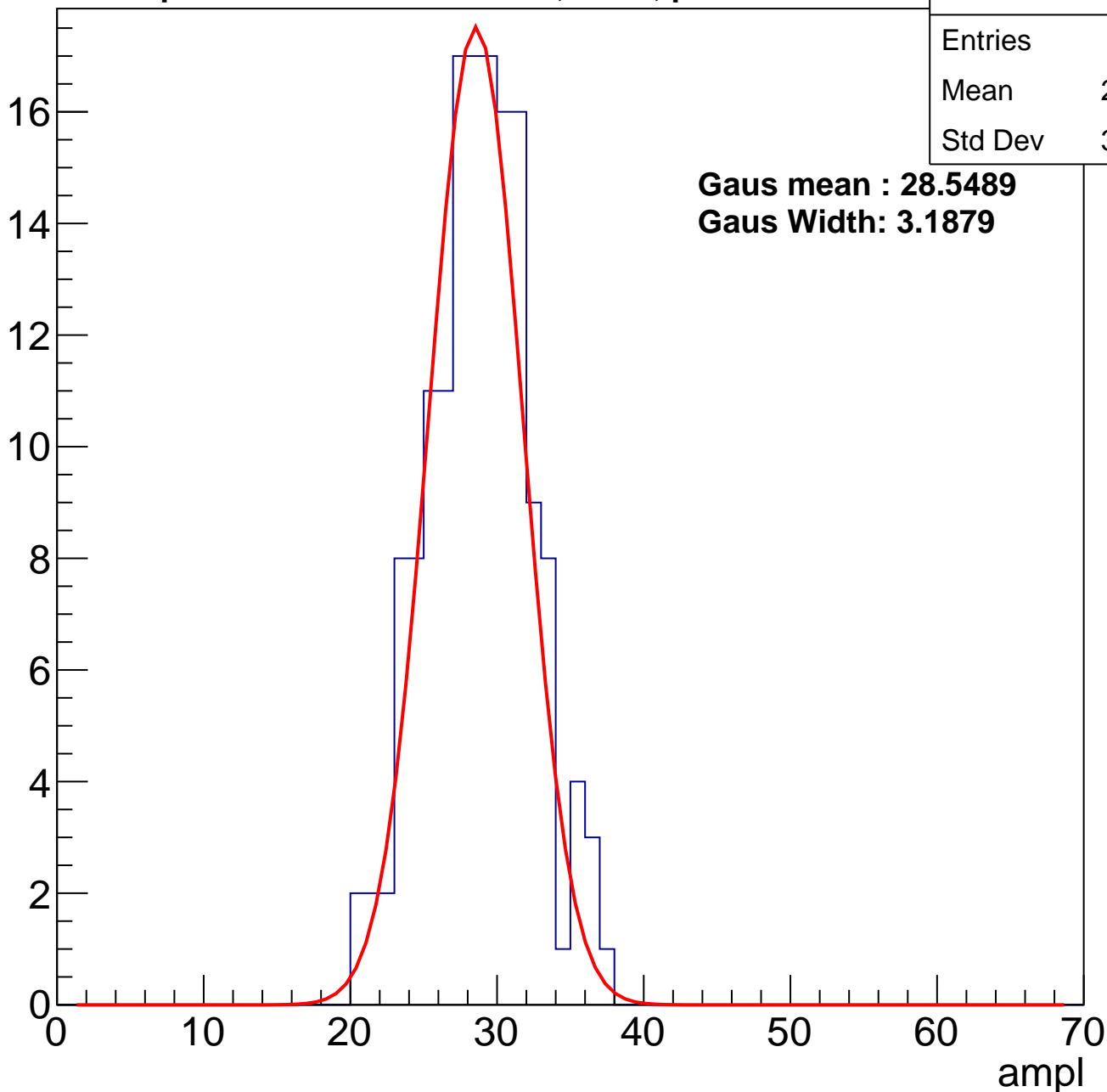
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	28.35
Std Dev	3.469

Gaus mean : 28.5489

Gaus Width: 3.1879

Entry



B1L001S, U24-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	35.14
Std Dev	2.797

Gaus mean : 35.3988

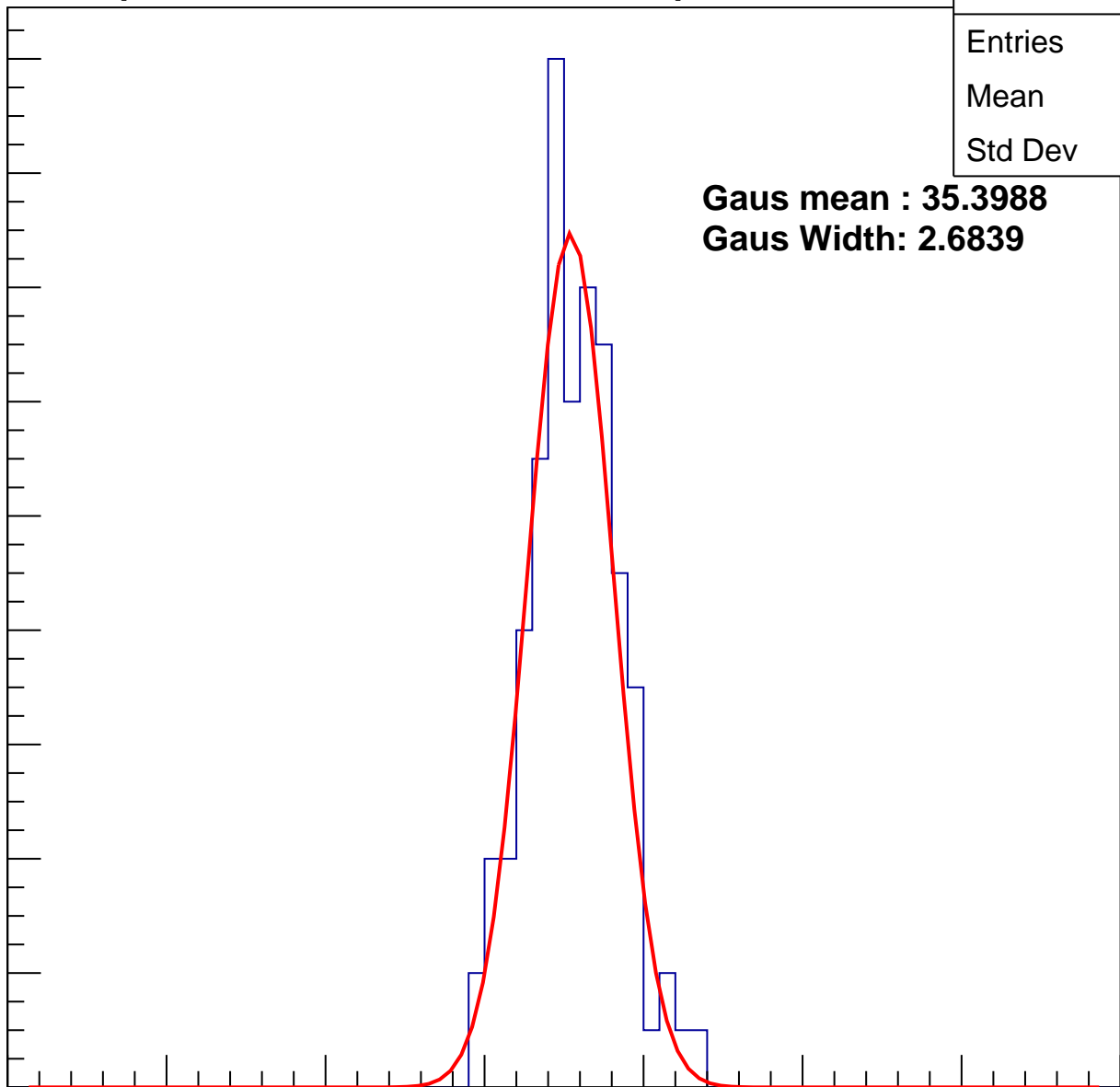
Gaus Width: 2.6839

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch13, adc2

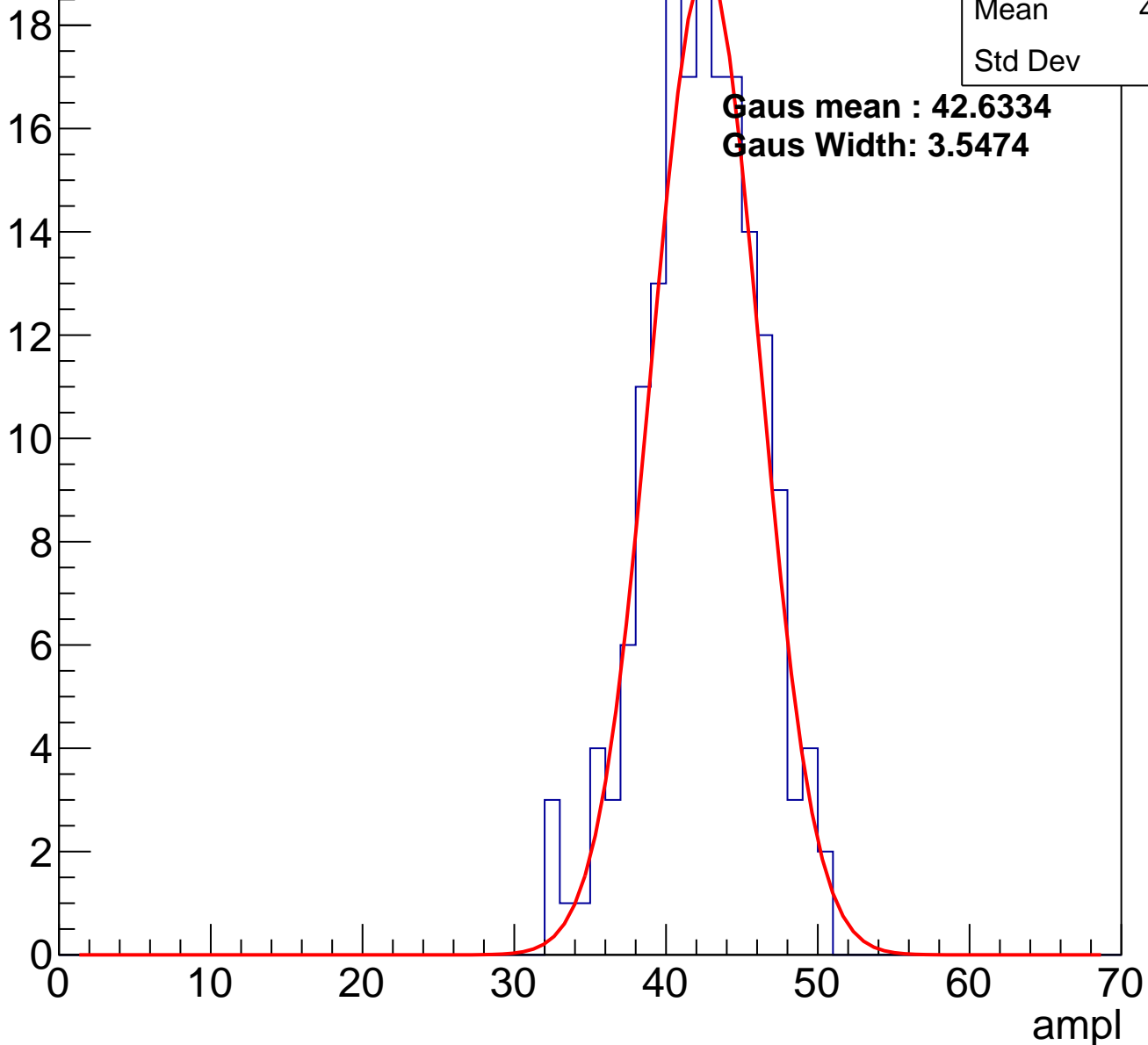
calib_packv5_042523_0143.root, FC#2, port C2

Entries	175
Mean	41.93
Std Dev	3.66

Gaus mean : 42.6334

Gaus Width: 3.5474

Entry



B1L001S, U24-ch13, adc3

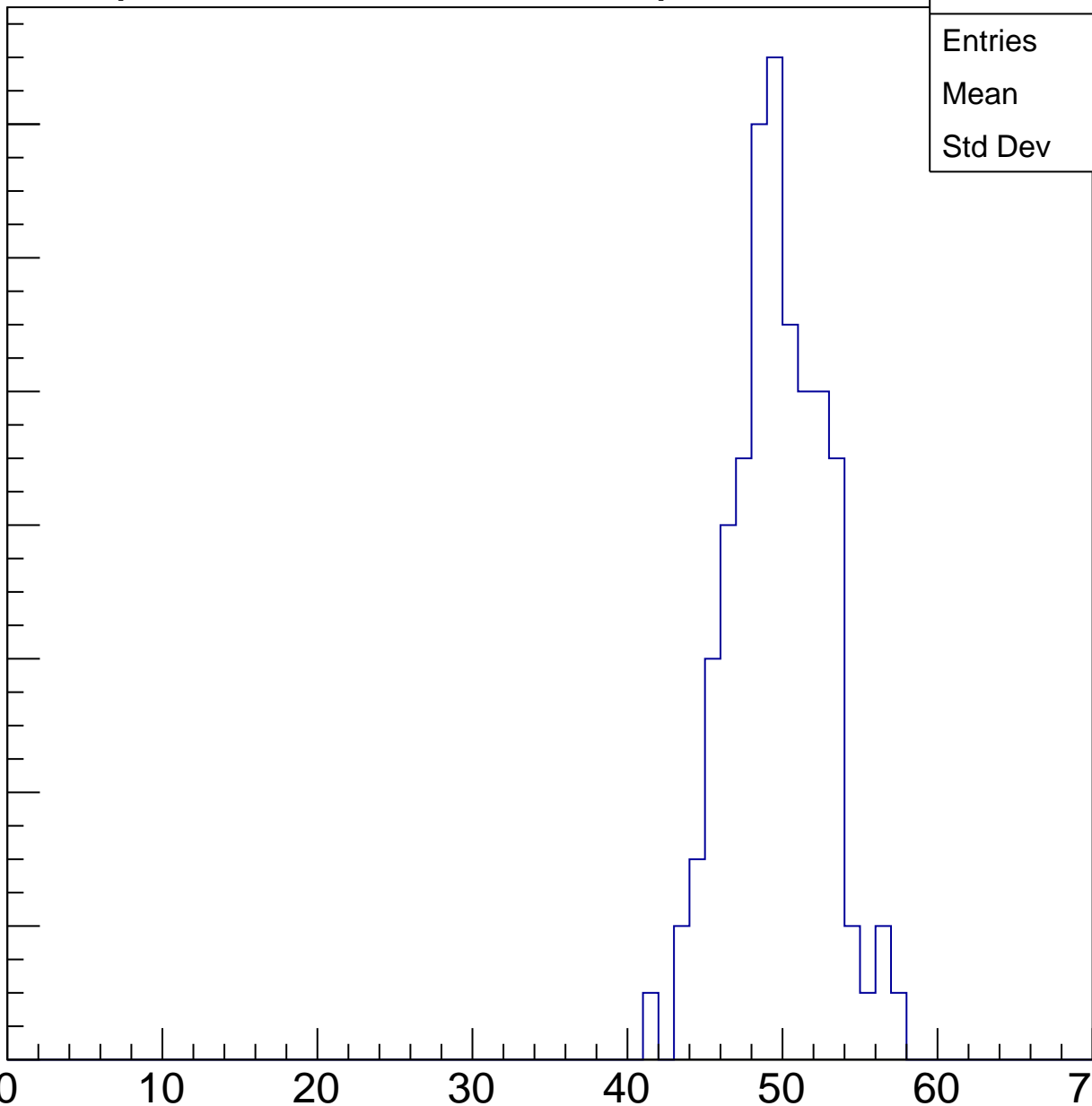
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	49.19
Std Dev	3.035

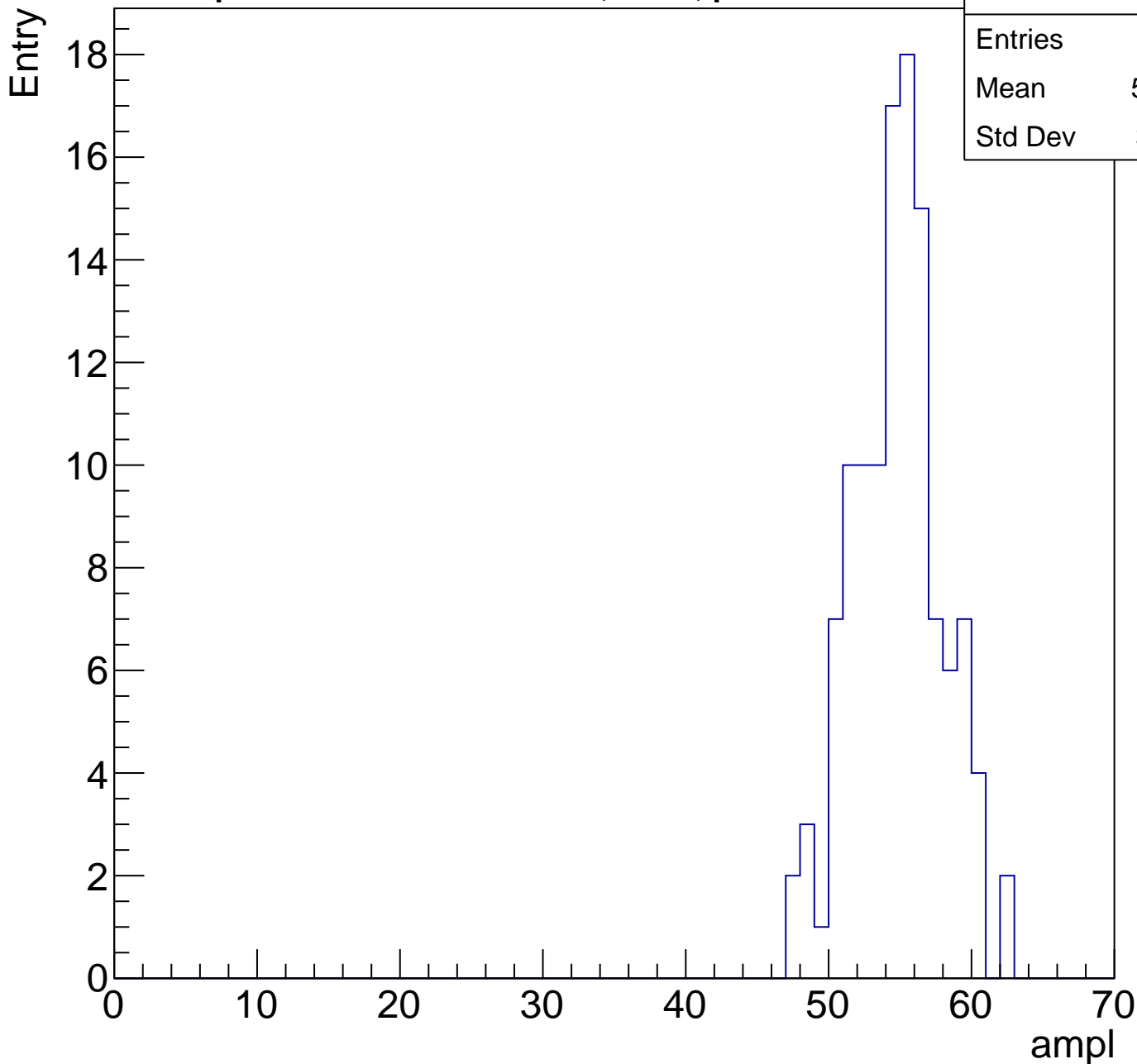
ampl



B1L001S, U24-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.36
Std Dev	3.111



B1L001S, U24-ch13, adc5

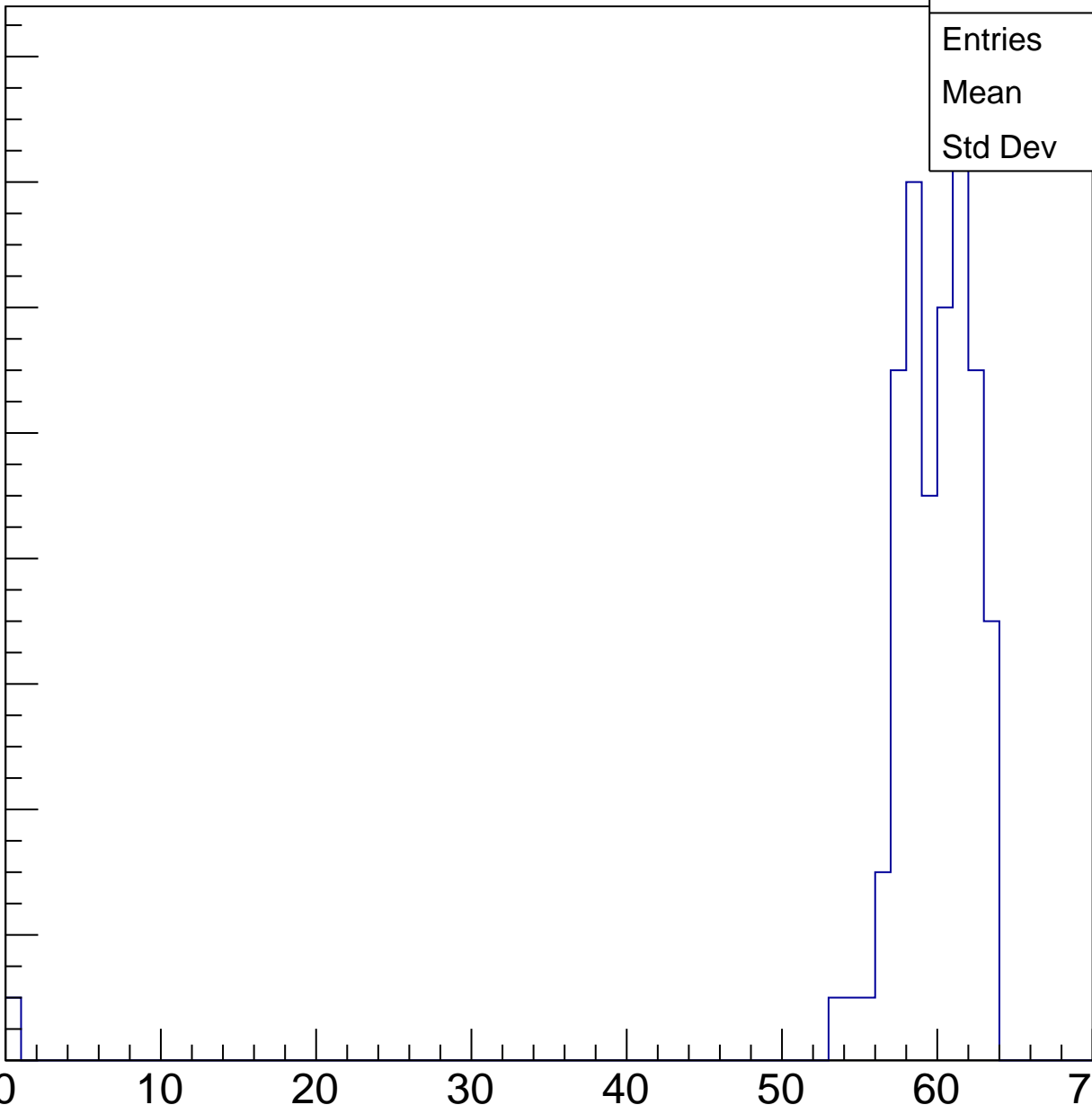
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.84
Std Dev	6.719

ampl



B1L001S, U24-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

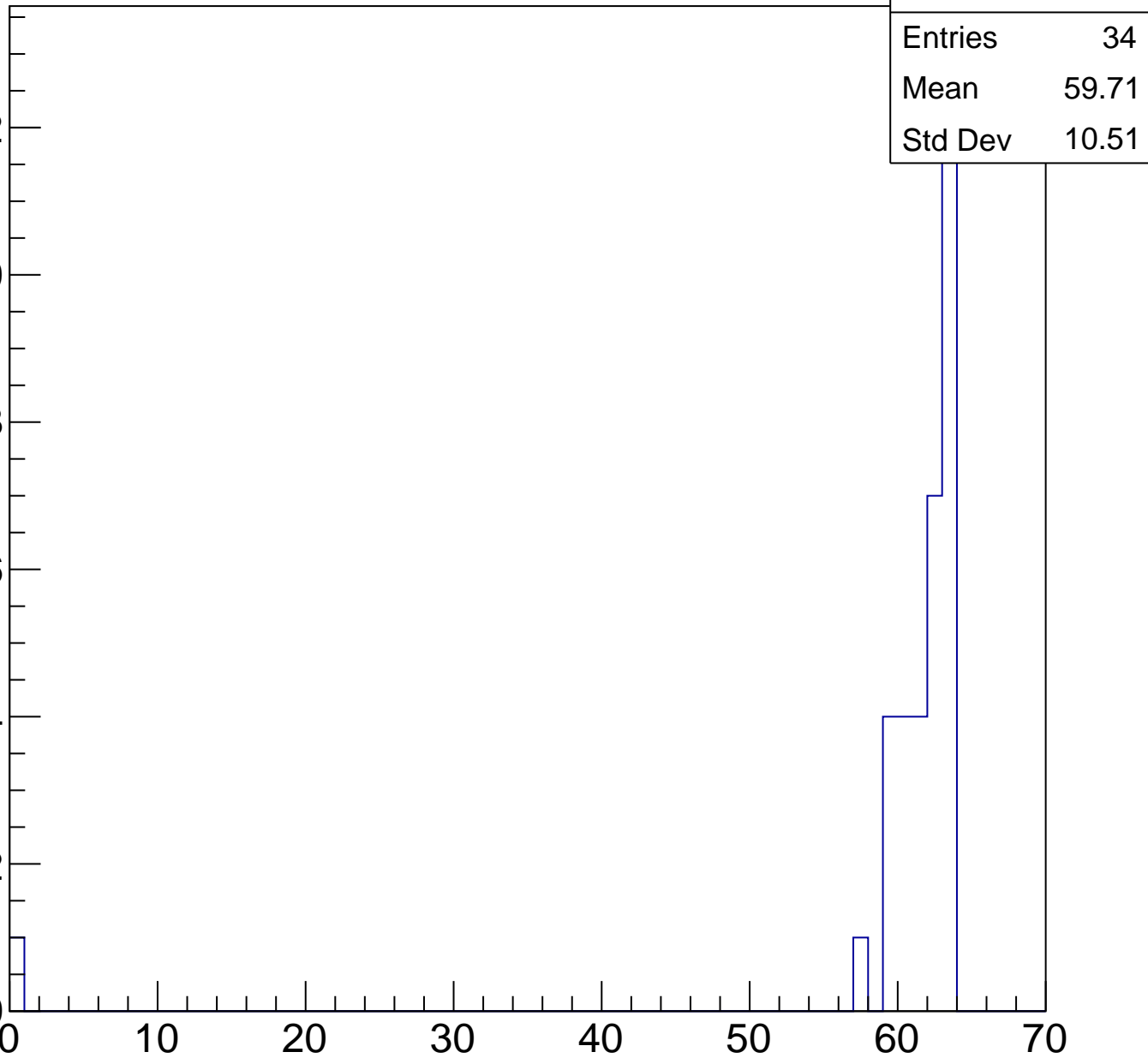
4

2

0

Entries	34
Mean	59.71
Std Dev	10.51

ampl



B1L001S, U24-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch14, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	29.72
Std Dev	3.066

Gaus mean : 30.1551

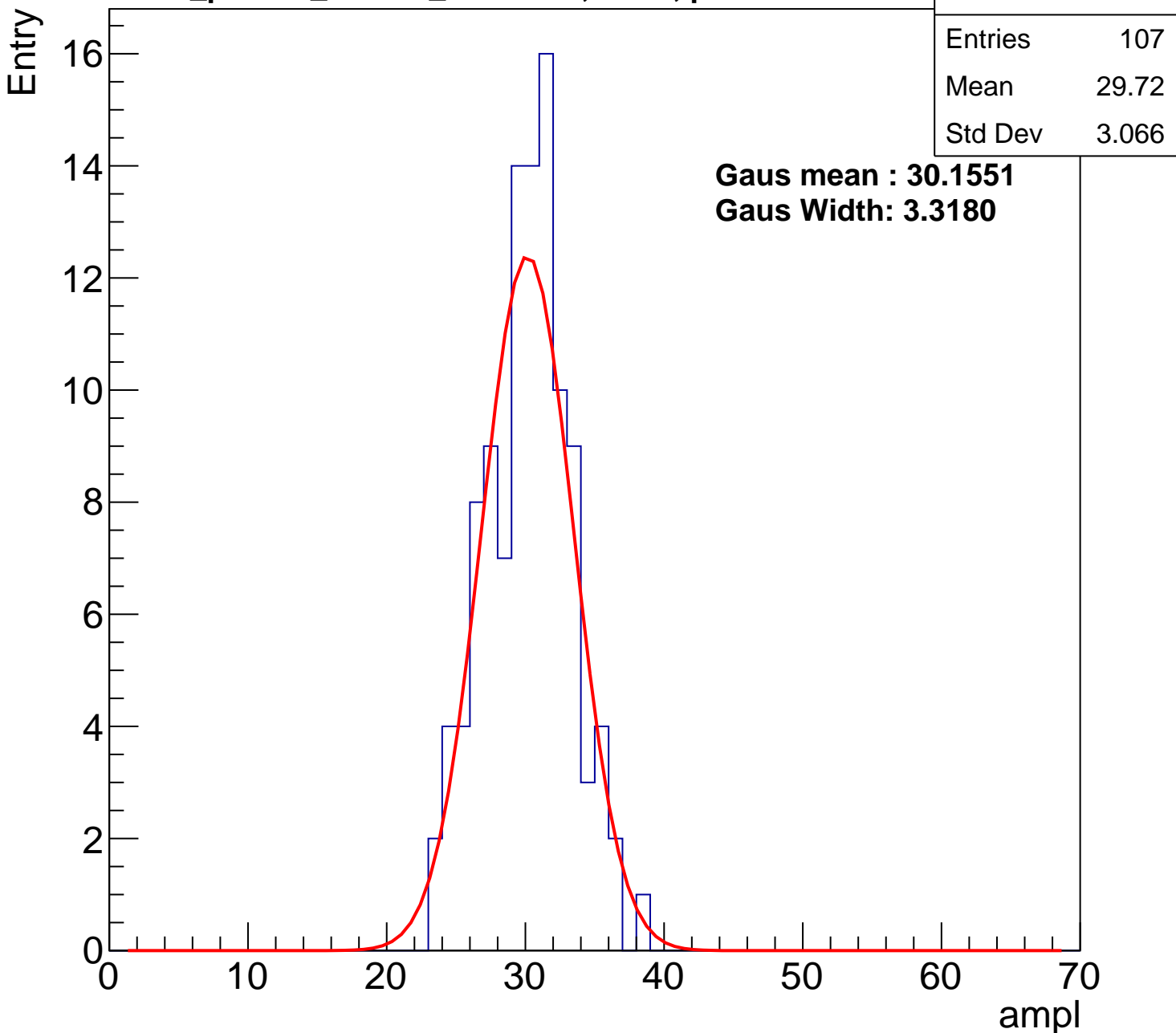
Gaus Width: 3.3180

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



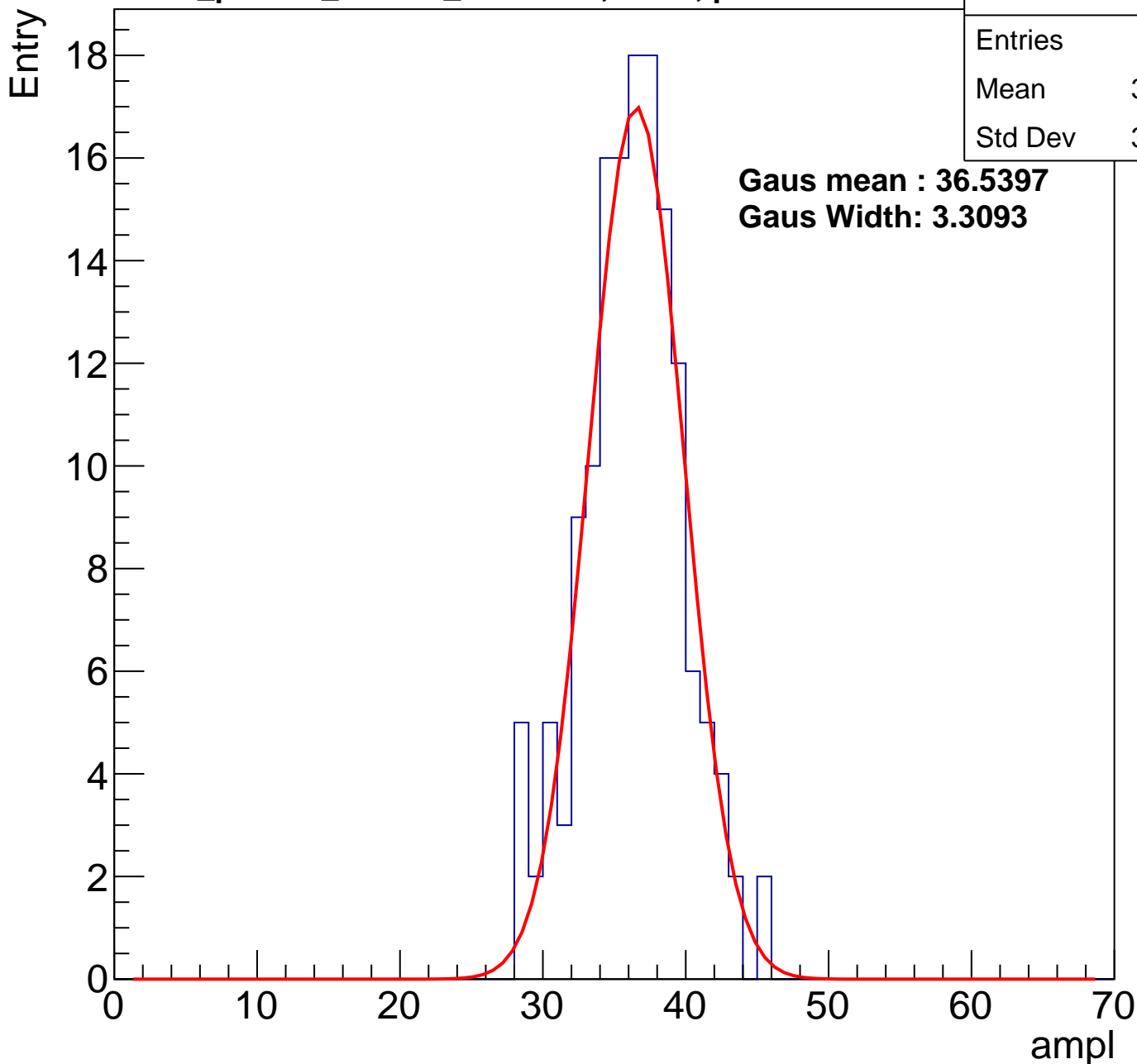
B1L001S, U24-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.84
Std Dev	3.482

Gaus mean : 36.5397

Gaus Width: 3.3093



B1L001S, U24-ch14, adc2

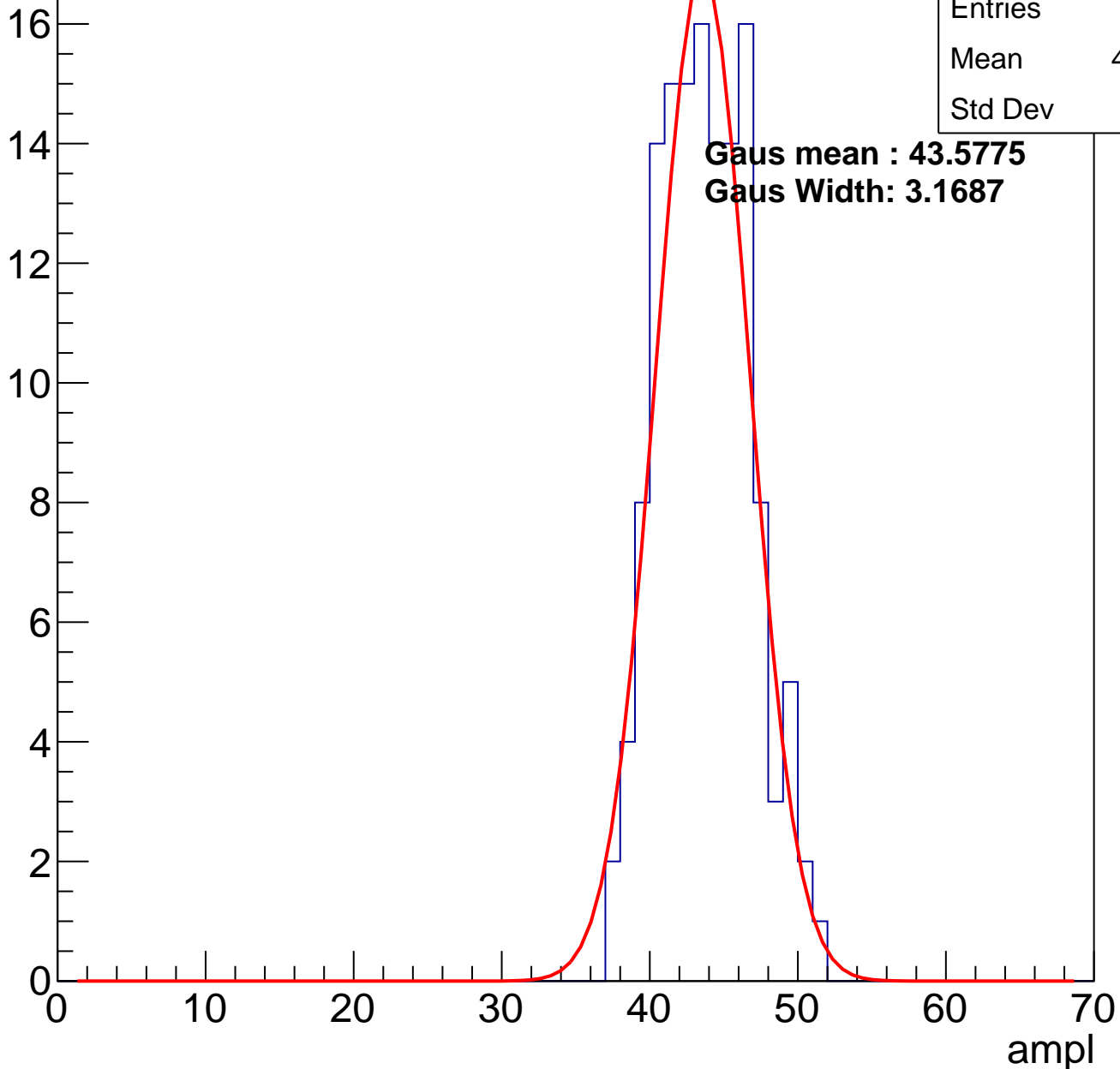
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	43.28
Std Dev	3.02

Entry

Gaus mean : 43.5775

Gaus Width: 3.1687



B1L001S, U24-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

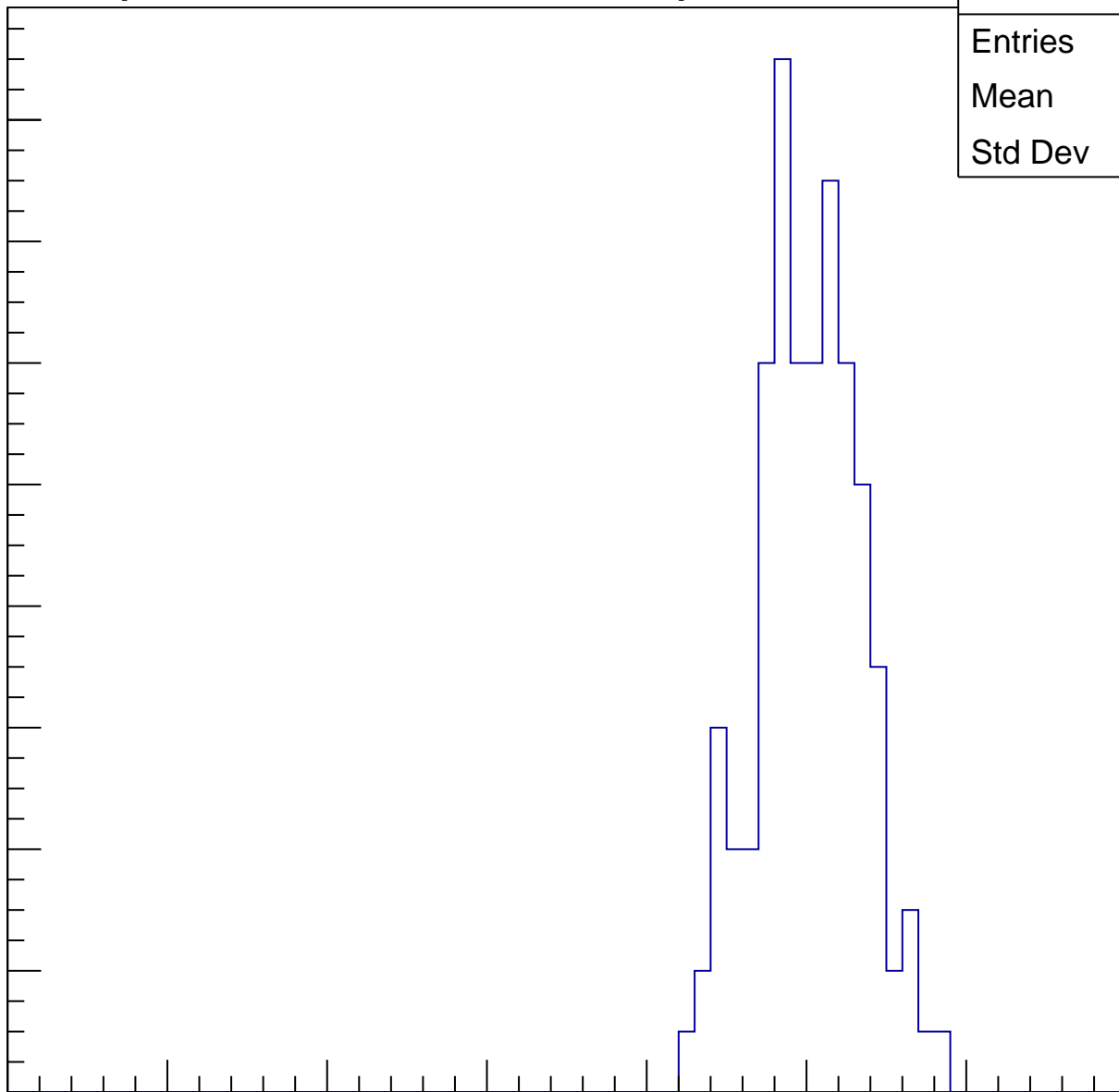
Entries	121
Mean	49.7
Std Dev	3.221

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

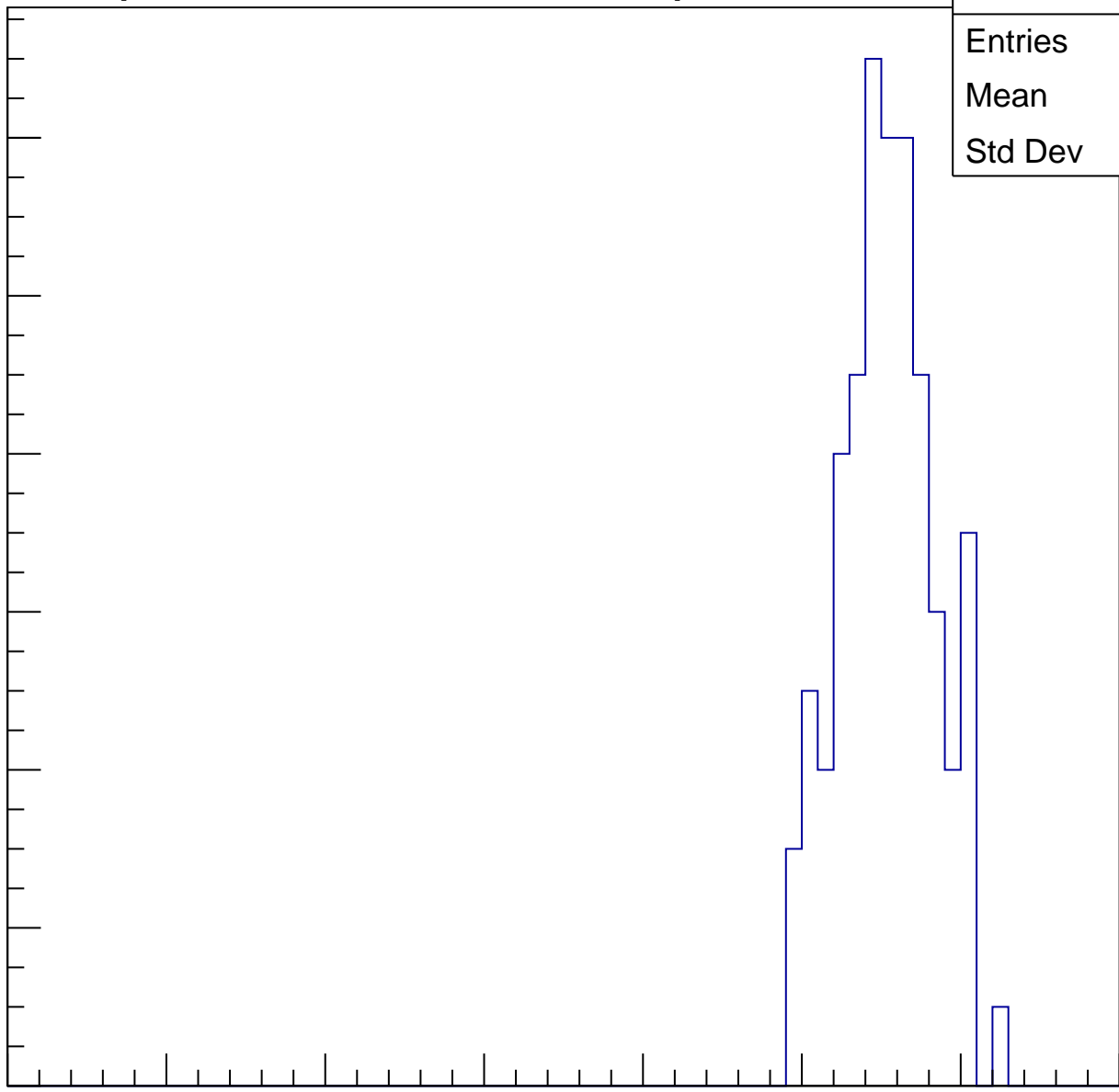
2

0

Entries	93
Mean	54.91
Std Dev	2.946

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch14, adc5

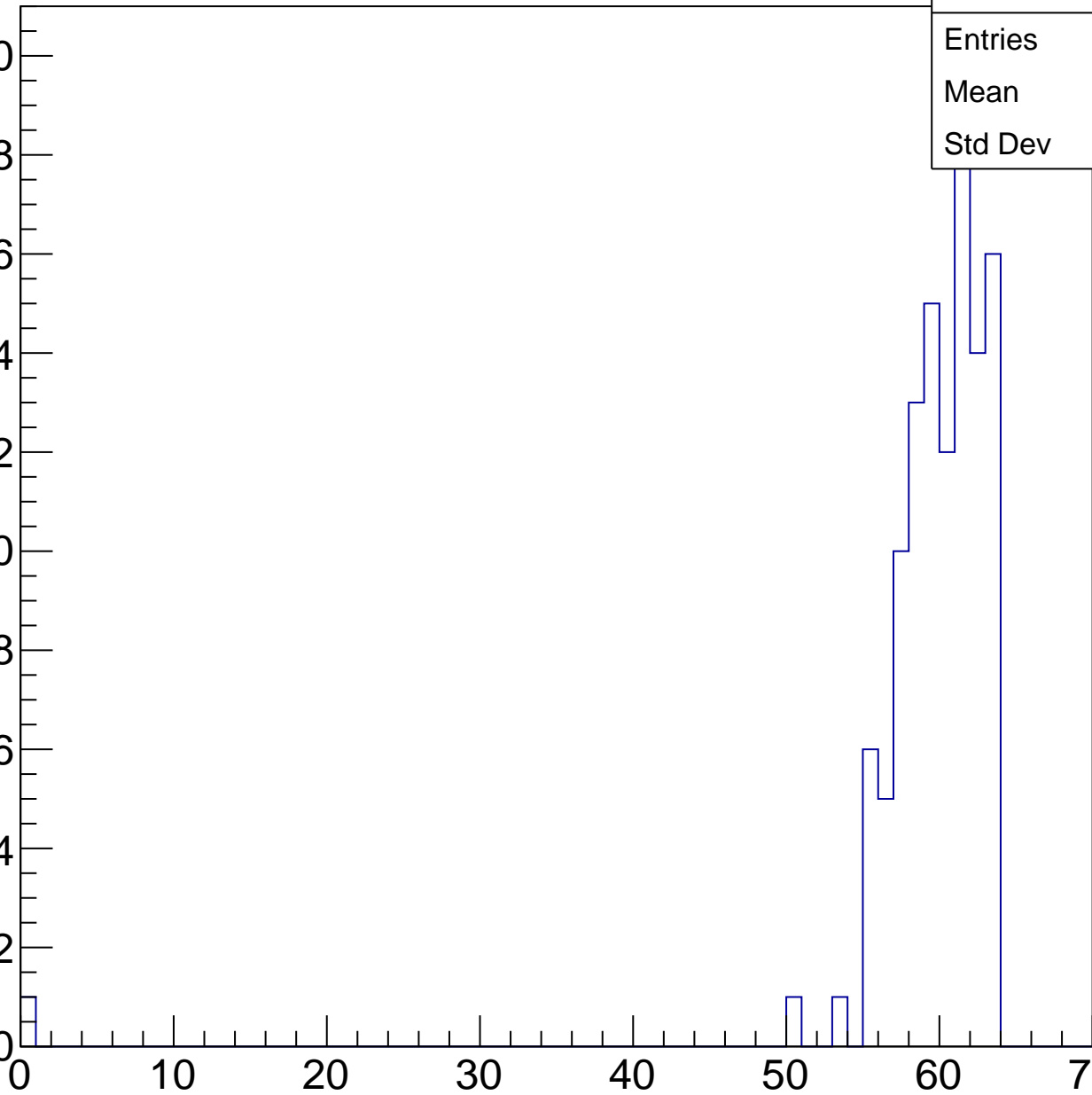
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	114
Mean	59.11
Std Dev	6.116

ampl

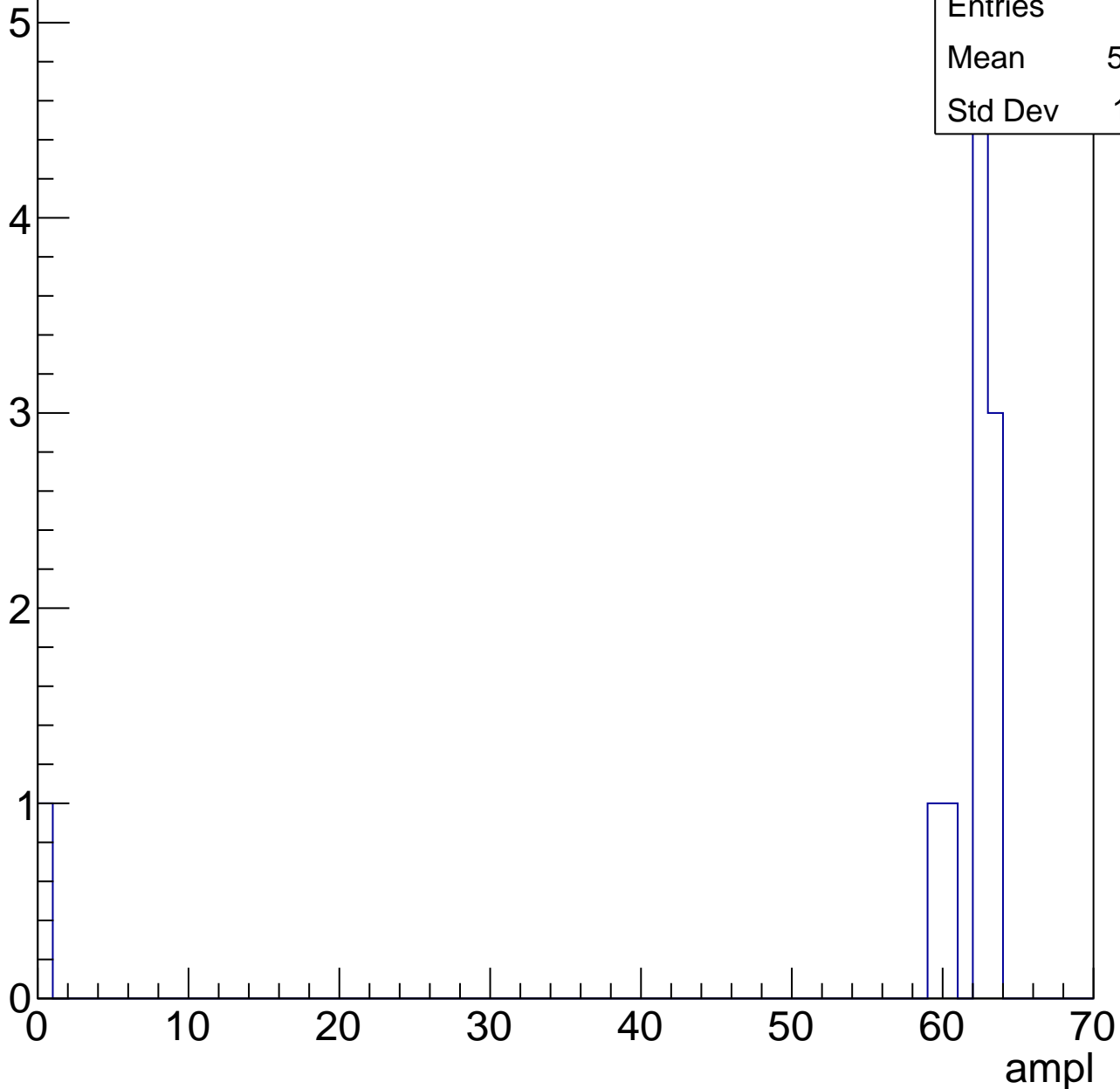


B1L001S, U24-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.18
Std Dev	17.81



B1L001S, U24-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



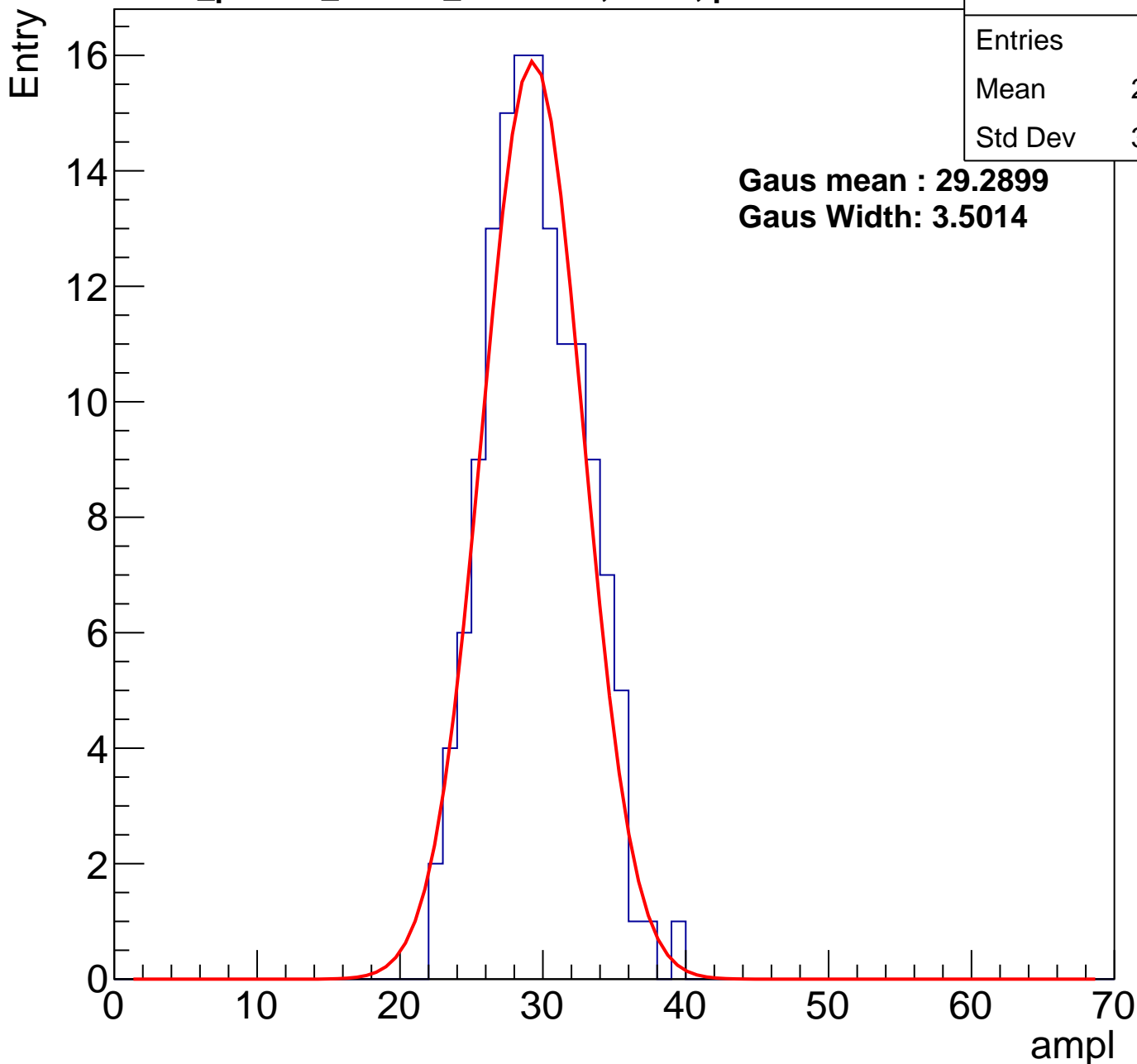
B1L001S, U24-ch15, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.04
Std Dev	3.377

Gaus mean : 29.2899

Gaus Width: 3.5014



B1L001S, U24-ch15, adc1

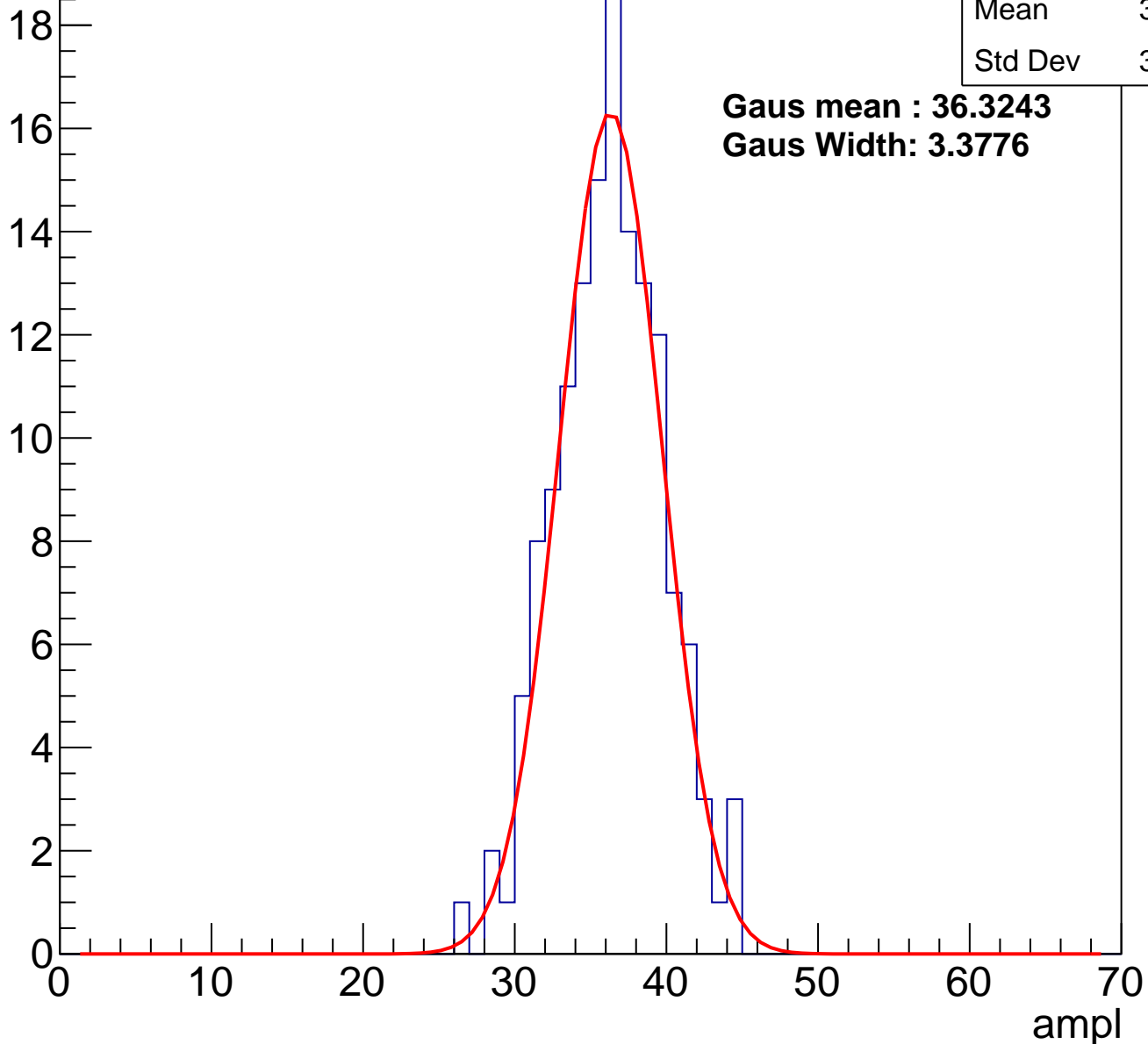
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	35.79
Std Dev	3.494

Gaus mean : 36.3243

Gaus Width: 3.3776

Entry



B1L001S, U24-ch15, adc2

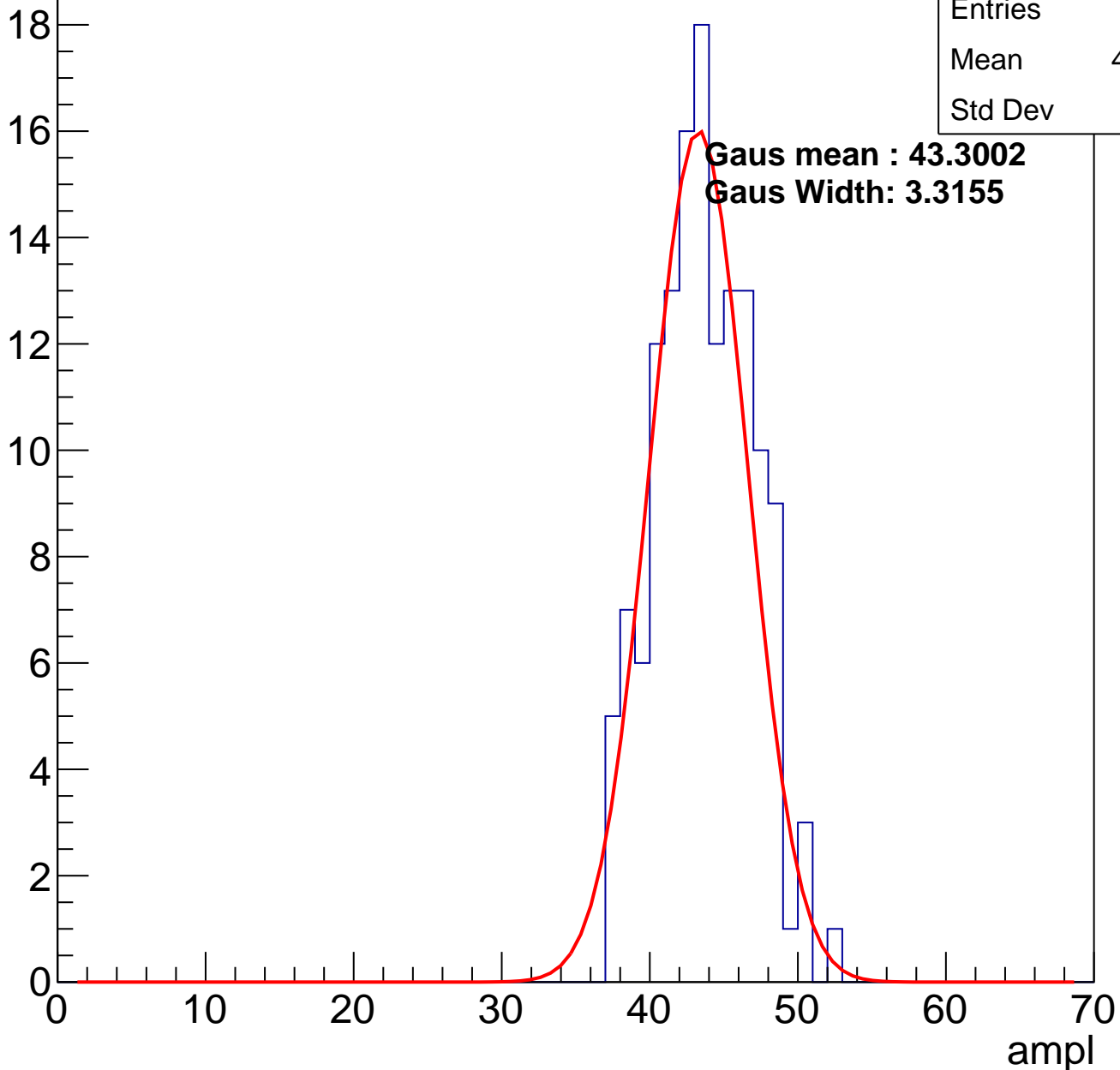
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.22
Std Dev	3.23

Gaus mean : 43.3002

Gaus Width: 3.3155

Entry

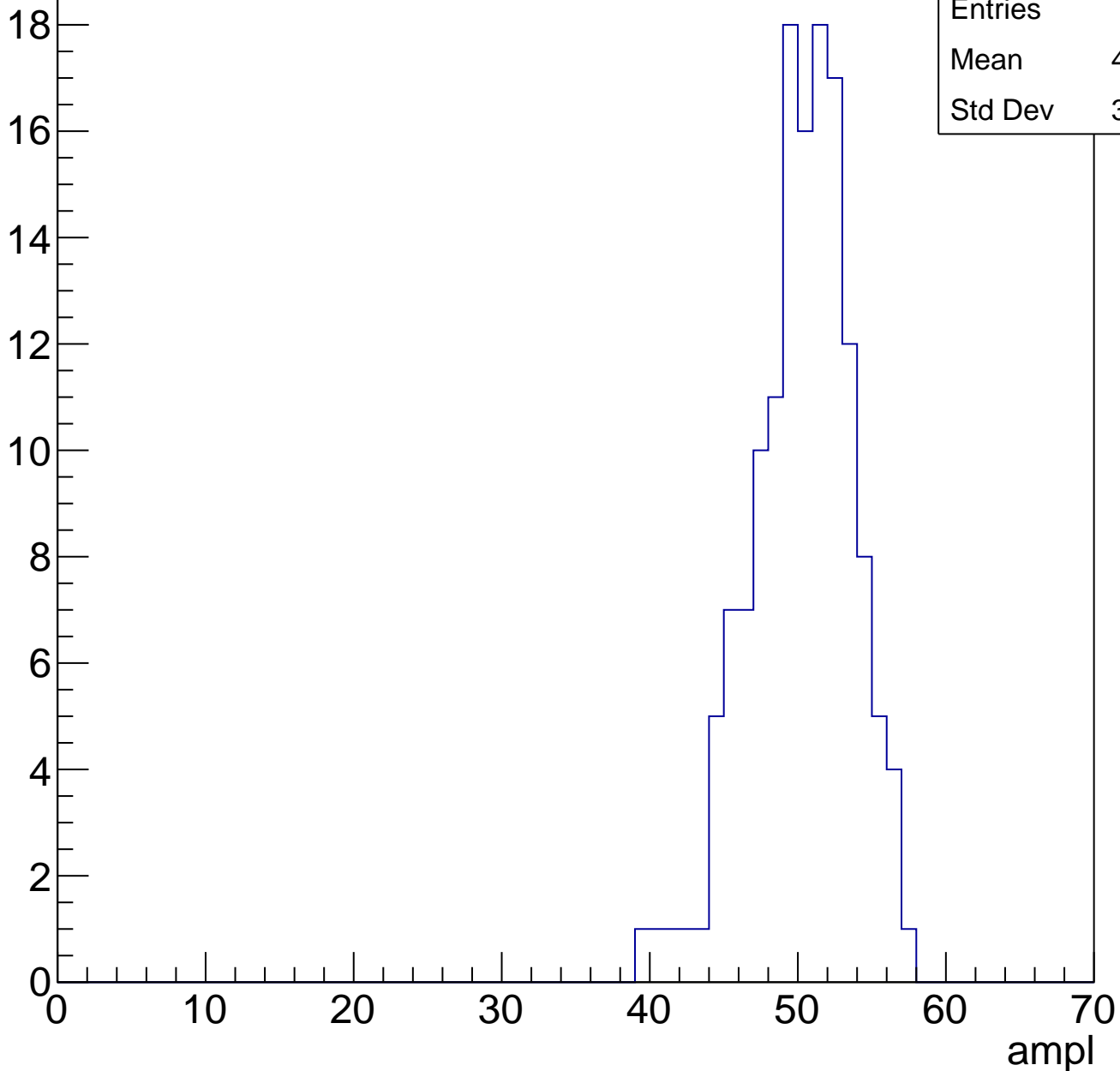


B1L001S, U24-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	49.78
Std Dev	3.402

Entry



B1L001S, U24-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

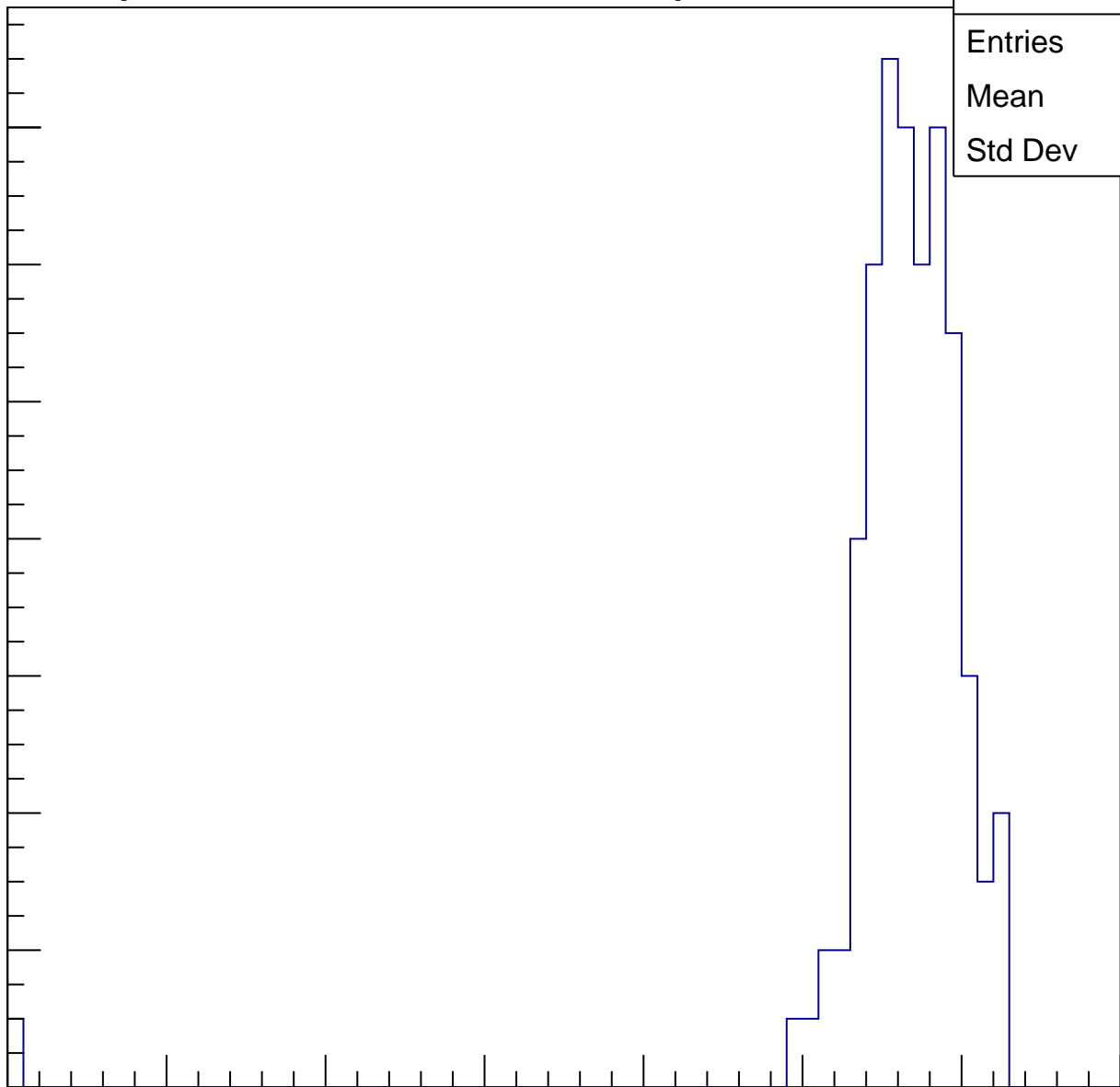
Entries	106
Mean	55.87
Std Dev	6.08

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch15, adc5

calib_packv5_042523_0143.root, FC#2, port C2

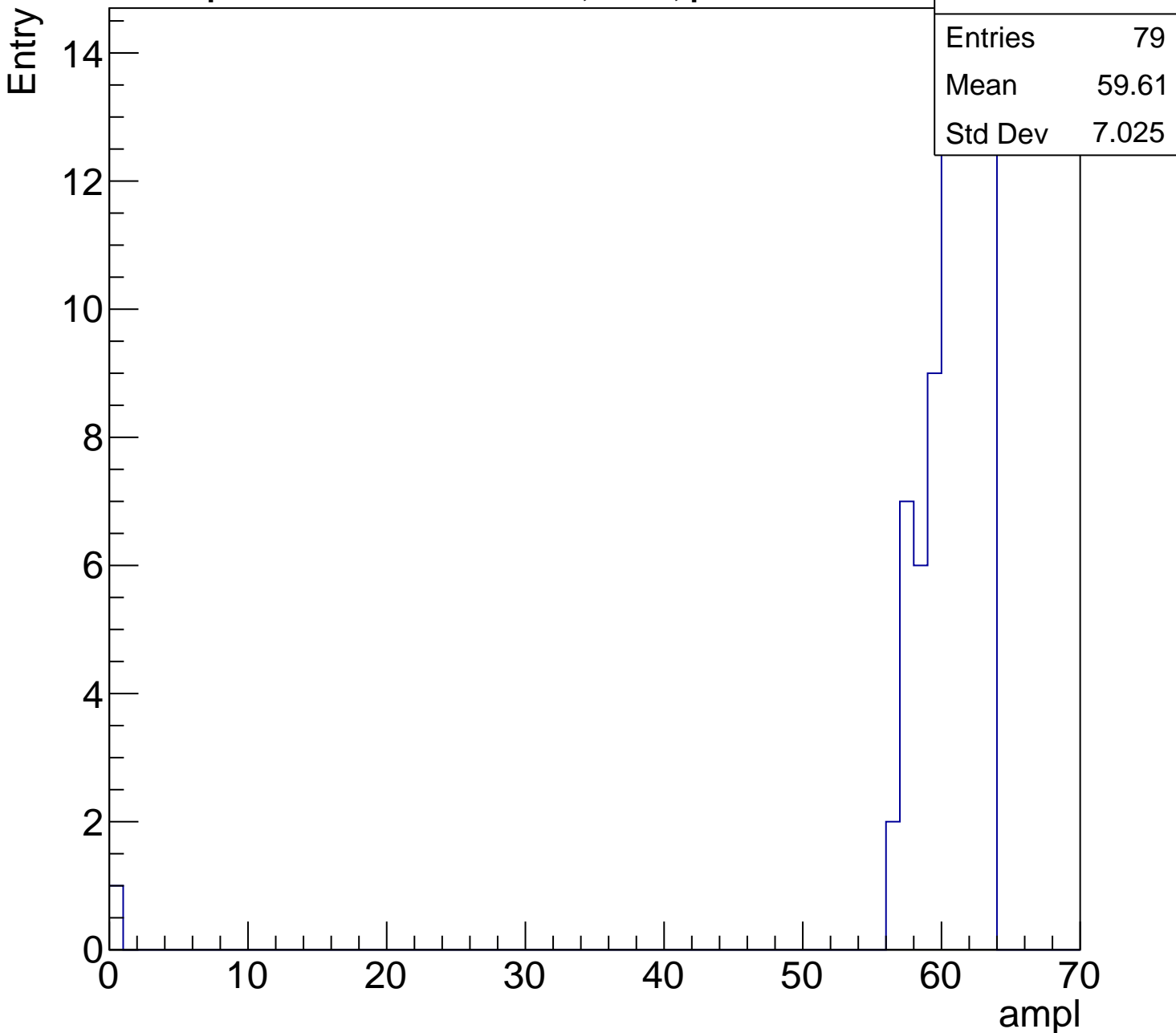
Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	59.61
Std Dev	7.025

ampl

0 10 20 30 40 50 60 70

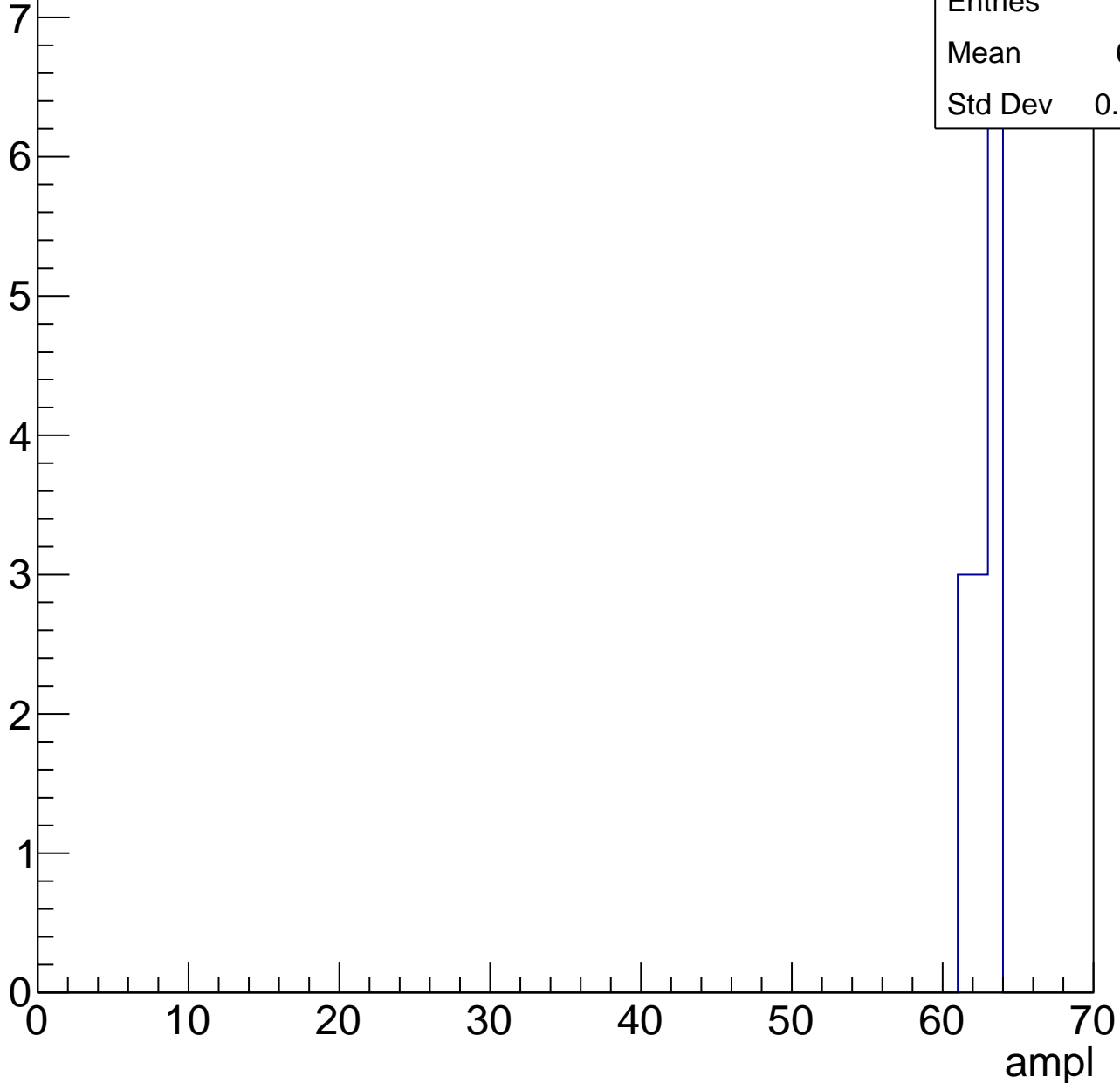


B1L001S, U24-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62.31
Std Dev	0.8213



B1L001S, U24-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L001S, U24-ch16, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	28.99
Std Dev	3.141

Gaus mean : 29.2130

Gaus Width: 3.0527

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

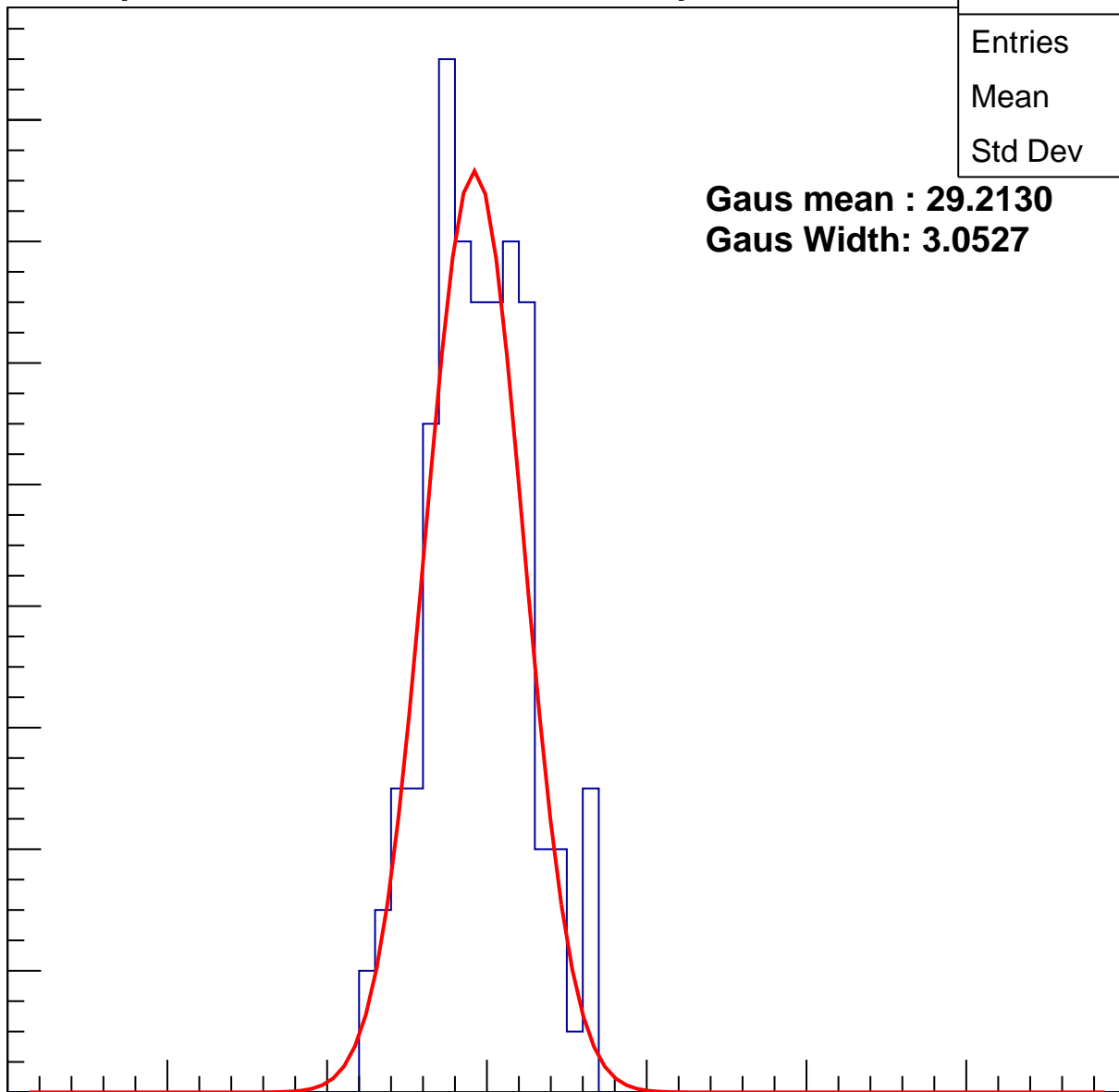
40

50

60

70

ampl



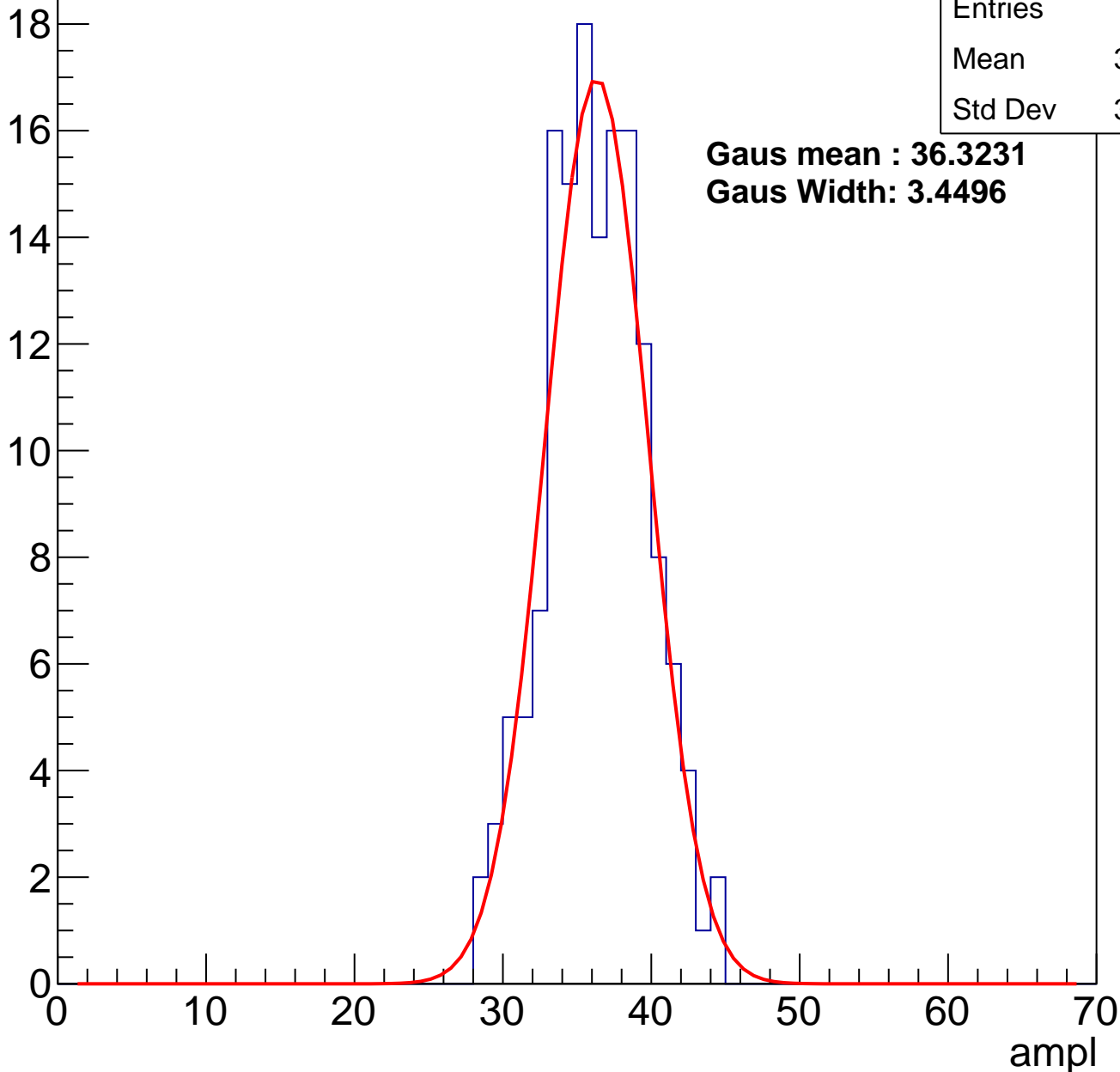
B1L001S, U24-ch16, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	35.85
Std Dev	3.364

Gaus mean : 36.3231
Gaus Width: 3.4496

Entry



B1L001S, U24-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

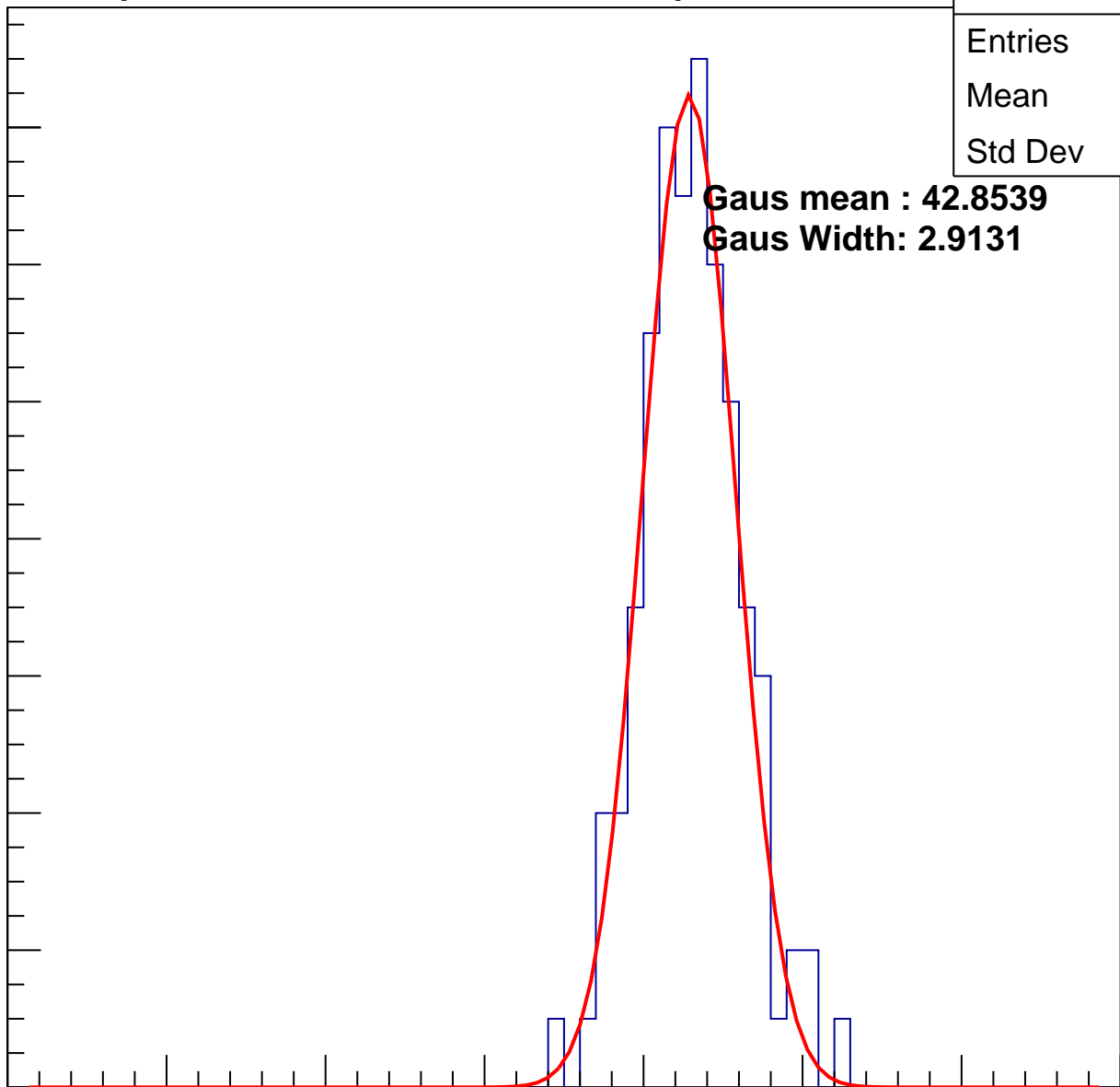
Entries	111
Mean	42.59
Std Dev	3.18

Gaus mean : 42.8539

Gaus Width: 2.9131

0 10 20 30 40 50 60 70

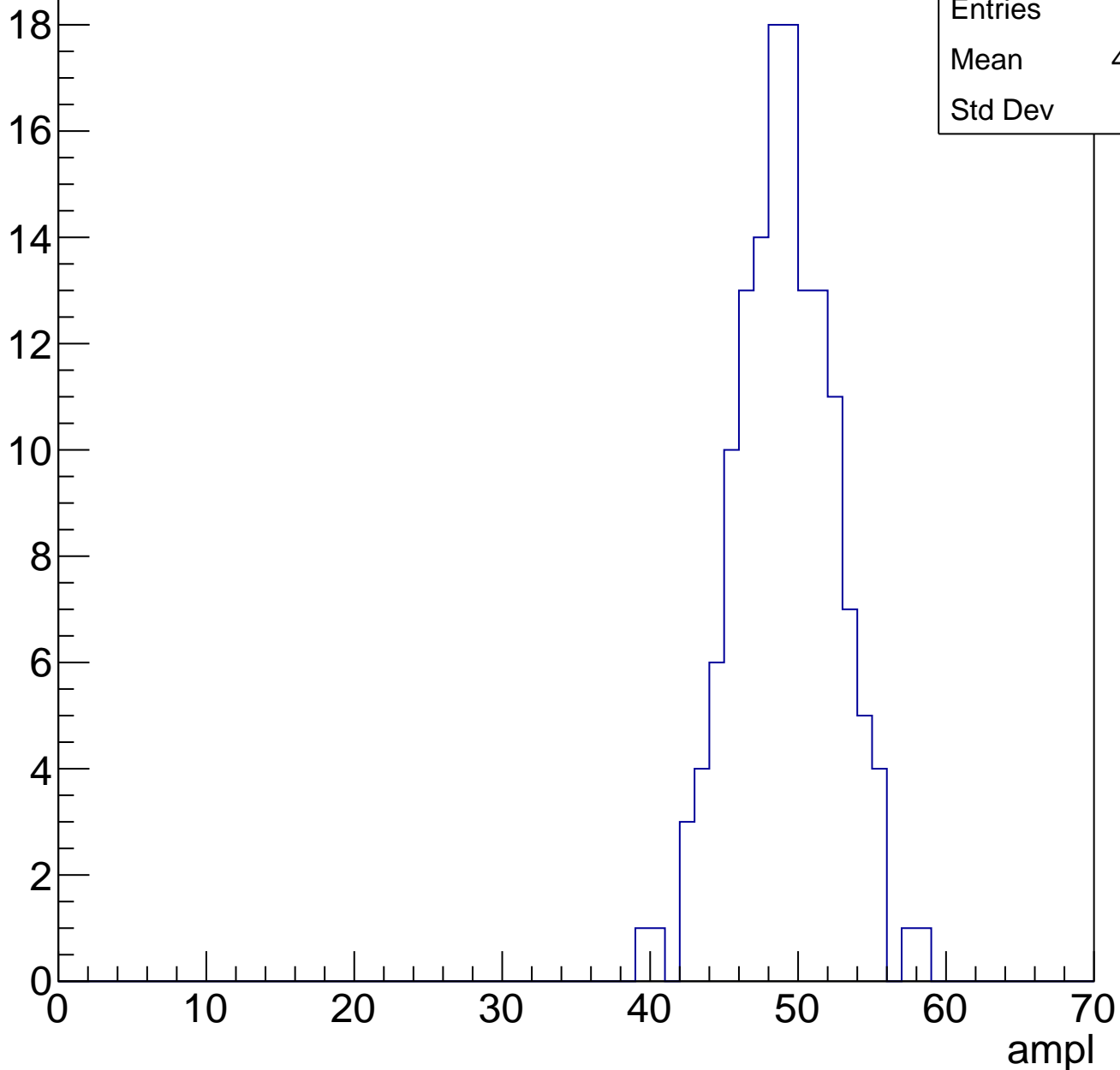
ampl



B1L001S, U24-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch16, adc4

calib_packv5_042523_0143.root, FC#2, port C2

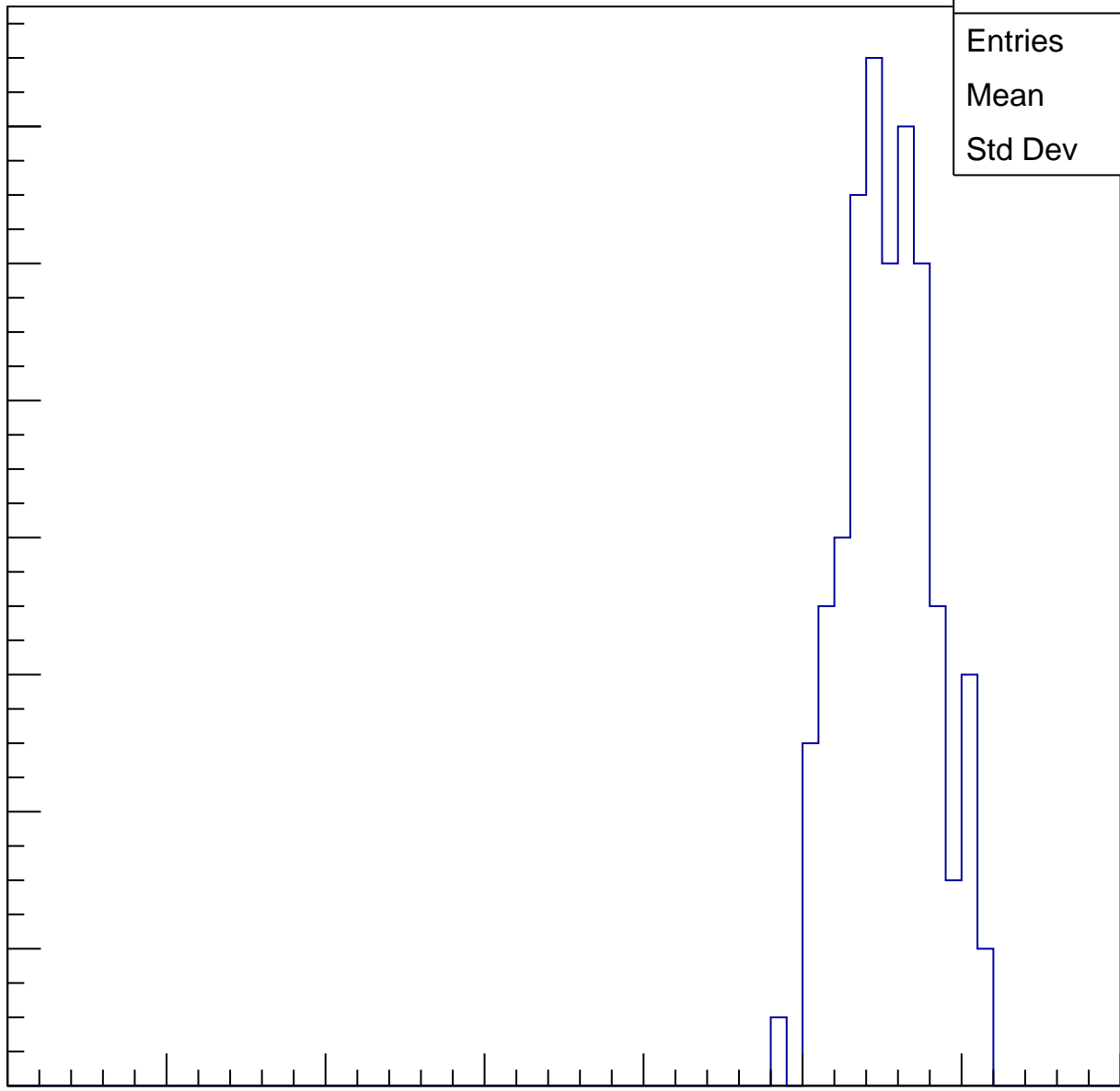
Entries	105
Mean	54.89
Std Dev	2.792

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch16, adc5

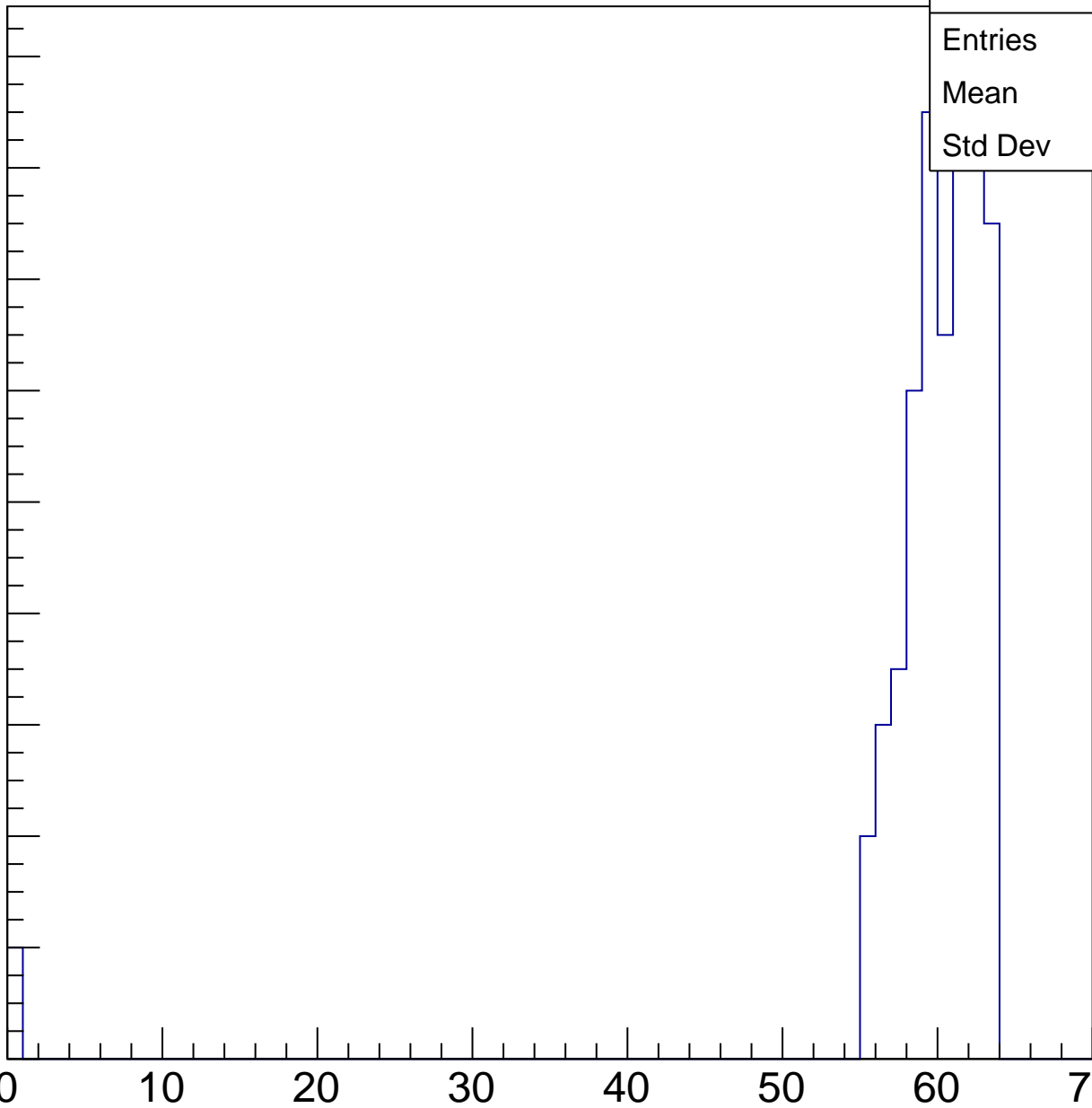
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.83
Std Dev	8.31

ampl

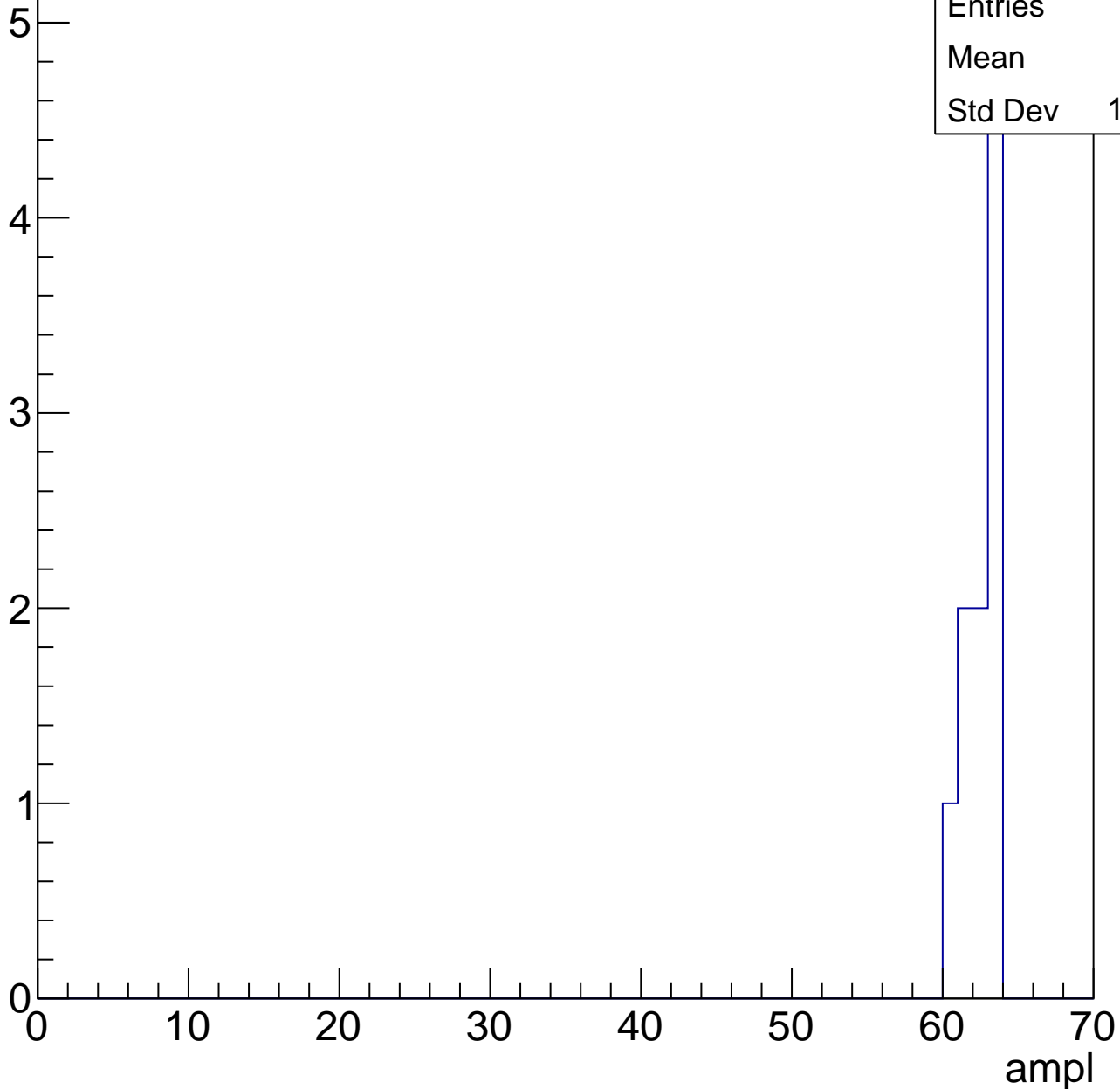


B1L001S, U24-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

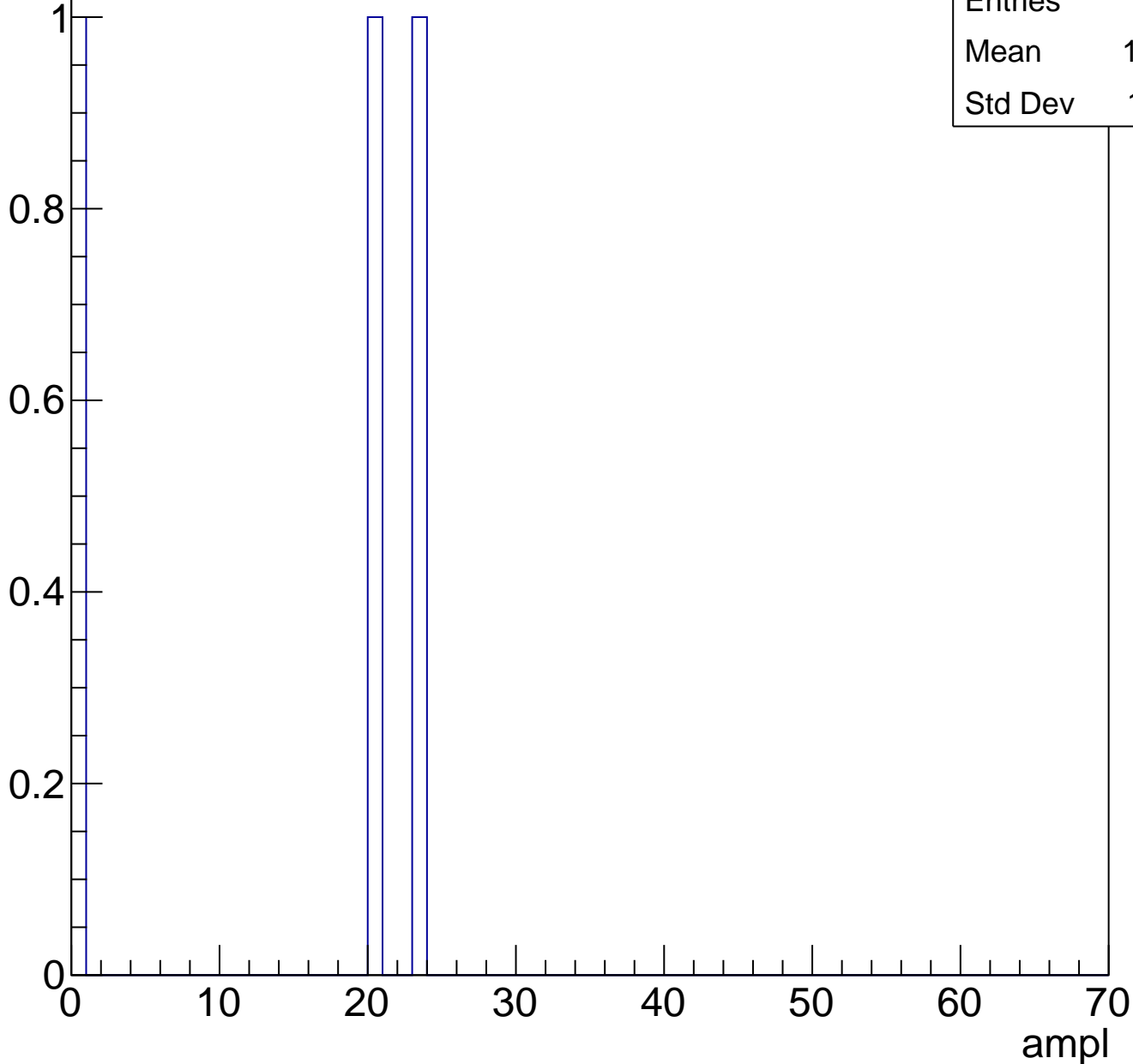
Entries	10
Mean	62.1
Std Dev	1.044



B1L001S, U24-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch17, adc0

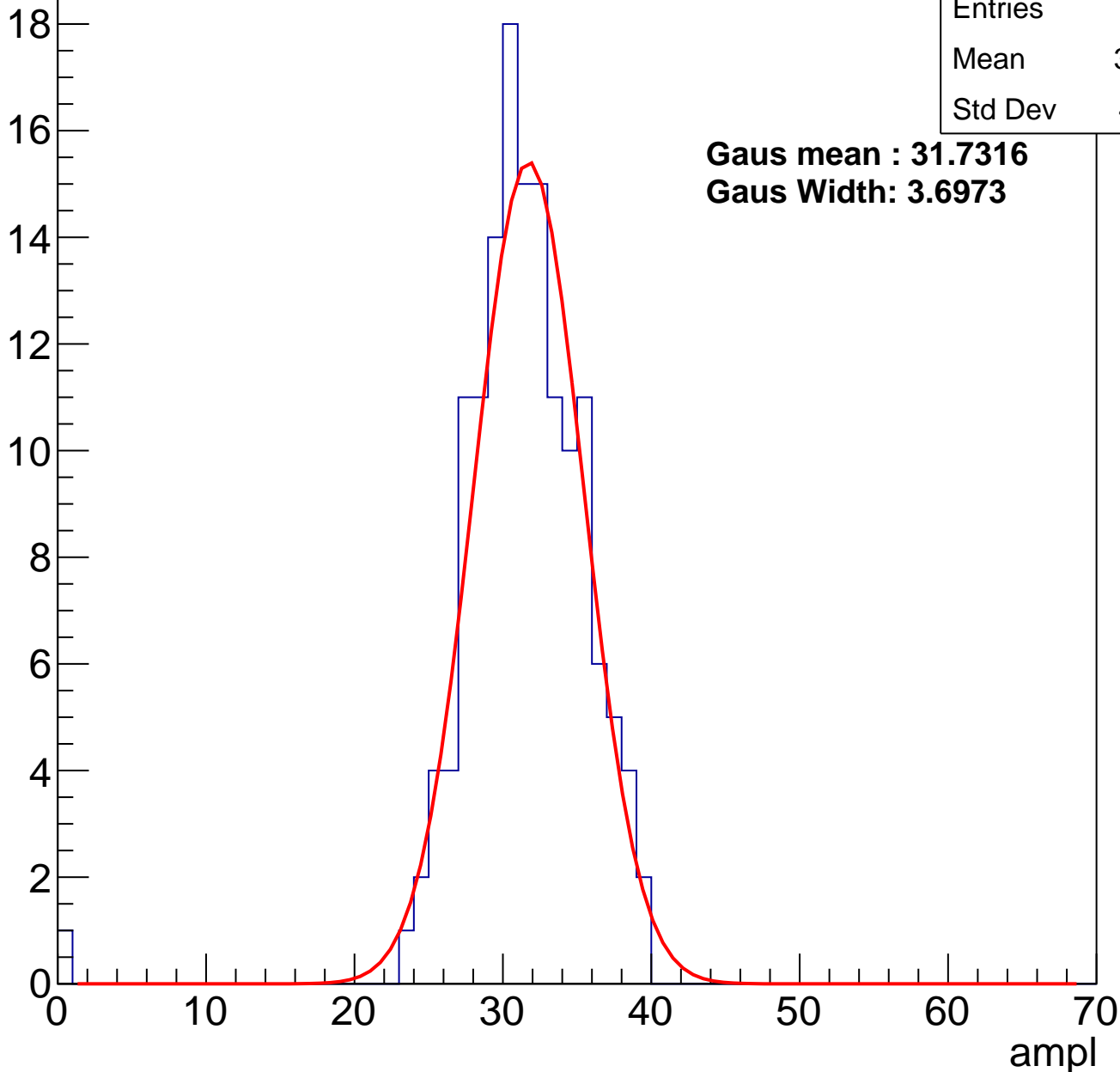
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	30.97
Std Dev	4.311

Gaus mean : 31.7316

Gaus Width: 3.6973

Entry

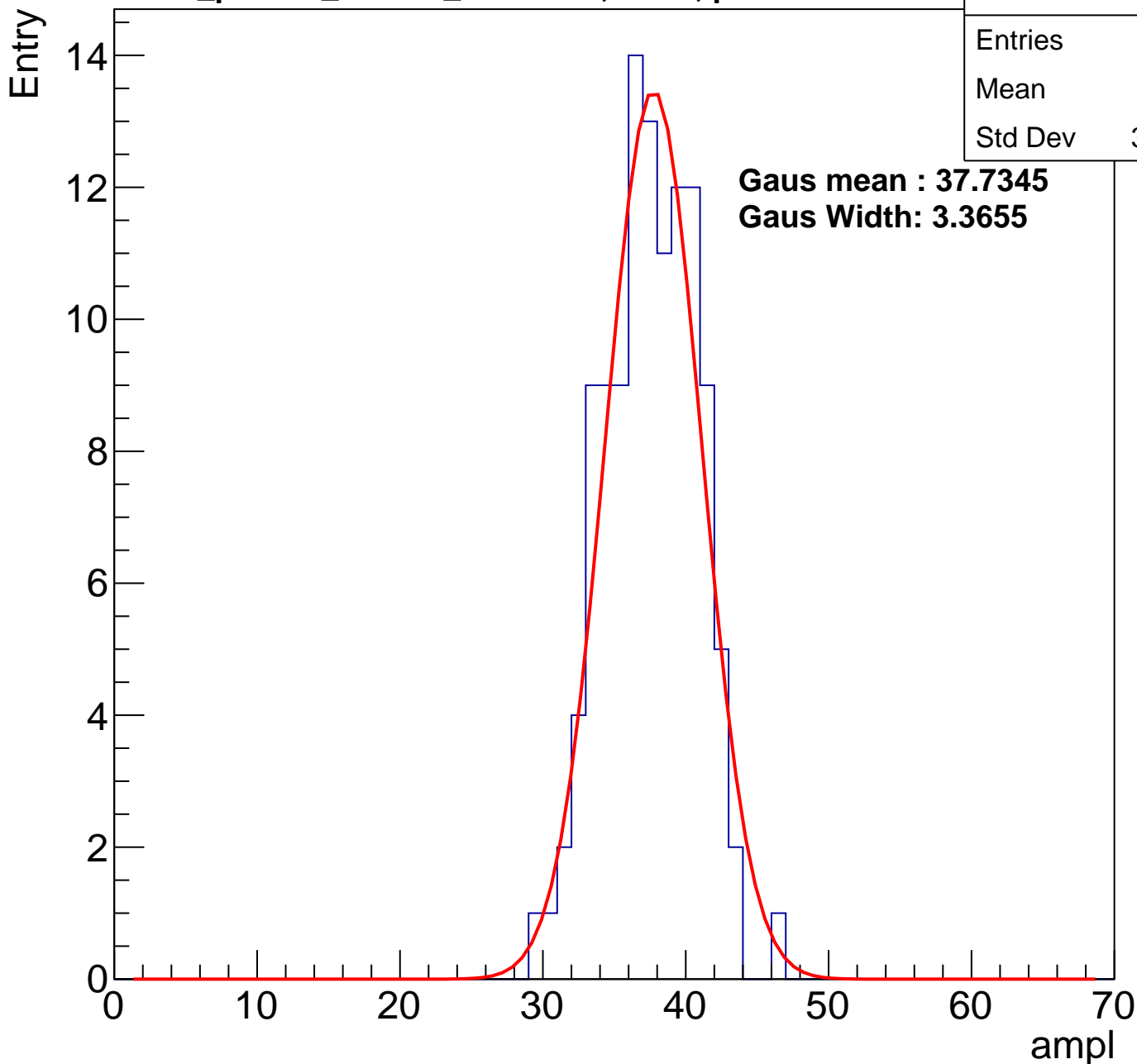


B1L001S, U24-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	37.1
Std Dev	3.173

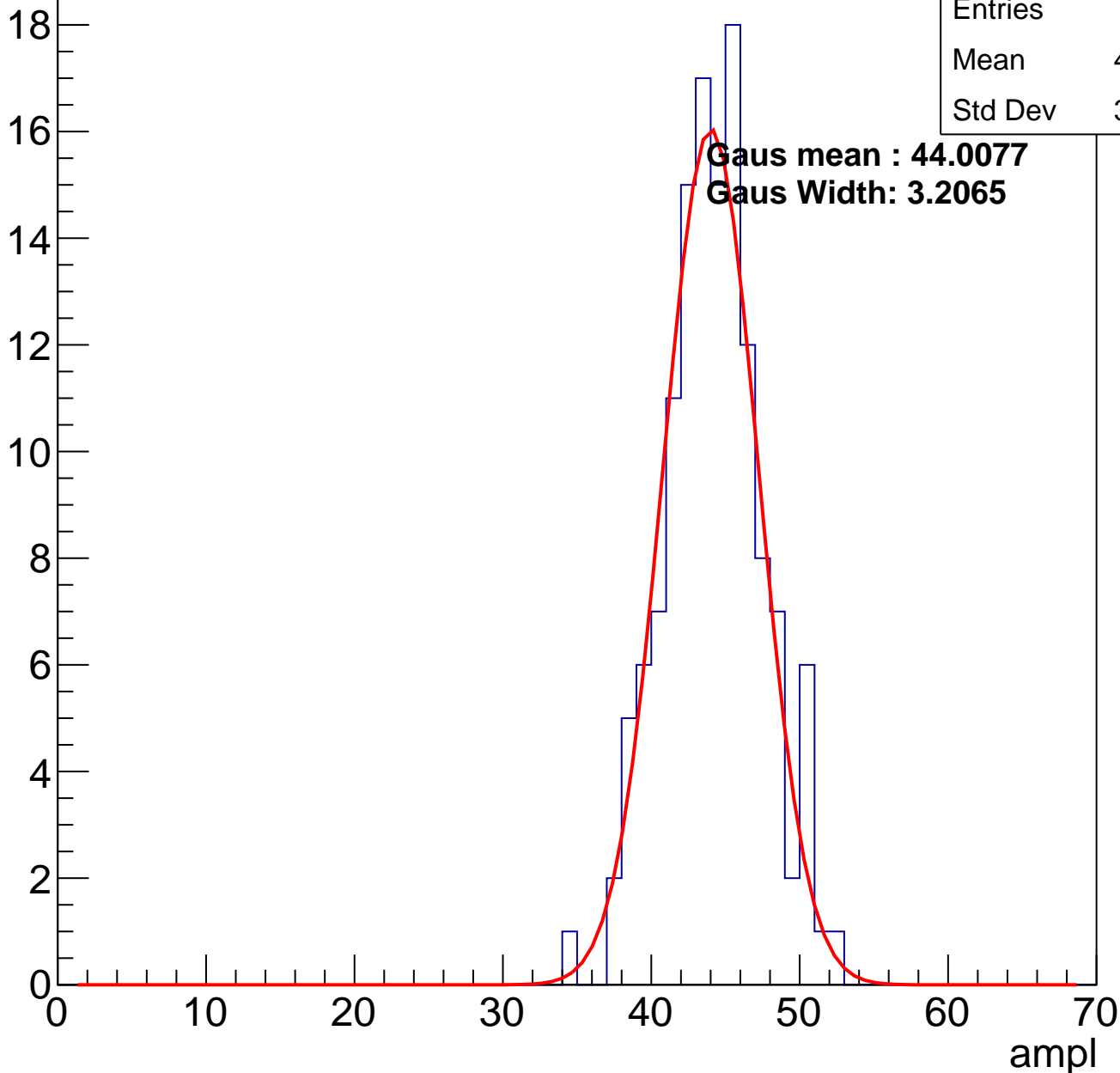
Gaus mean : 37.7345
Gaus Width: 3.3655



B1L001S, U24-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	50.29
Std Dev	3.534

Entry

16
14
12
10
8
6
4
2
0

0

10

20

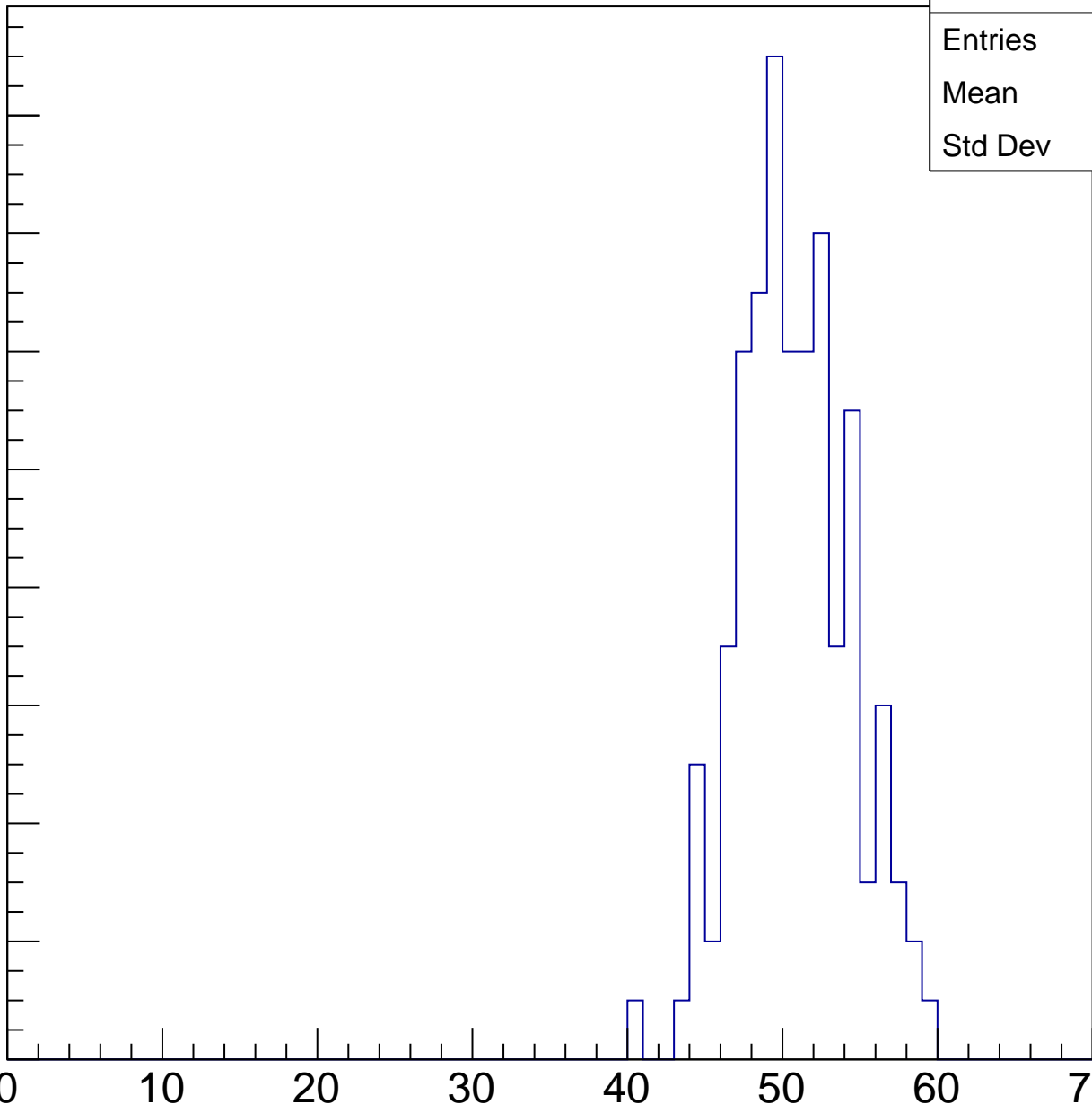
30

40

50

60

ampl



B1L001S, U24-ch17, adc4

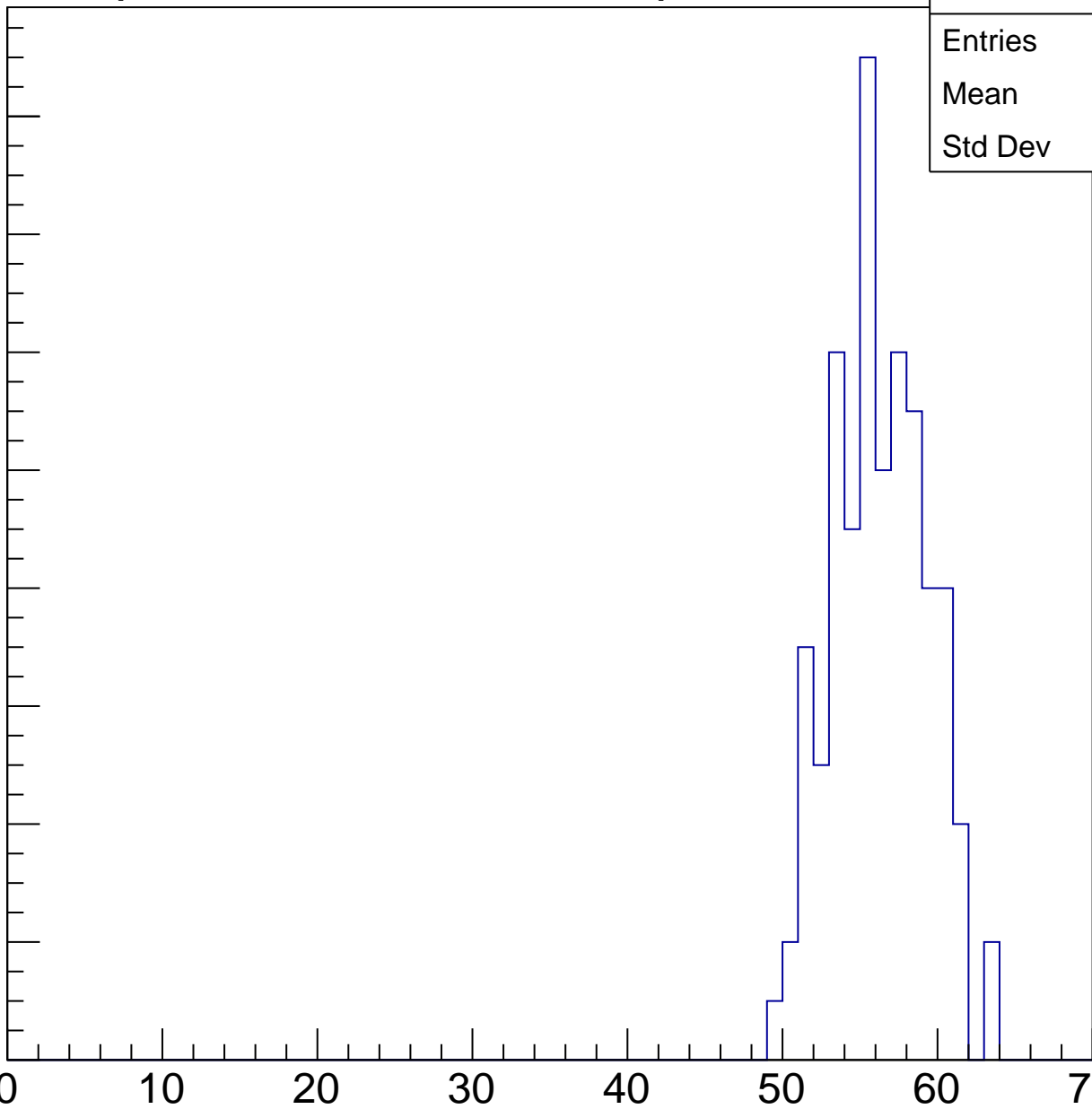
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	55.81
Std Dev	3.023

ampl



B1L001S, U24-ch17, adc5

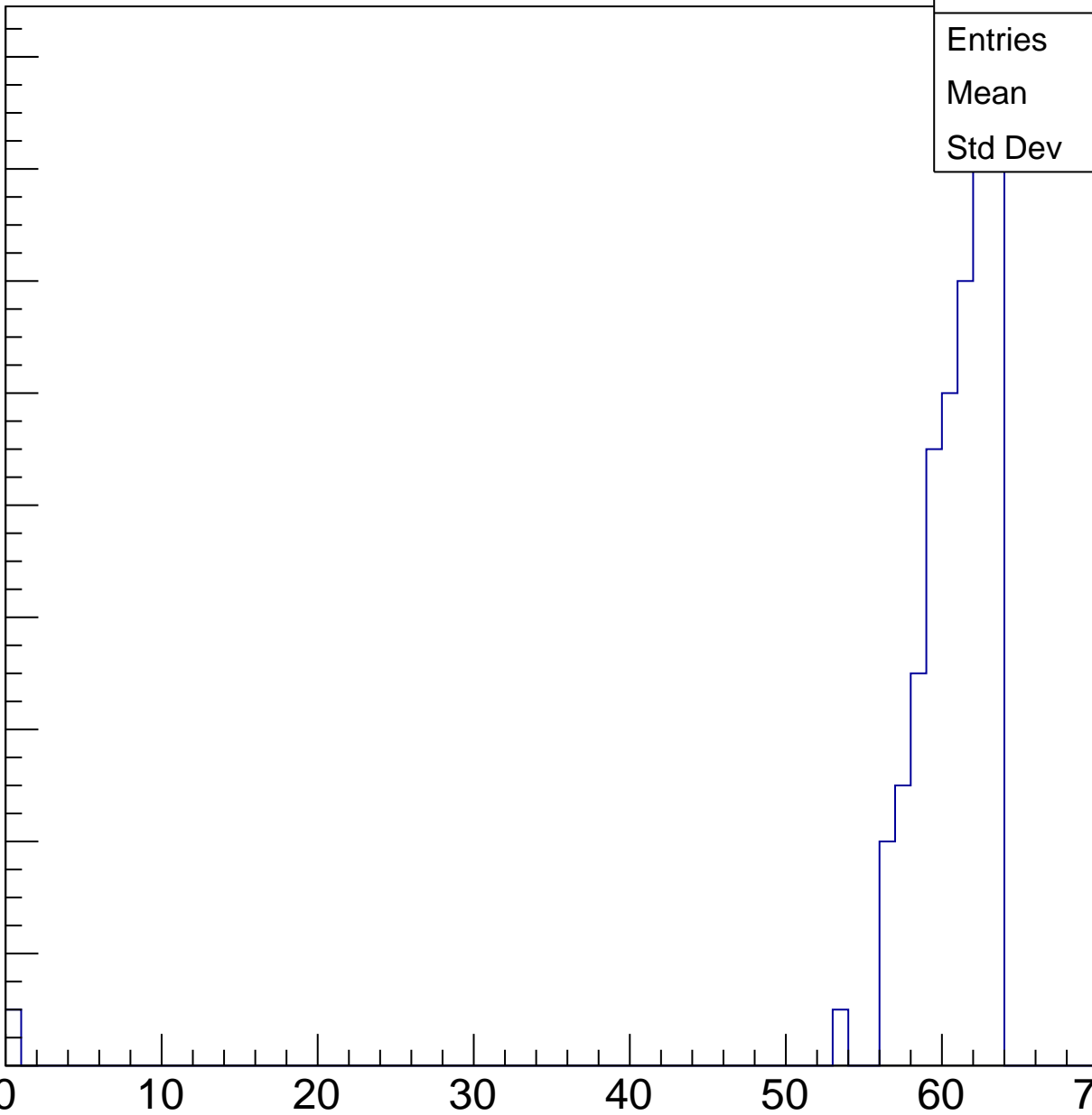
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.72
Std Dev	6.721

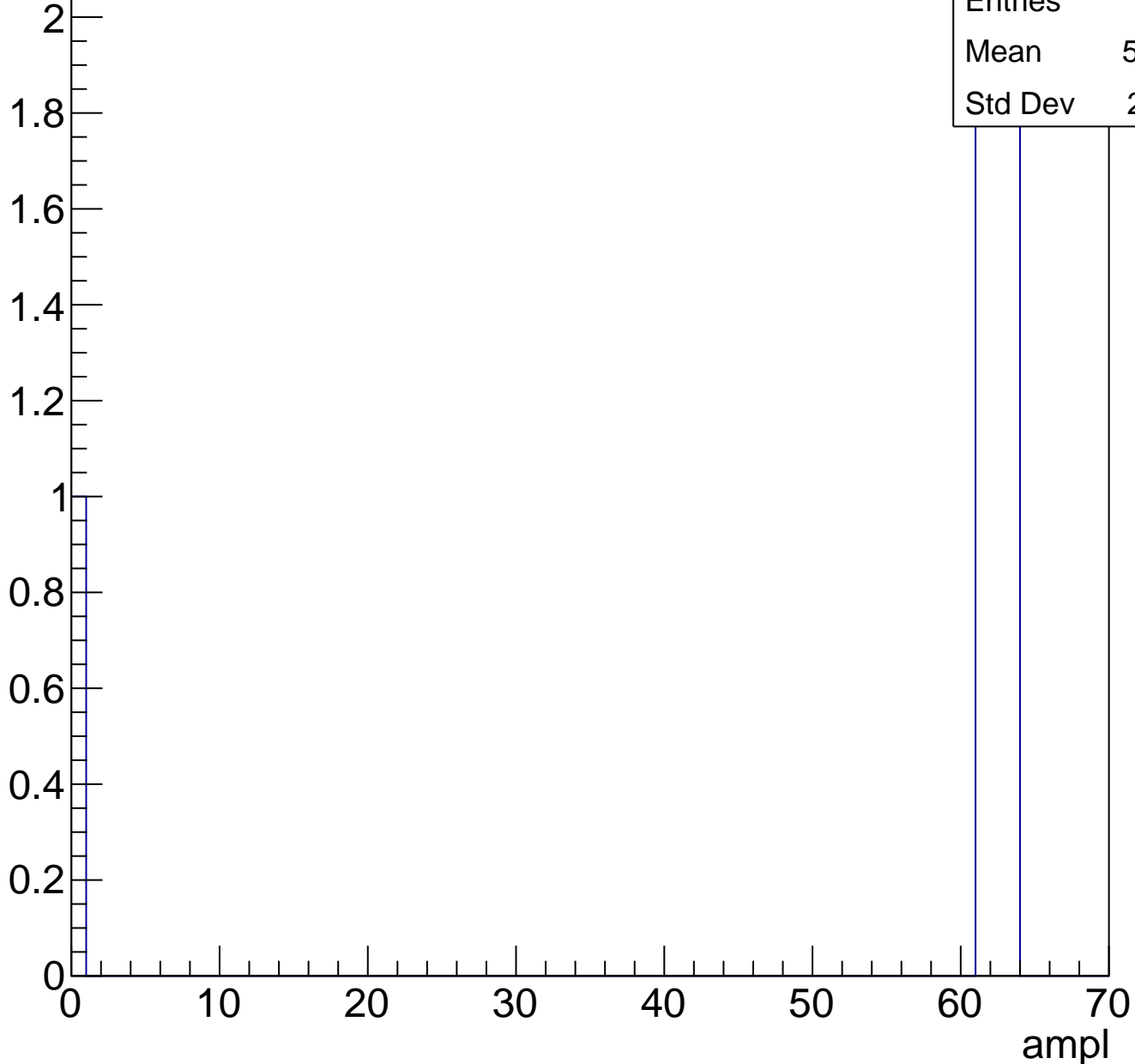
ampl



B1L001S, U24-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch18, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	29.72
Std Dev	4.341

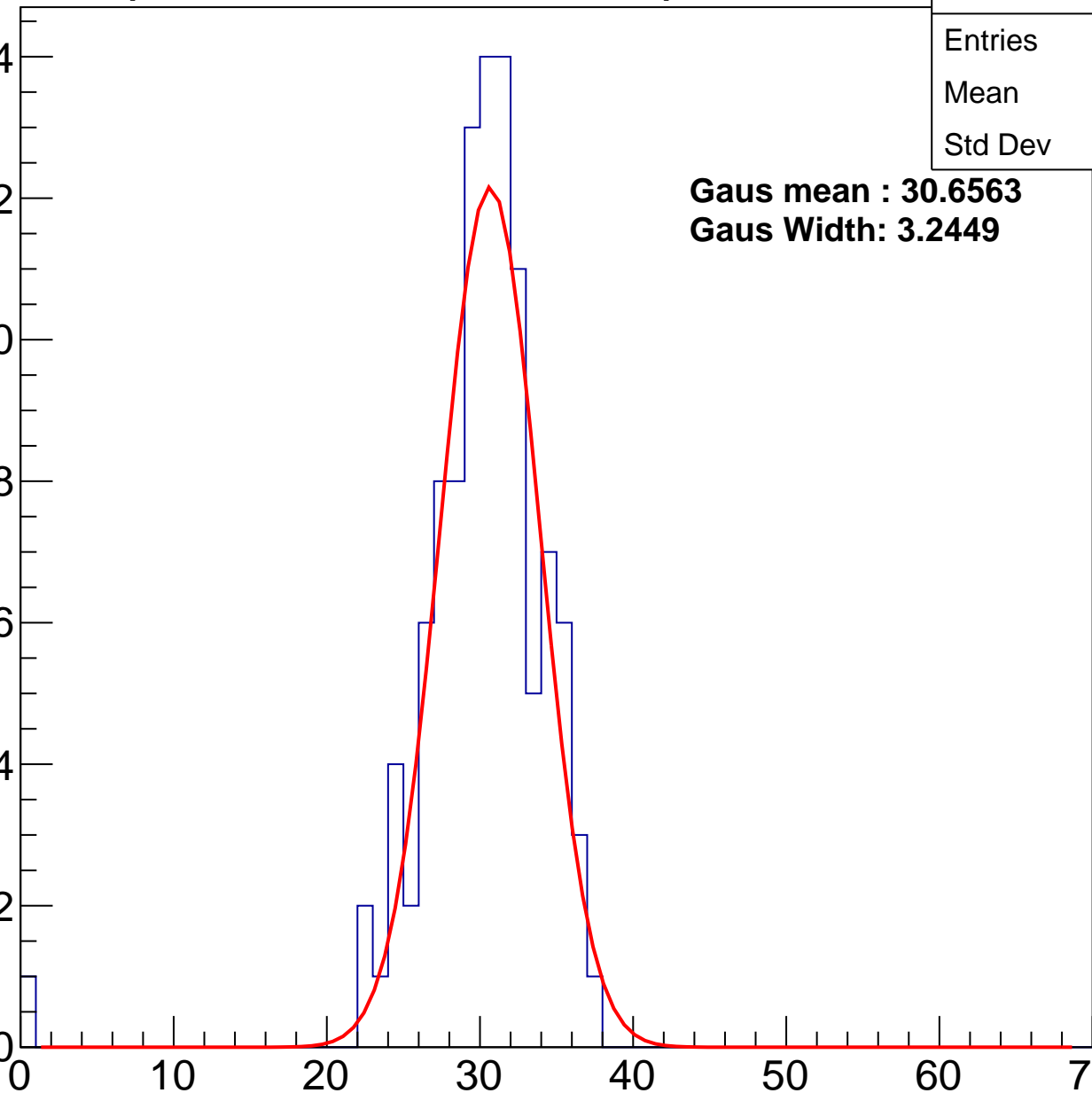
Gaus mean : 30.6563

Gaus Width: 3.2449

Entry

14
12
10
8
6
4
2
0

ampl



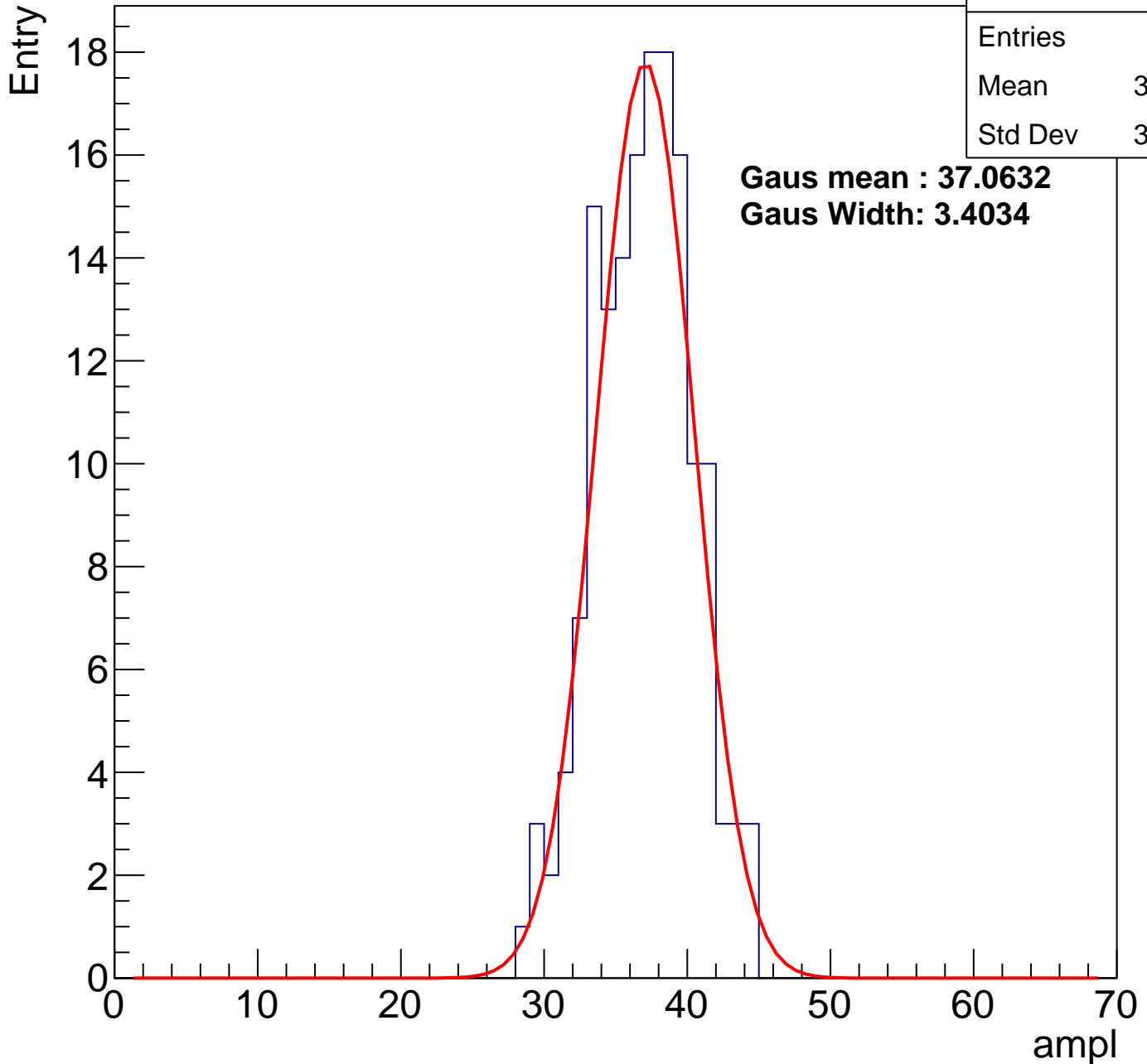
B1L001S, U24-ch18, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	36.52
Std Dev	3.344

Gaus mean : 37.0632

Gaus Width: 3.4034



B1L001S, U24-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

126

Mean

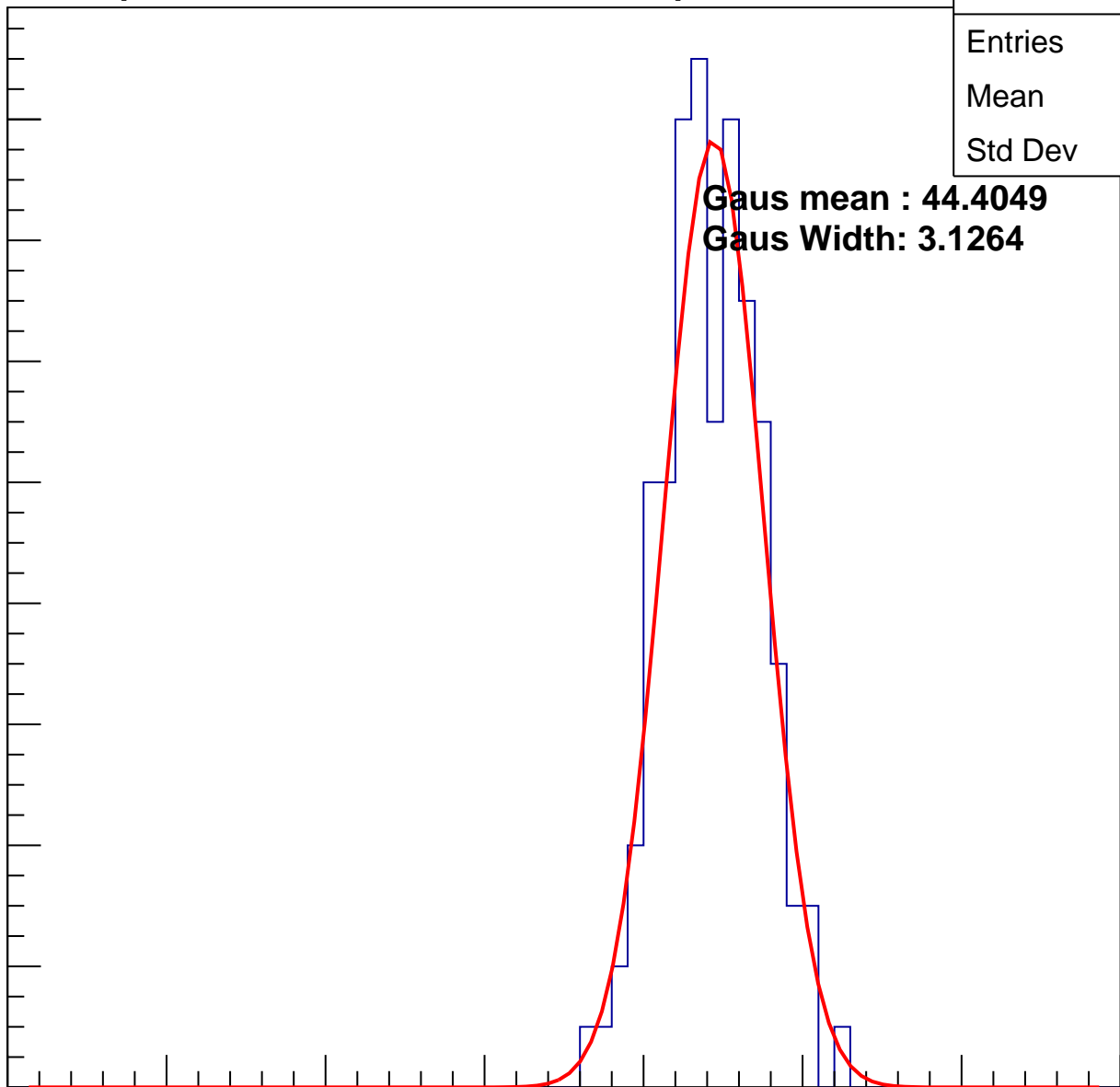
43.83

Std Dev

3.005

Gaus mean : 44.4049

Gaus Width: 3.1264



B1L001S, U24-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	50.55
Std Dev	3.518

Entry

16

14

12

10

8

6

4

2

0

0

10

20

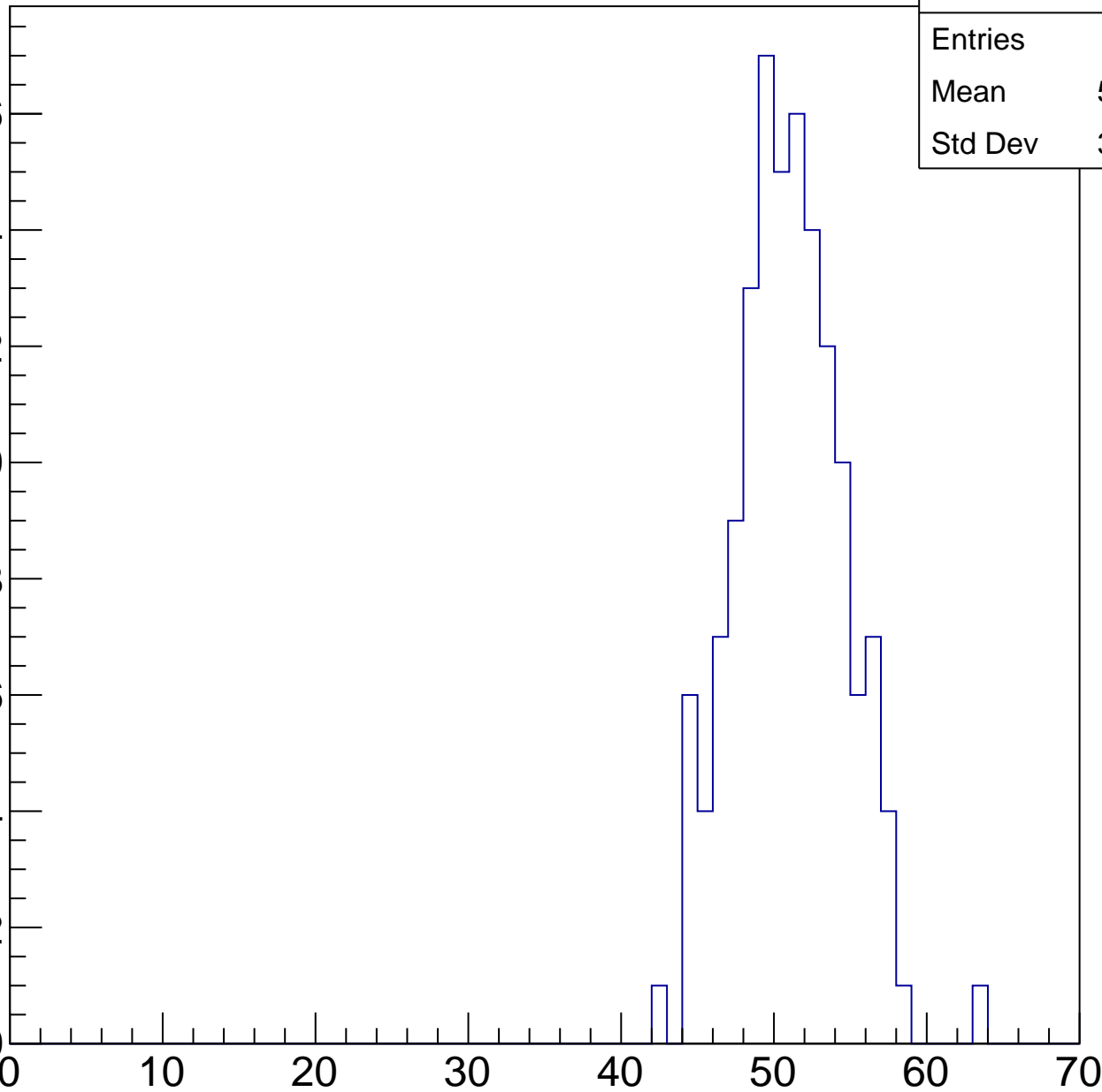
30

40

50

60

ampl

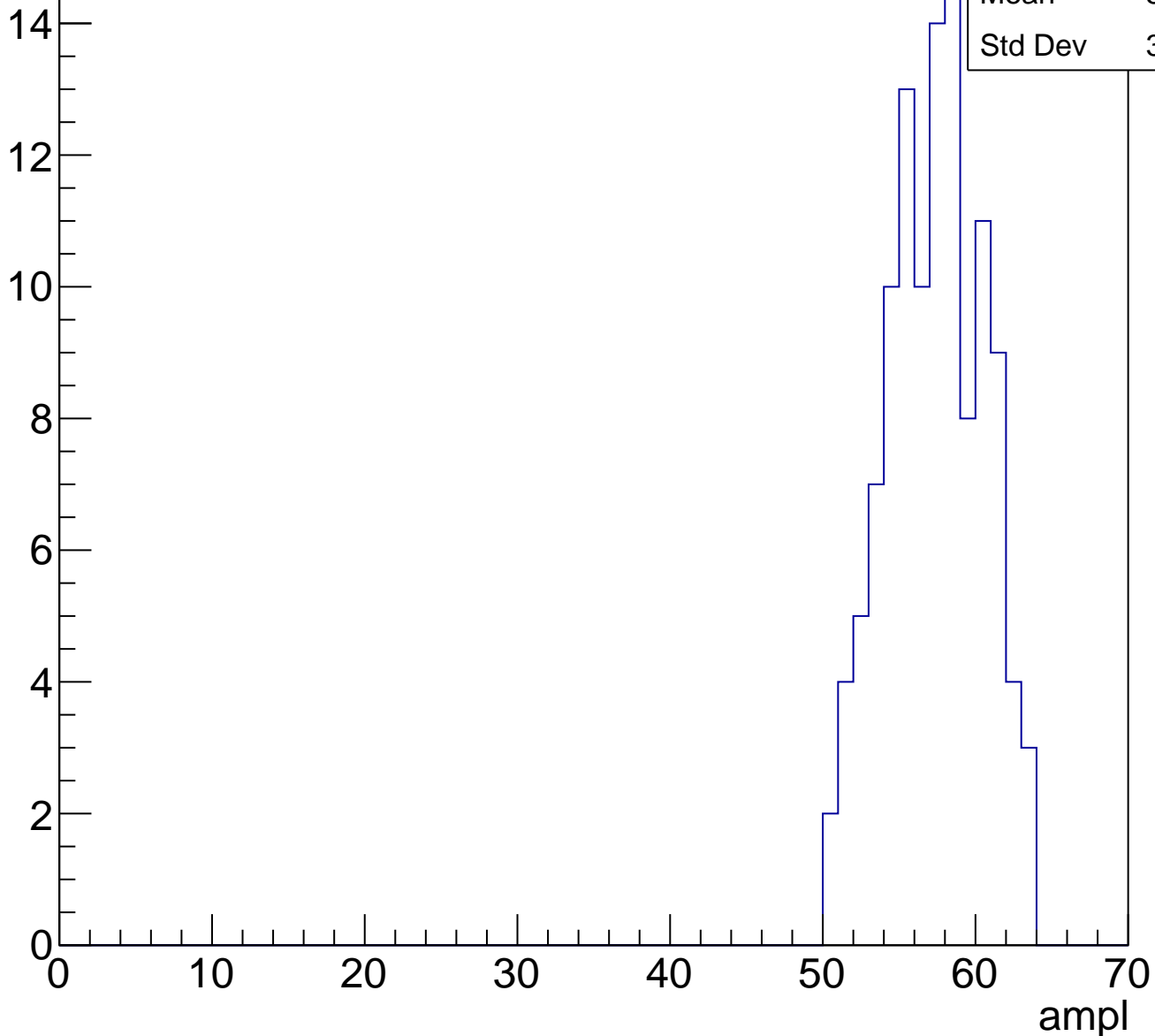


B1L001S, U24-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	56.83
Std Dev	3.129

Entry



B1L001S, U24-ch18, adc5

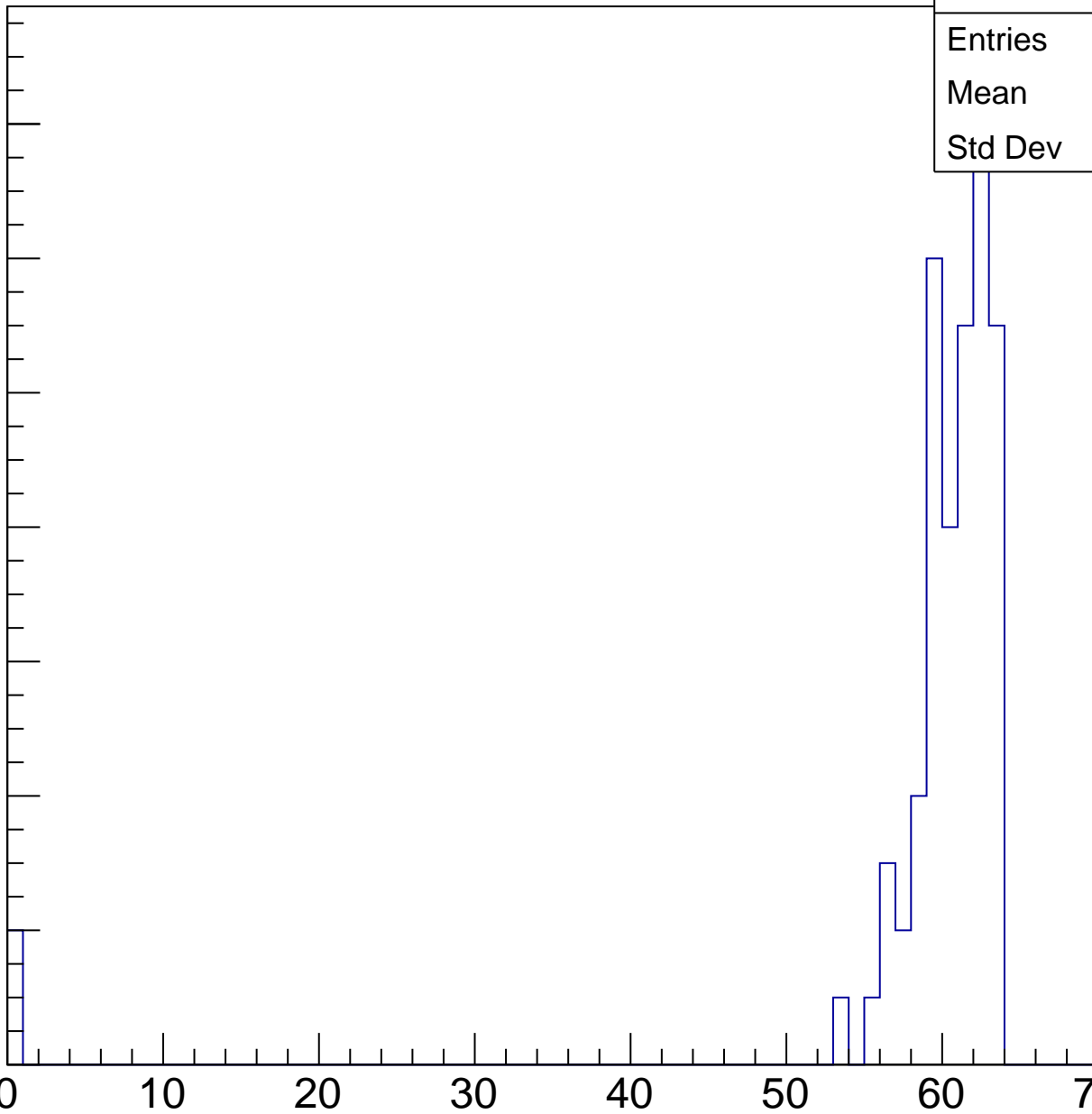
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.63
Std Dev	10.29

ampl

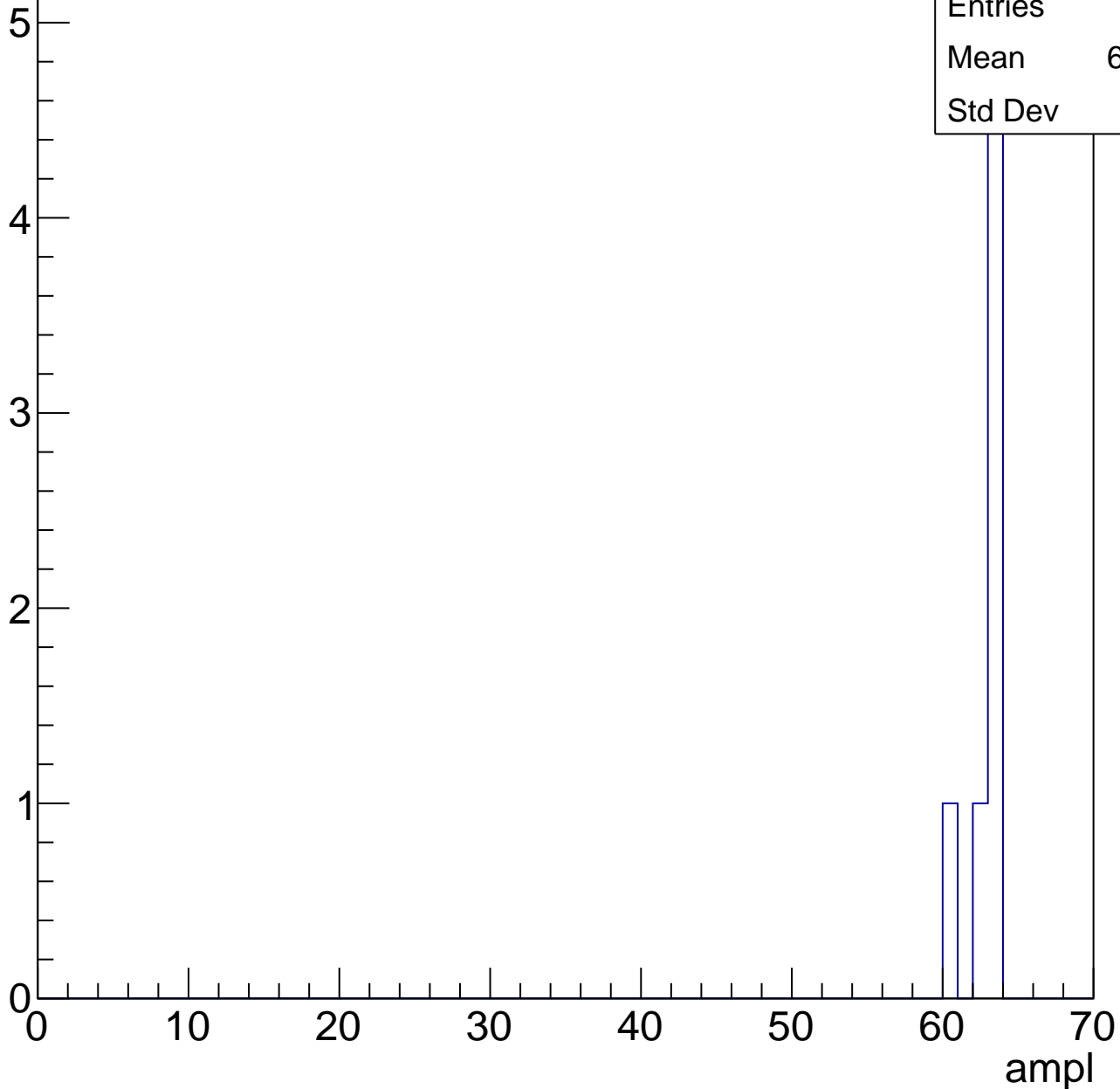


B1L001S, U24-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.43
Std Dev	1.05



B1L001S, U24-ch18, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

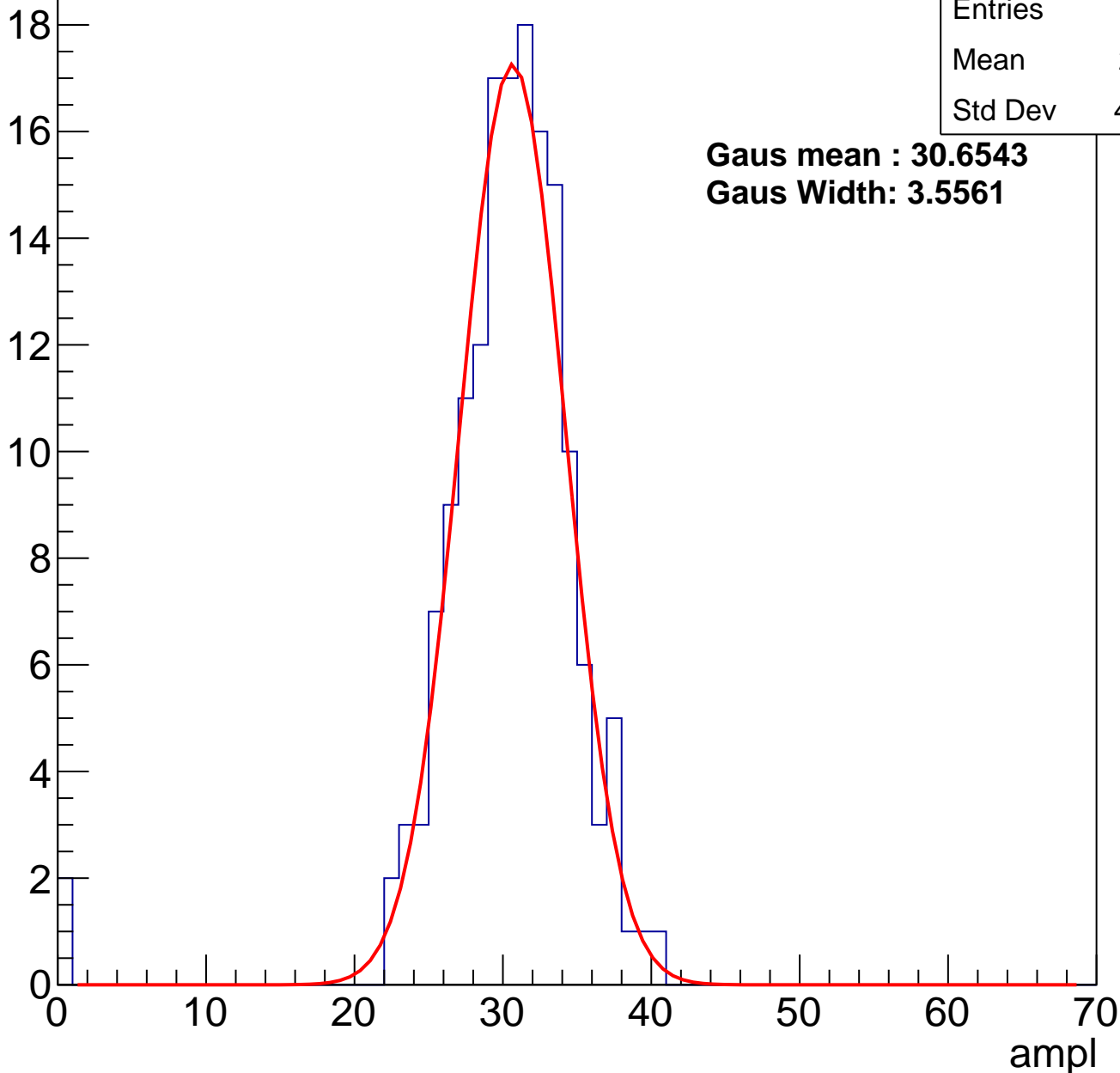


Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



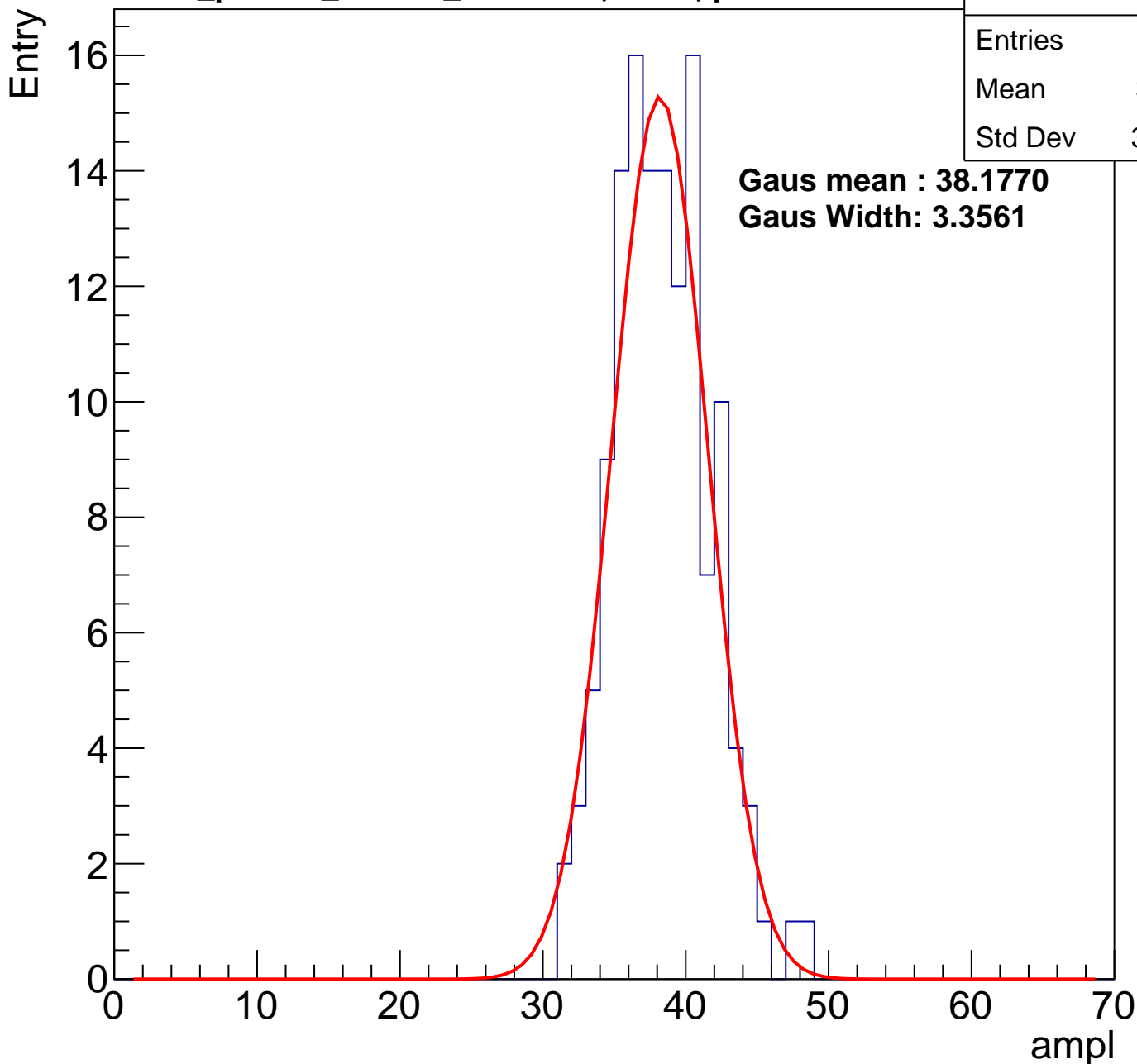
B1L001S, U24-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	37.91
Std Dev	3.274

Gaus mean : 38.1770

Gaus Width: 3.3561



B1L001S, U24-ch19, adc2

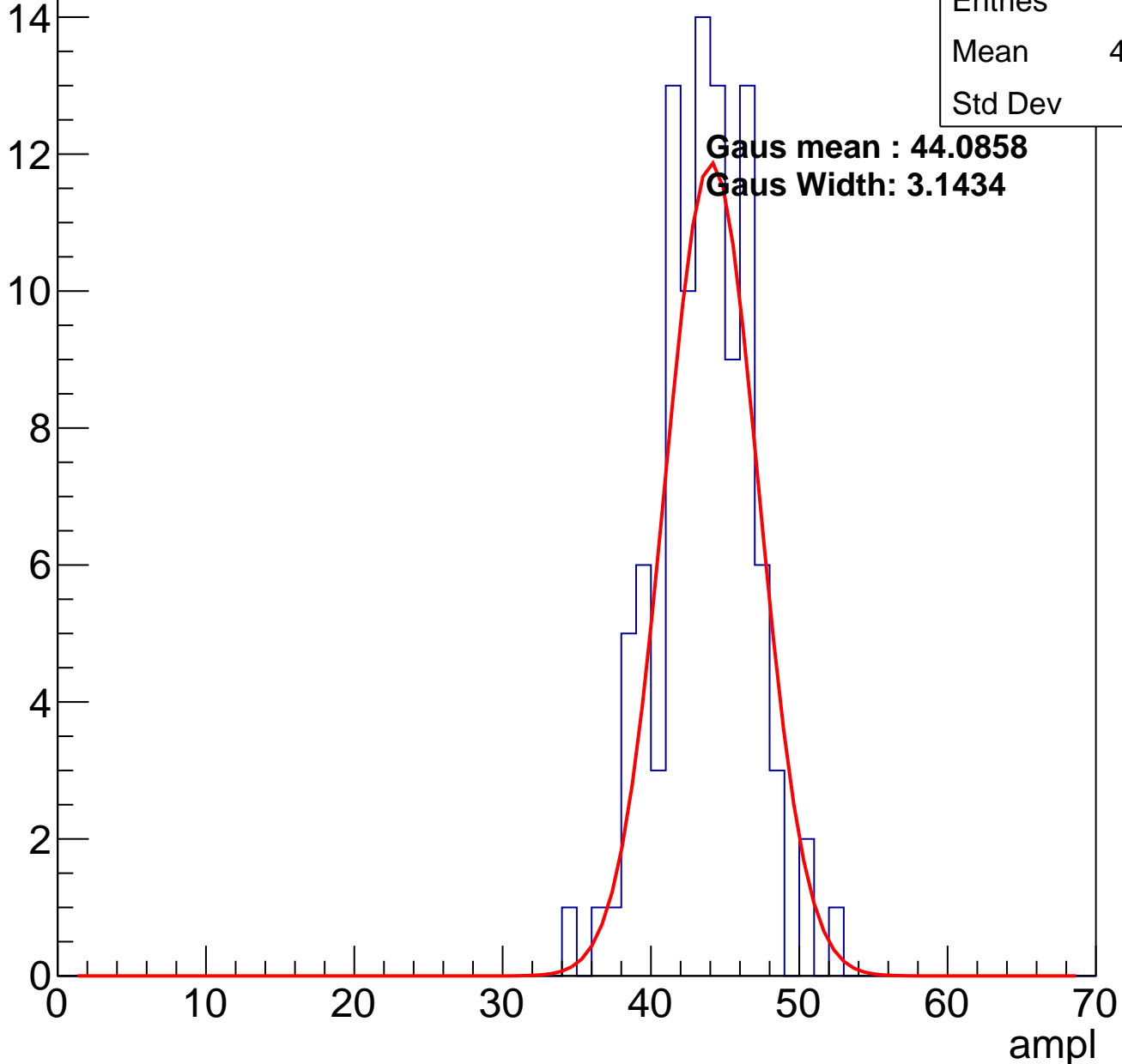
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	101
Mean	43.16
Std Dev	3.13

Gaus mean : 44.0858

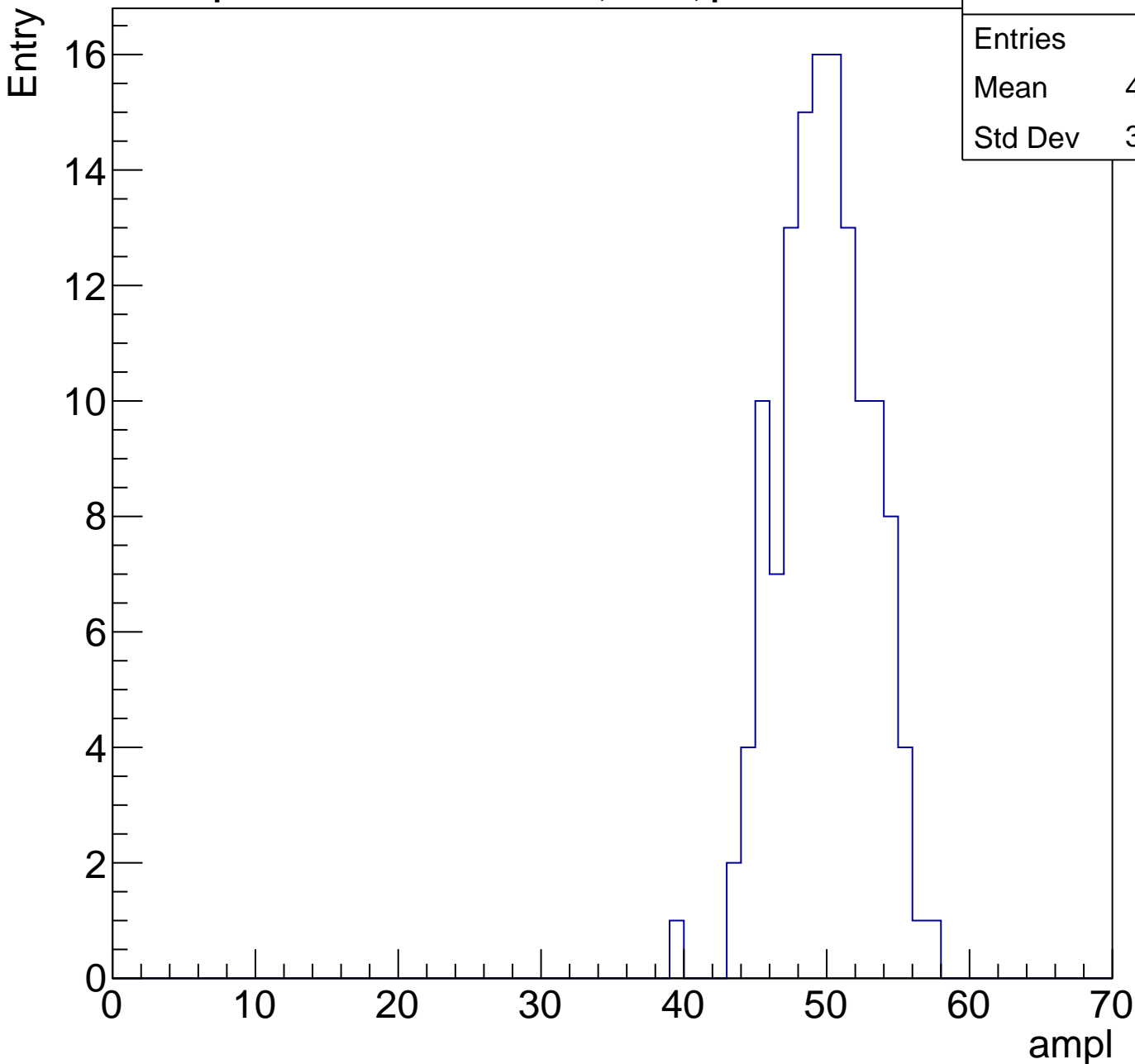
Gaus Width: 3.1434



B1L001S, U24-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

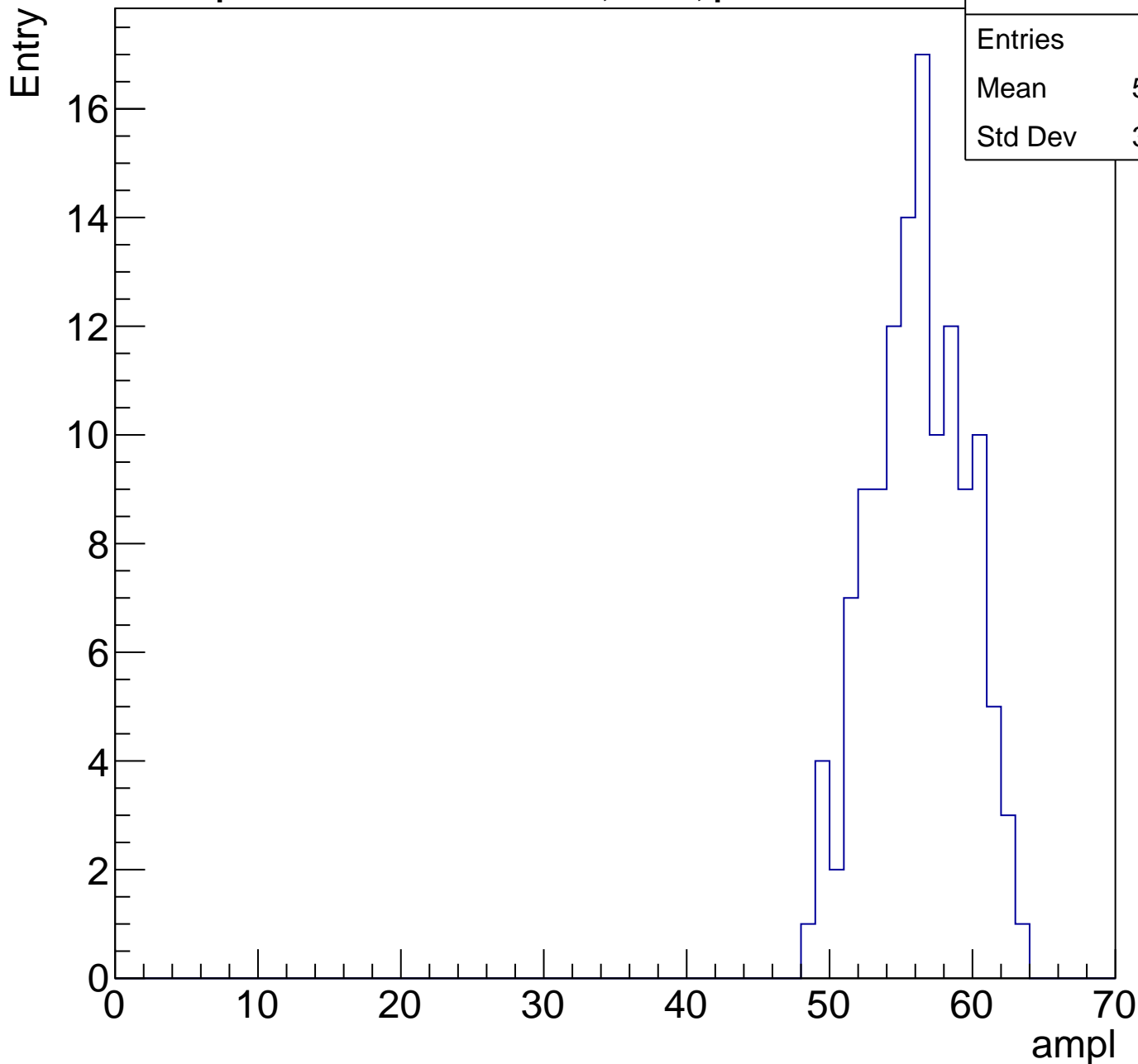
Entries	131
Mean	49.36
Std Dev	3.174



B1L001S, U24-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.74
Std Dev	3.306



B1L001S, U24-ch19, adc5

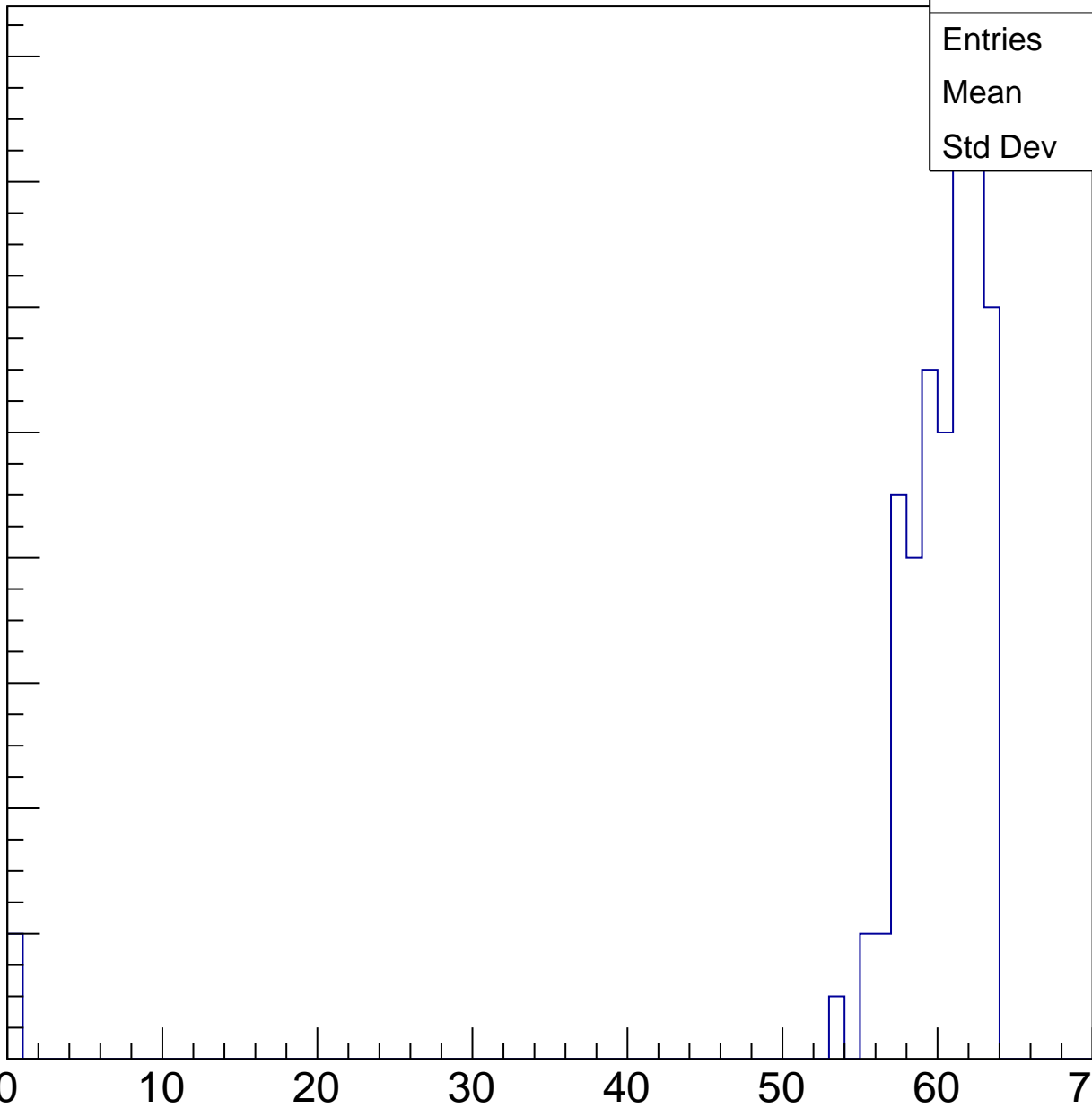
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	58.68
Std Dev	9.225

ampl

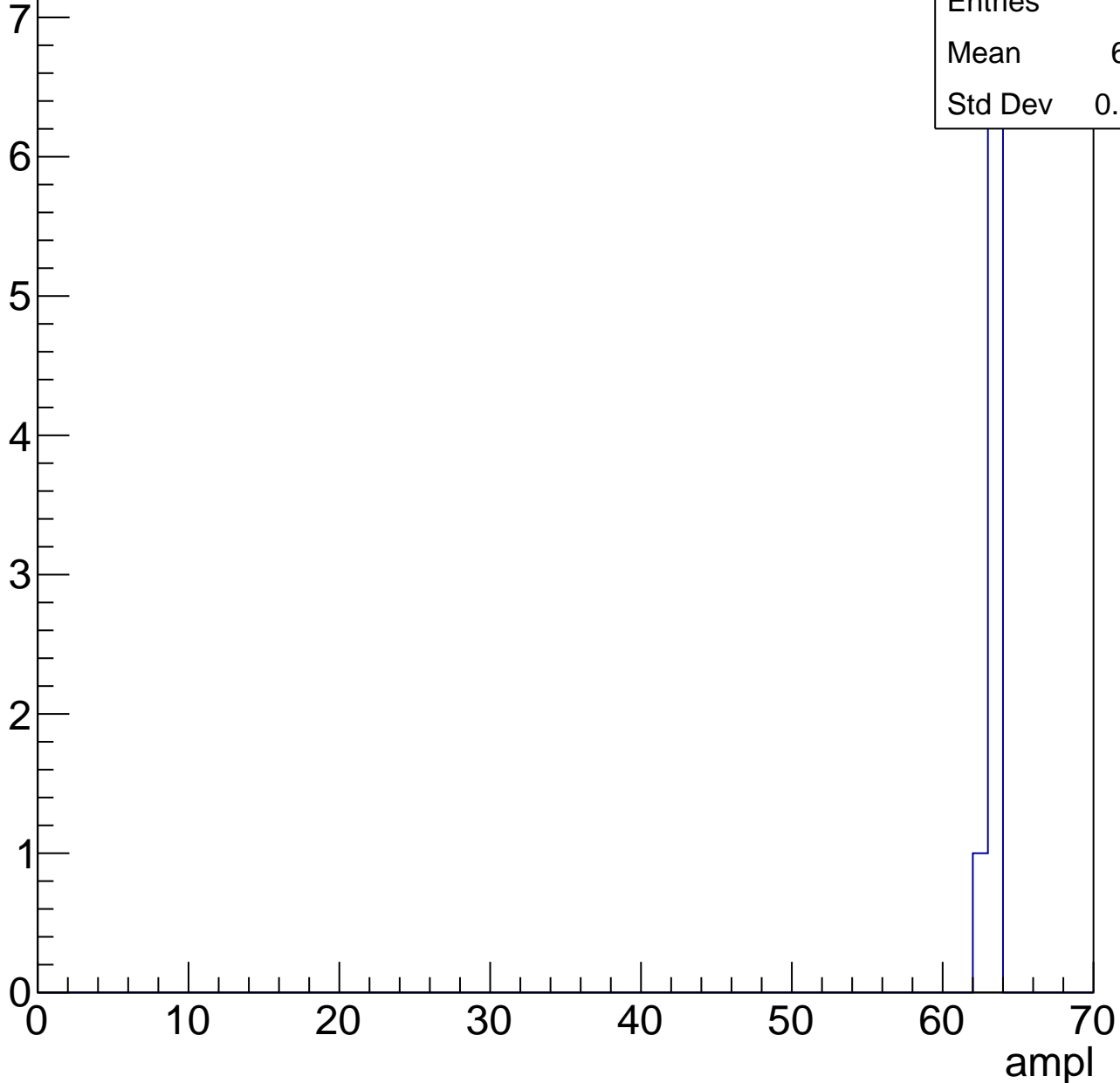


B1L001S, U24-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

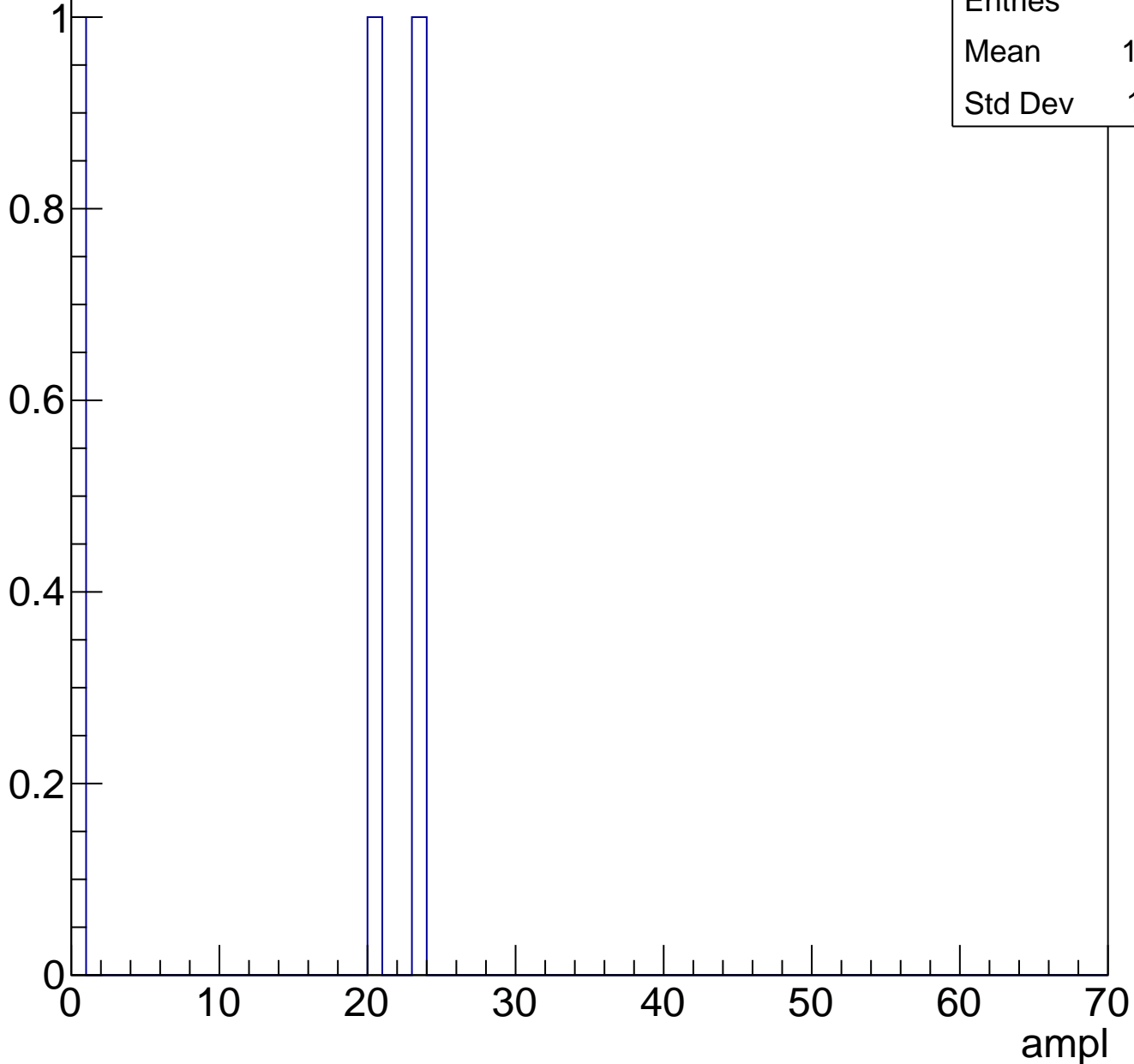
Entries	8
Mean	62.88
Std Dev	0.3307



B1L001S, U24-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch20, adc0

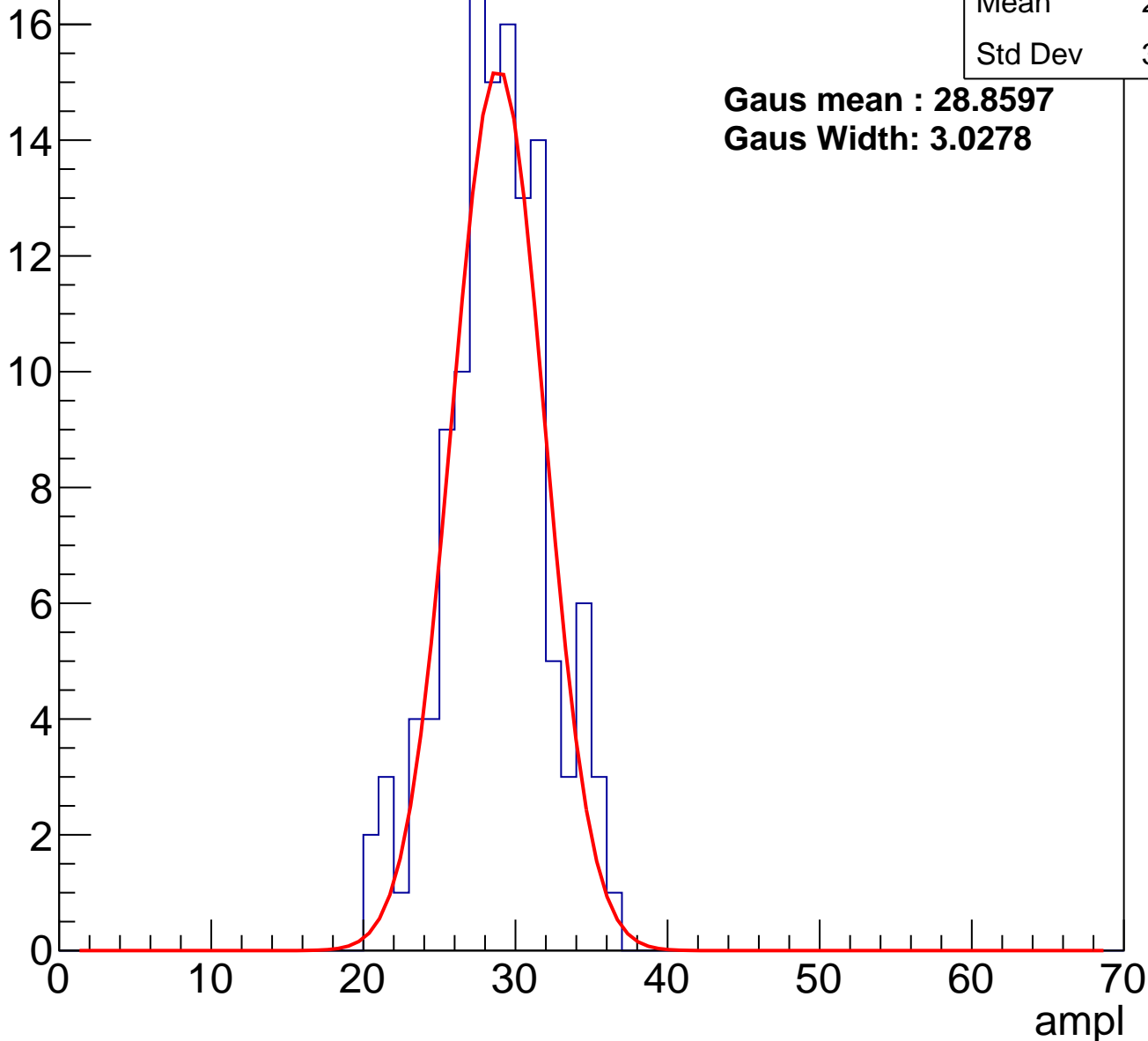
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	126
Mean	28.33
Std Dev	3.304

Gaus mean : 28.8597

Gaus Width: 3.0278



B1L001S, U24-ch20, adc1

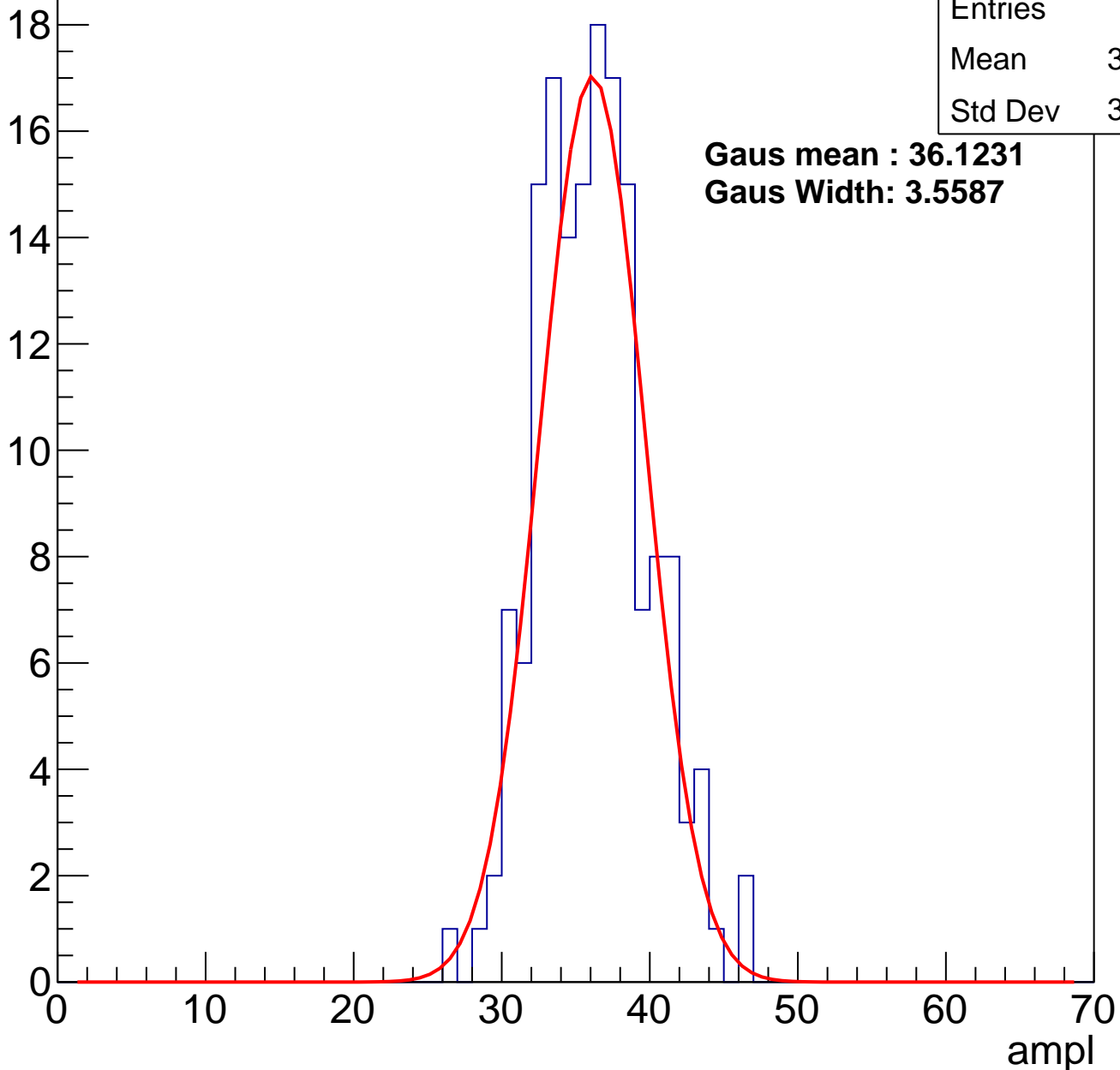
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	35.73
Std Dev	3.656

Gaus mean : 36.1231

Gaus Width: 3.5587

Entry



B1L001S, U24-ch20, adc2

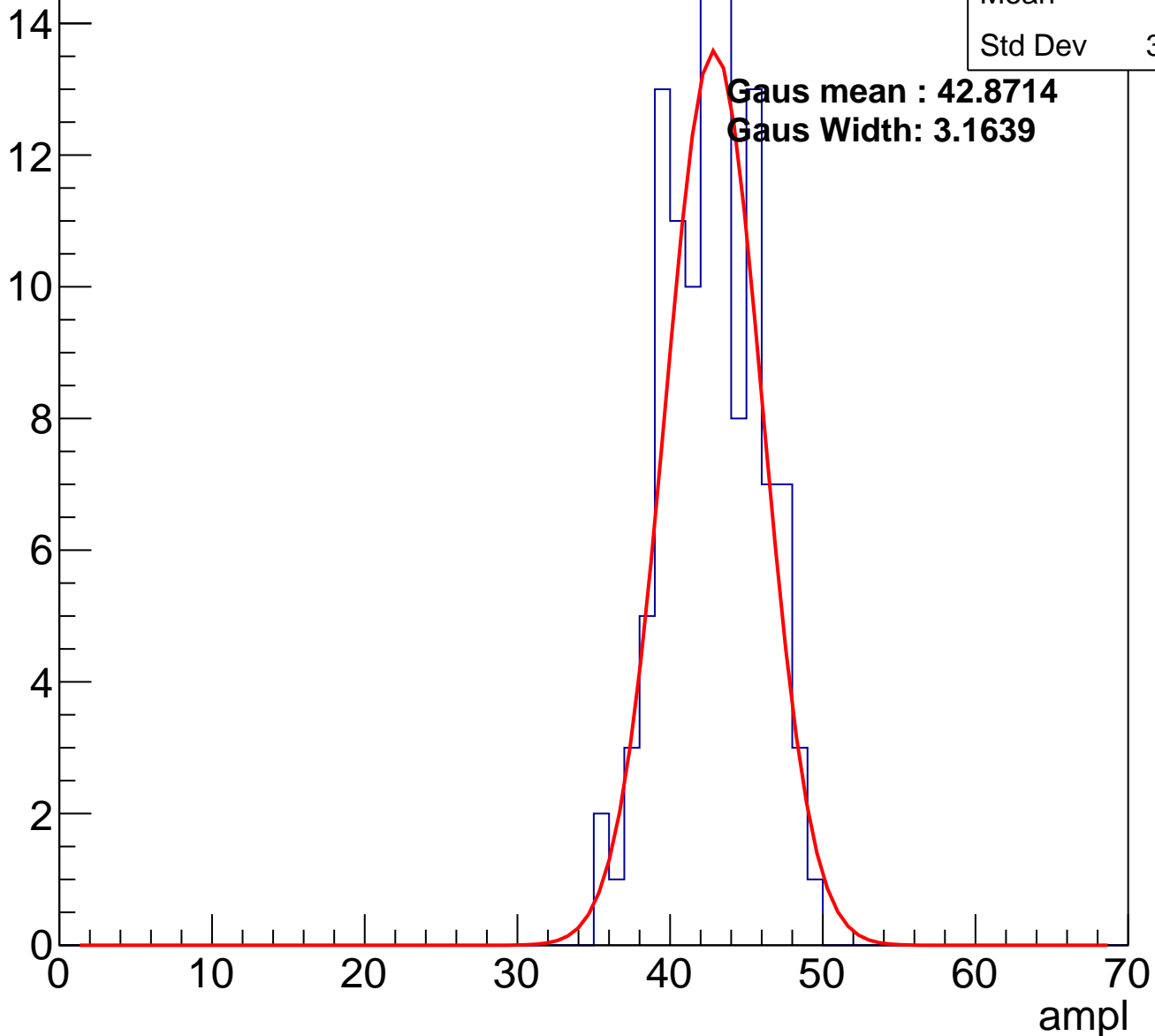
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	42.28
Std Dev	3.048

Gaus mean : 42.8714

Gaus Width: 3.1639

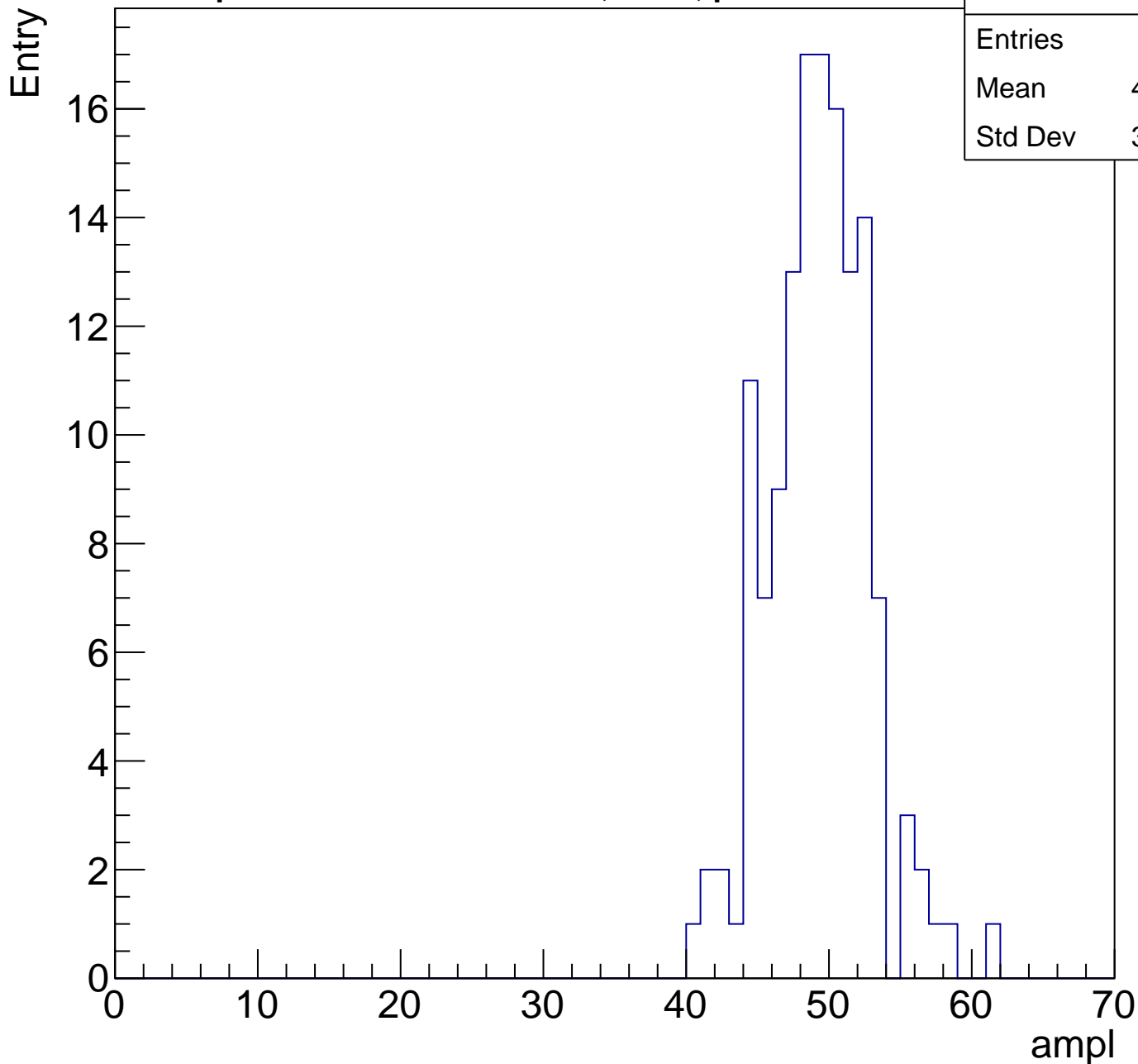
Entry



B1L001S, U24-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

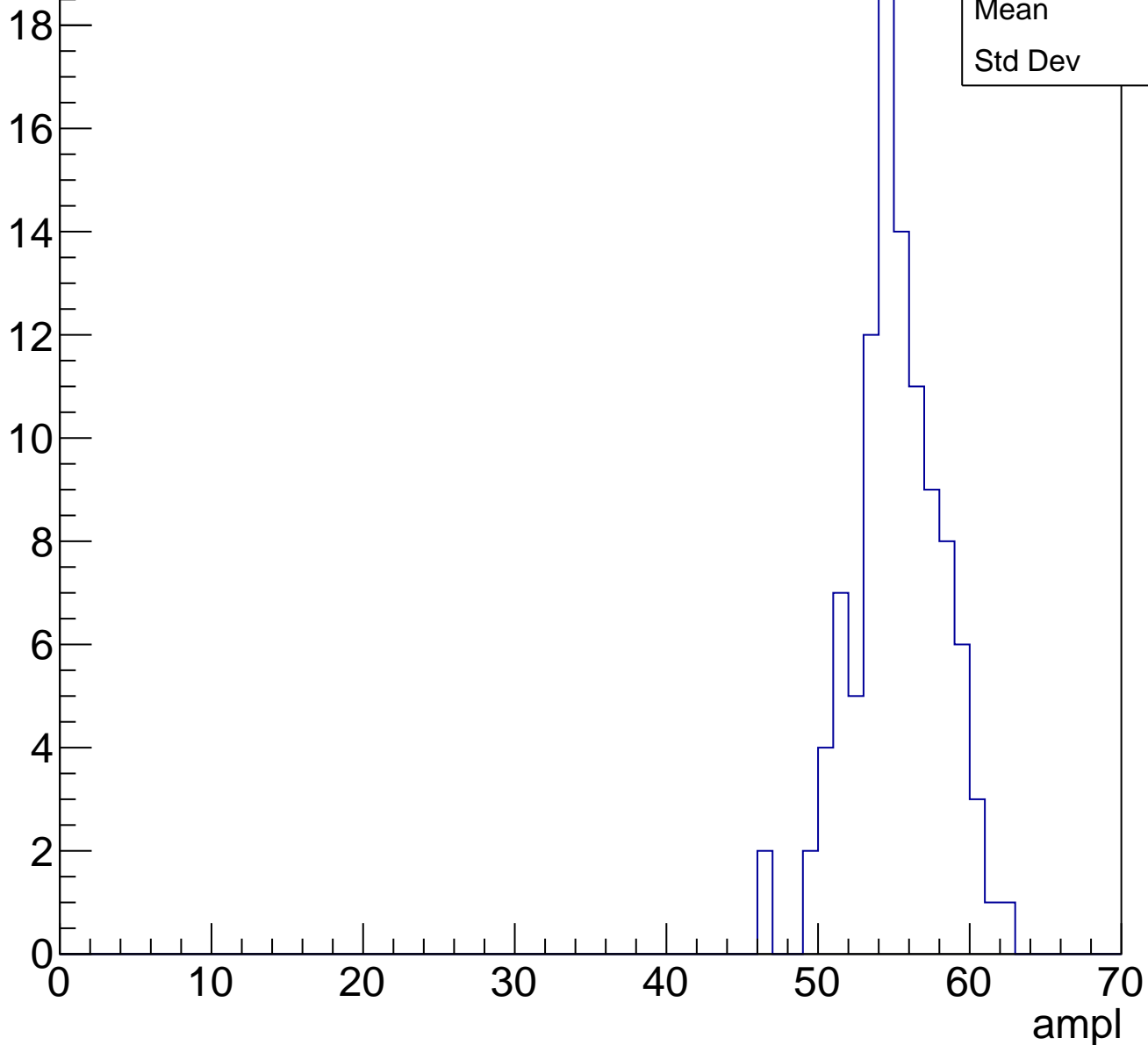
Entries	138
Mean	48.82
Std Dev	3.485



B1L001S, U24-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

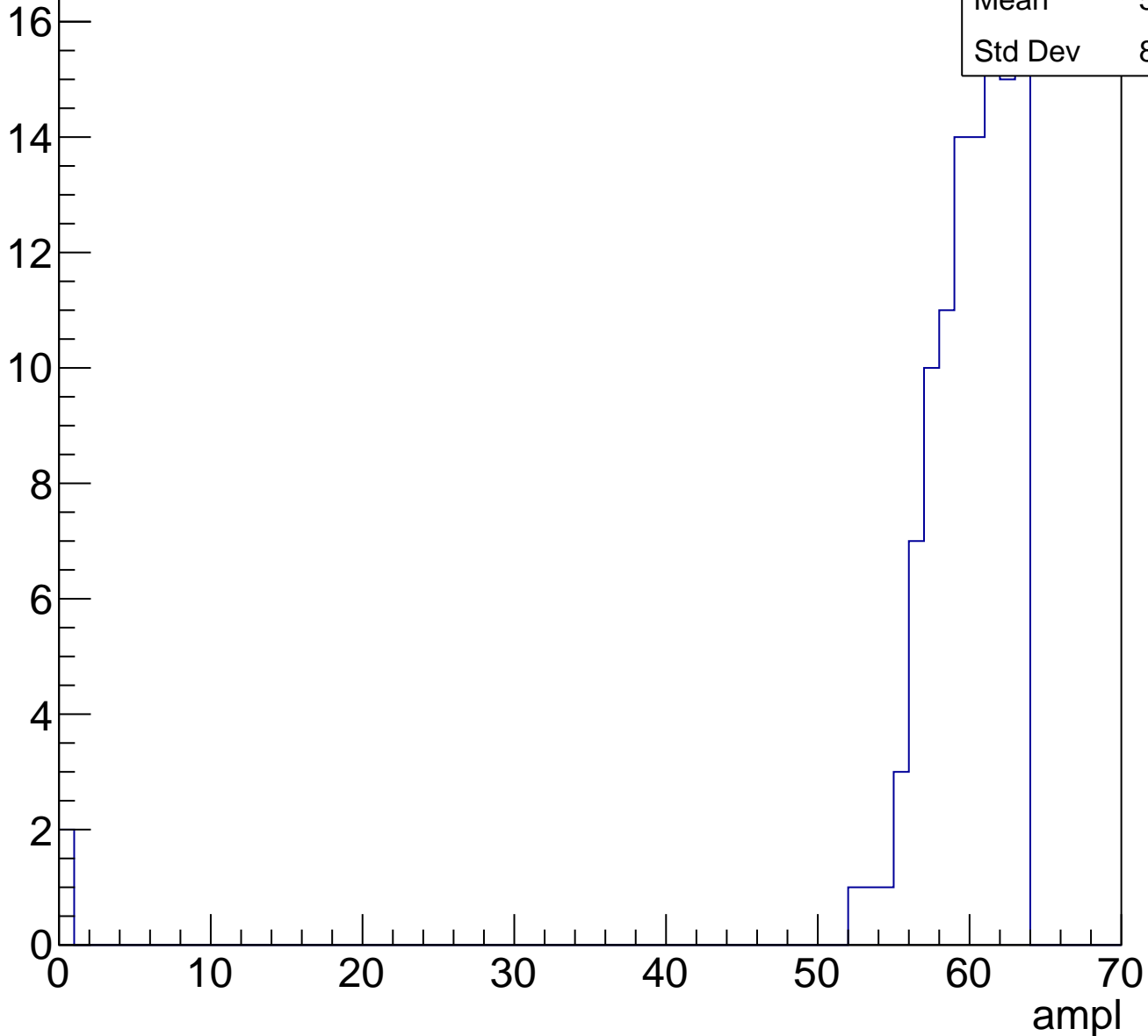


Entries	104
Mean	54.7
Std Dev	2.98

B1L001S, U24-ch20, adc5

calib_packv5_042523_0143.root, FC#2, port C2

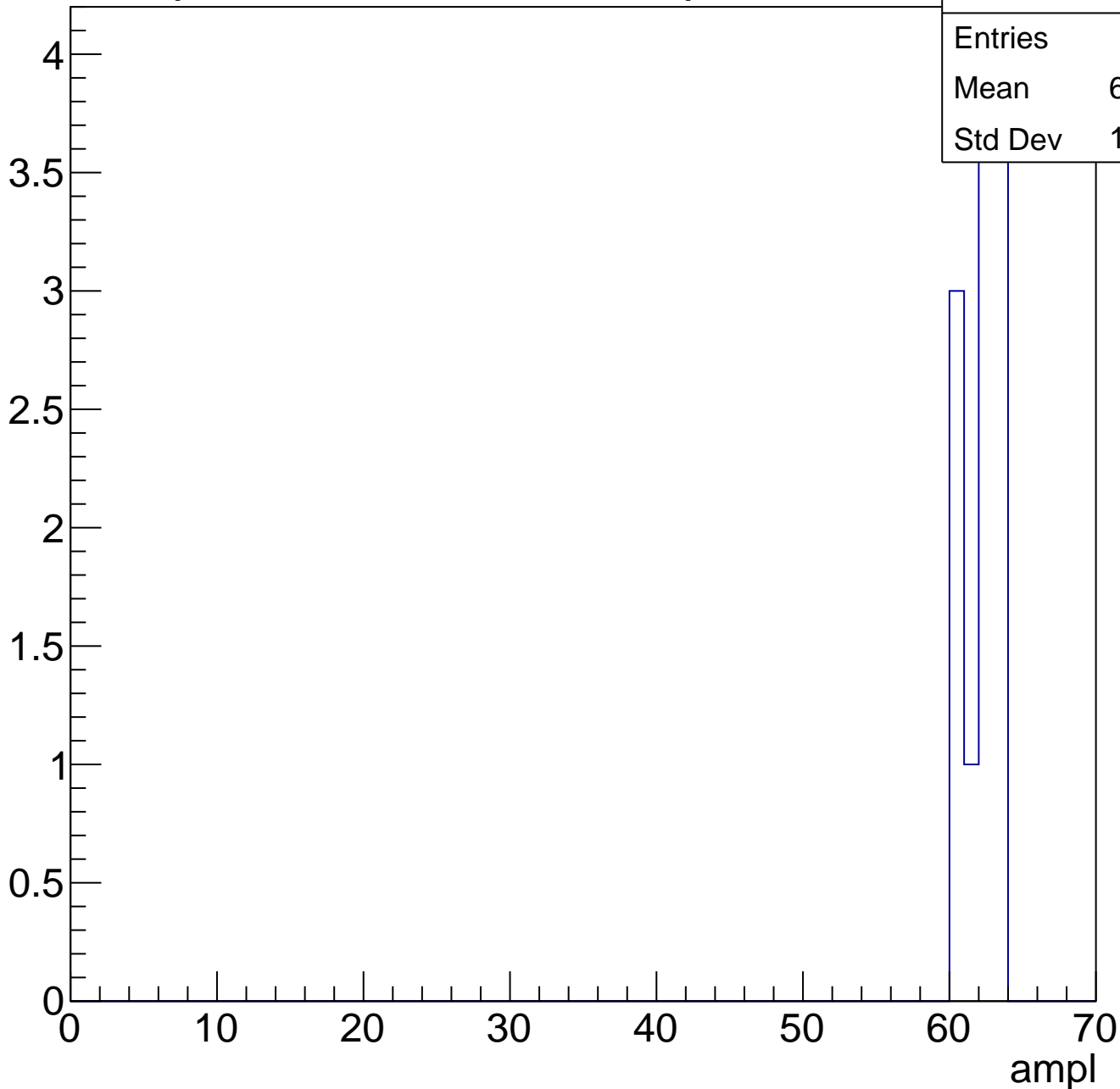
Entry



B1L001S, U24-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L001S, U24-ch21, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.36
Std Dev	3.579

Gaus mean : 30.8359

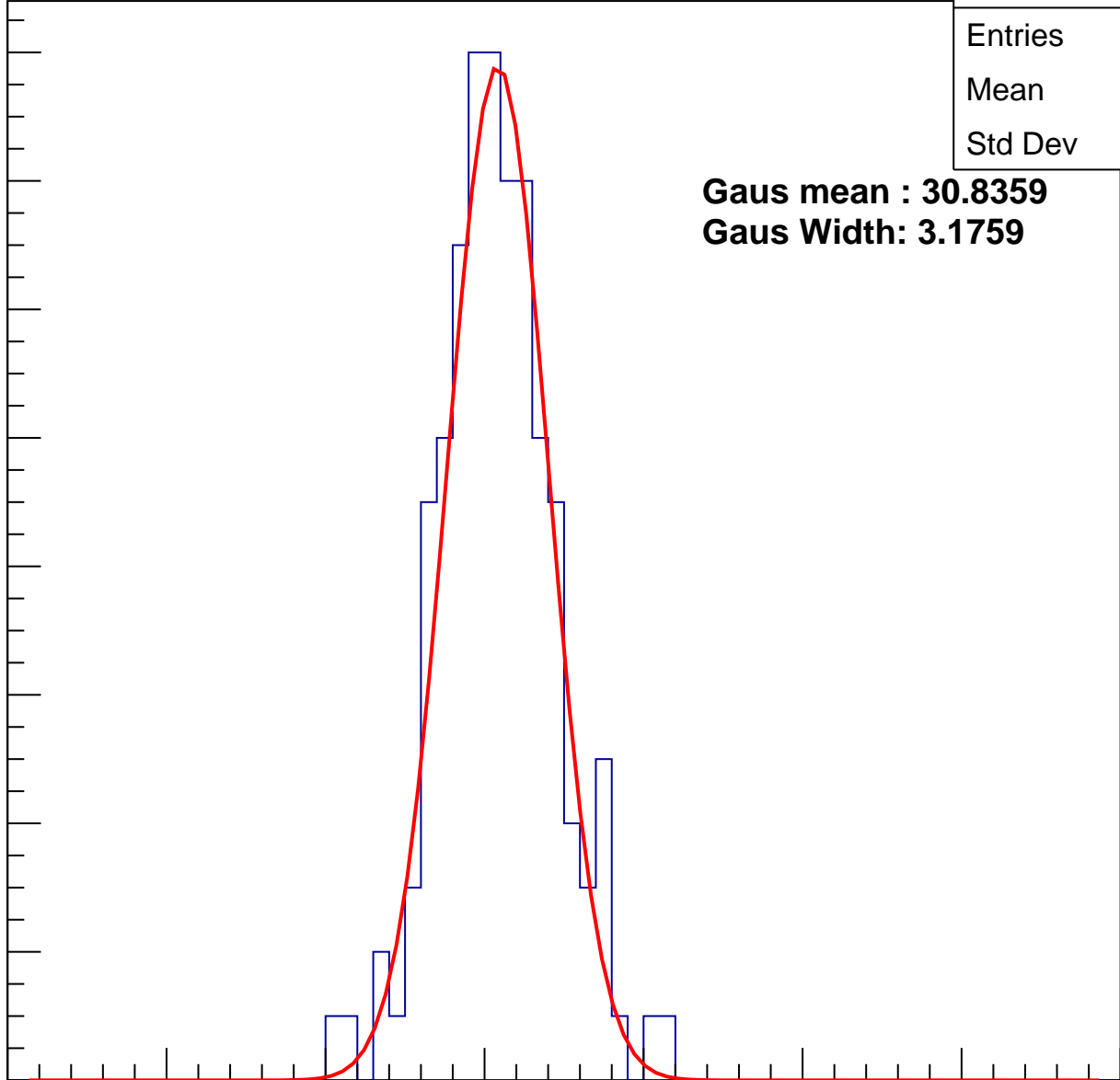
Gaus Width: 3.1759

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch21, adc1

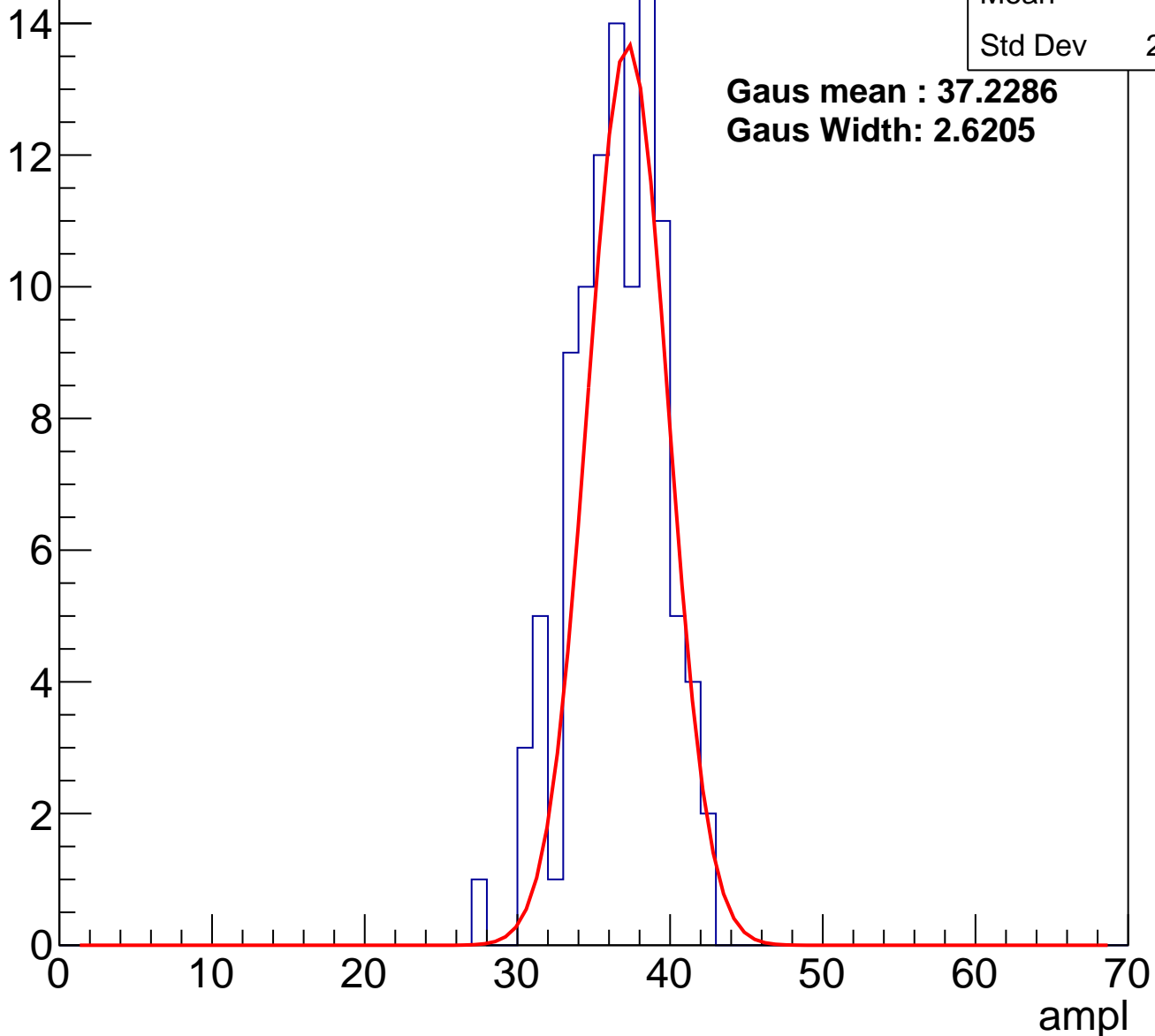
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	36.1
Std Dev	2.942

Gaus mean : 37.2286

Gaus Width: 2.6205

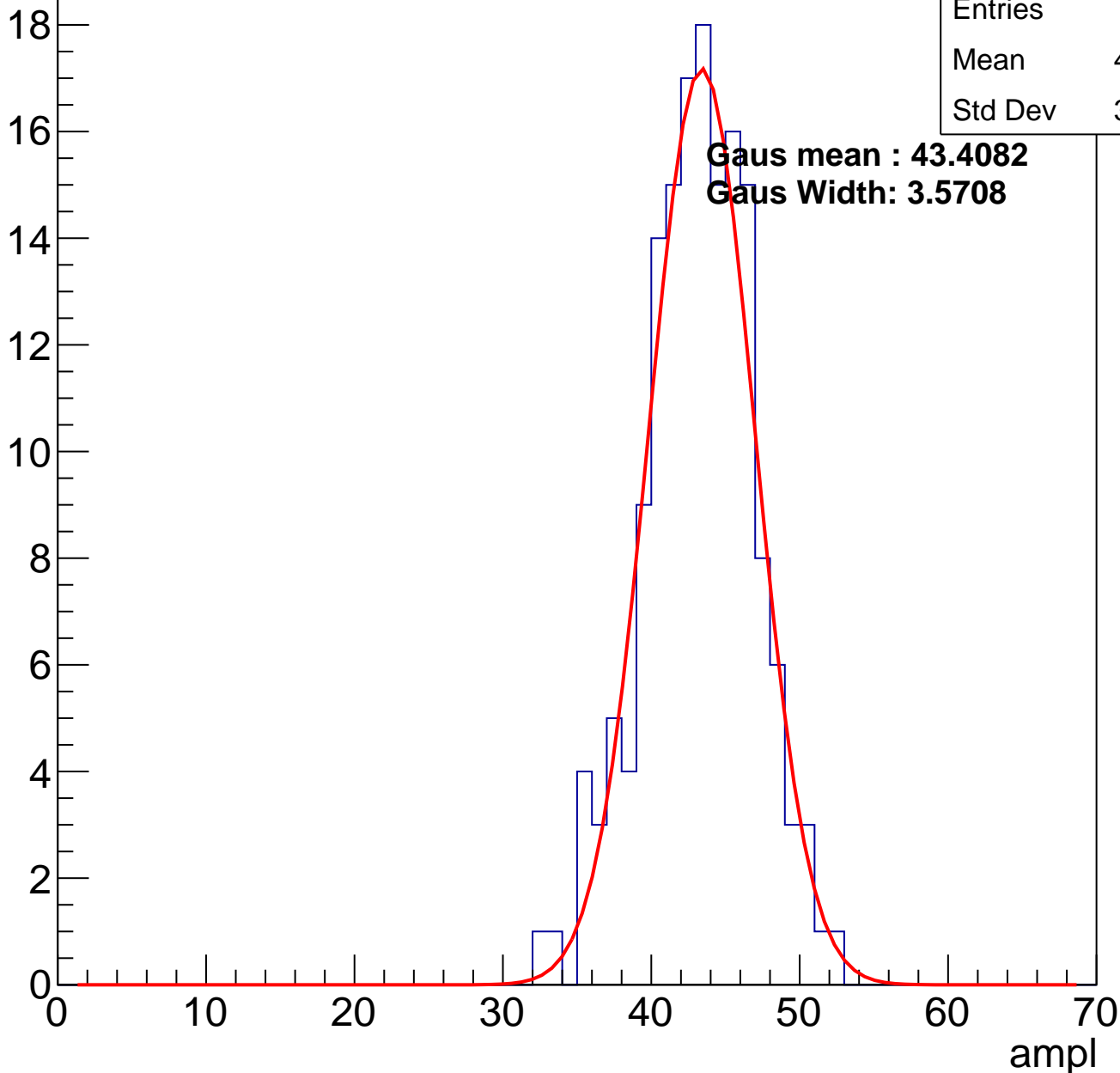
Entry



B1L001S, U24-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

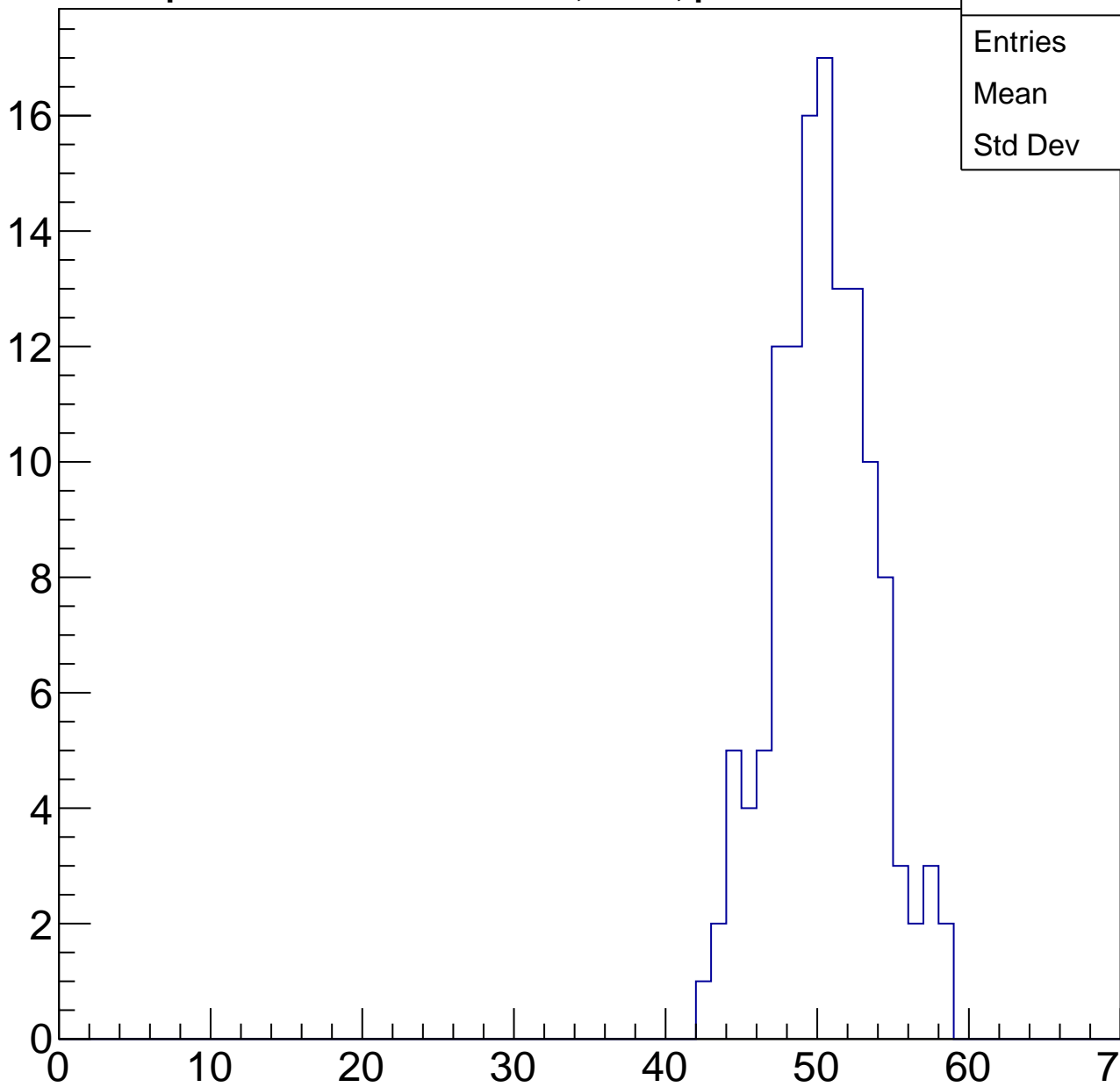
Entries	128
Mean	49.98
Std Dev	3.306

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

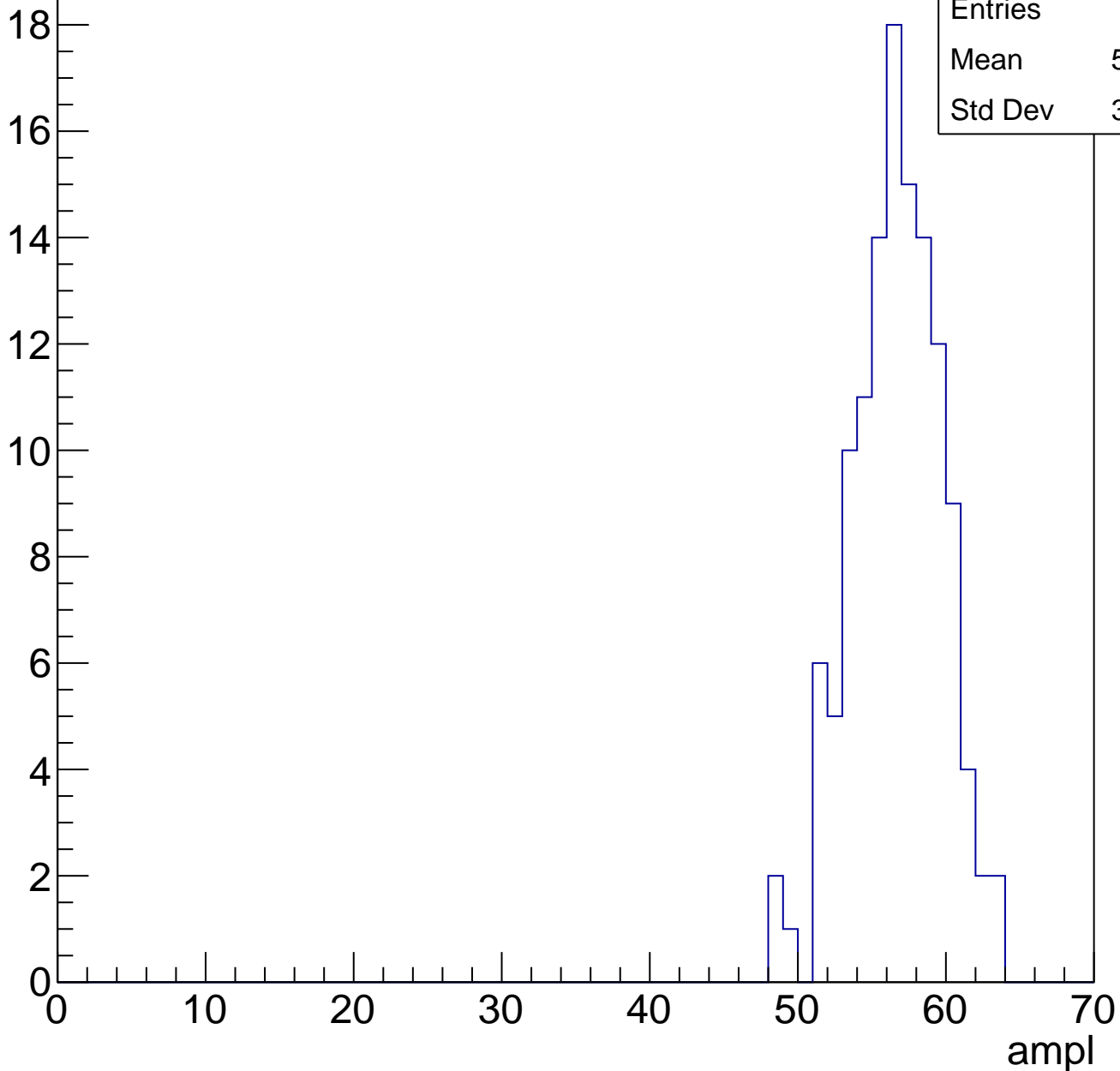
ampl



B1L001S, U24-ch21, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch21, adc5

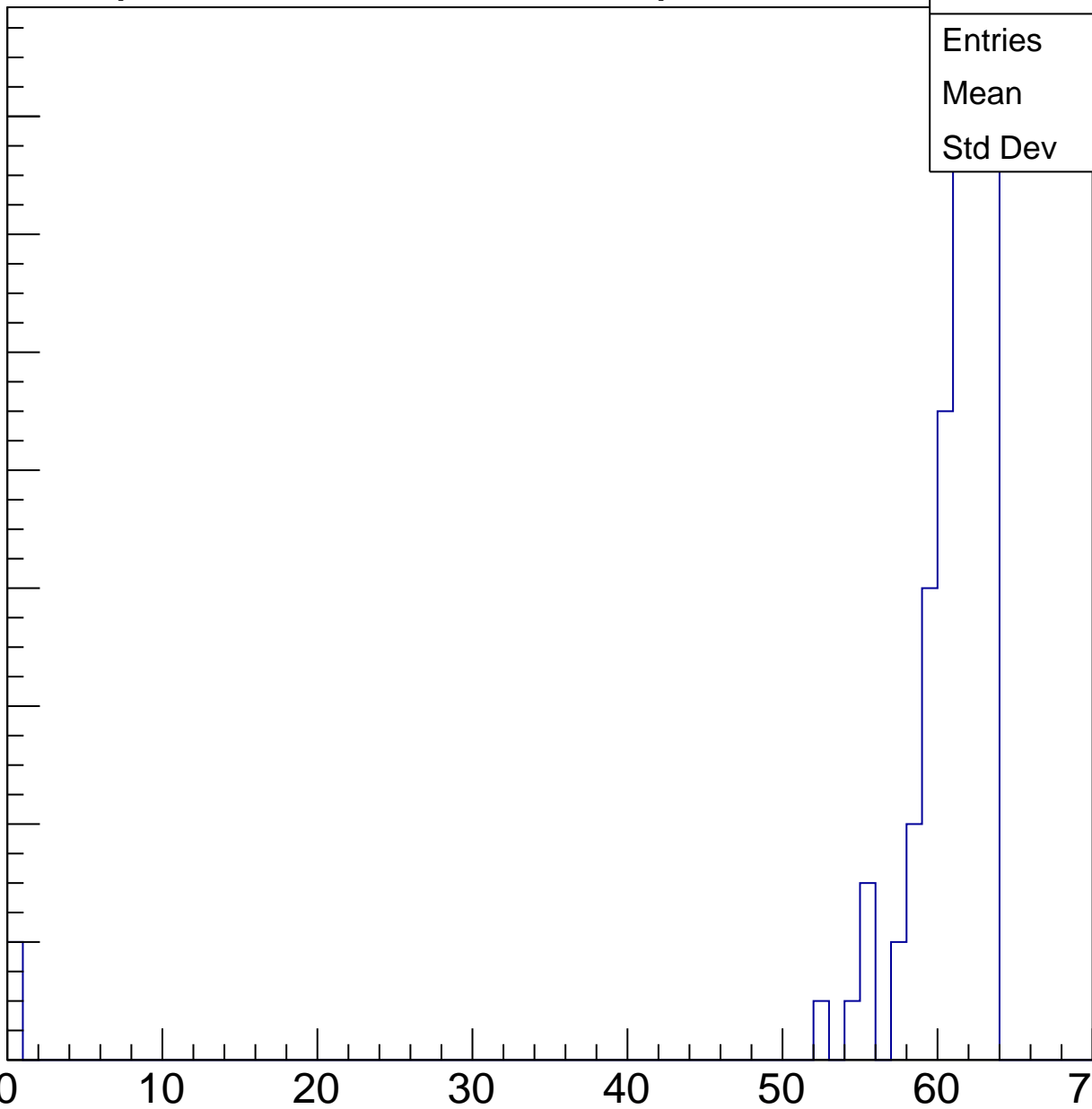
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.1
Std Dev	9.673

ampl



B1L001S, U24-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	29.91
Std Dev	4.937

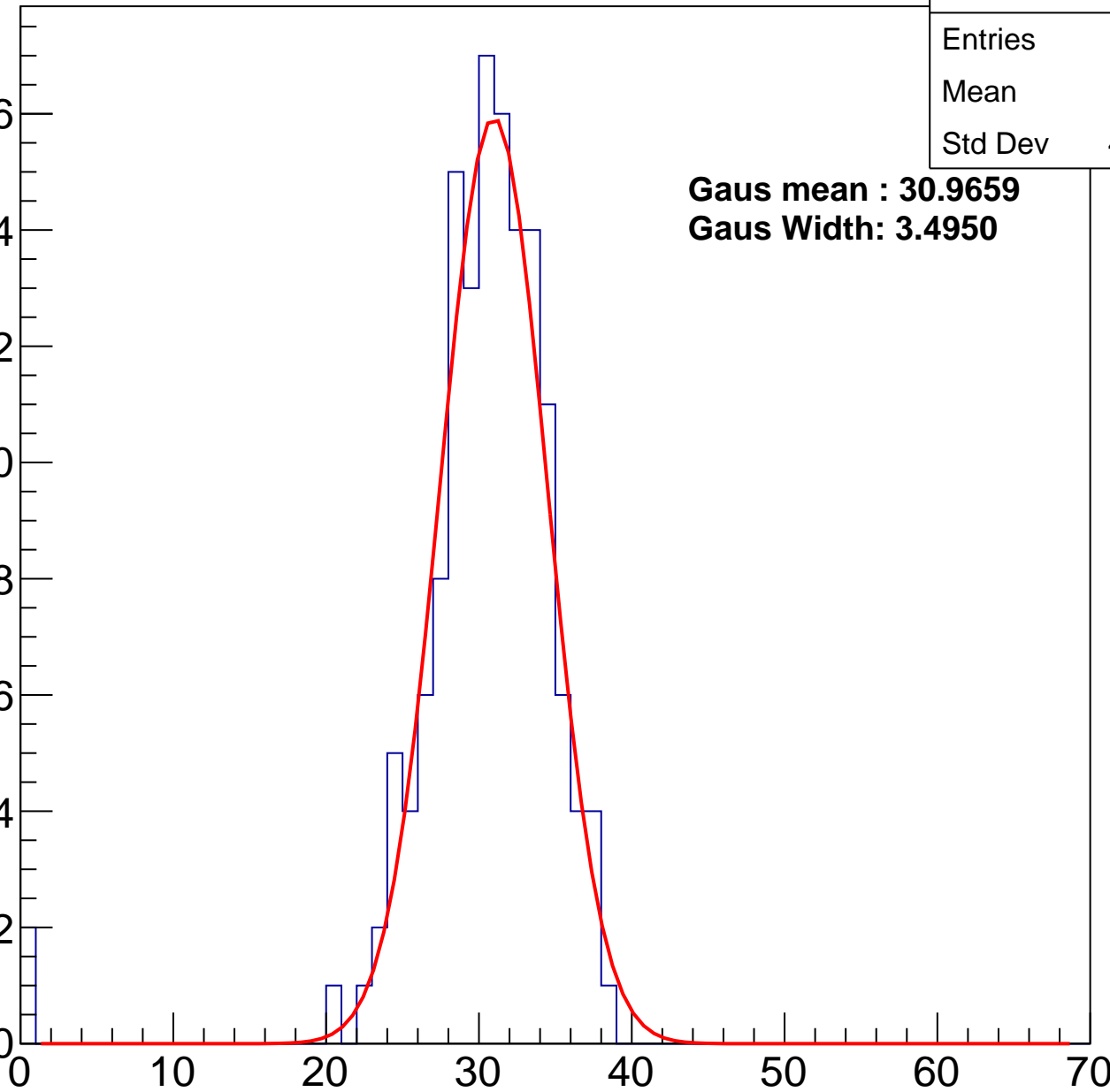
Gaus mean : 30.9659

Gaus Width: 3.4950

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch22, adc1

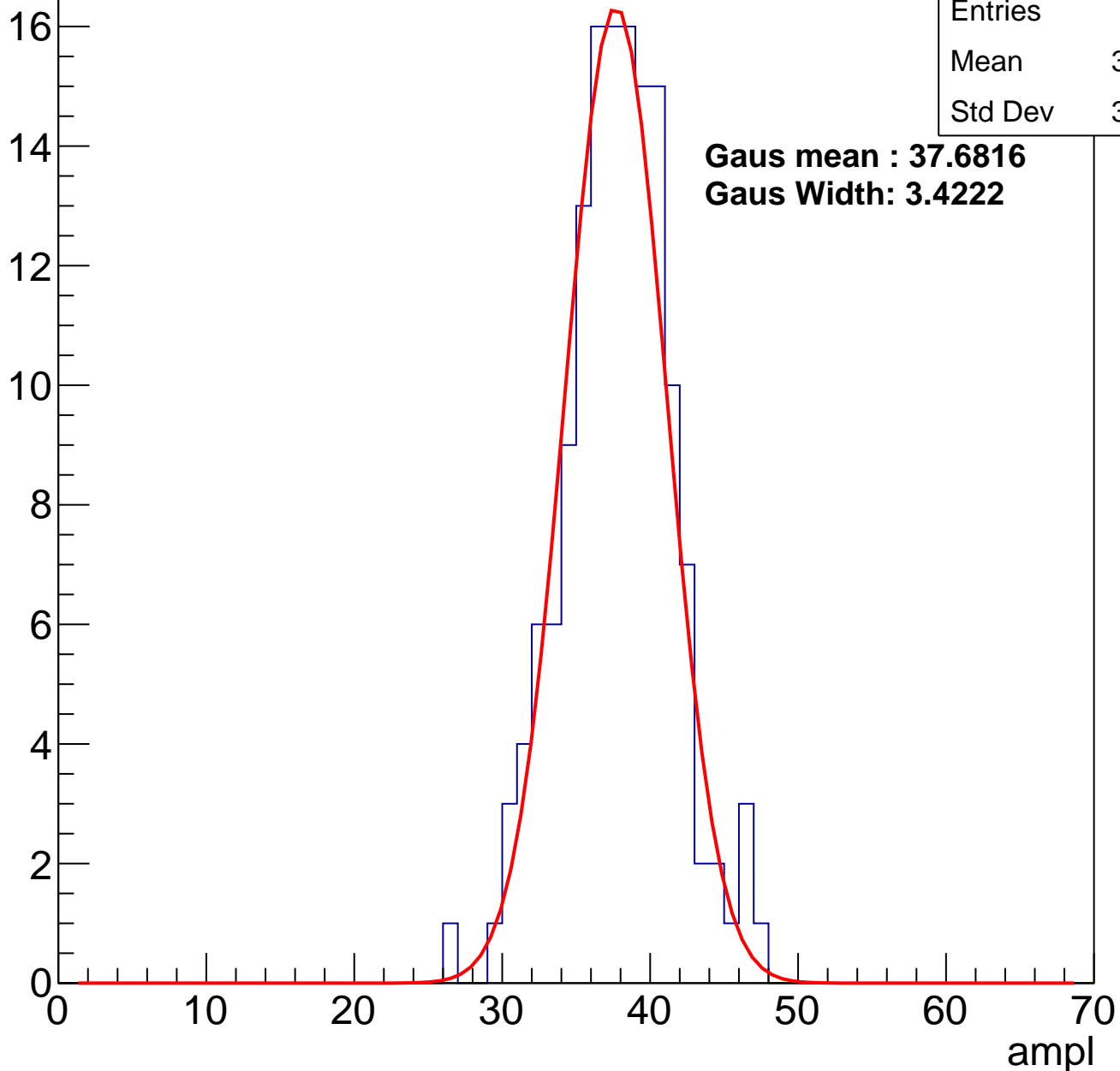
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.34
Std Dev	3.667

Gaus mean : 37.6816

Gaus Width: 3.4222

Entry



B1L001S, U24-ch22, adc2

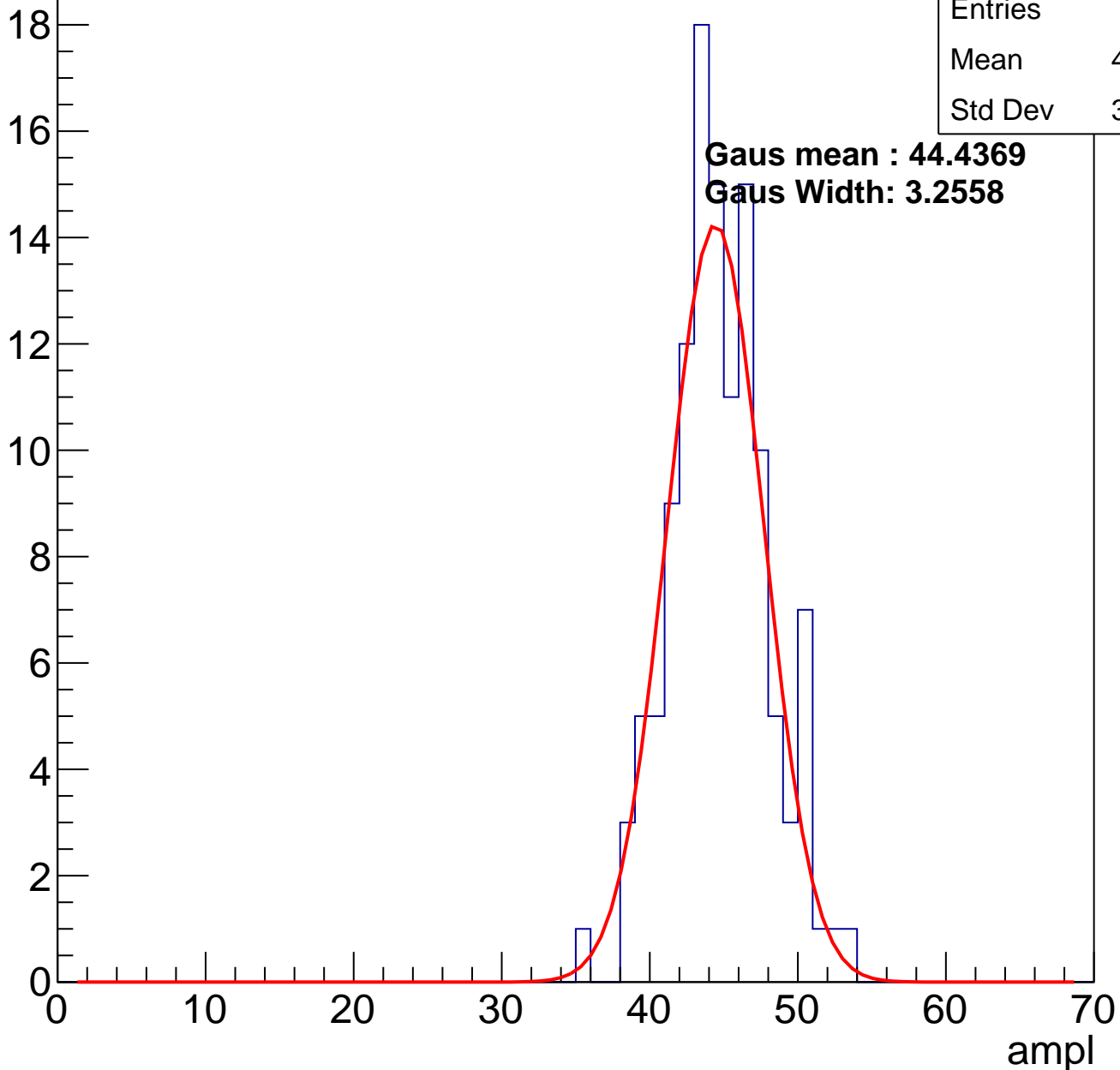
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	44.25
Std Dev	3.258

Gaus mean : 44.4369

Gaus Width: 3.2558

Entry



B1L001S, U24-ch22, adc3

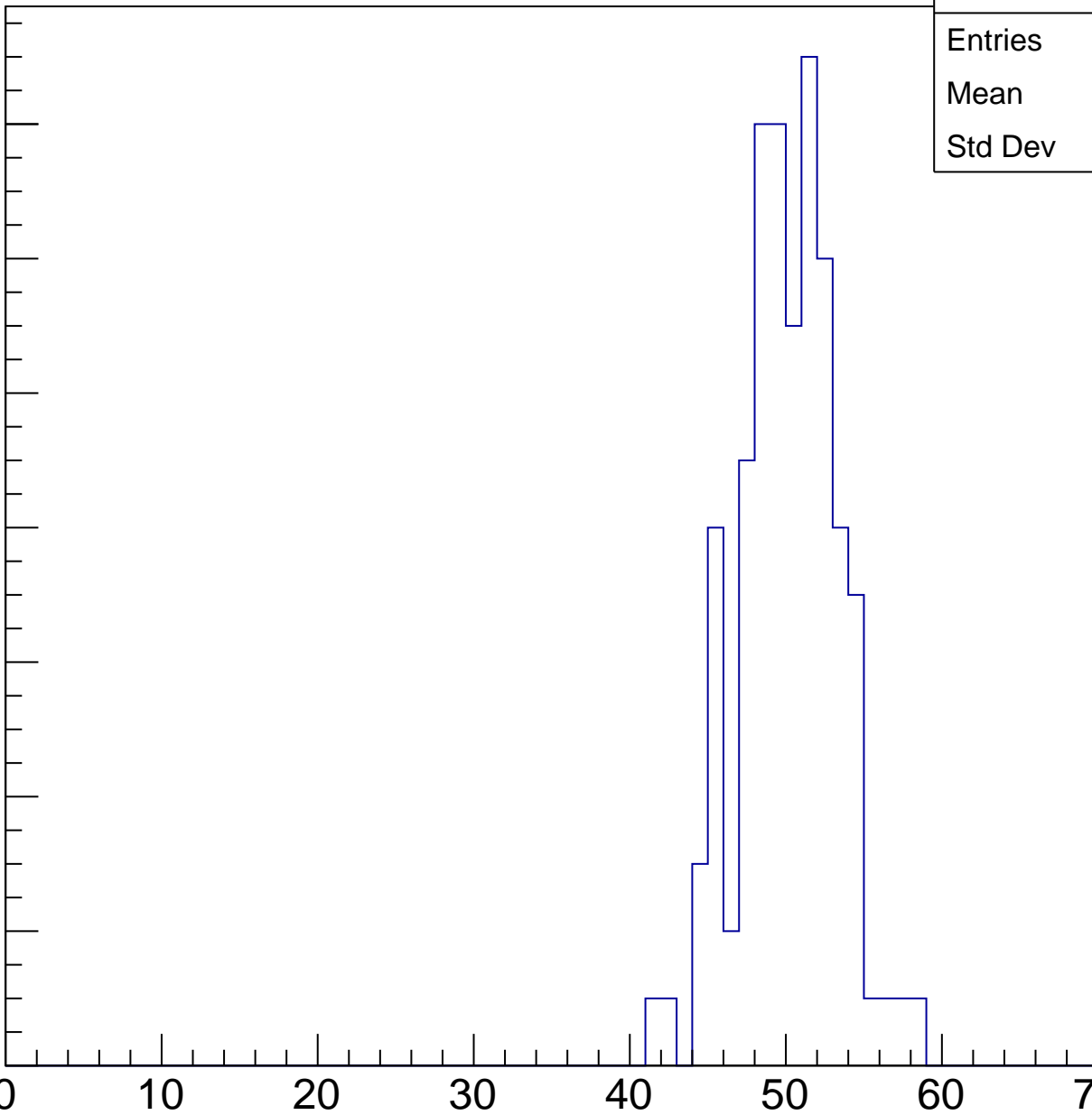
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	49.68
Std Dev	3.097

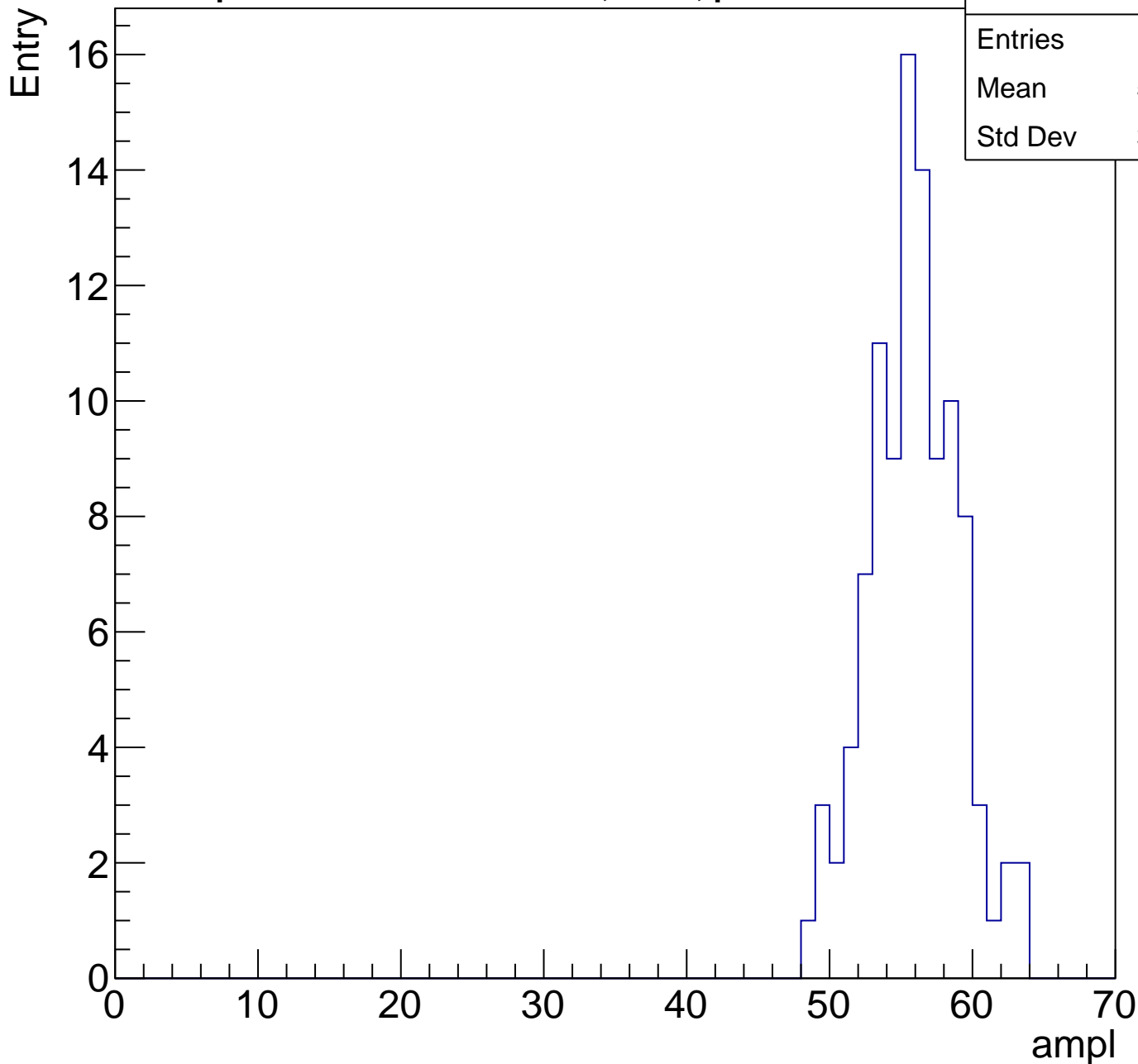
ampl



B1L001S, U24-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

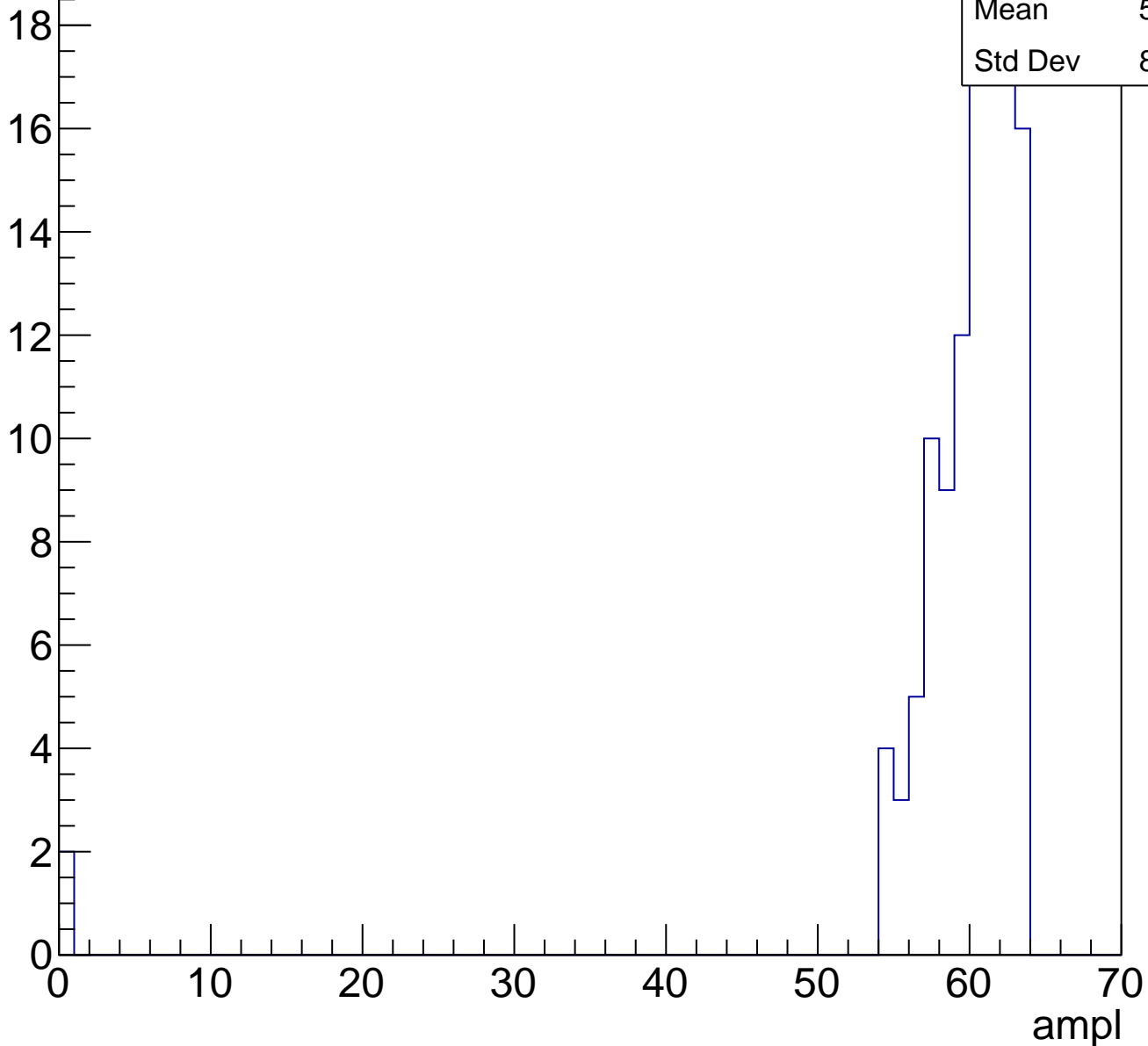
Entries	102
Mean	55.41
Std Dev	3.091



B1L001S, U24-ch22, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch23, adc0

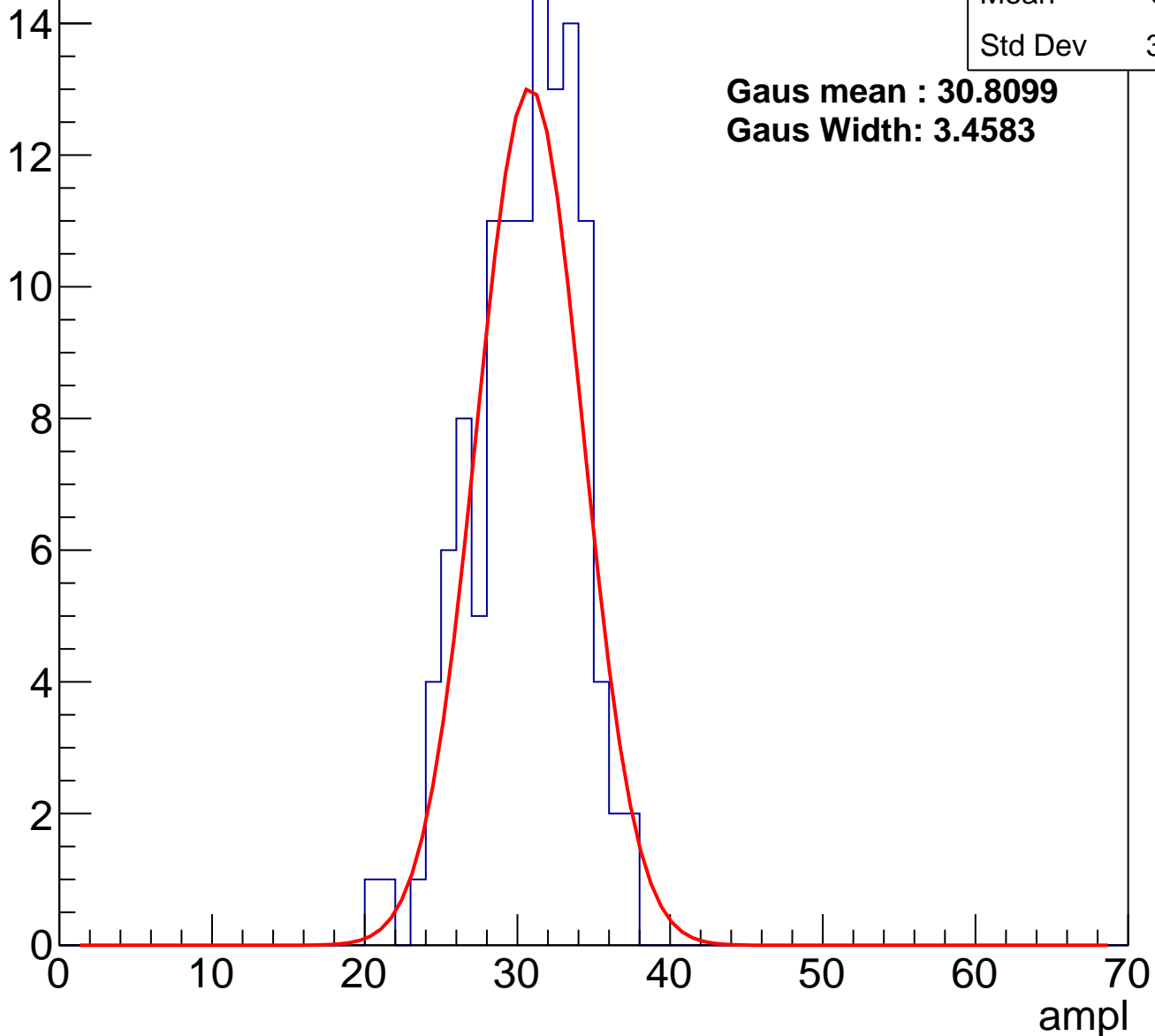
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	30.11
Std Dev	3.403

Gaus mean : 30.8099

Gaus Width: 3.4583

Entry



B1L001S, U24-ch23, adc1

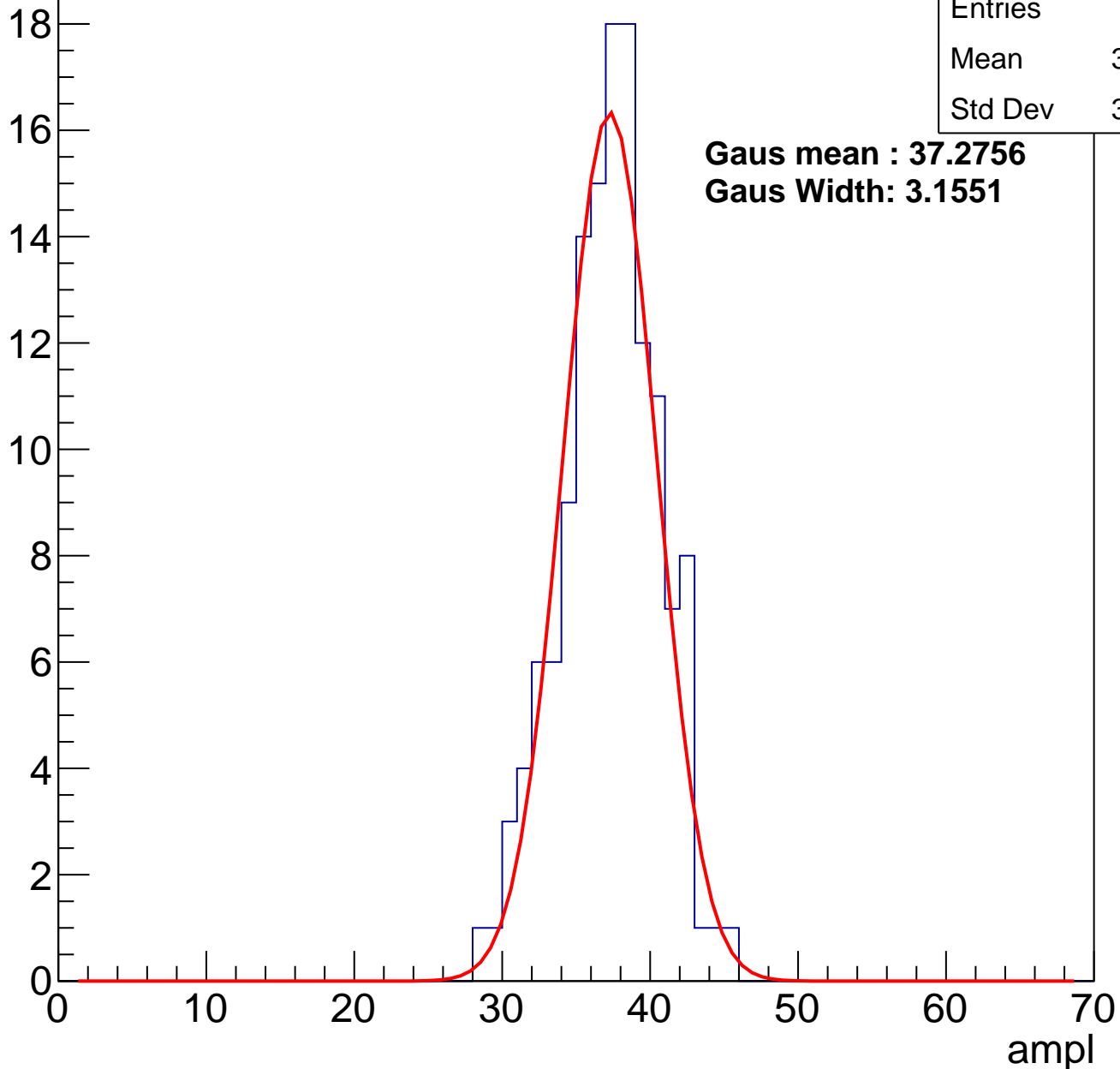
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.84
Std Dev	3.272

Gaus mean : 37.2756

Gaus Width: 3.1551

Entry



B1L001S, U24-ch23, adc2

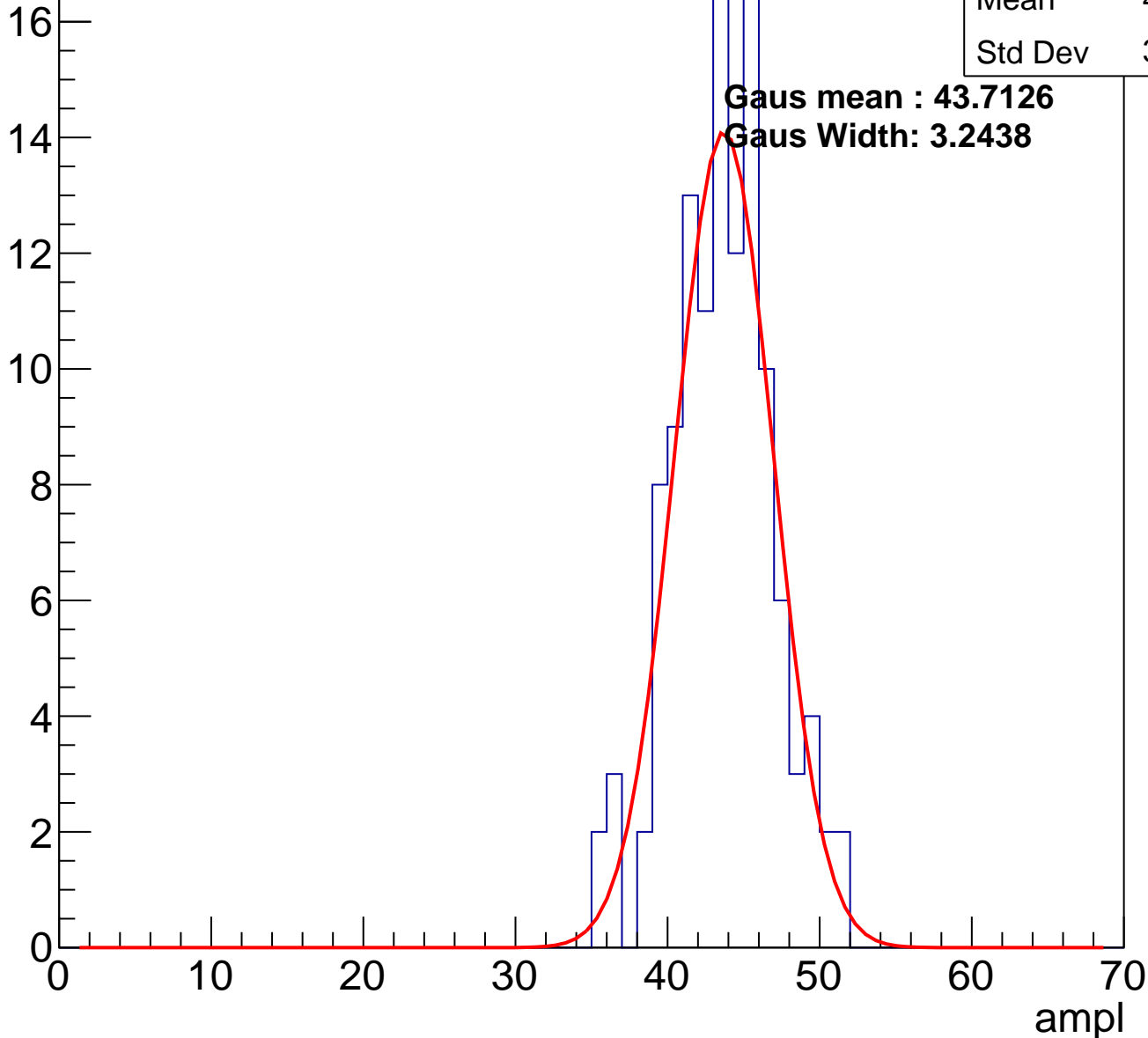
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	121
Mean	43.21
Std Dev	3.281

Gaus mean : 43.7126

Gaus Width: 3.2438



B1L001S, U24-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	49.9
Std Dev	3.278

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

12

14

16

18

20

22

24

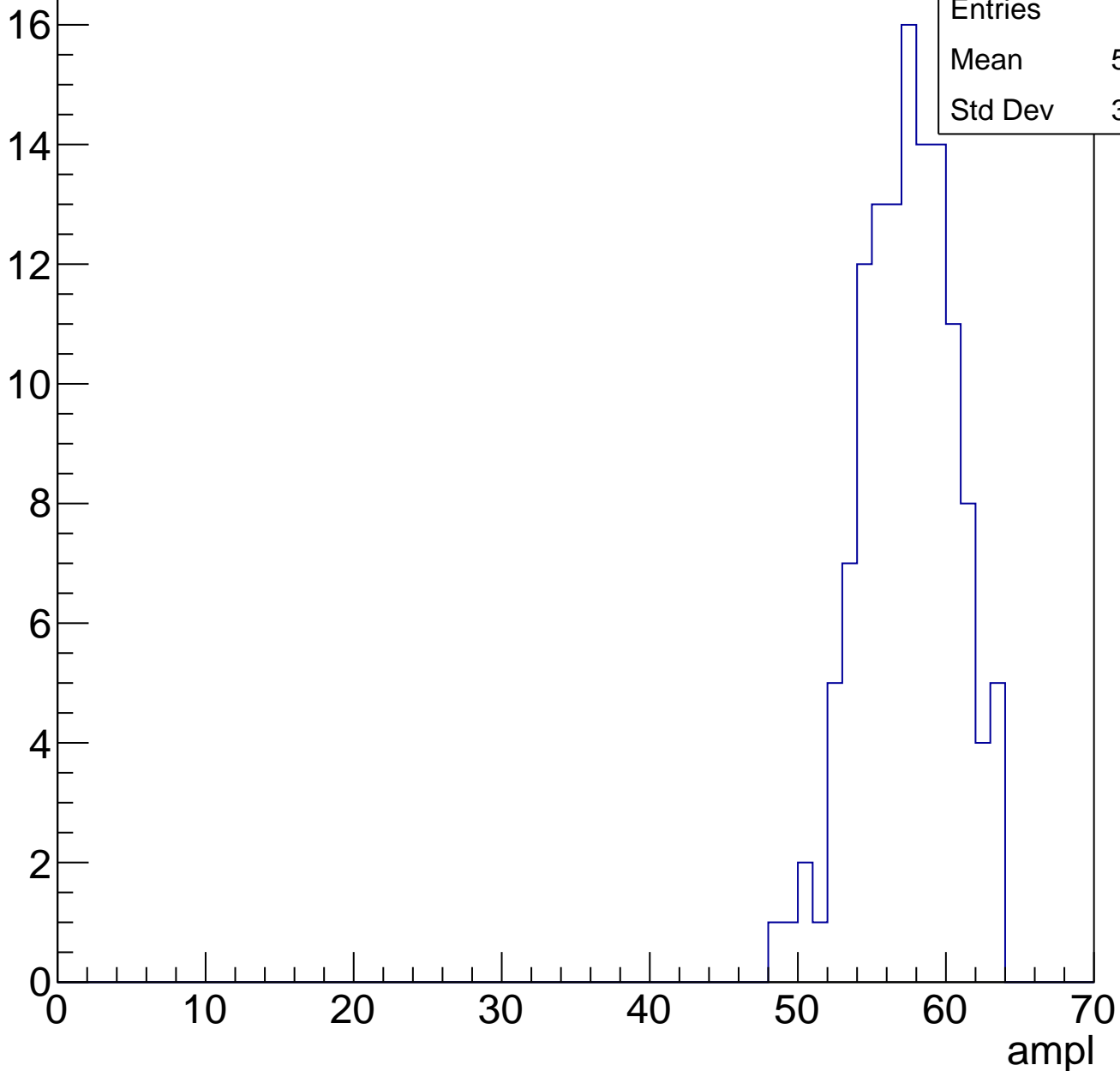
26

28

B1L001S, U24-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch23, adc5

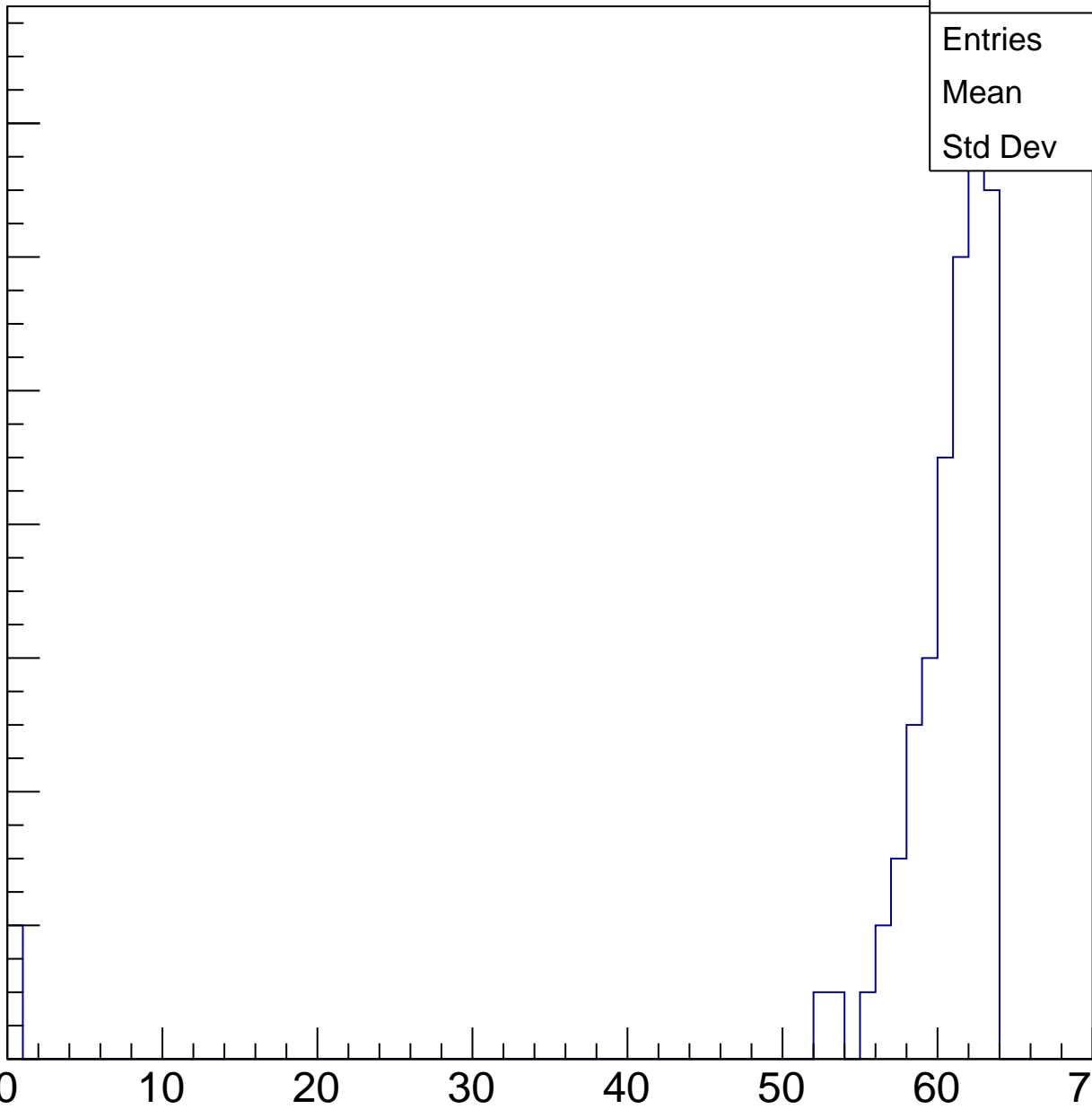
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.69
Std Dev	10.34

ampl



B1L001S, U24-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch24, adc0

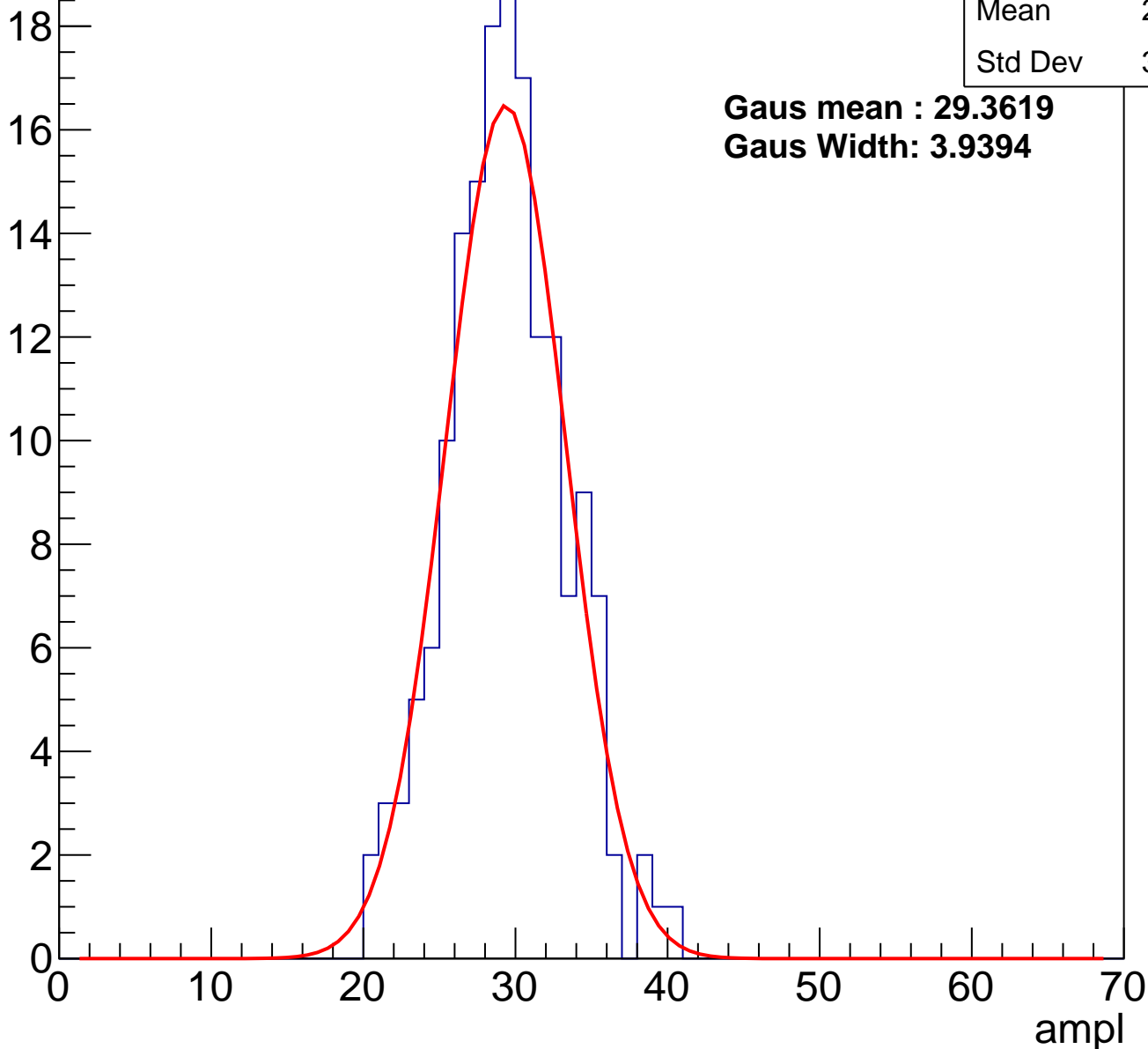
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	28.95
Std Dev	3.844

Gaus mean : 29.3619

Gaus Width: 3.9394

Entry

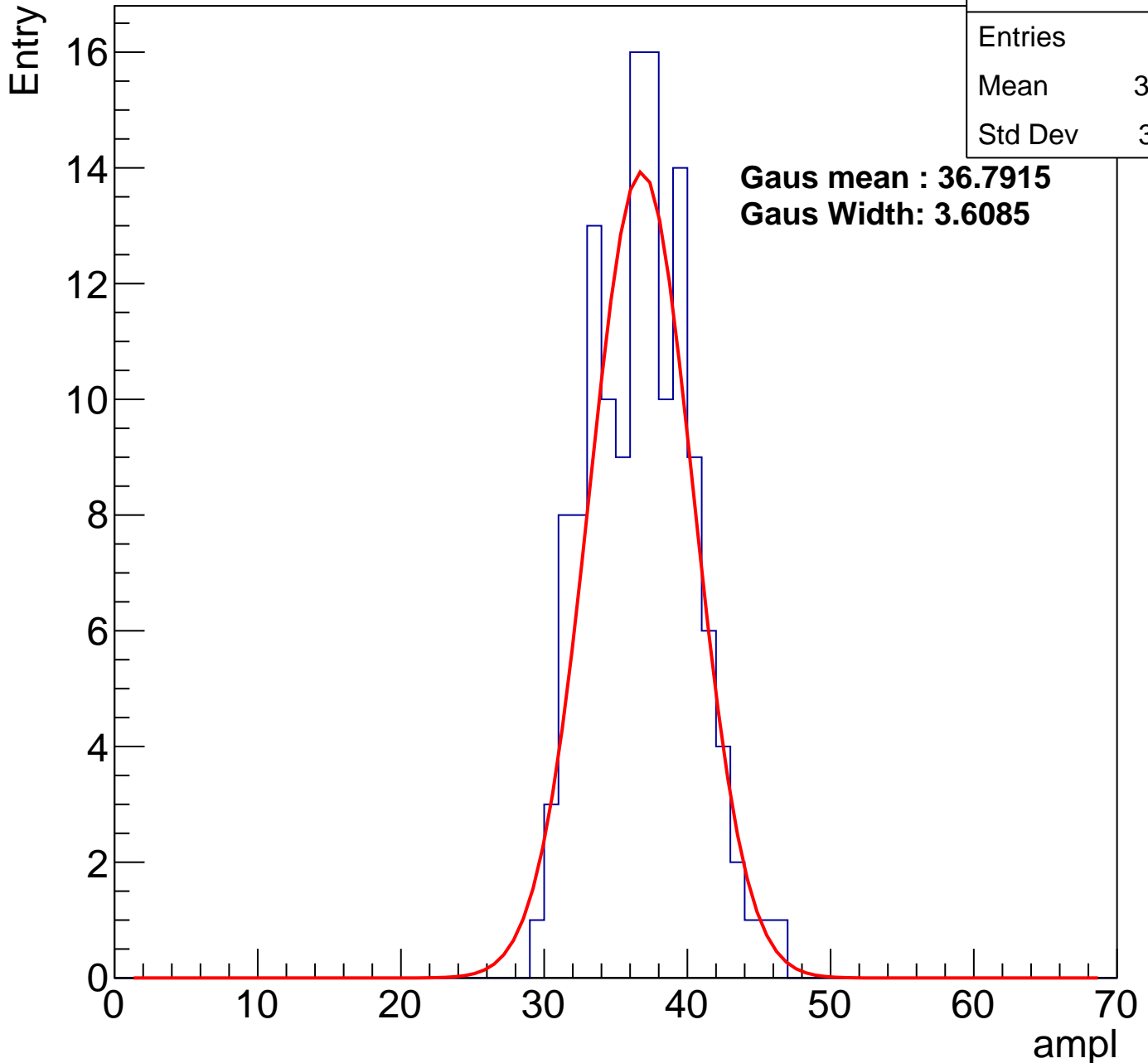


B1L001S, U24-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.33
Std Dev	3.481

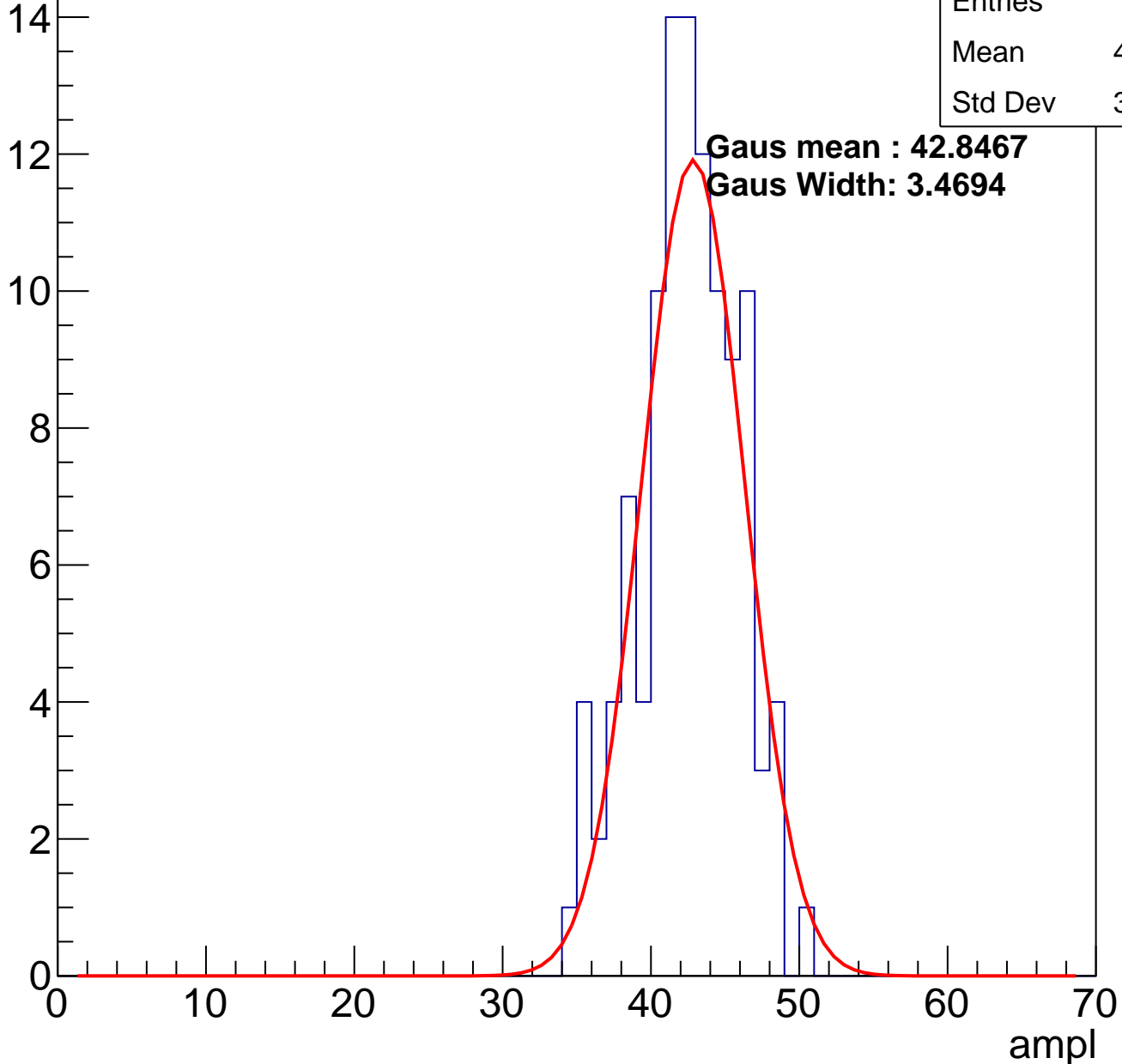
Gaus mean : 36.7915
Gaus Width: 3.6085



B1L001S, U24-ch24, adc2

calib_packv5_042523_0143.root, FC#2, port C2

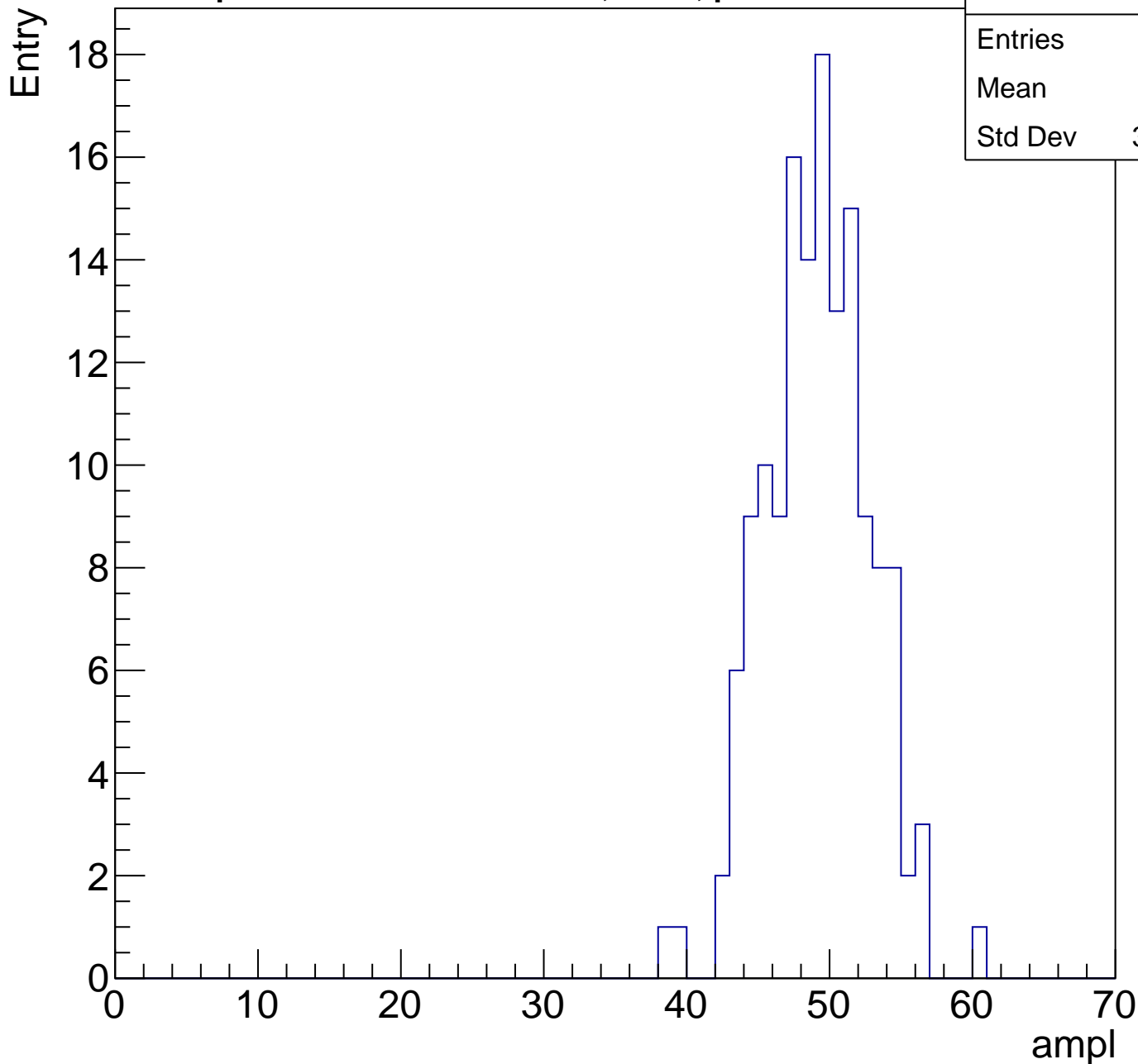
Entry



B1L001S, U24-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

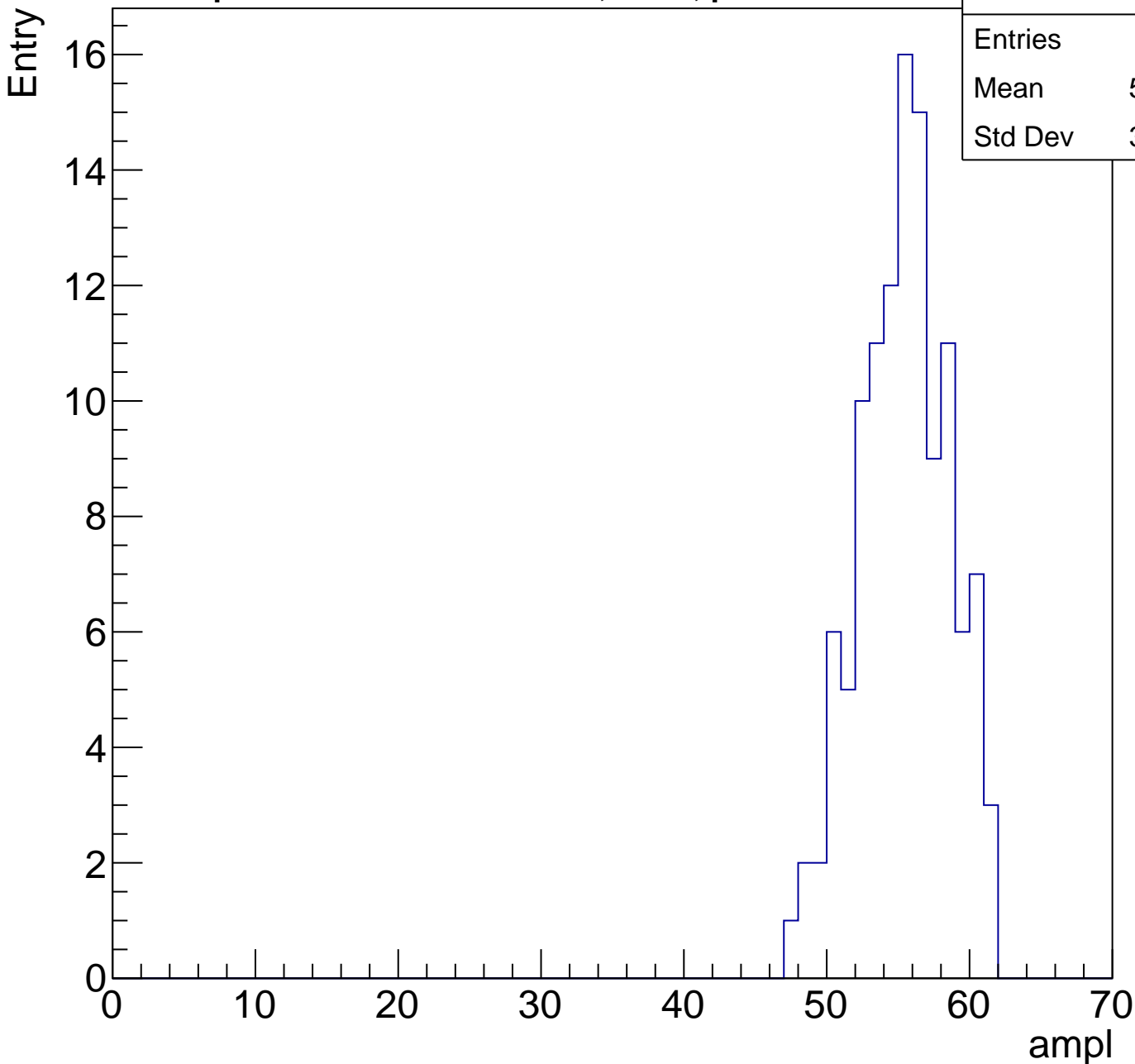
Entries	145
Mean	48.7
Std Dev	3.609



B1L001S, U24-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	54.96
Std Dev	3.133



B1L001S, U24-ch24, adc5

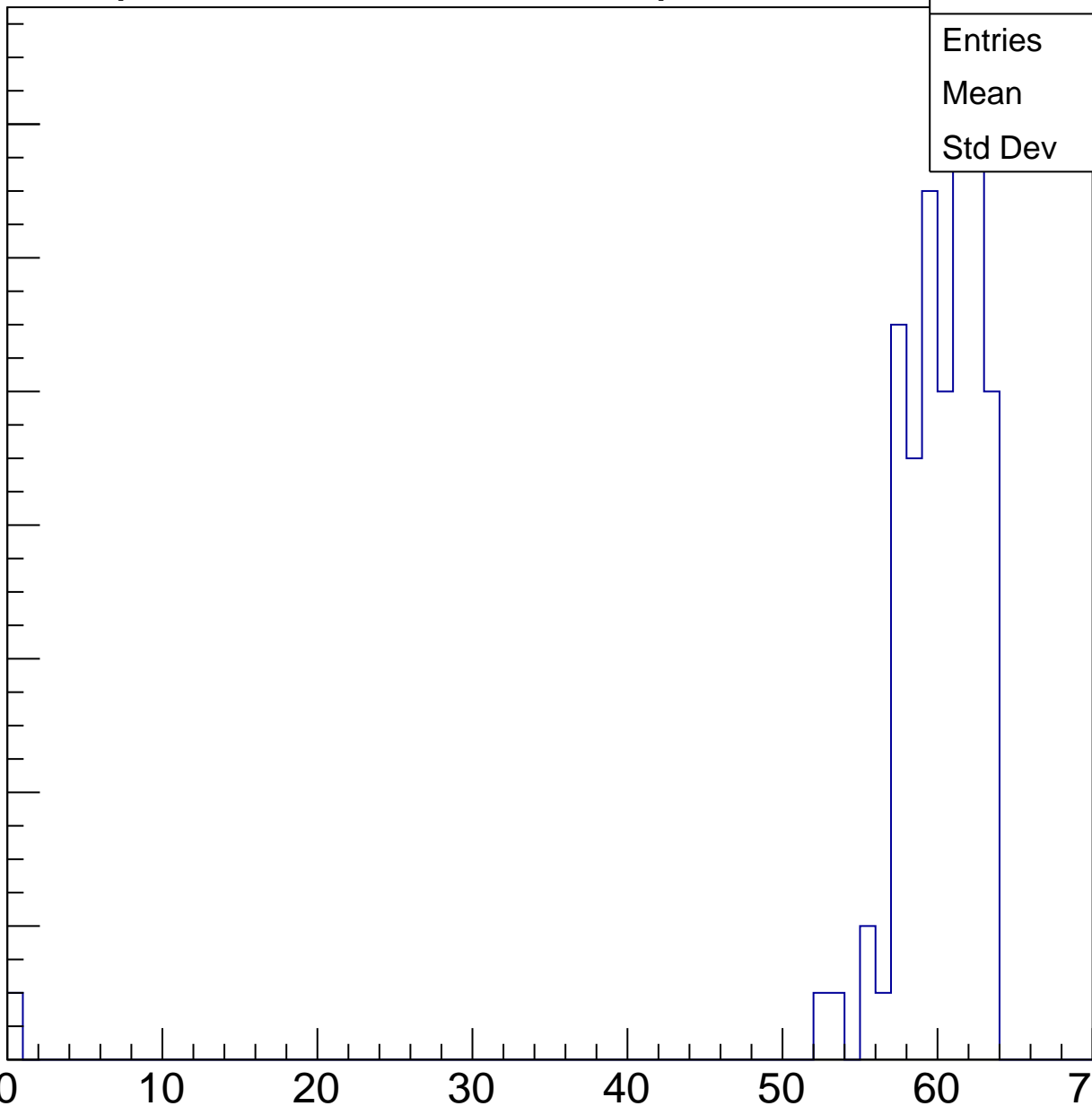
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	88
Mean	59.09
Std Dev	6.755

ampl

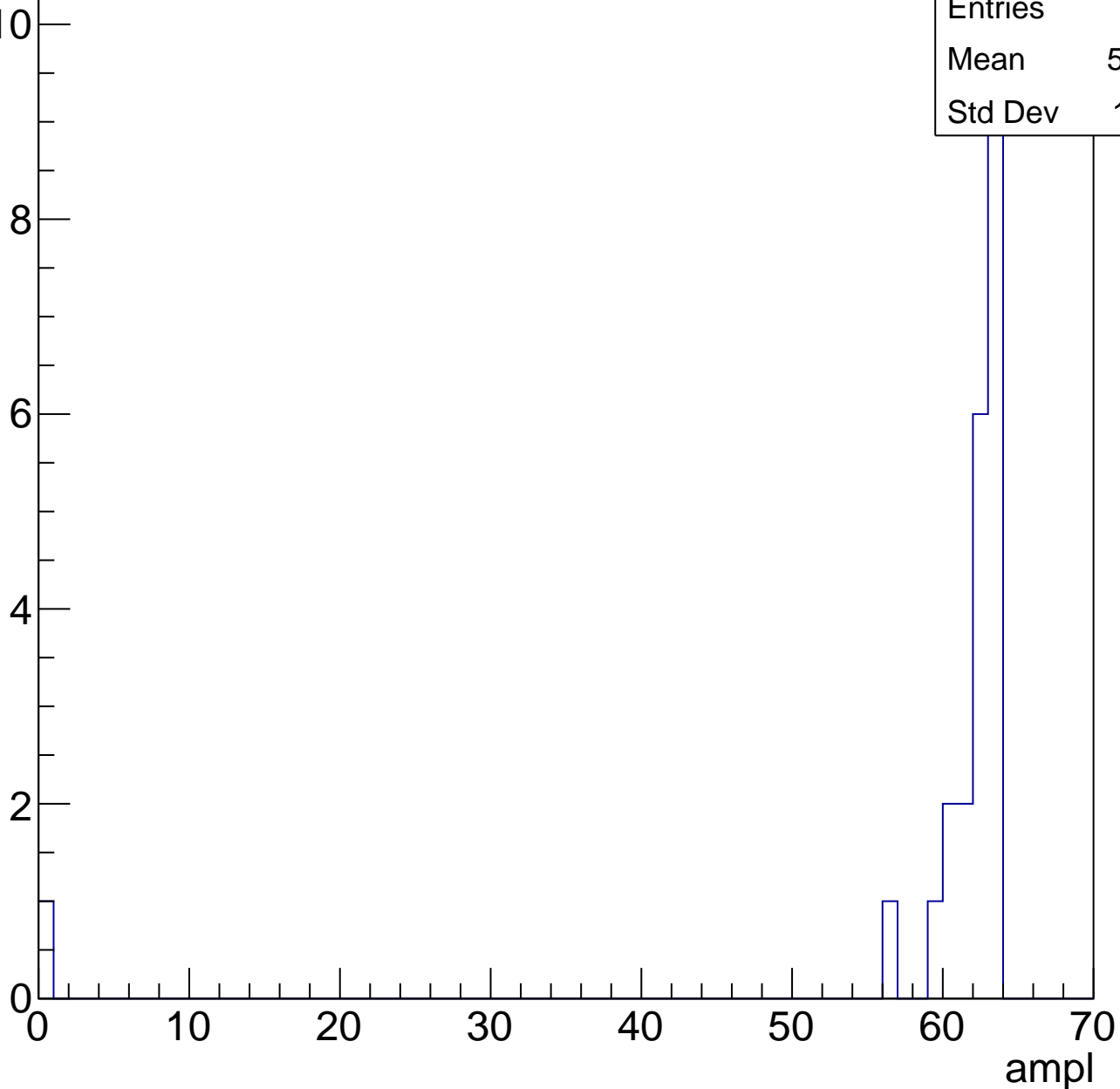


B1L001S, U24-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	59.09
Std Dev	12.71



B1L001S, U24-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch25, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.91
Std Dev	3.239

Gaus mean : 30.2695

Gaus Width: 3.3973

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

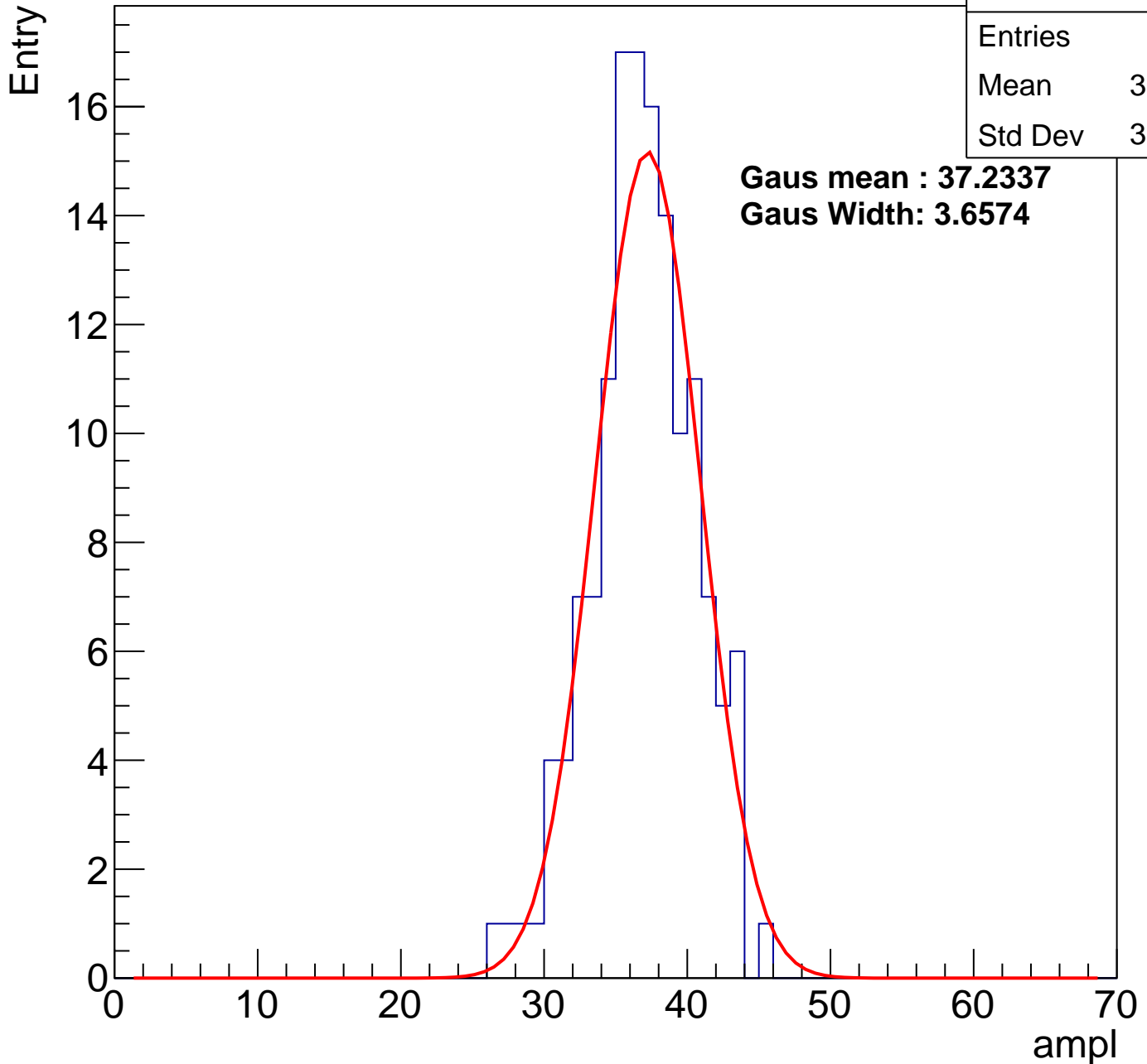
B1L001S, U24-ch25, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	36.48
Std Dev	3.582

Gaus mean : 37.2337

Gaus Width: 3.6574



B1L001S, U24-ch25, adc2

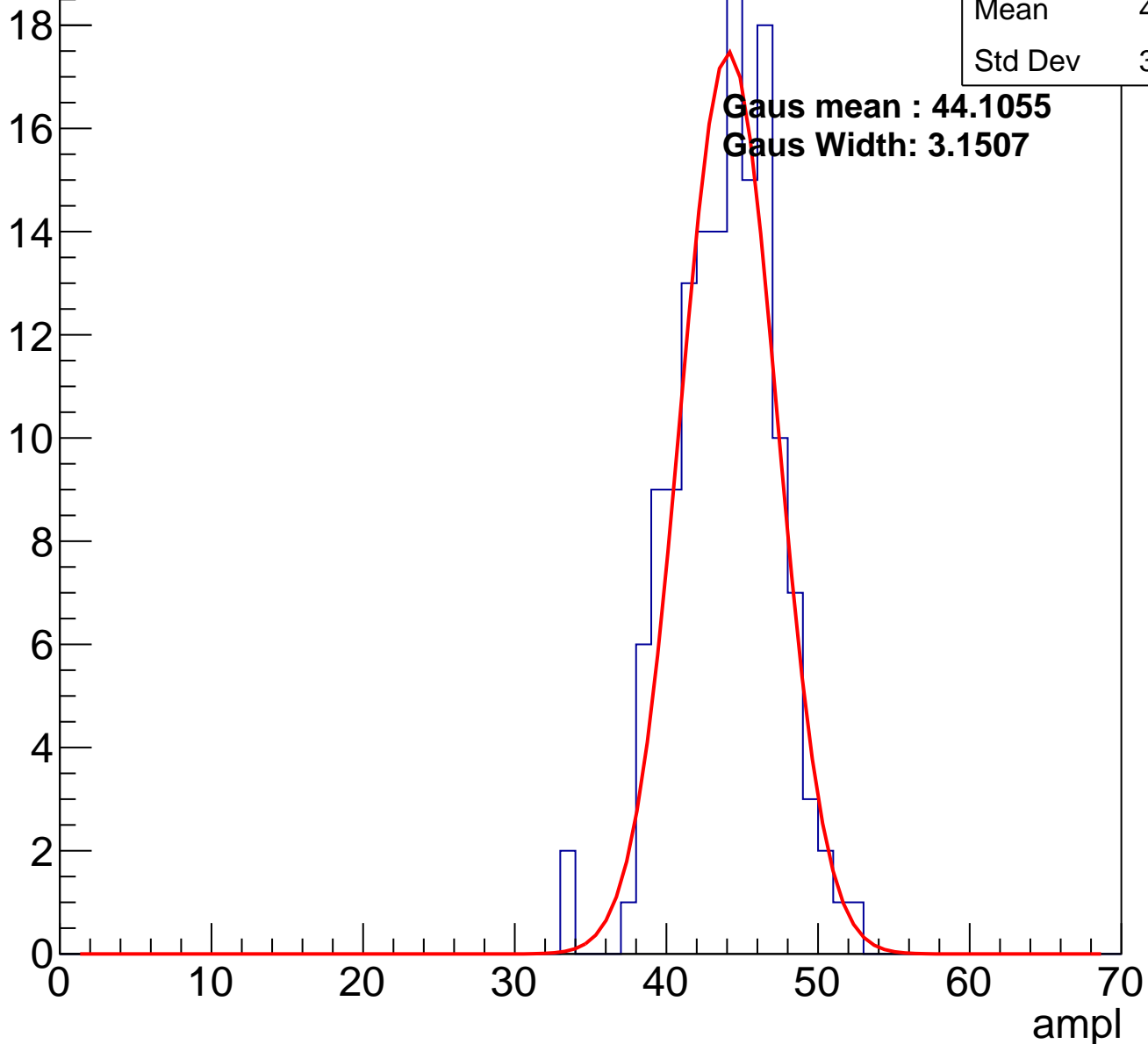
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	43.47
Std Dev	3.304

Gaus mean : 44.1055

Gaus Width: 3.1507

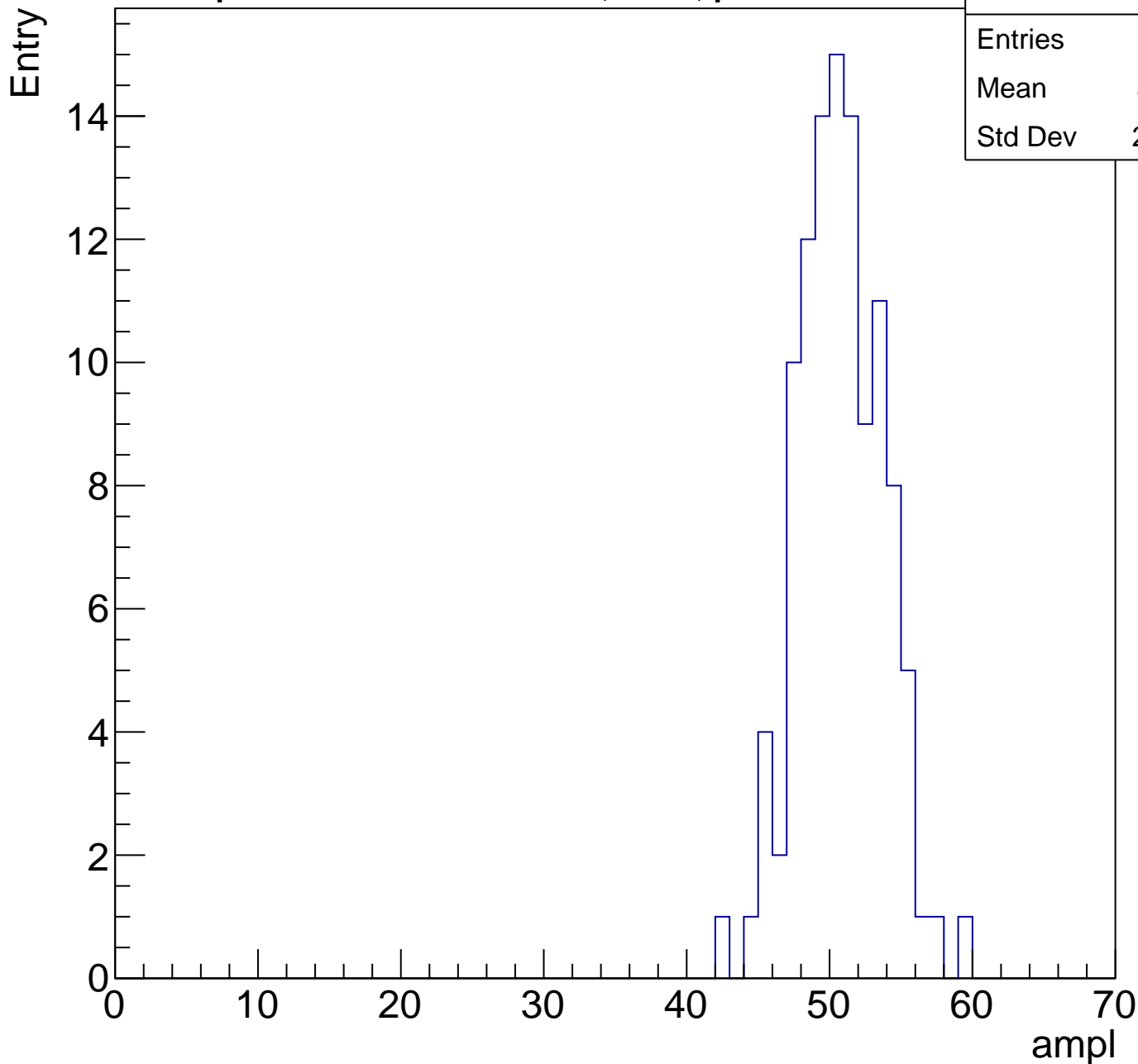
Entry



B1L001S, U24-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	50.31
Std Dev	2.939

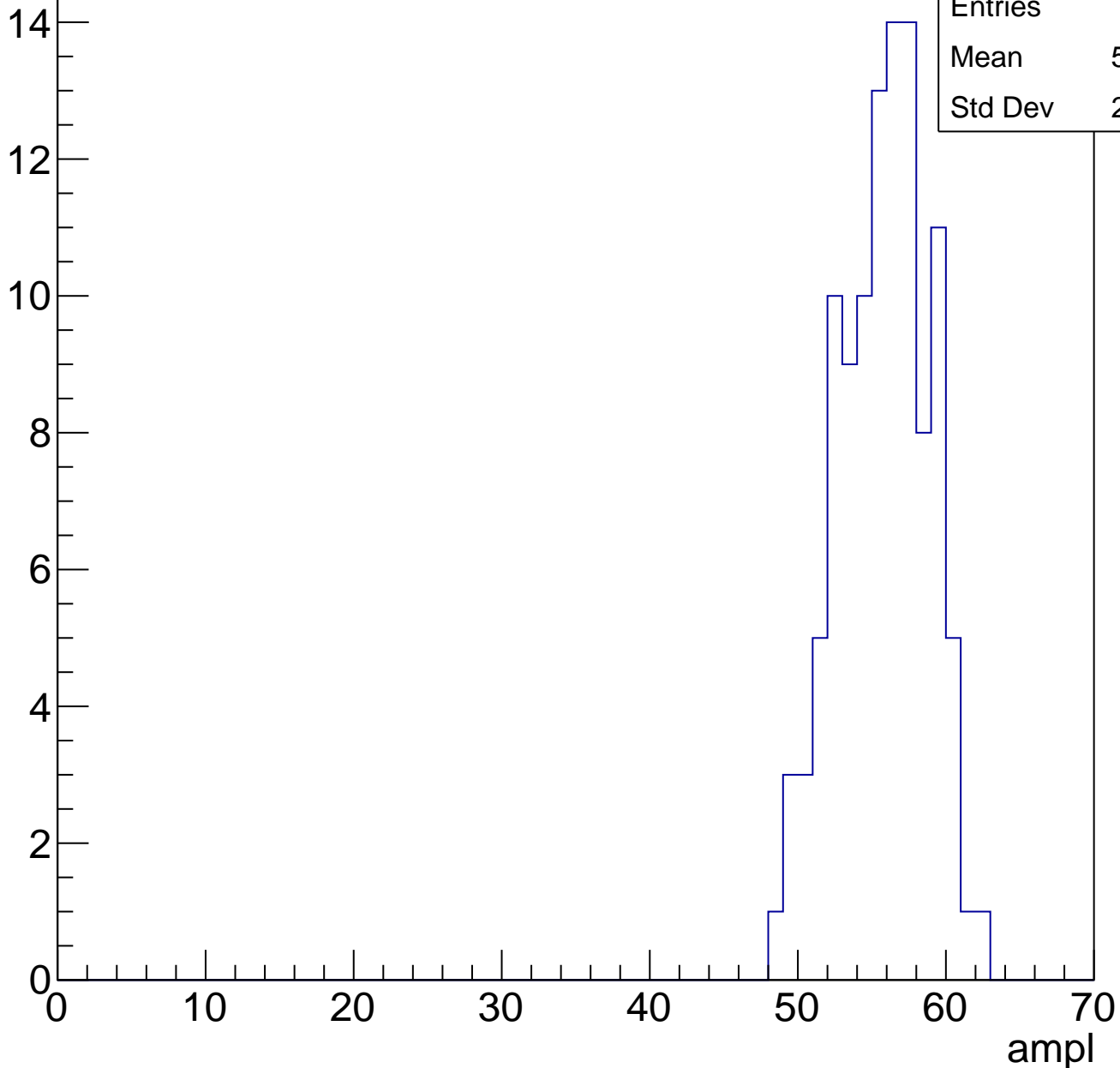


B1L001S, U24-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	55.28
Std Dev	2.993

Entry



B1L001S, U24-ch25, adc5

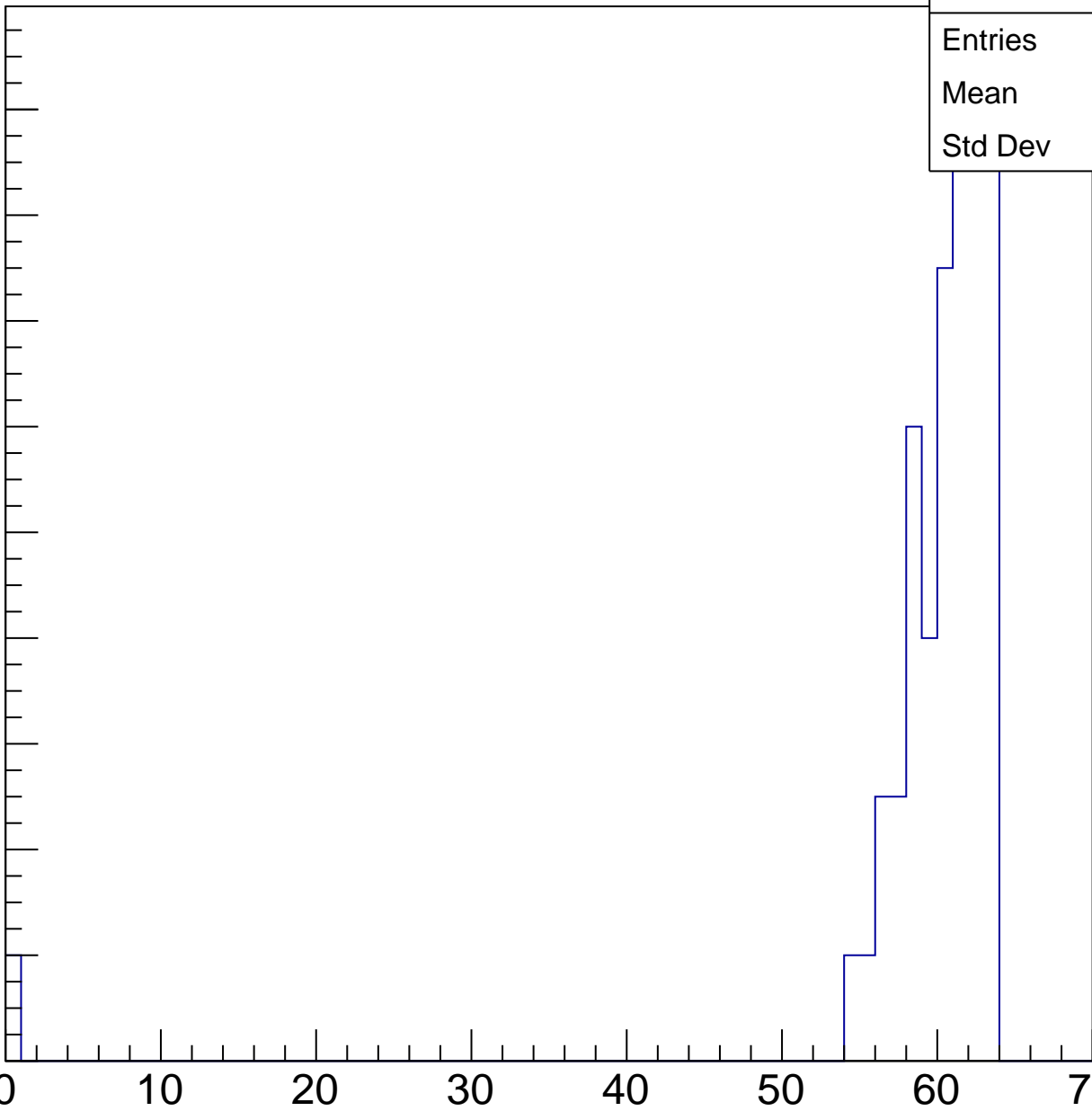
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	105
Mean	59.02
Std Dev	8.533

ampl



B1L001S, U24-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

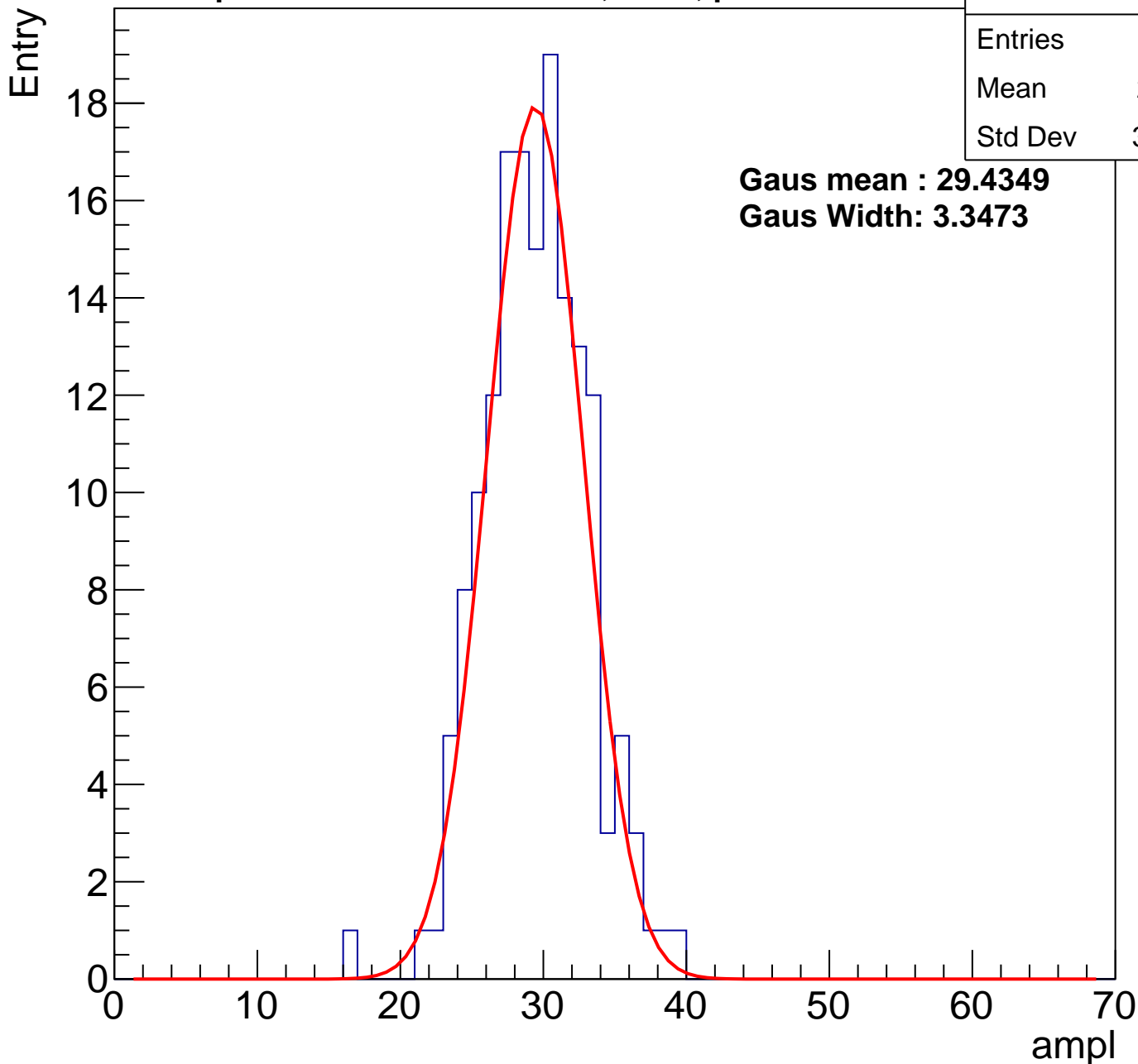
B1L001S, U24-ch26, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	29.01
Std Dev	3.587

Gaus mean : 29.4349

Gaus Width: 3.3473



B1L001S, U24-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.05
Std Dev	3.525

Gaus mean : 36.7120

Gaus Width: 3.3605

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

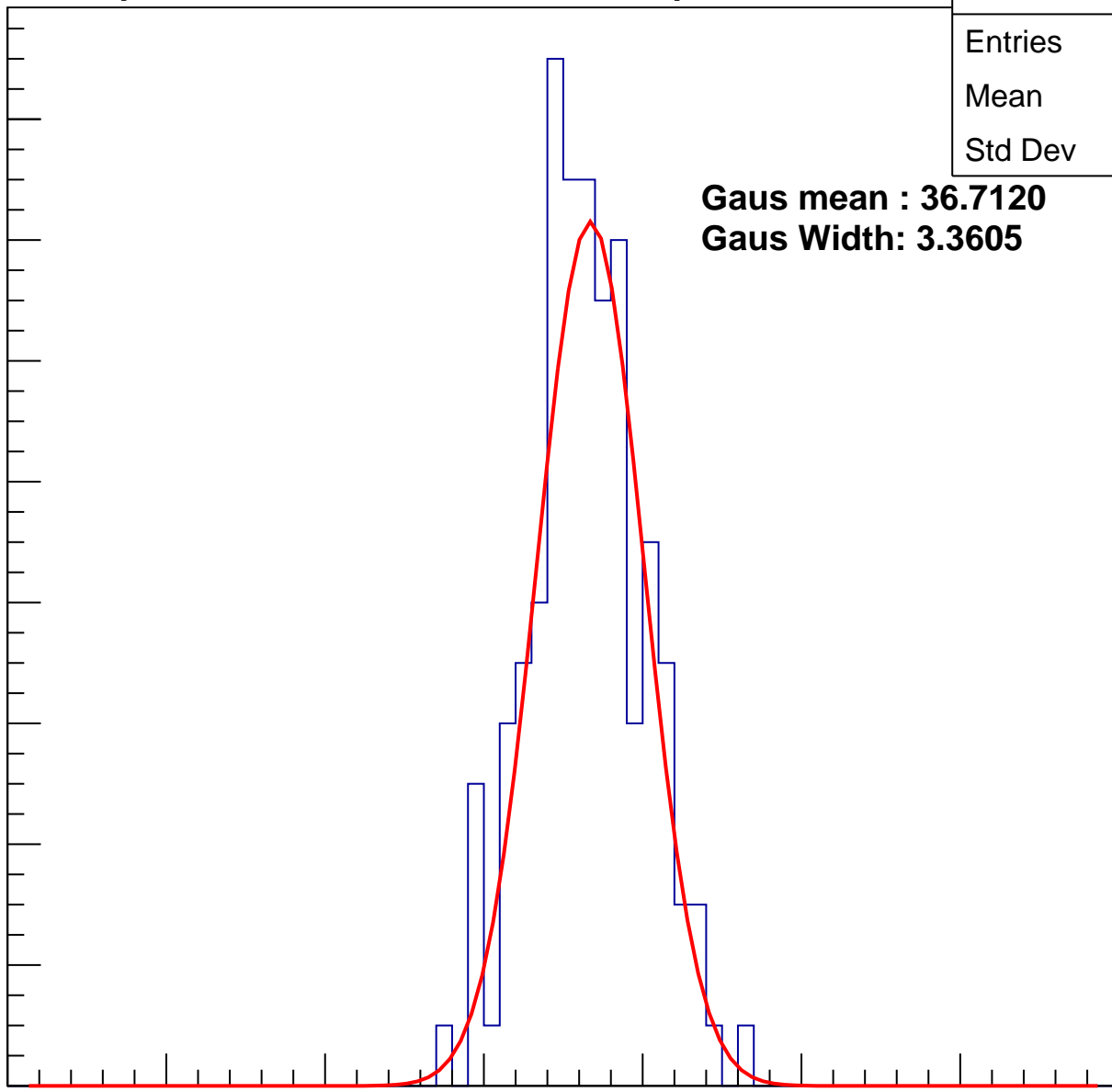
40

50

60

70

ampl



B1L001S, U24-ch26, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	42.19
Std Dev	2.941

Gaus mean : 42.9056

Gaus Width: 3.1491

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

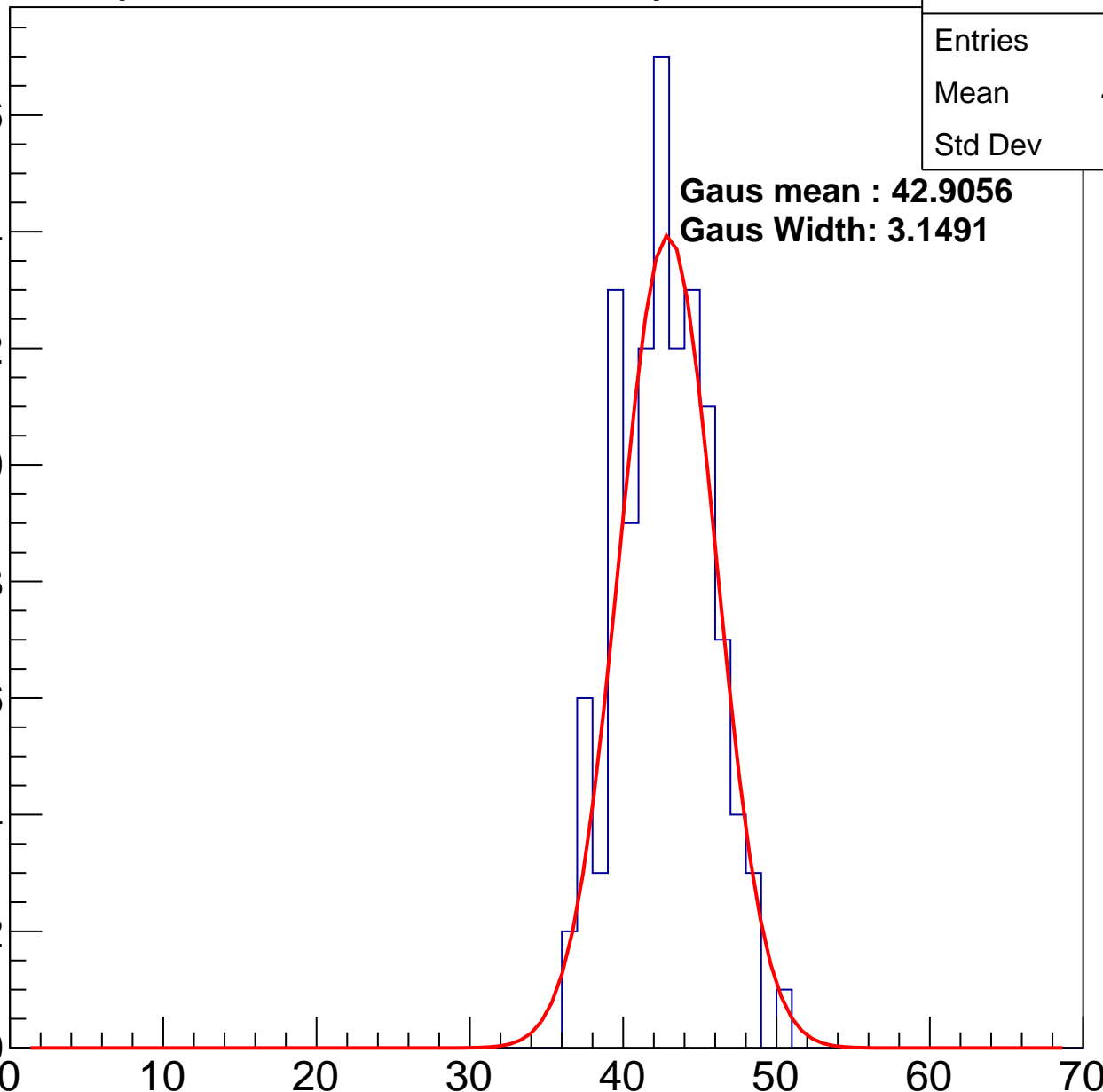
40

50

60

70

ampl



B1L001S, U24-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	48.17
Std Dev	3.283

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

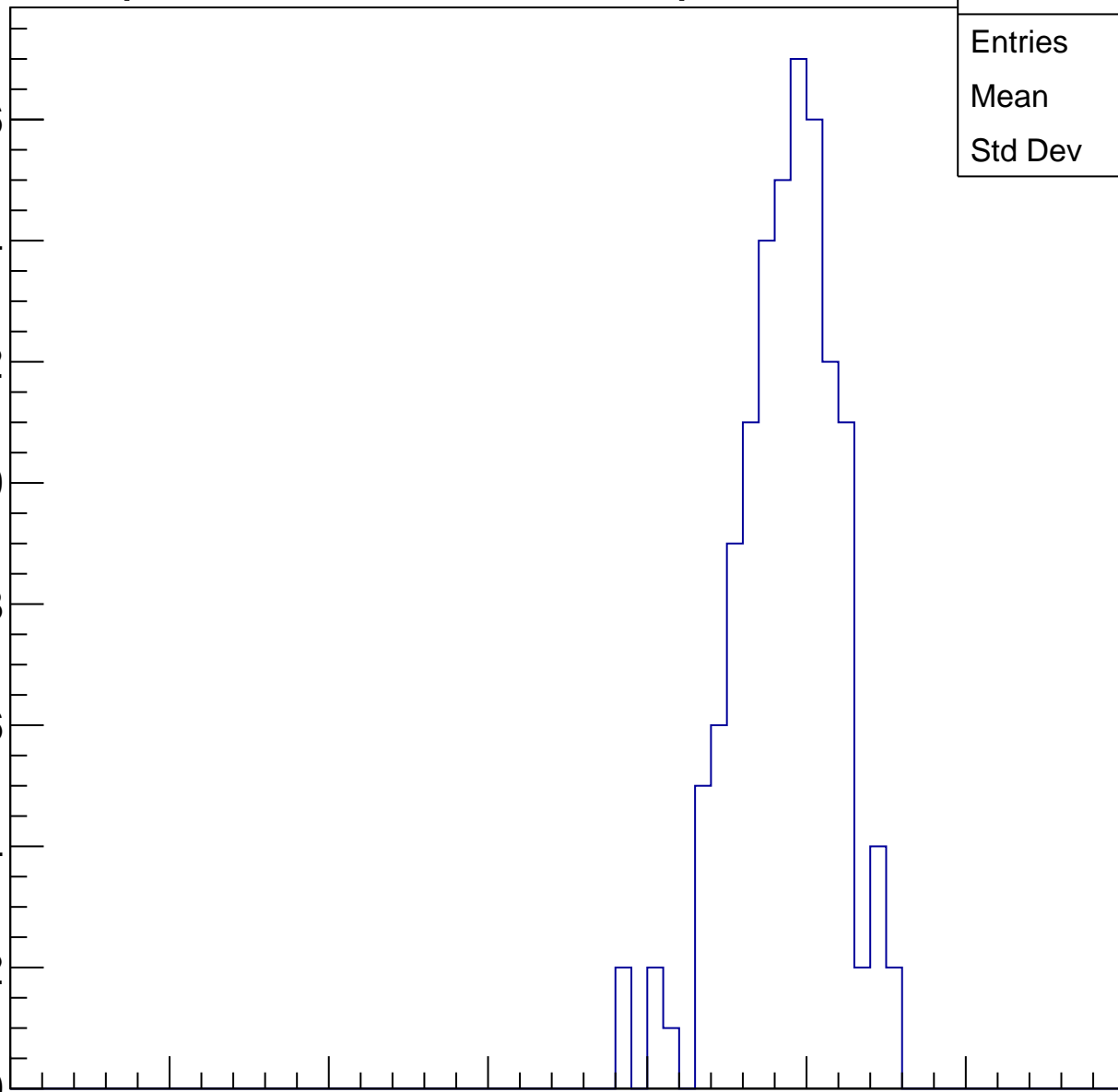
40

50

60

70

ampl



B1L001S, U24-ch26, adc4

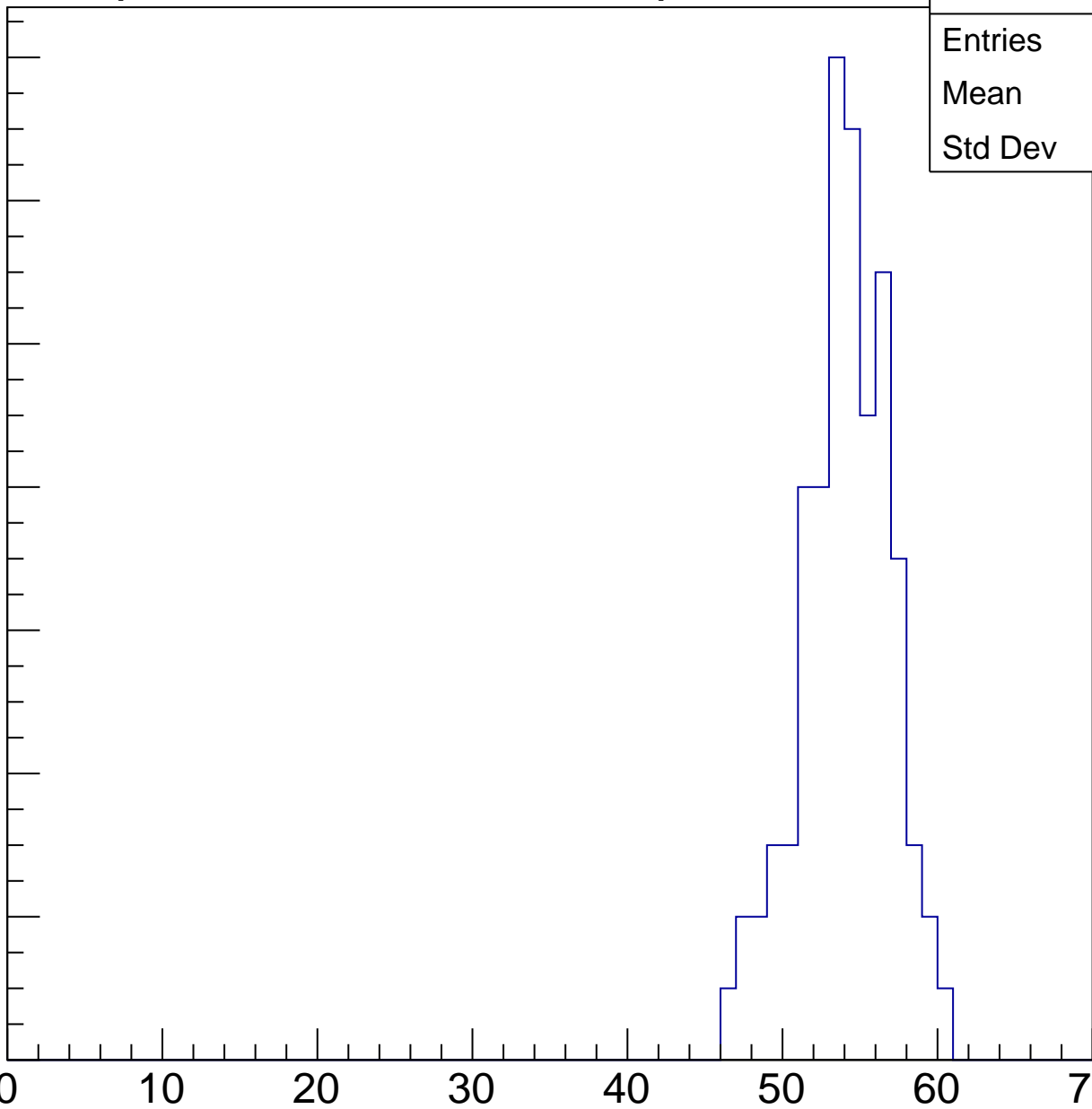
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	53.6
Std Dev	2.859

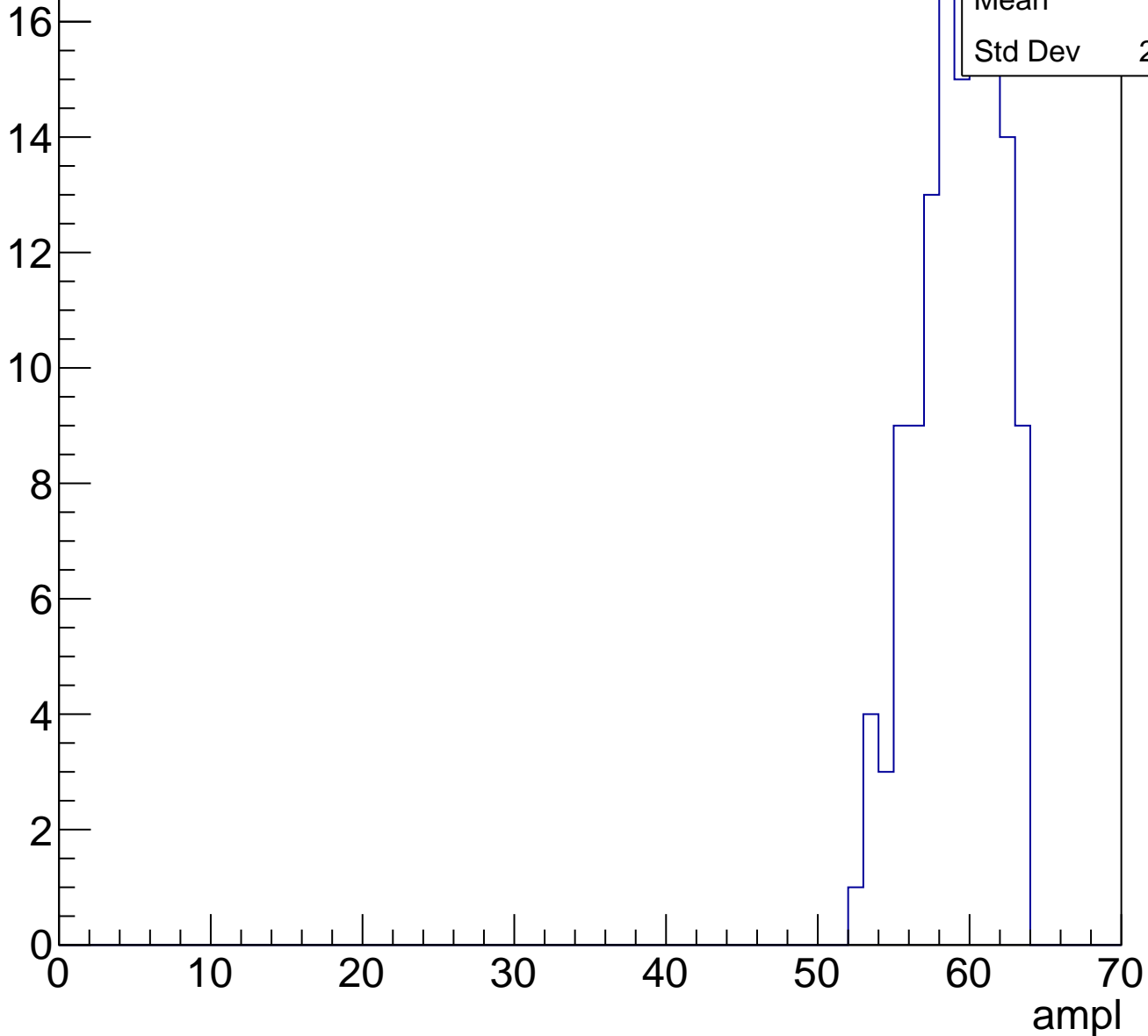
ampl



B1L001S, U24-ch26, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	127
Mean	58.8
Std Dev	2.674

B1L001S, U24-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	28
Mean	57.36
Std Dev	15.96

B1L001S, U24-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch27, adc0

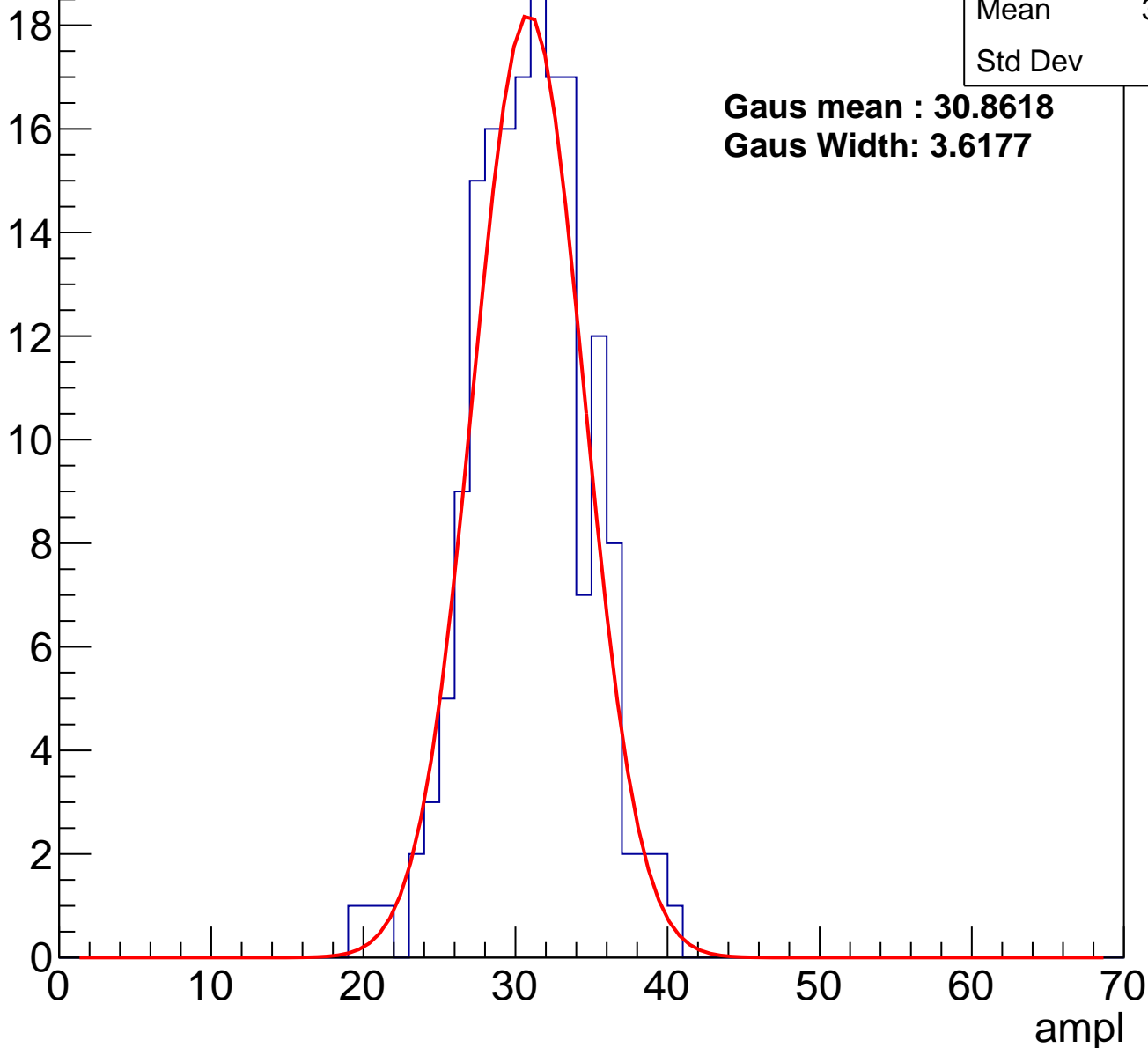
calib_packv5_042523_0143.root, FC#2, port C2

Entries	173
Mean	30.47
Std Dev	3.71

Gaus mean : 30.8618

Gaus Width: 3.6177

Entry



B1L001S, U24-ch27, adc1

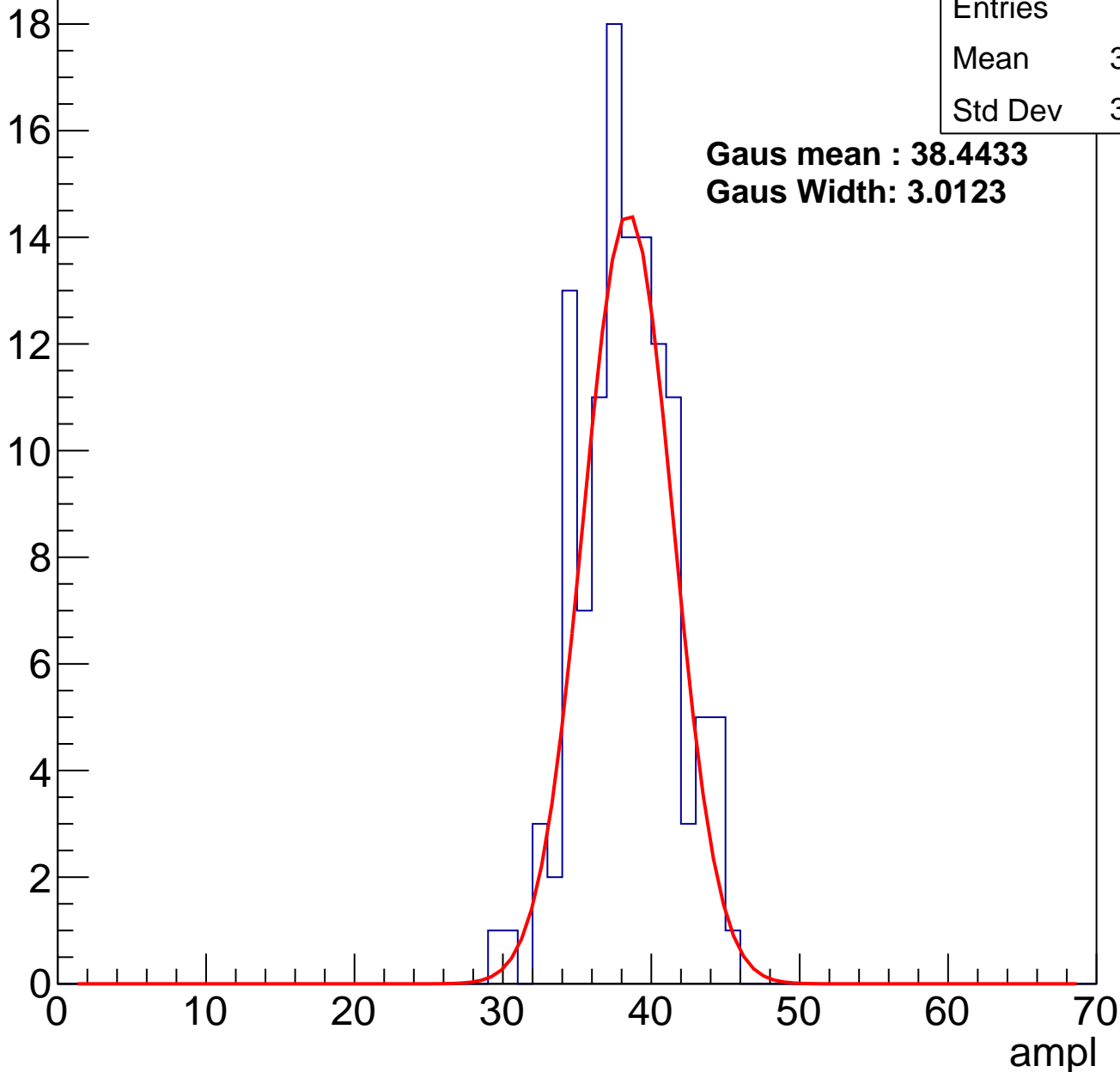
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	37.89
Std Dev	3.146

Gaus mean : 38.4433

Gaus Width: 3.0123

Entry



B1L001S, U24-ch27, adc2

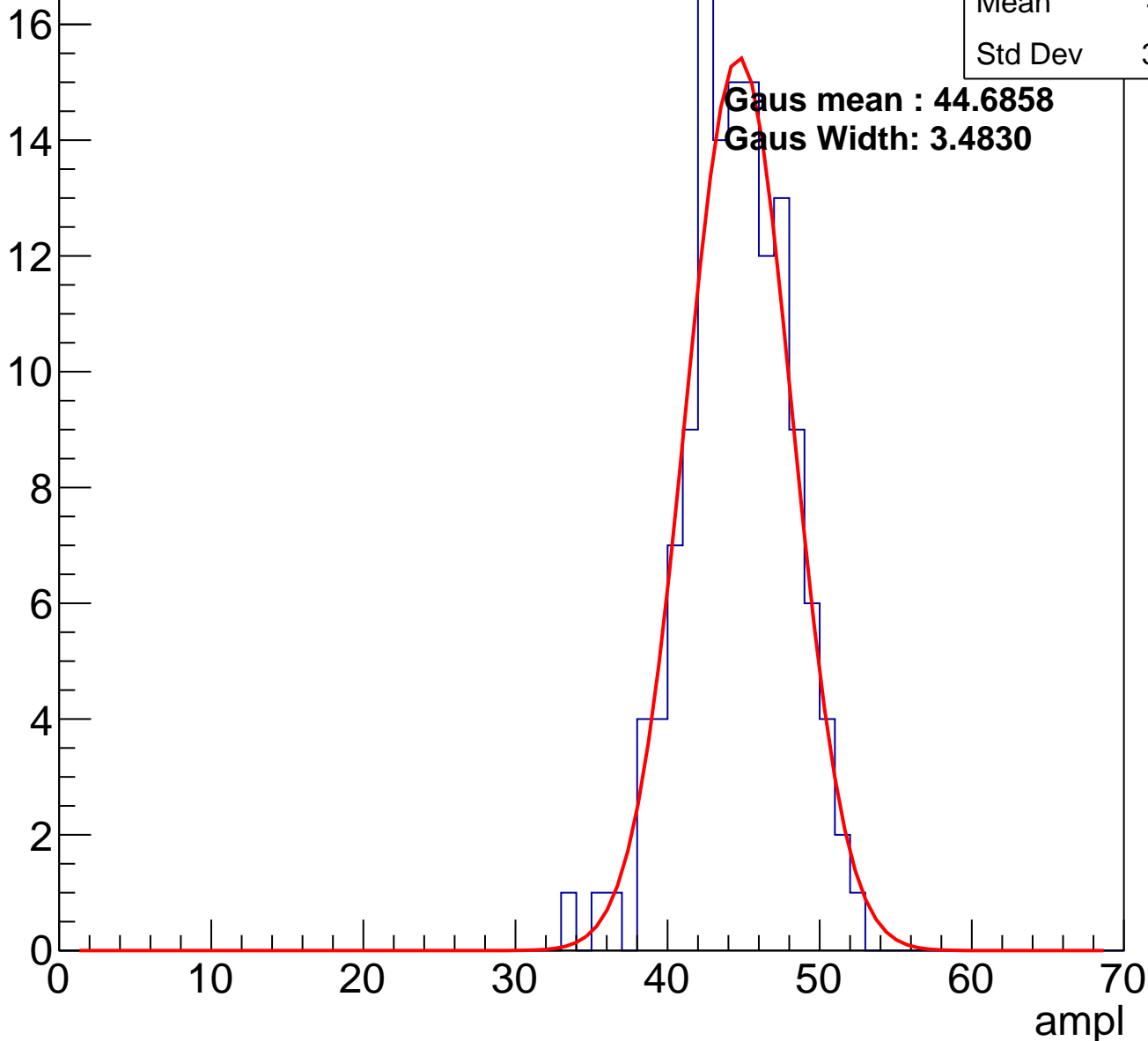
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	135
Mean	44.11
Std Dev	3.403

Gaus mean : 44.6858

Gaus Width: 3.4830

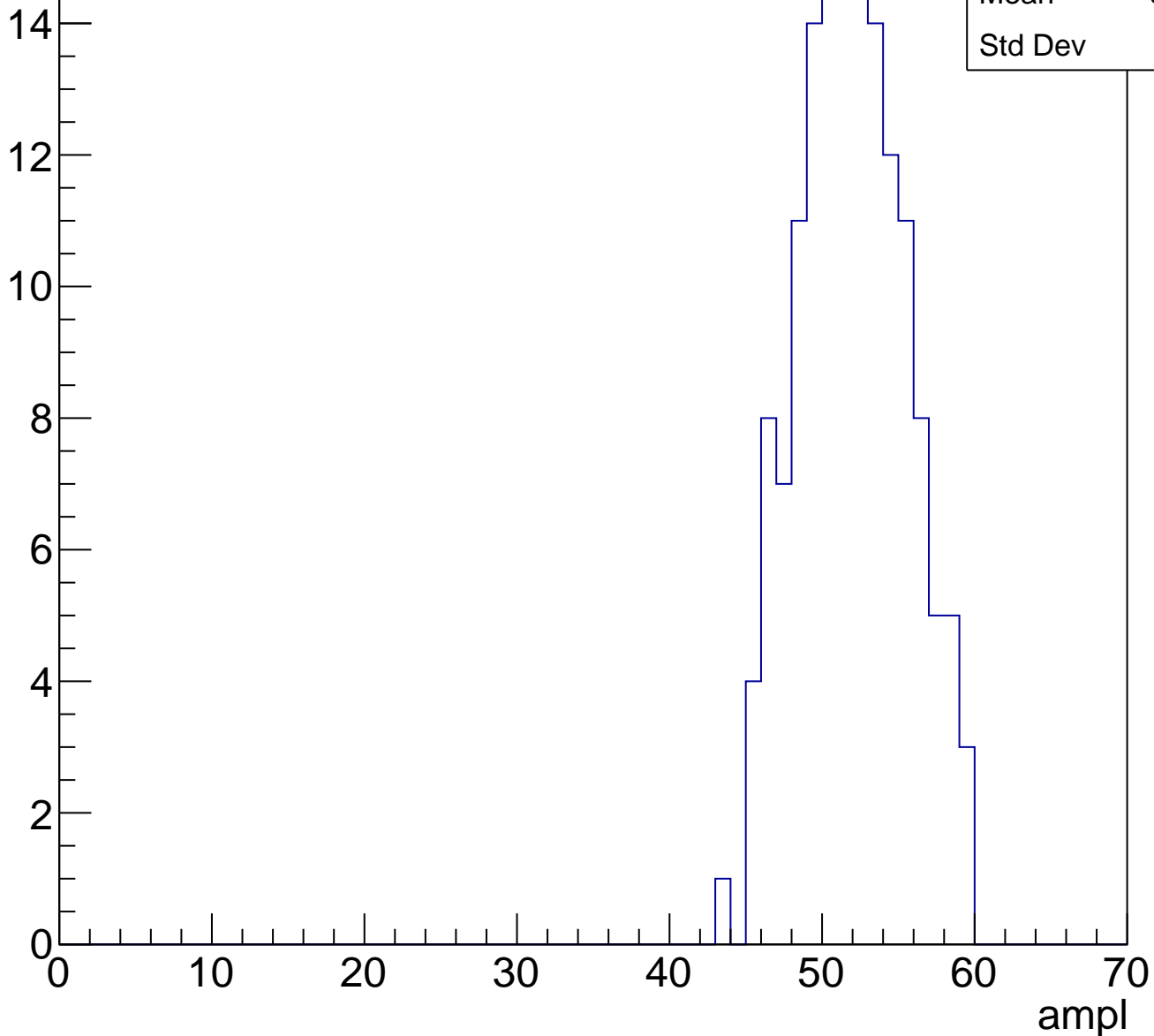


B1L001S, U24-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	51.51
Std Dev	3.51

Entry



B1L001S, U24-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

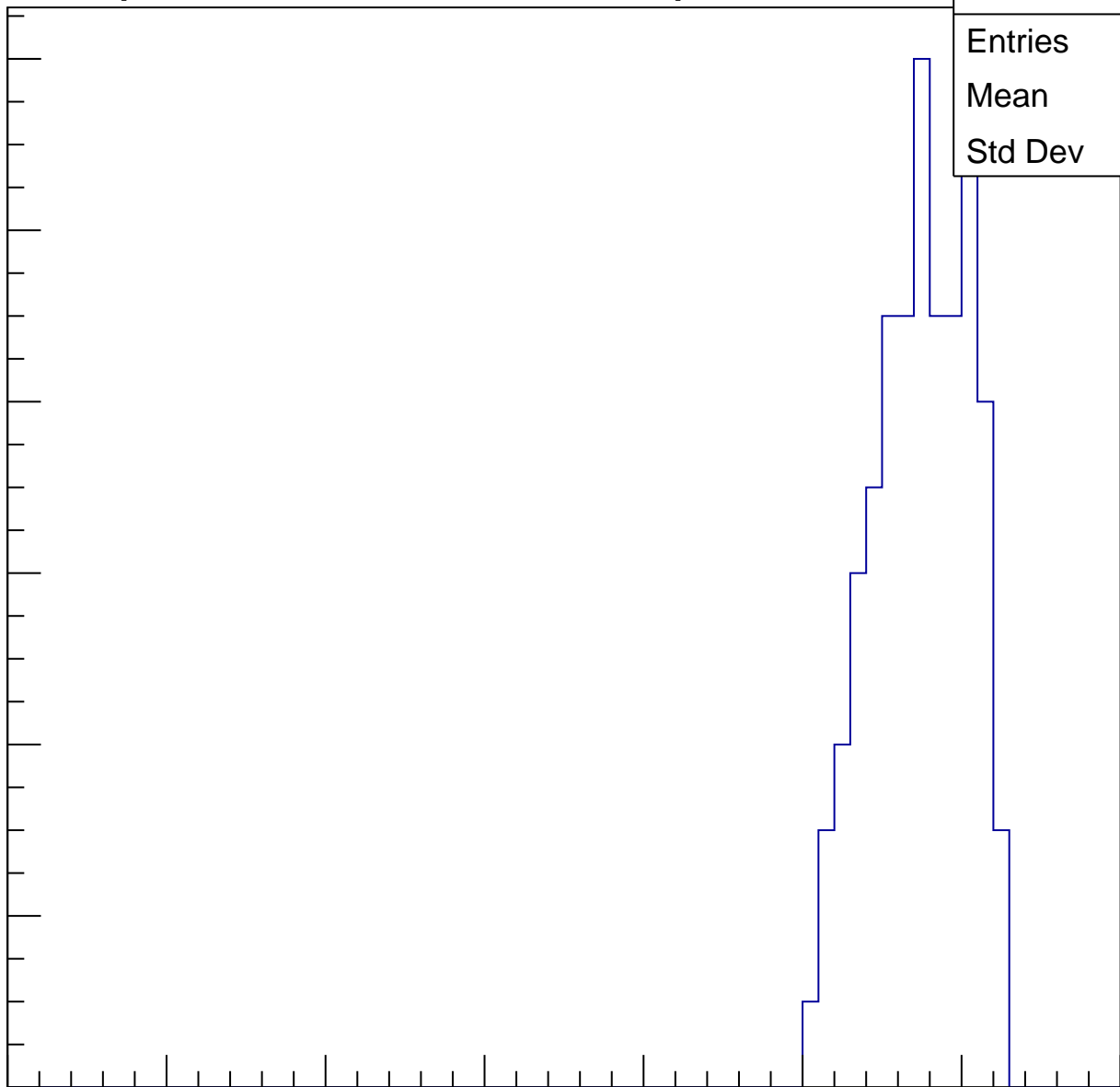
Entries	91
Mean	56.89
Std Dev	2.978

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

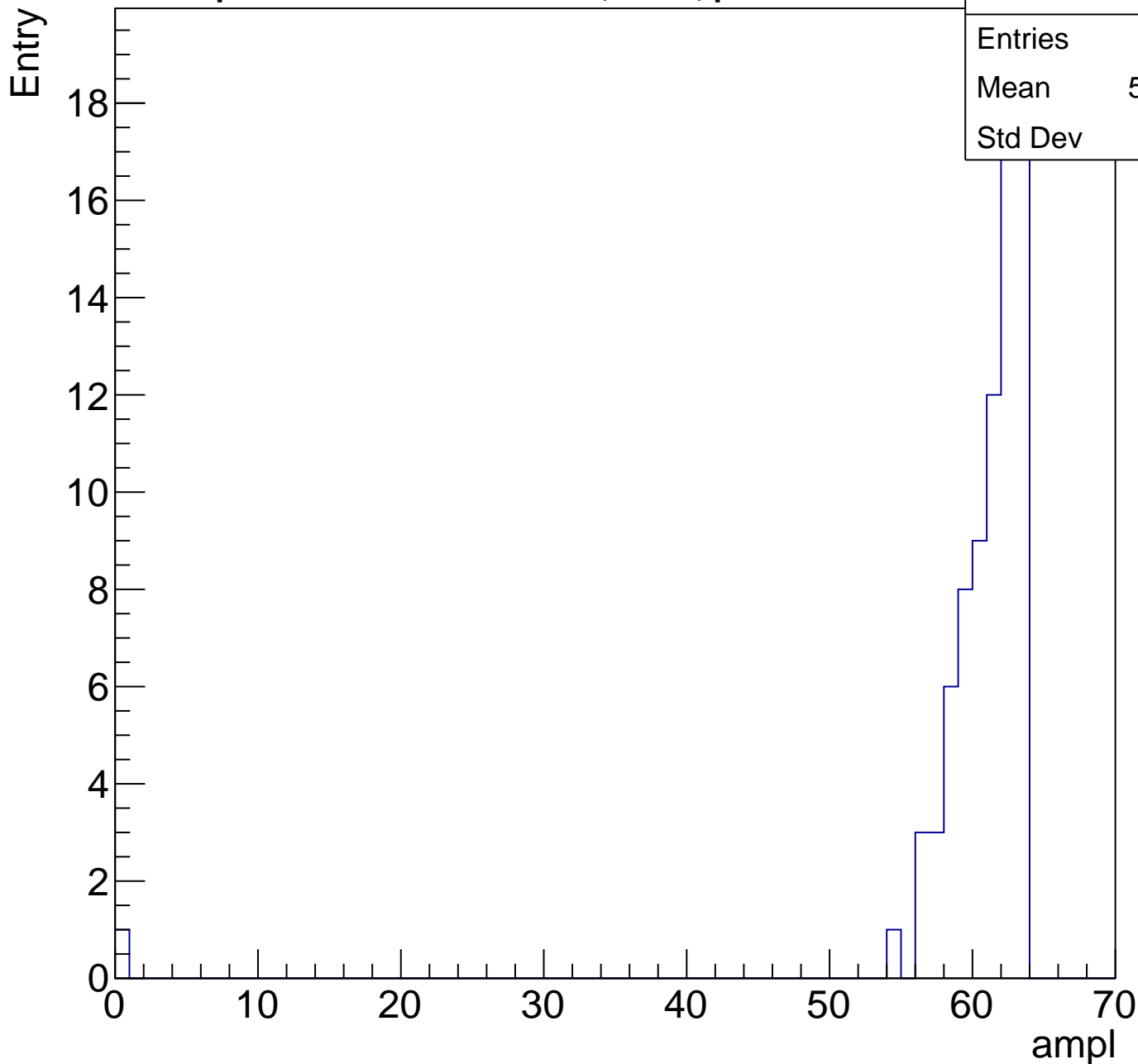
ampl



B1L001S, U24-ch27, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	59.95
Std Dev	7.11



B1L001S, U24-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch27, adc7

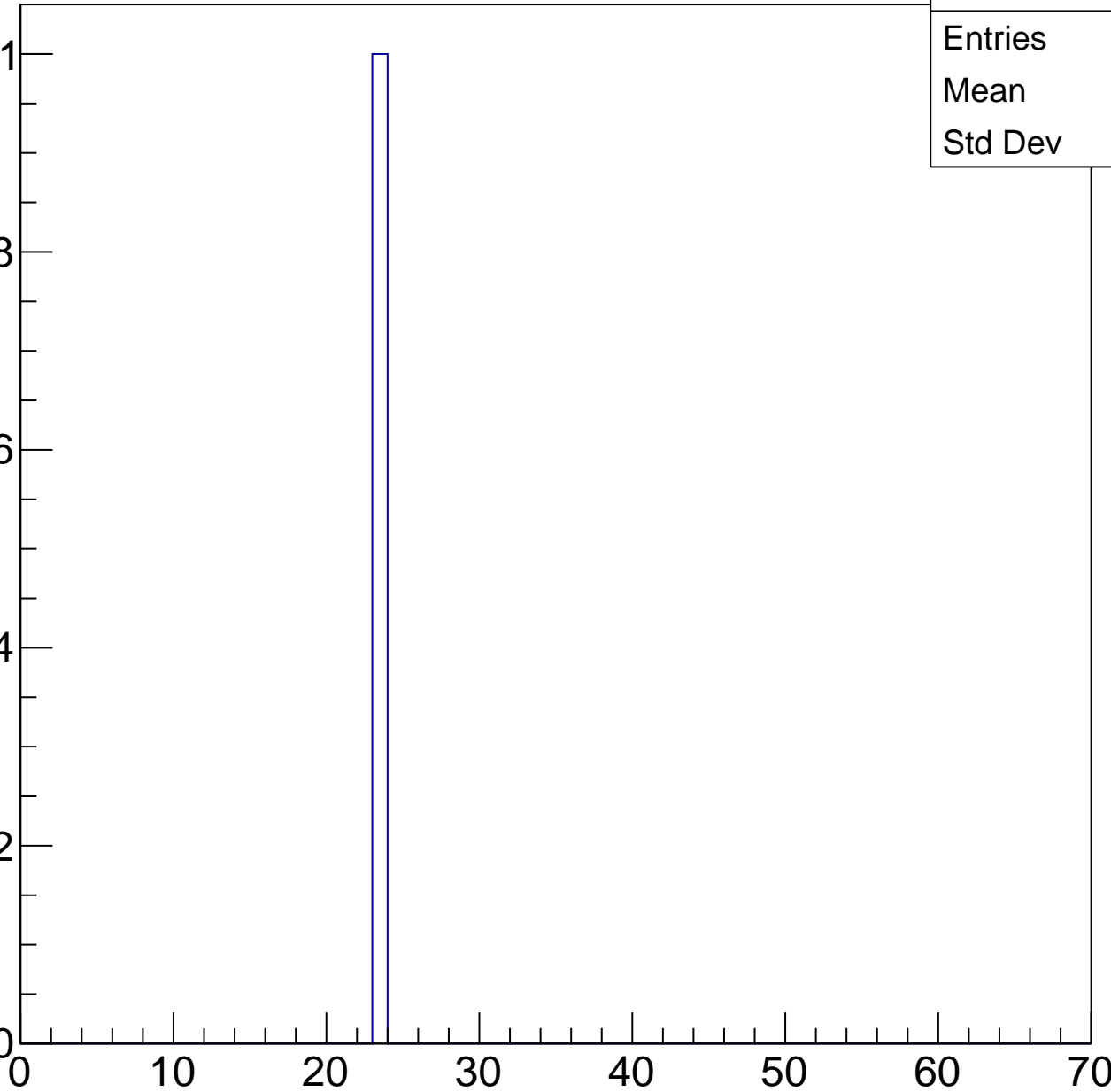
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L001S, U24-ch28, adc0

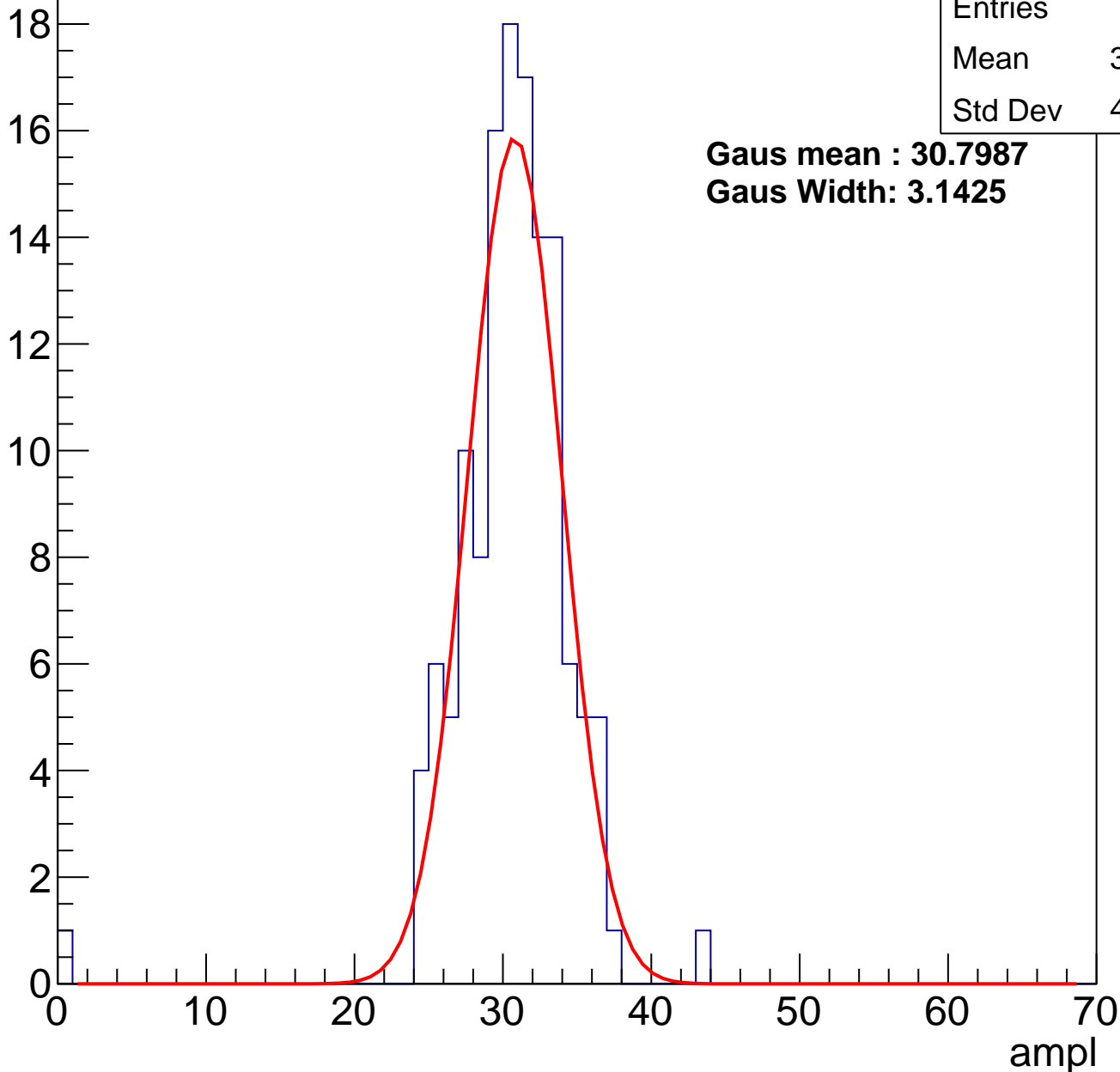
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.15
Std Dev	4.123

Gaus mean : 30.7987

Gaus Width: 3.1425

Entry



B1L001S, U24-ch28, adc1

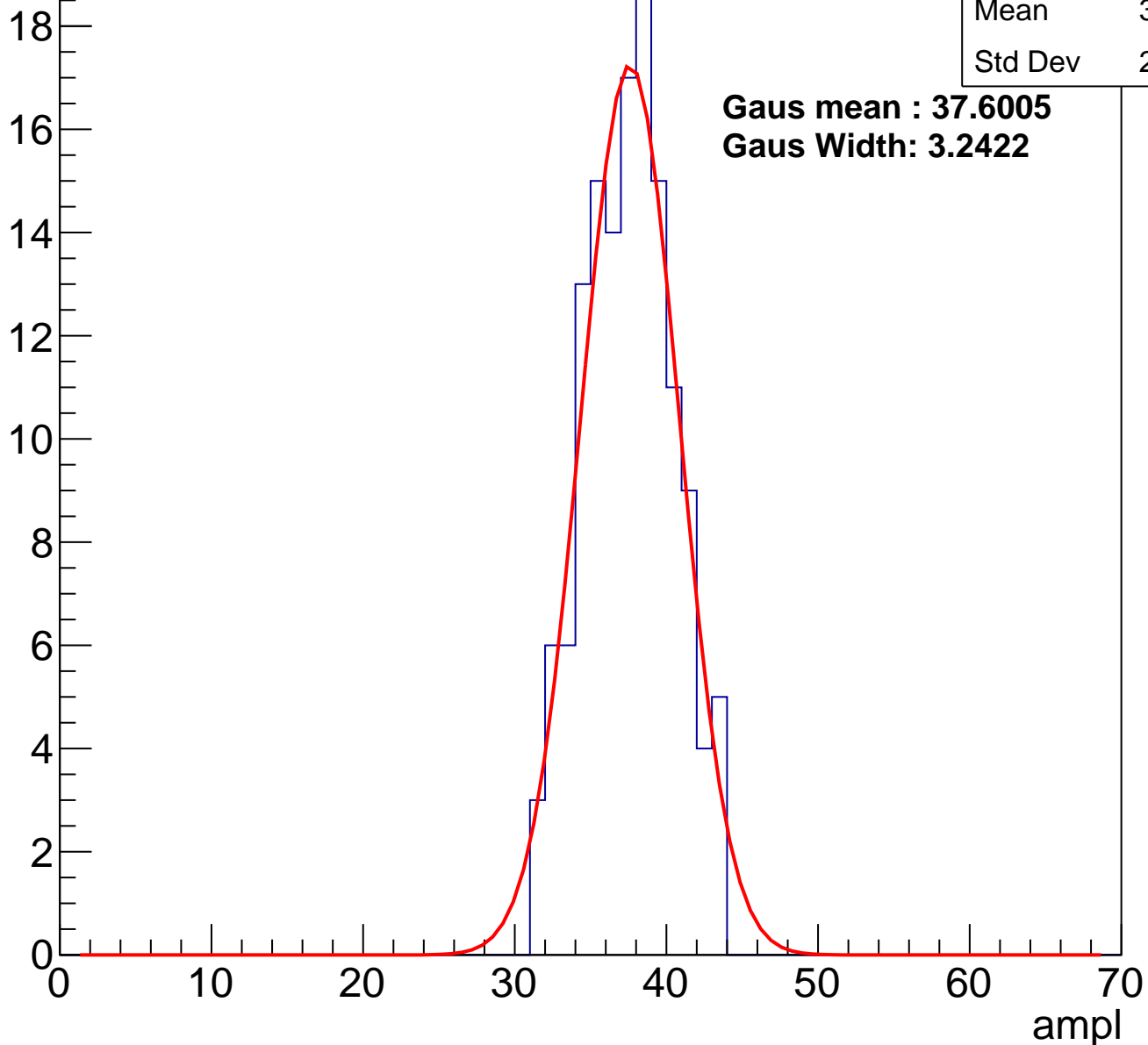
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	37.09
Std Dev	2.892

Gaus mean : 37.6005

Gaus Width: 3.2422

Entry



B1L001S, U24-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	43.72
Std Dev	3.194

Gaus mean : 44.2800

Gaus Width: 2.8887

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

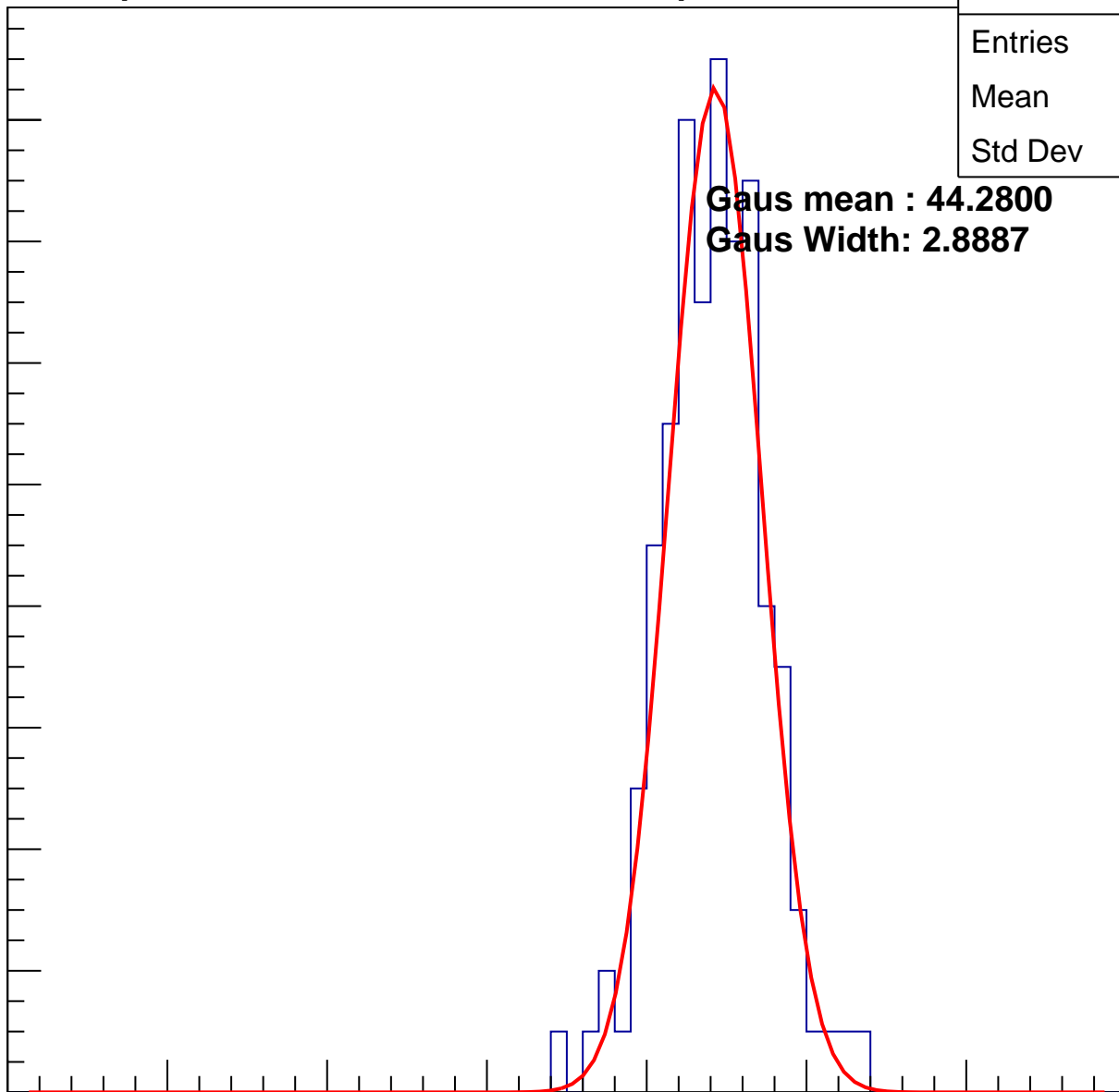
40

50

60

70

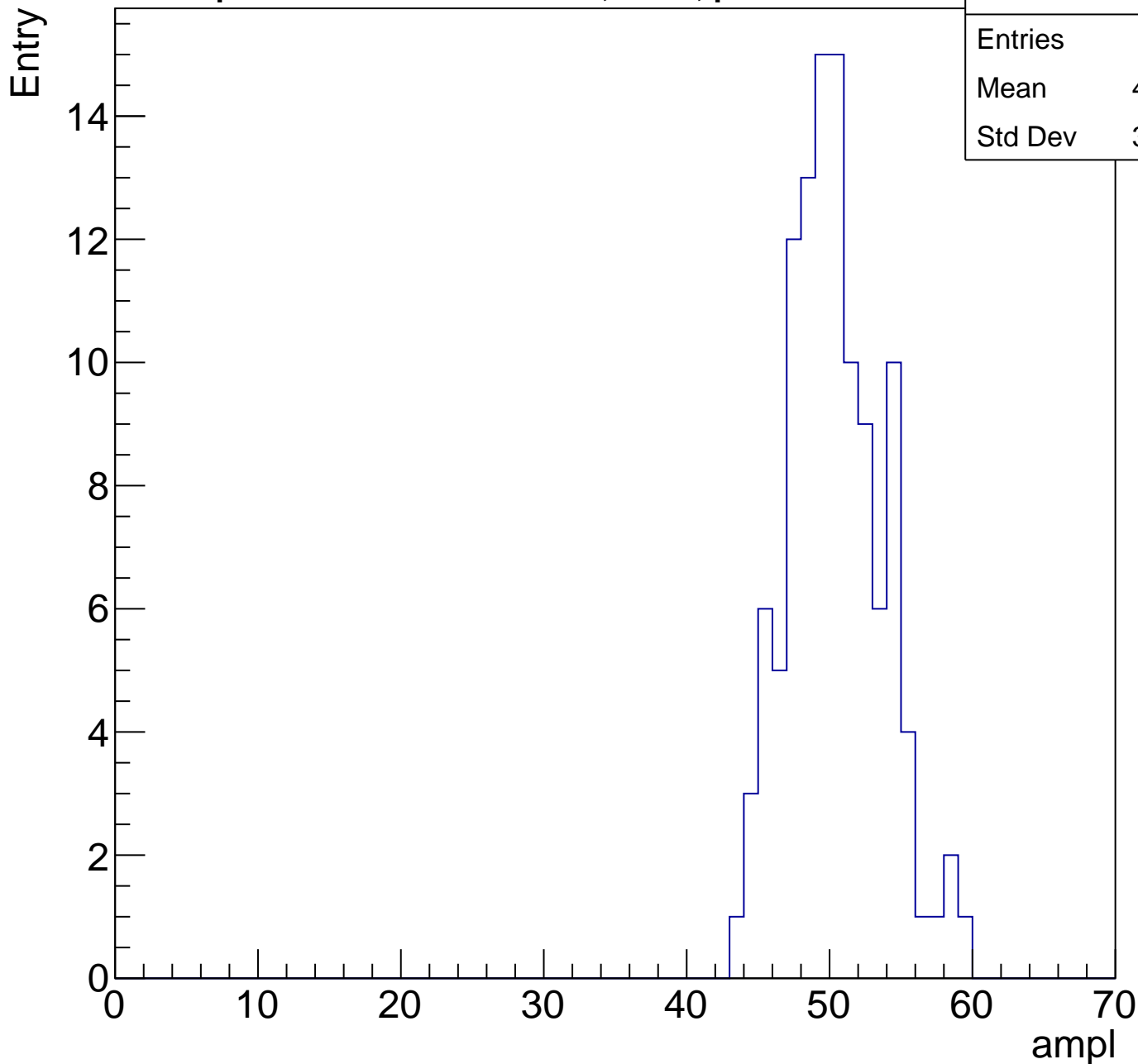
ampl



B1L001S, U24-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	49.93
Std Dev	3.265

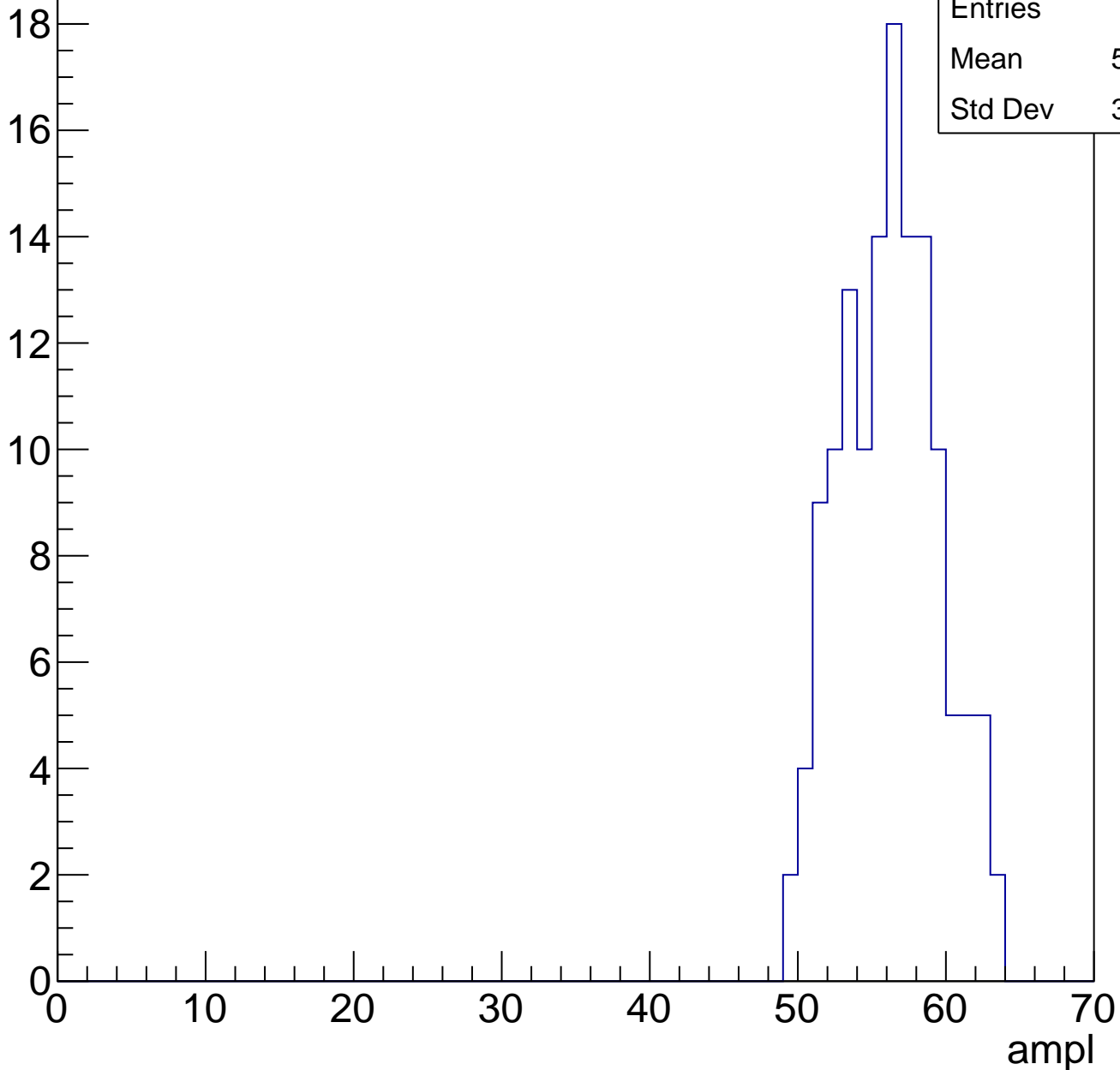


B1L001S, U24-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.74
Std Dev	3.257

Entry



B1L001S, U24-ch28, adc5

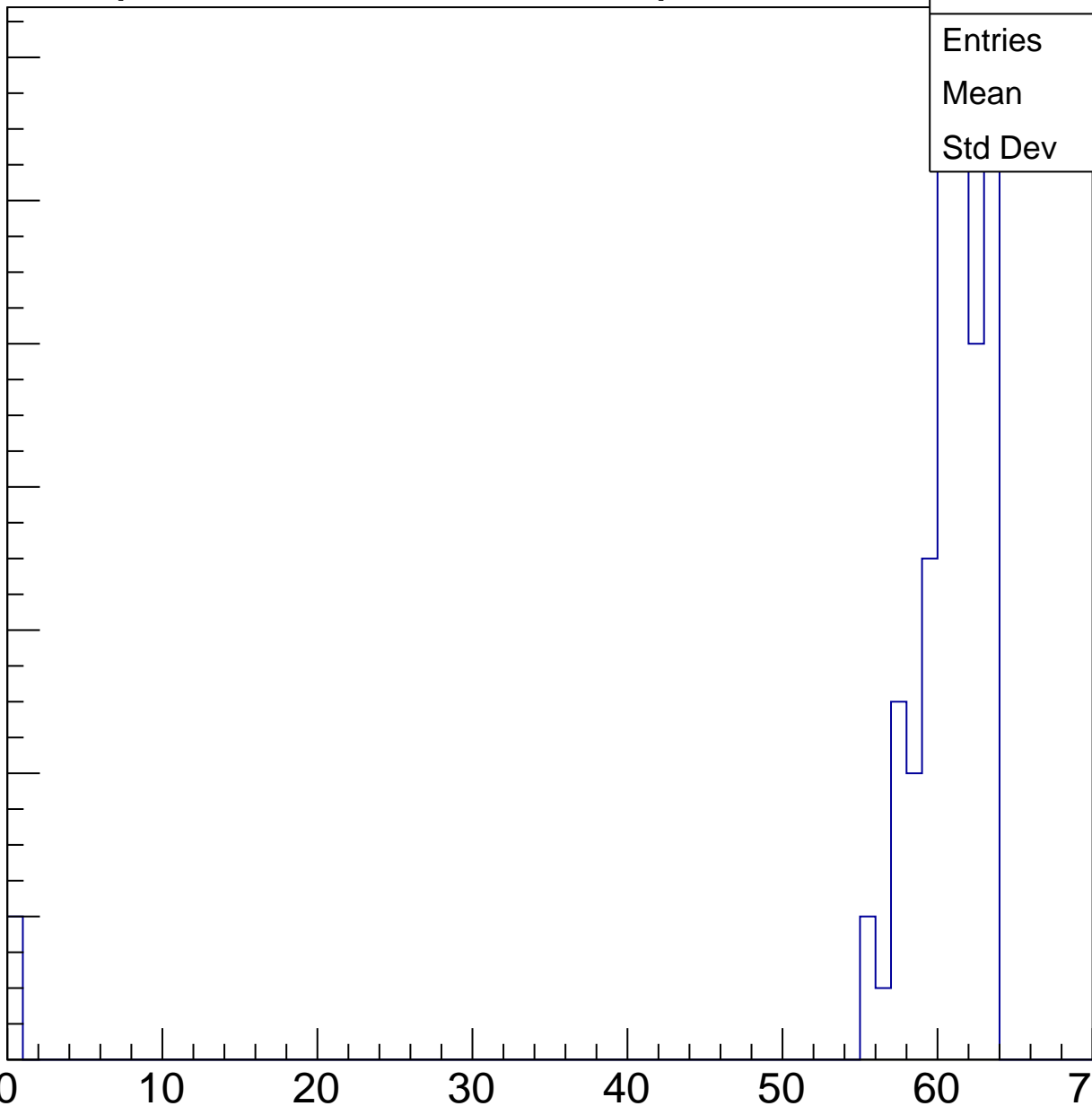
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.74
Std Dev	10.13

ampl

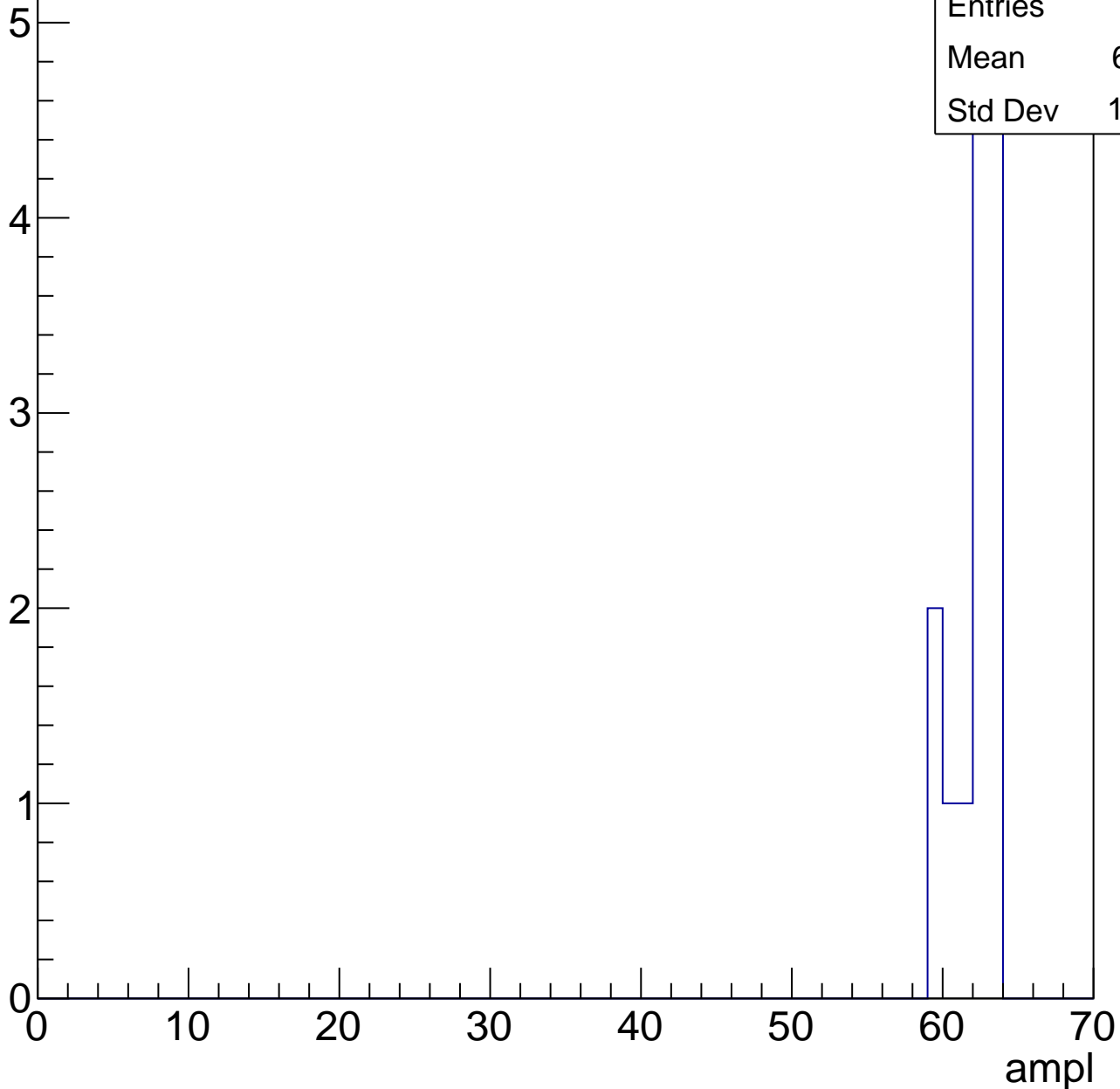


B1L001S, U24-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	61.71
Std Dev	1.385



B1L001S, U24-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L001S, U24-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	31.38
Std Dev	3.063

Gaus mean : 31.8075

Gaus Width: 3.2872

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch29, adc1

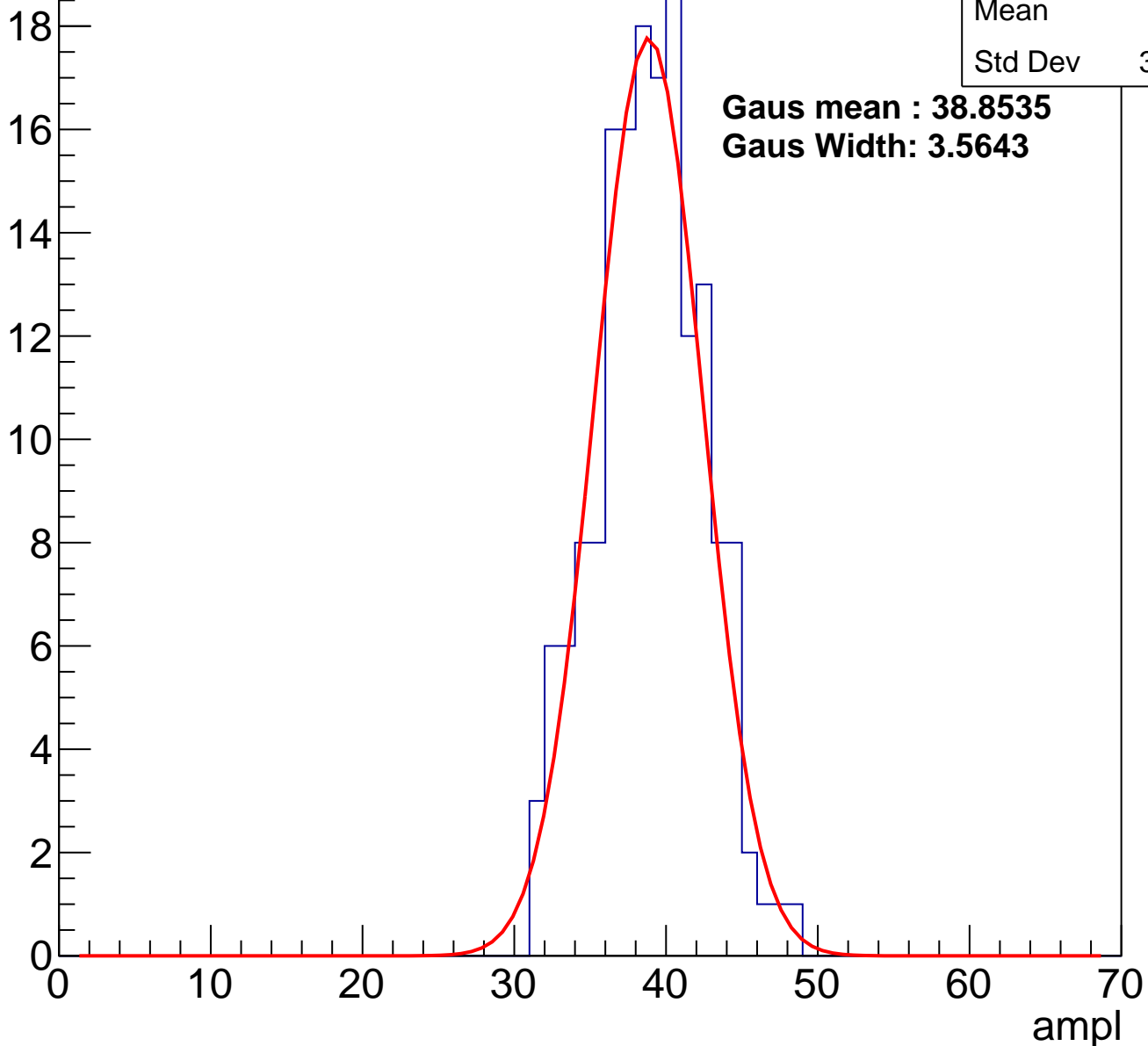
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	38.5
Std Dev	3.496

Gaus mean : 38.8535

Gaus Width: 3.5643

Entry



B1L001S, U24-ch29, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	45.76
Std Dev	3.401

Gaus mean : 46.5254

Gaus Width: 3.0567

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

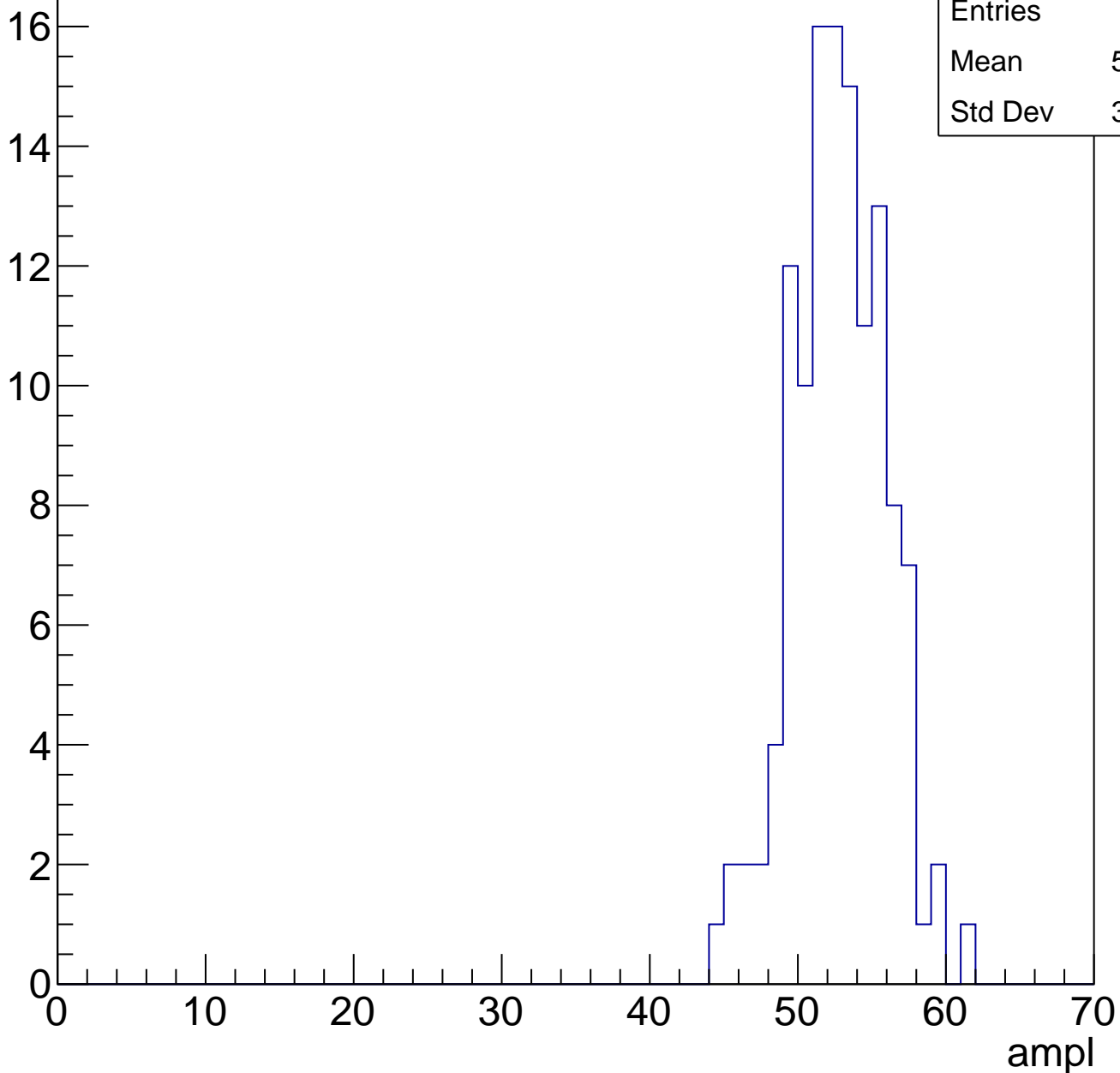
70

B1L001S, U24-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	52.33
Std Dev	3.114

Entry



B1L001S, U24-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

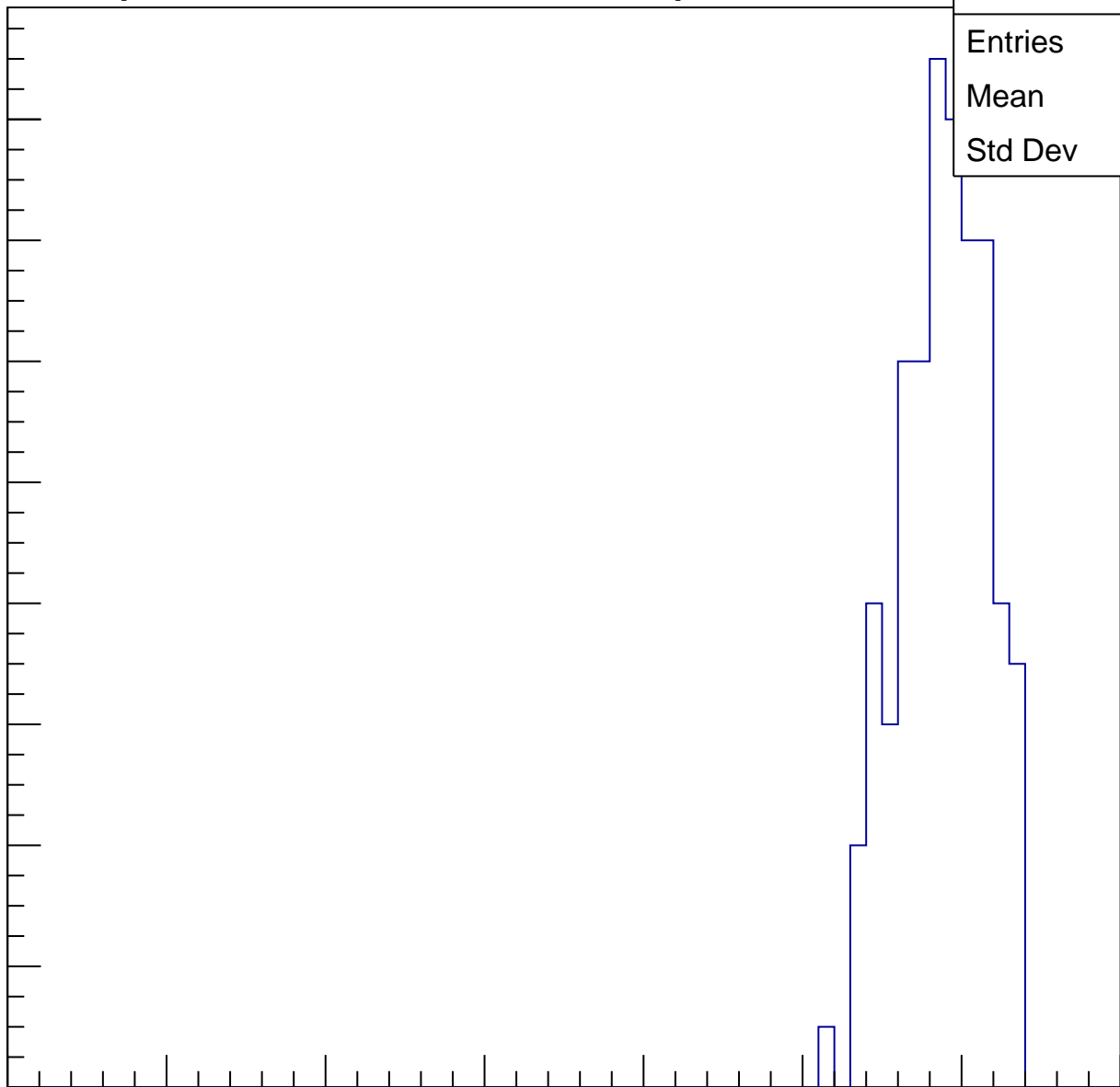
40

50

60

ampl

Entries	119
Mean	58.34
Std Dev	2.717



B1L001S, U24-ch29, adc5

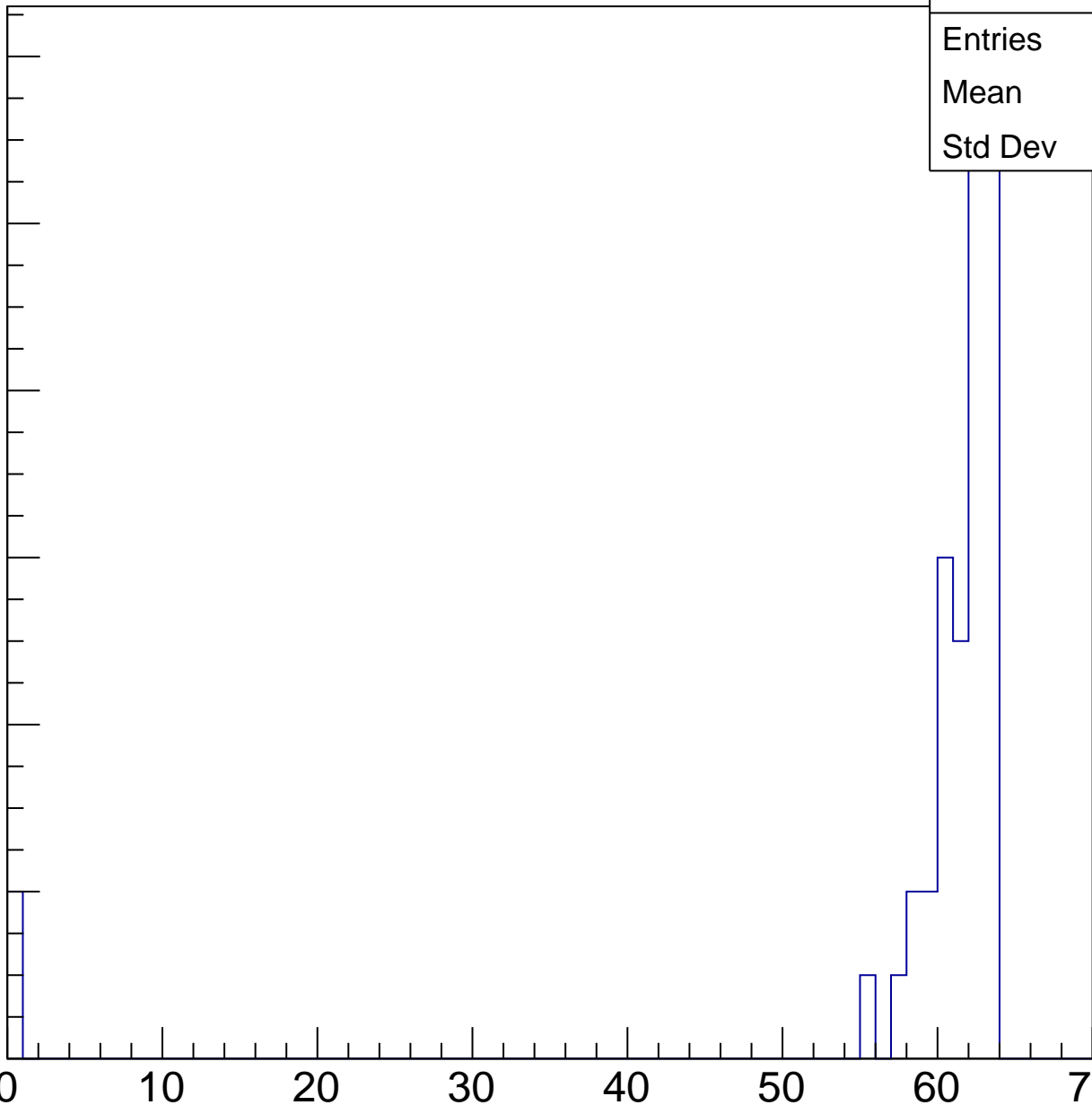
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	42
Mean	58.31
Std Dev	13.17

ampl



B1L001S, U24-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

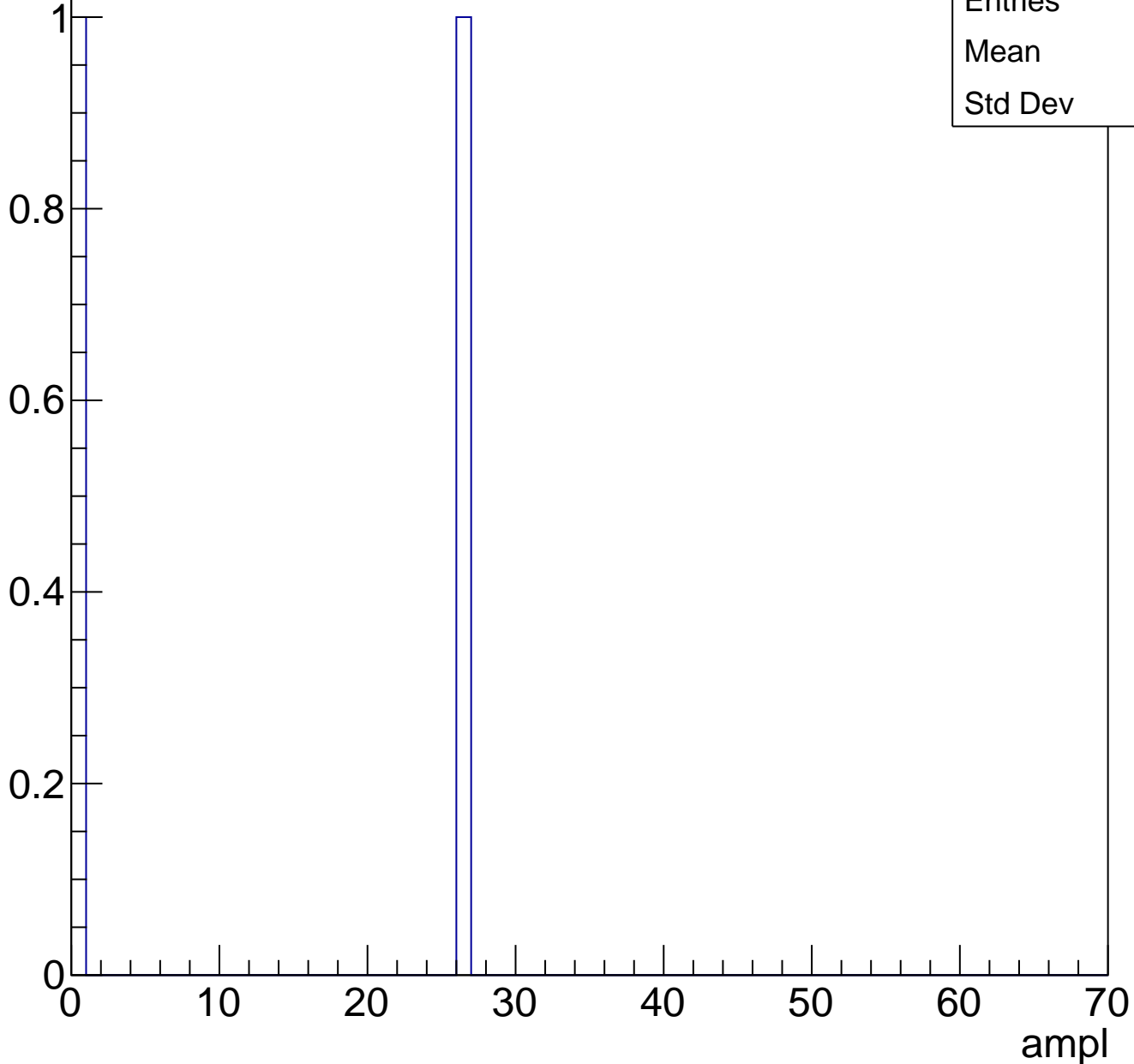
Entry



B1L001S, U24-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	13
Std Dev	13

B1L001S, U24-ch30, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	29.86
Std Dev	2.979

Gaus mean : 30.2519

Gaus Width: 2.9524

Entry

16

14

12

10

8

6

4

2

0

0

10

20

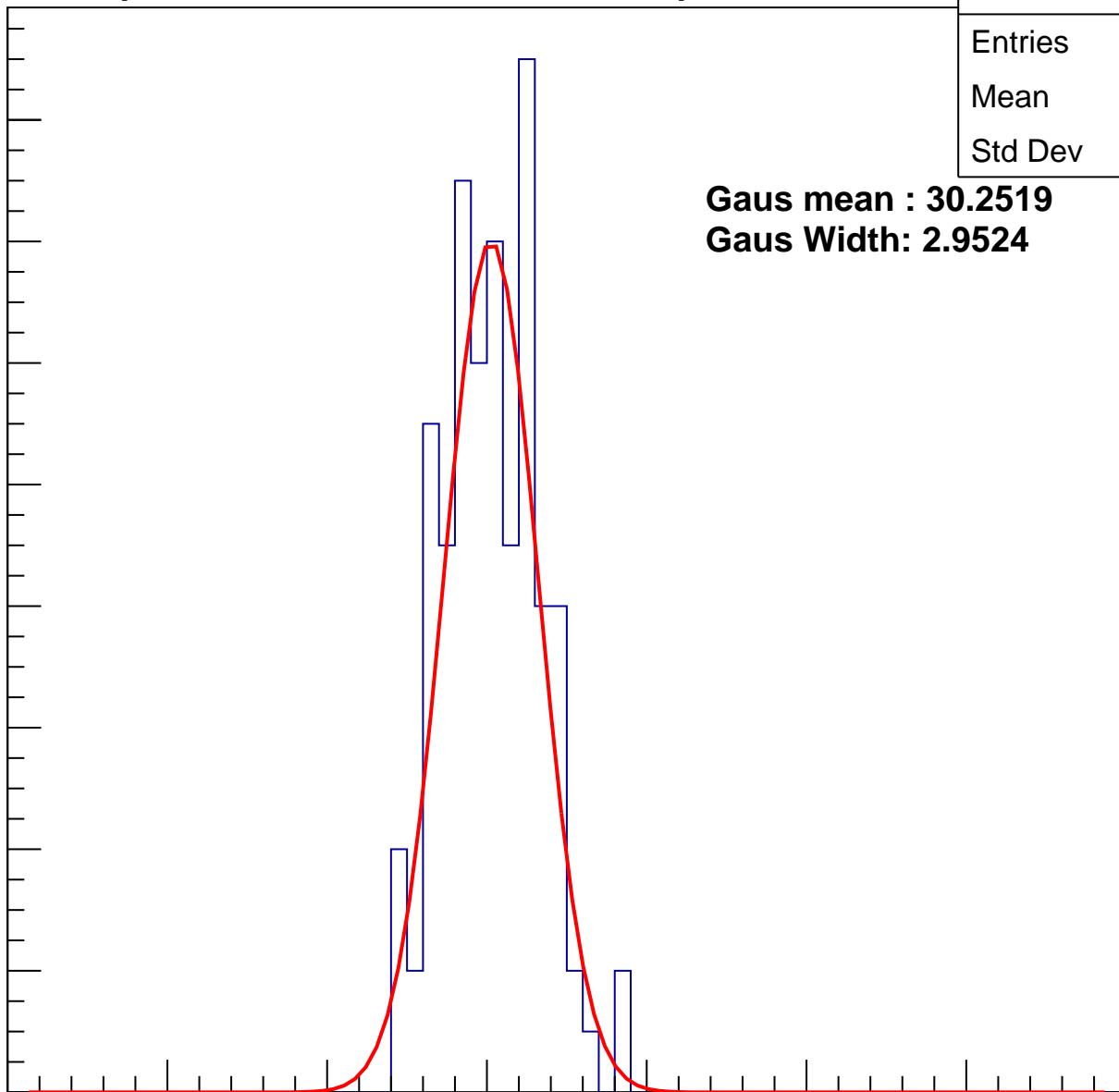
30

50

60

70

ampl



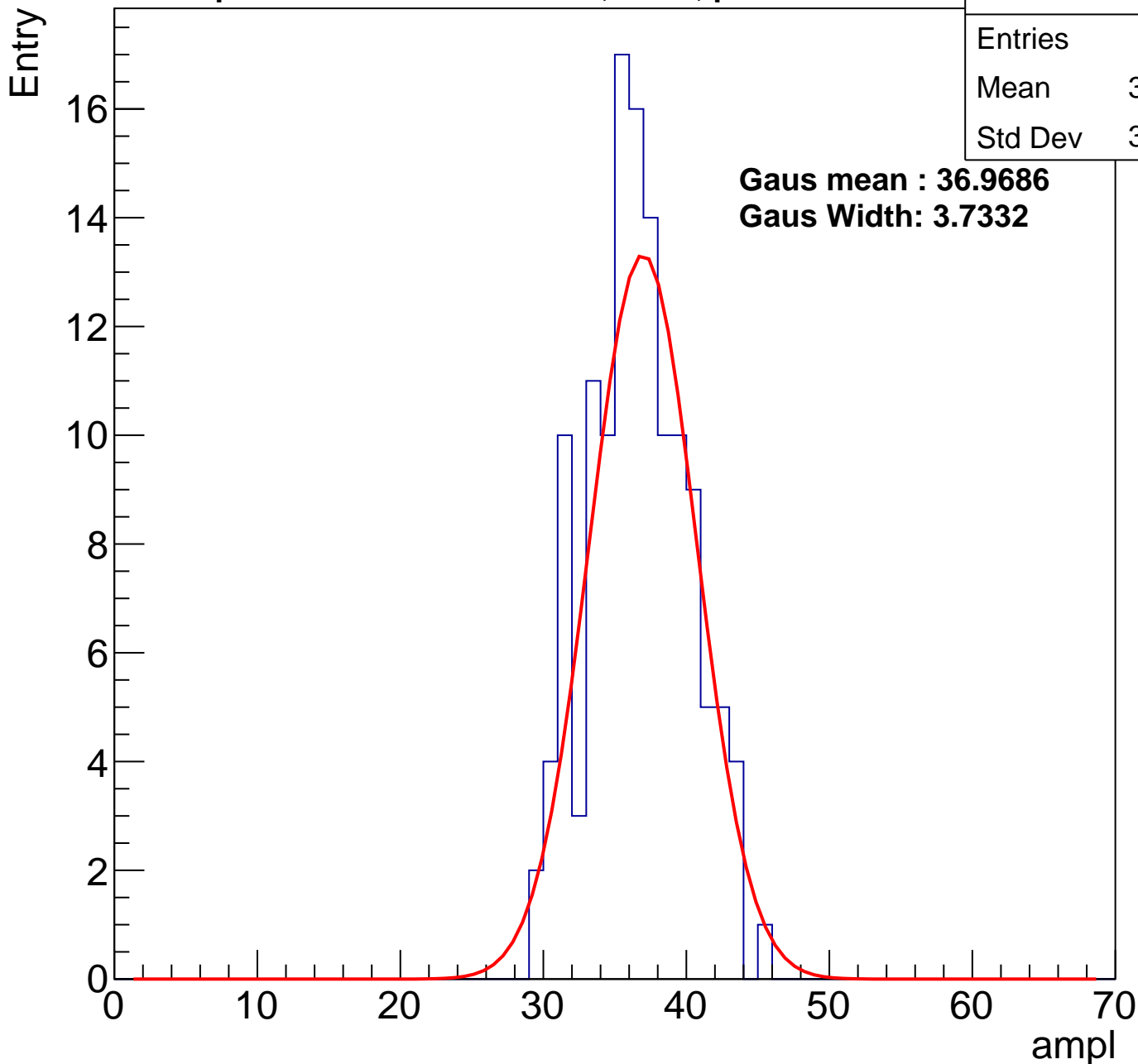
B1L001S, U24-ch30, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.17
Std Dev	3.453

Gaus mean : 36.9686

Gaus Width: 3.7332



B1L001S, U24-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	42.77
Std Dev	3.65

Gaus mean : 43.2451

Gaus Width: 3.7638

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

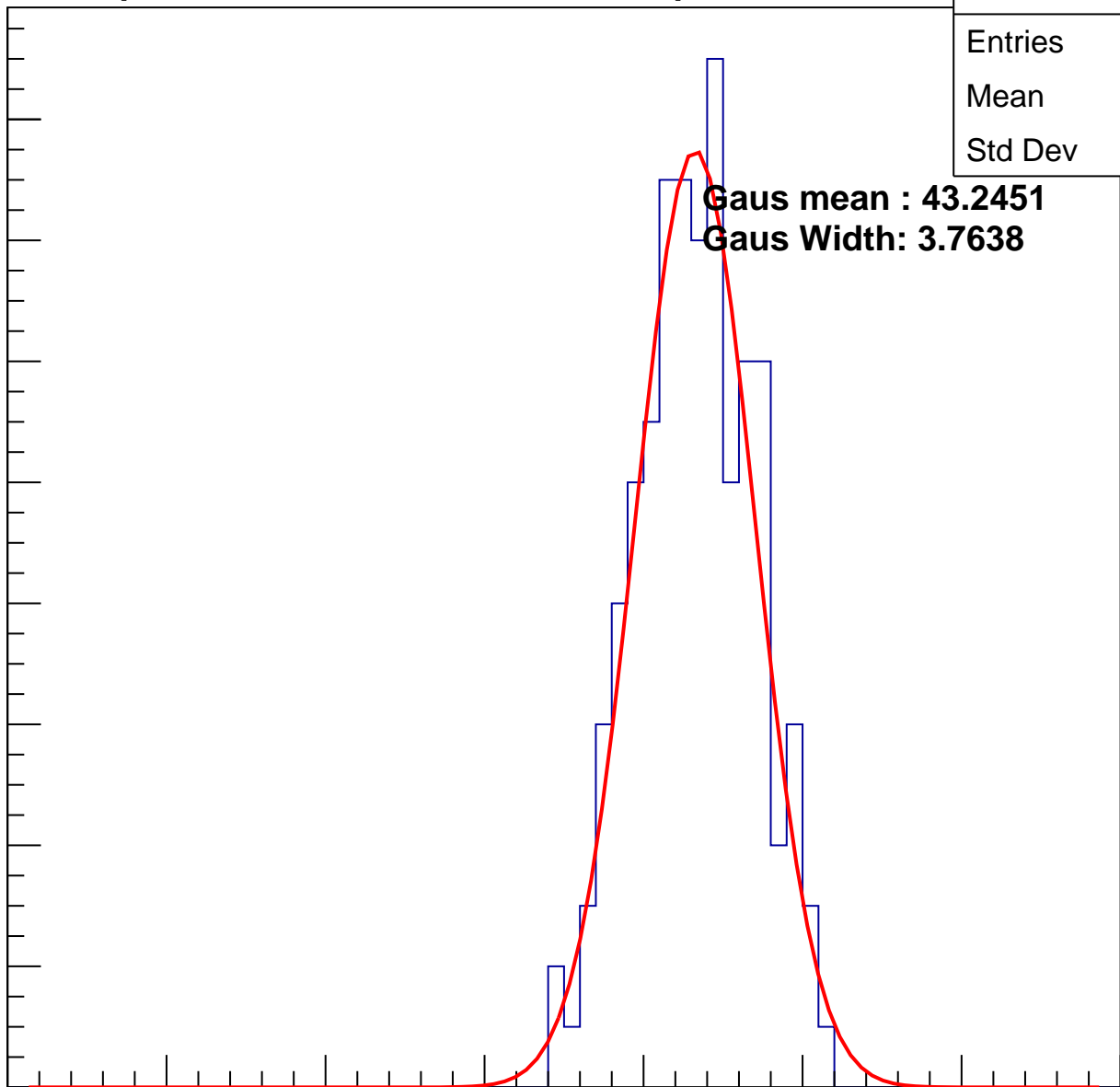
40

50

60

70

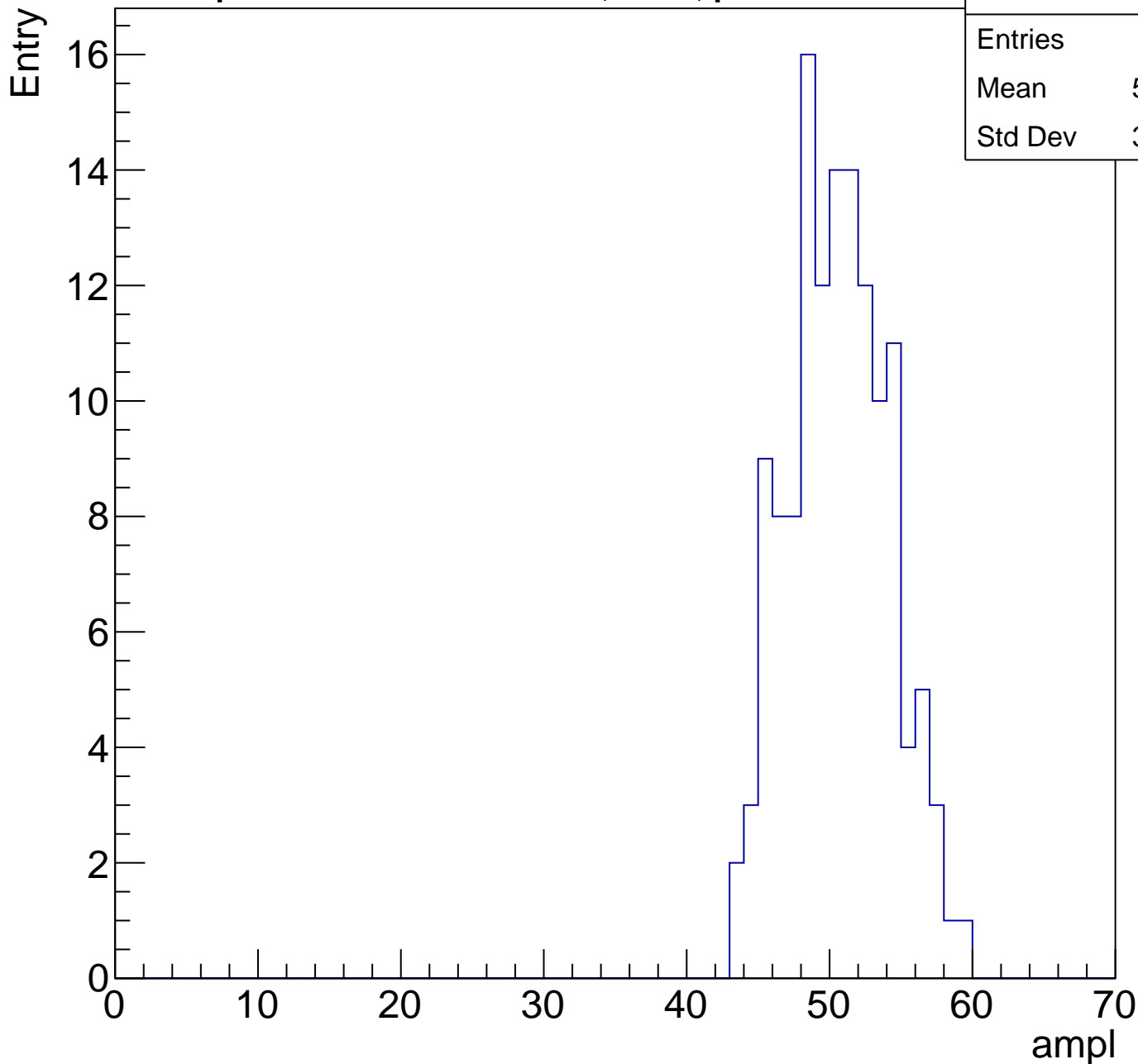
ampl



B1L001S, U24-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	50.17
Std Dev	3.472



B1L001S, U24-ch30, adc4

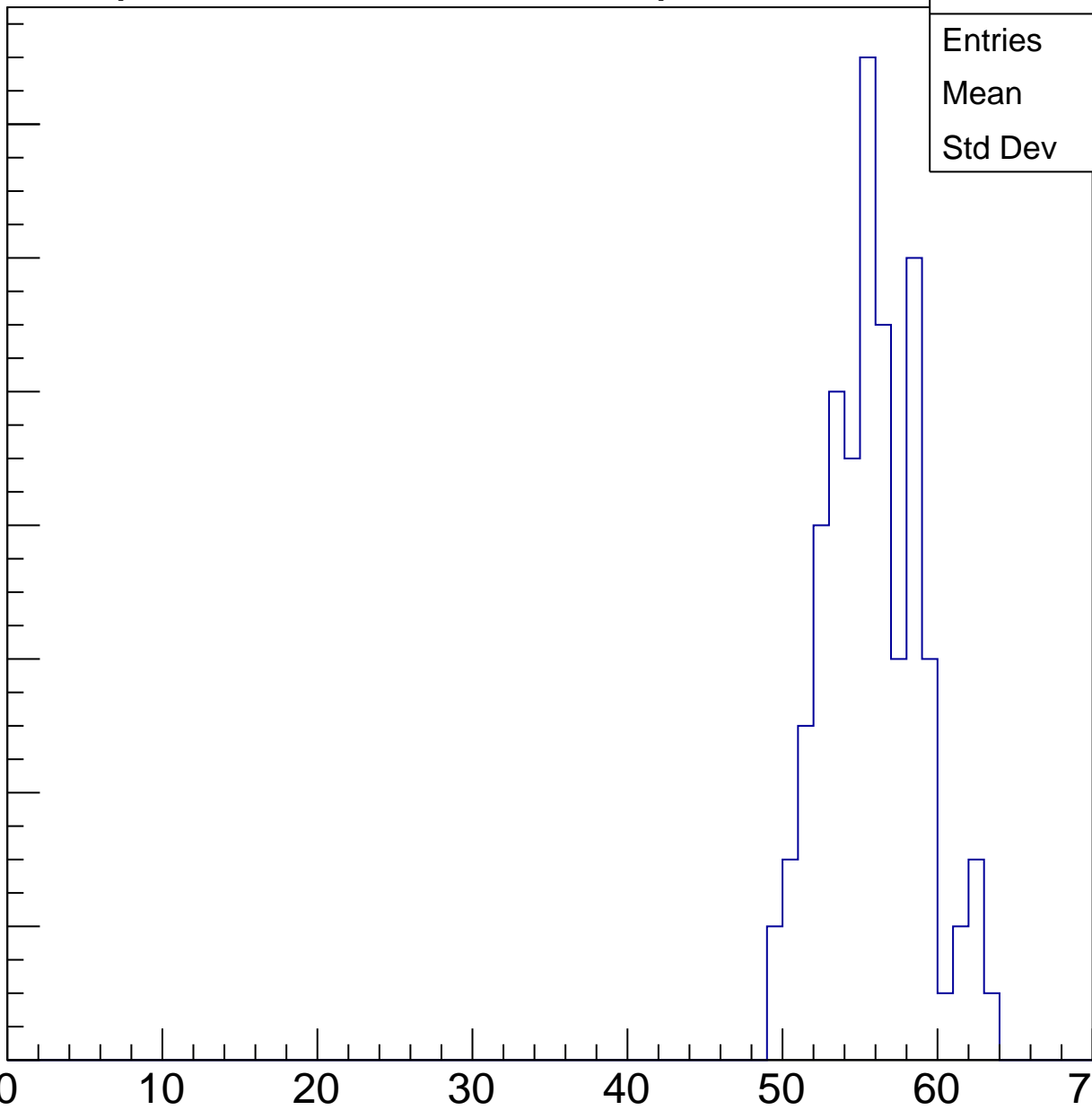
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	55.31
Std Dev	3.07

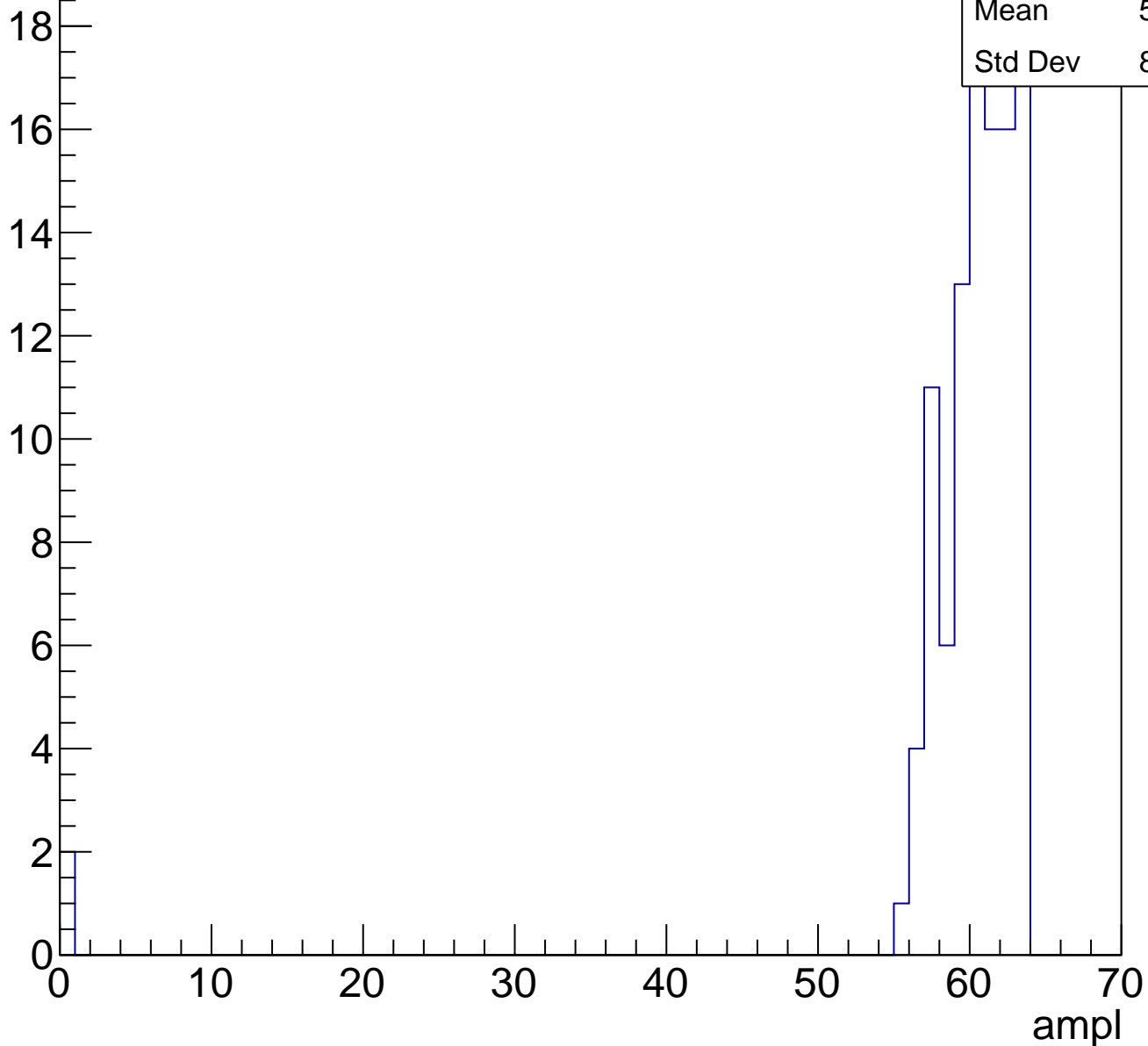
ampl



B1L001S, U24-ch30, adc5

calib_packv5_042523_0143.root, FC#2, port C2

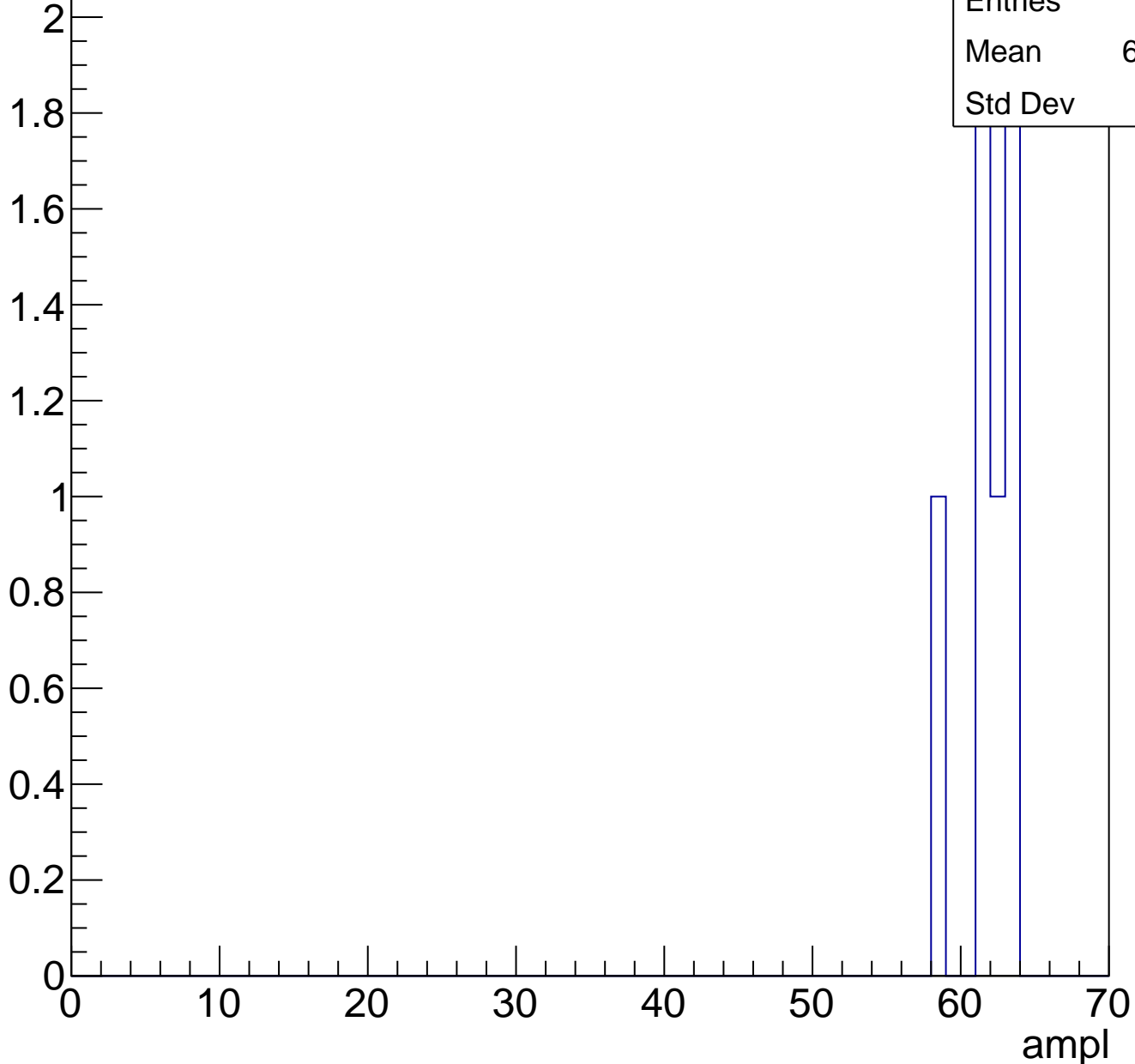
Entry



B1L001S, U24-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch31, adc0

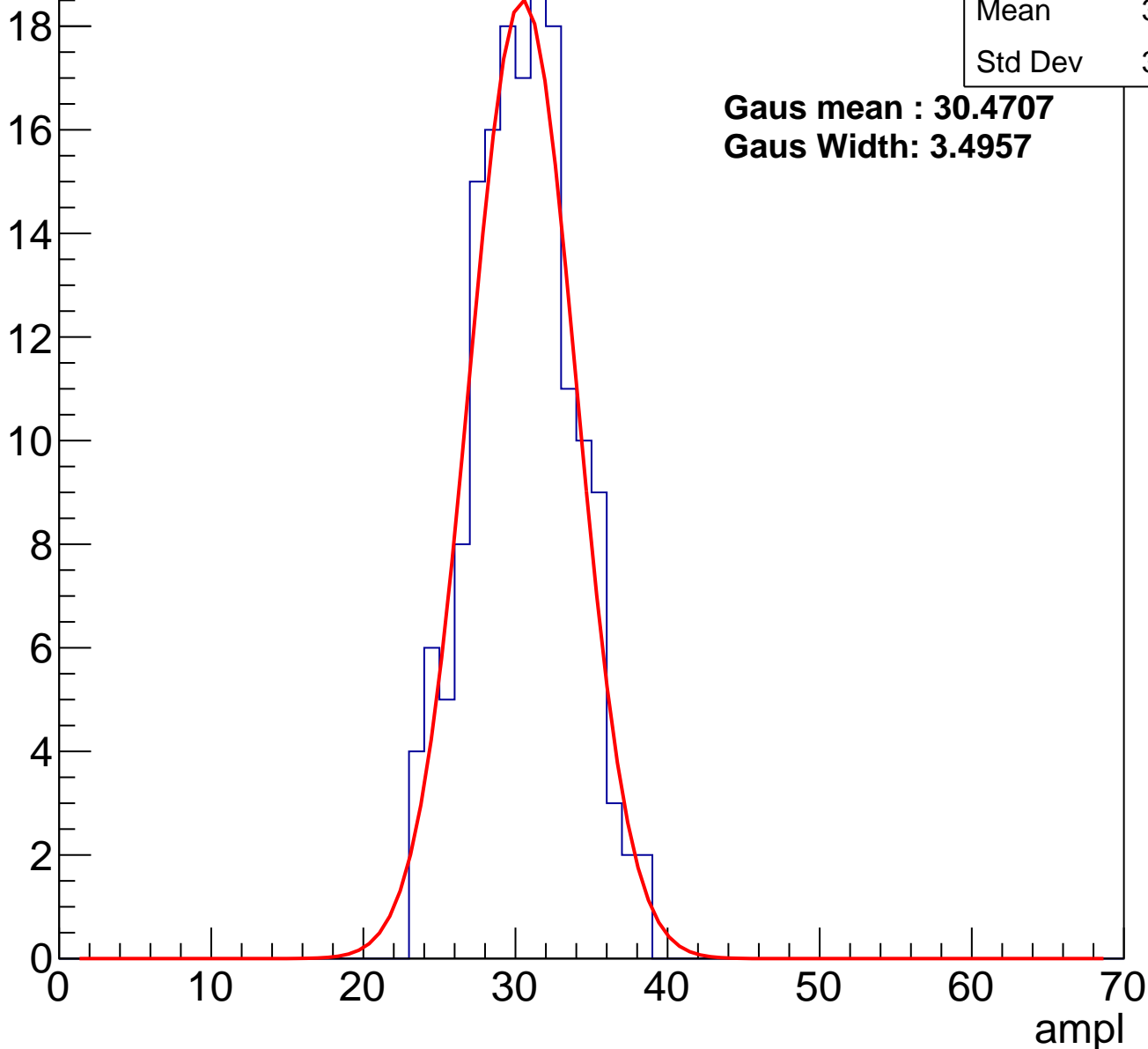
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	30.03
Std Dev	3.315

Gaus mean : 30.4707

Gaus Width: 3.4957

Entry

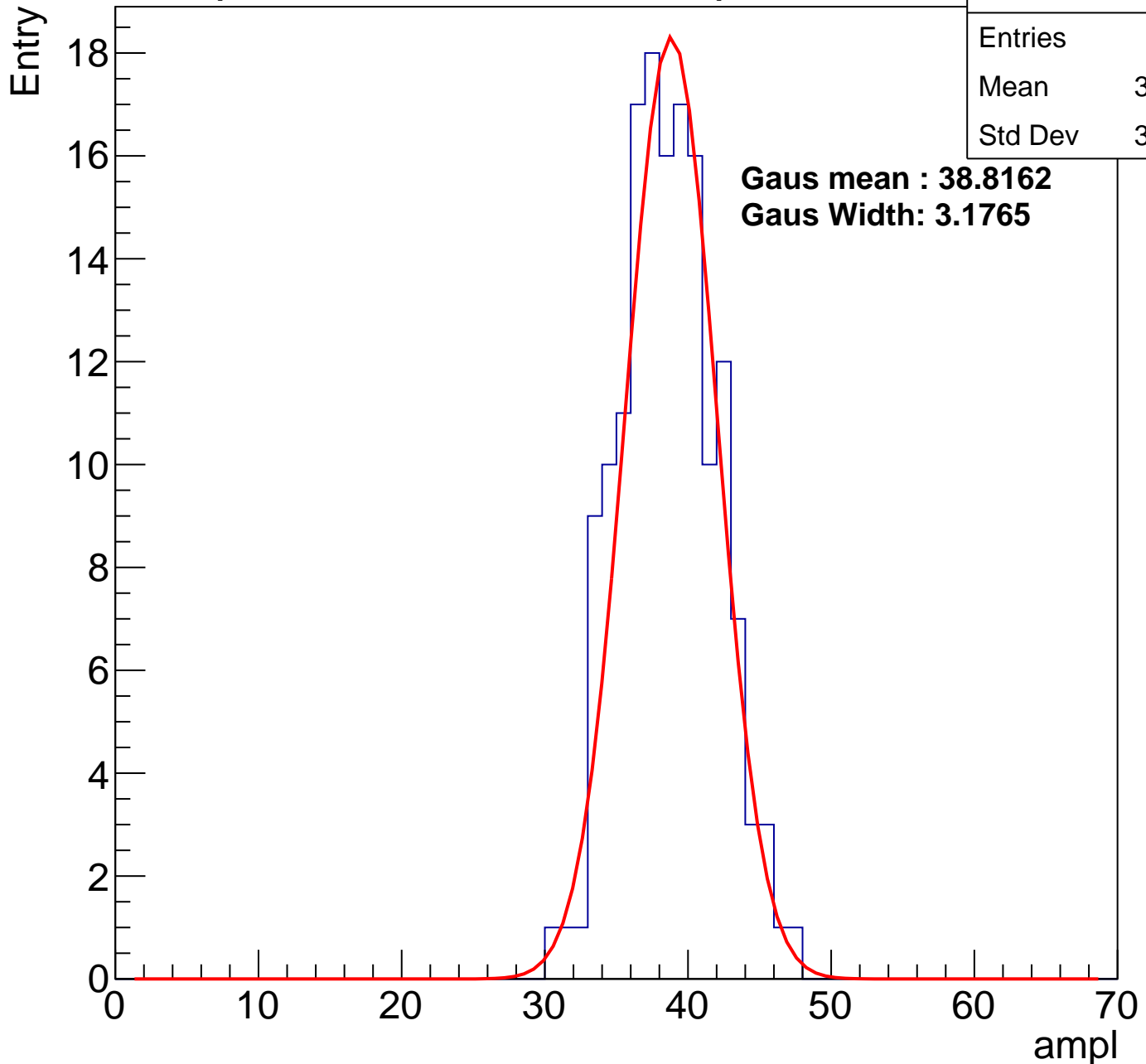


B1L001S, U24-ch31, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	38.18
Std Dev	3.276

Gaus mean : 38.8162
Gaus Width: 3.1765



B1L001S, U24-ch31, adc2

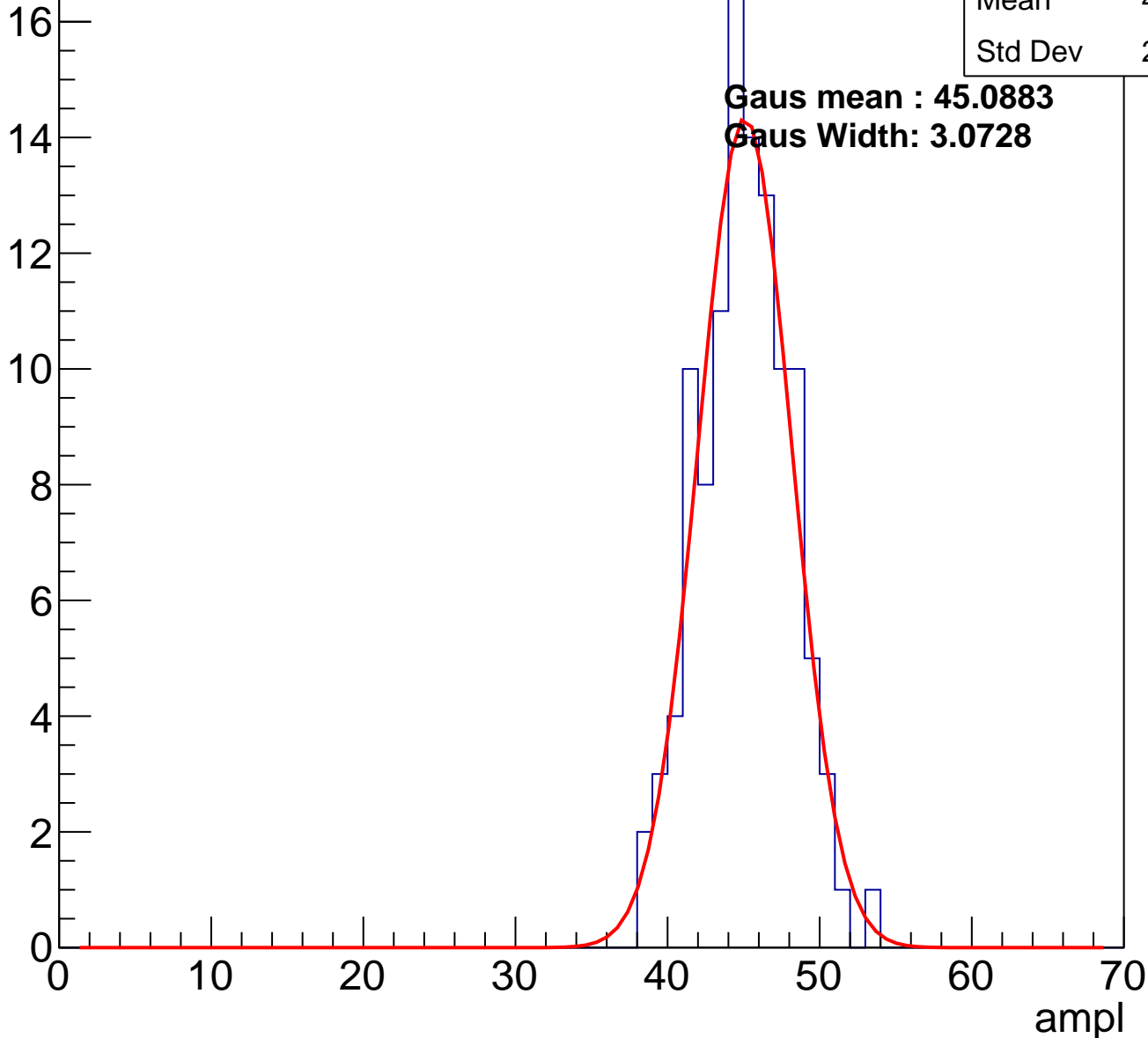
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	112
Mean	44.62
Std Dev	2.959

Gaus mean : 45.0883

Gaus Width: 3.0728

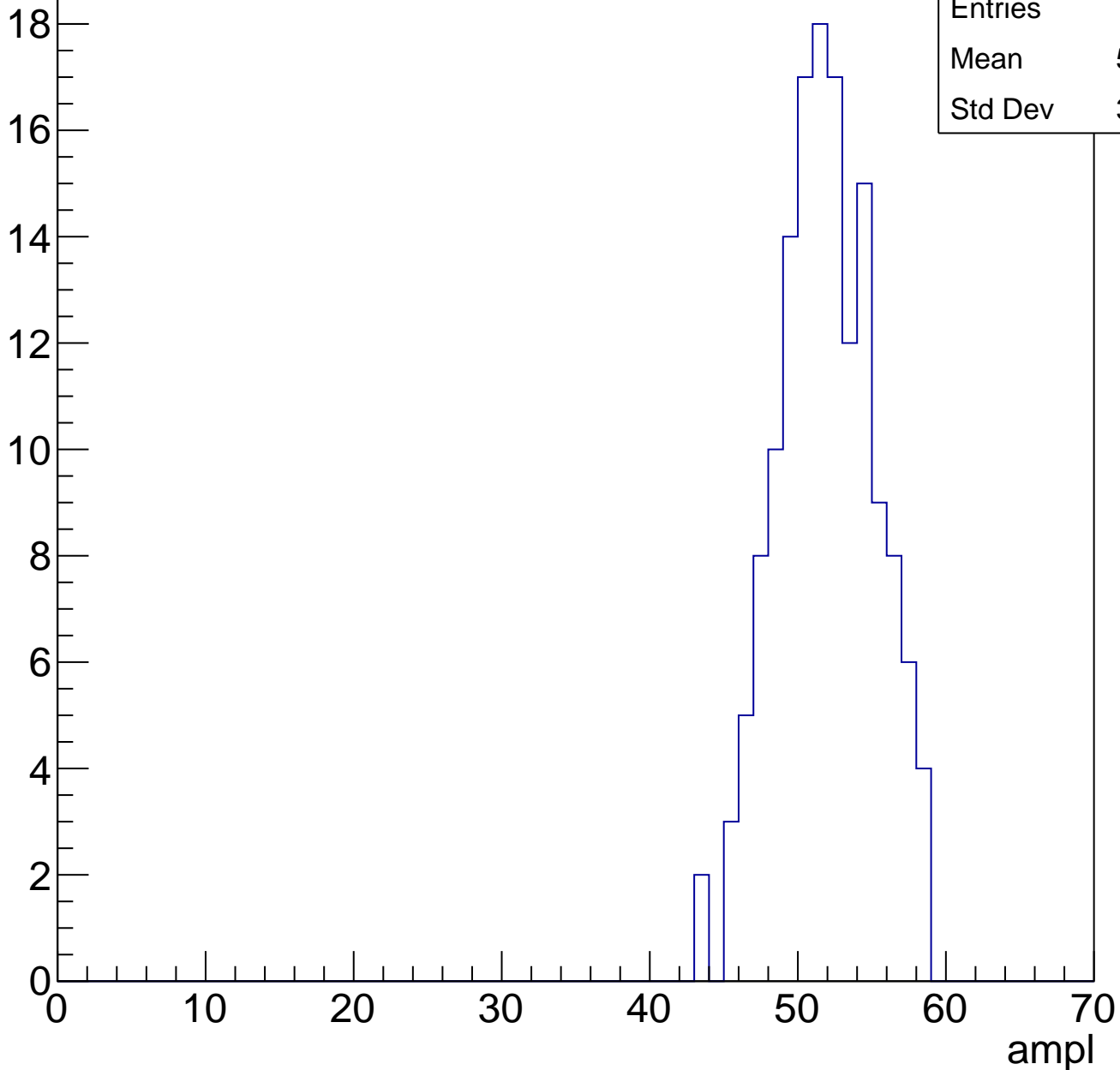


B1L001S, U24-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	51.41
Std Dev	3.271

Entry



B1L001S, U24-ch31, adc4

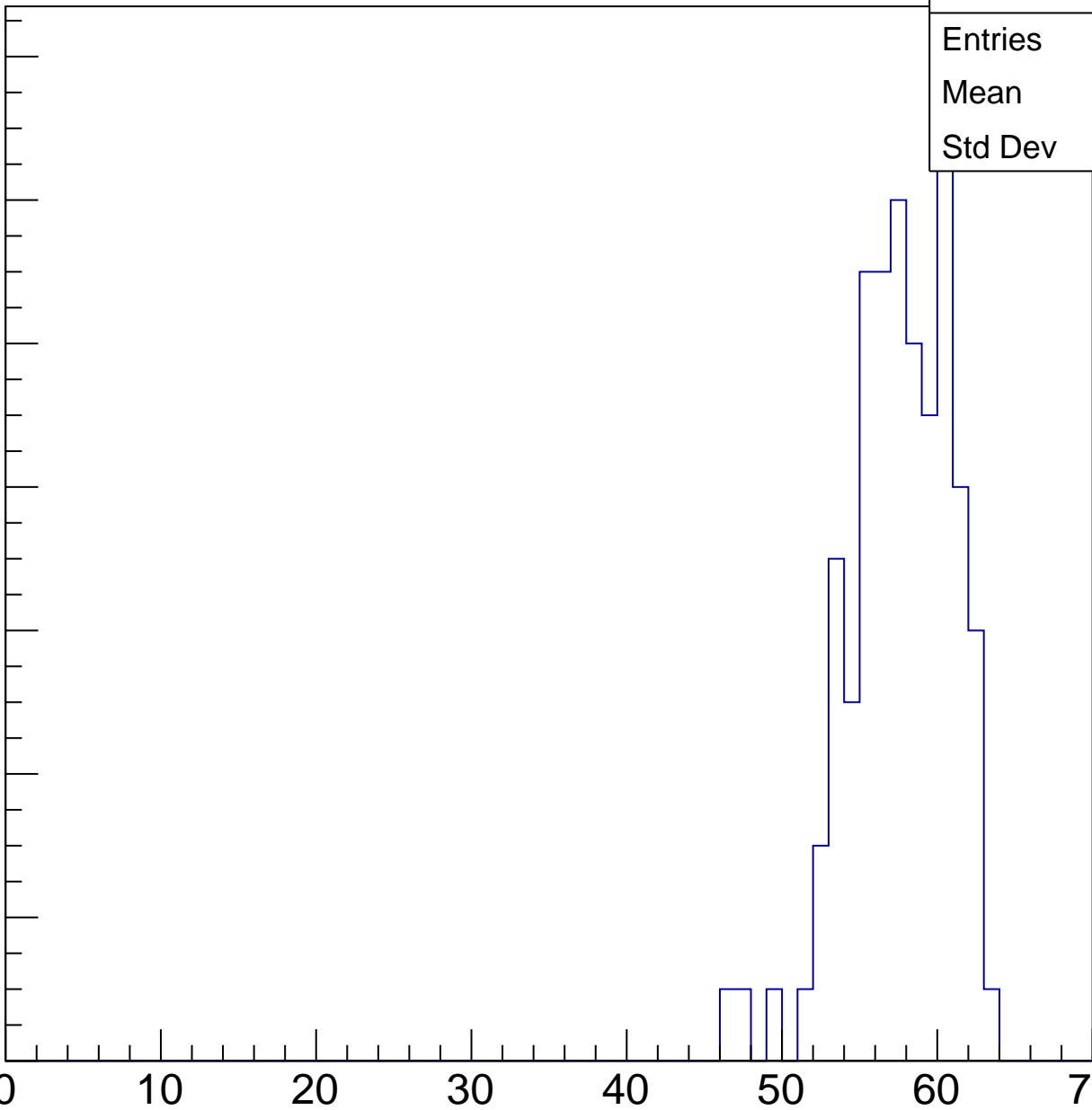
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	57.12
Std Dev	3.289

ampl



B1L001S, U24-ch31, adc5

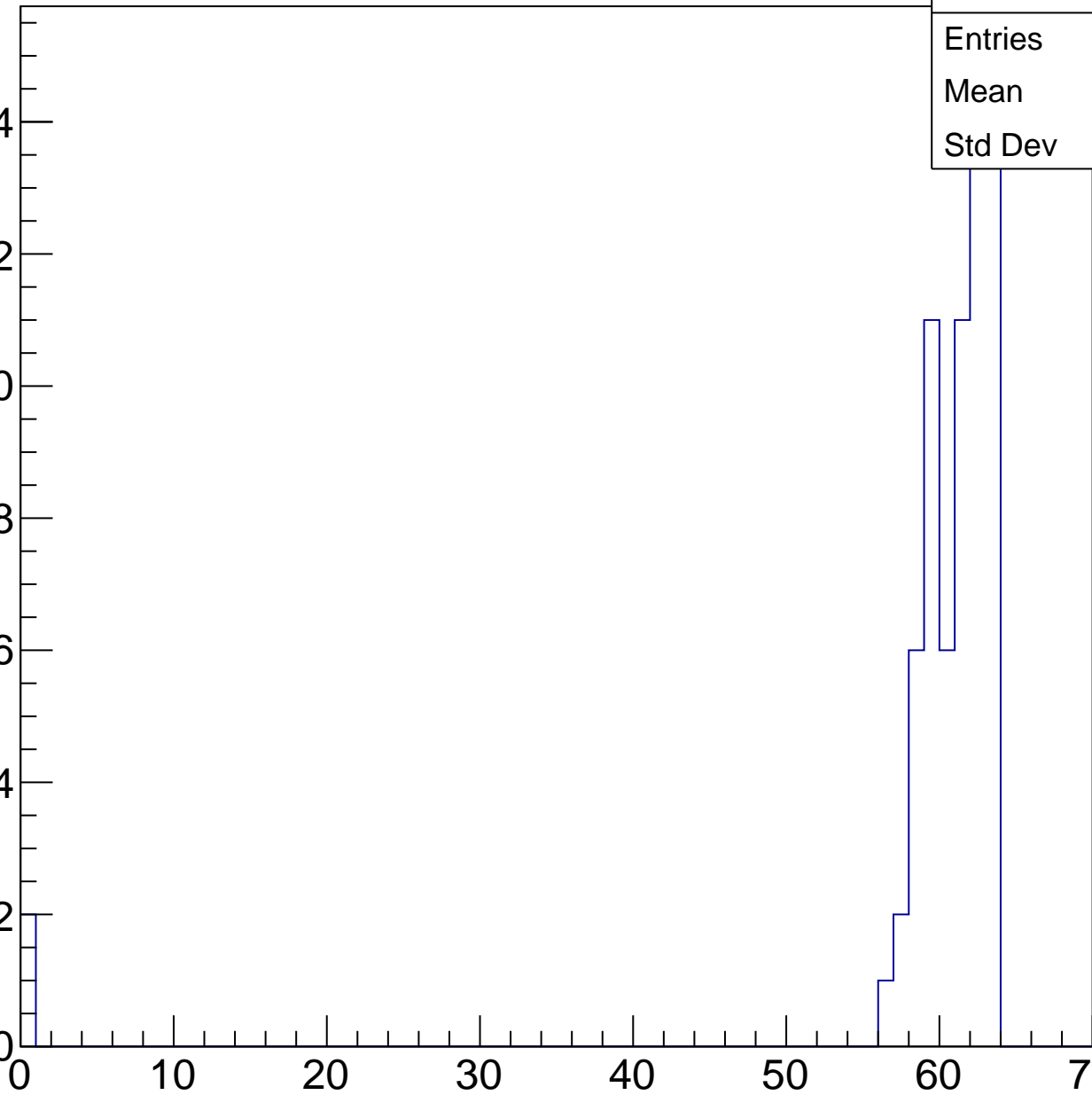
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.99
Std Dev	10.43

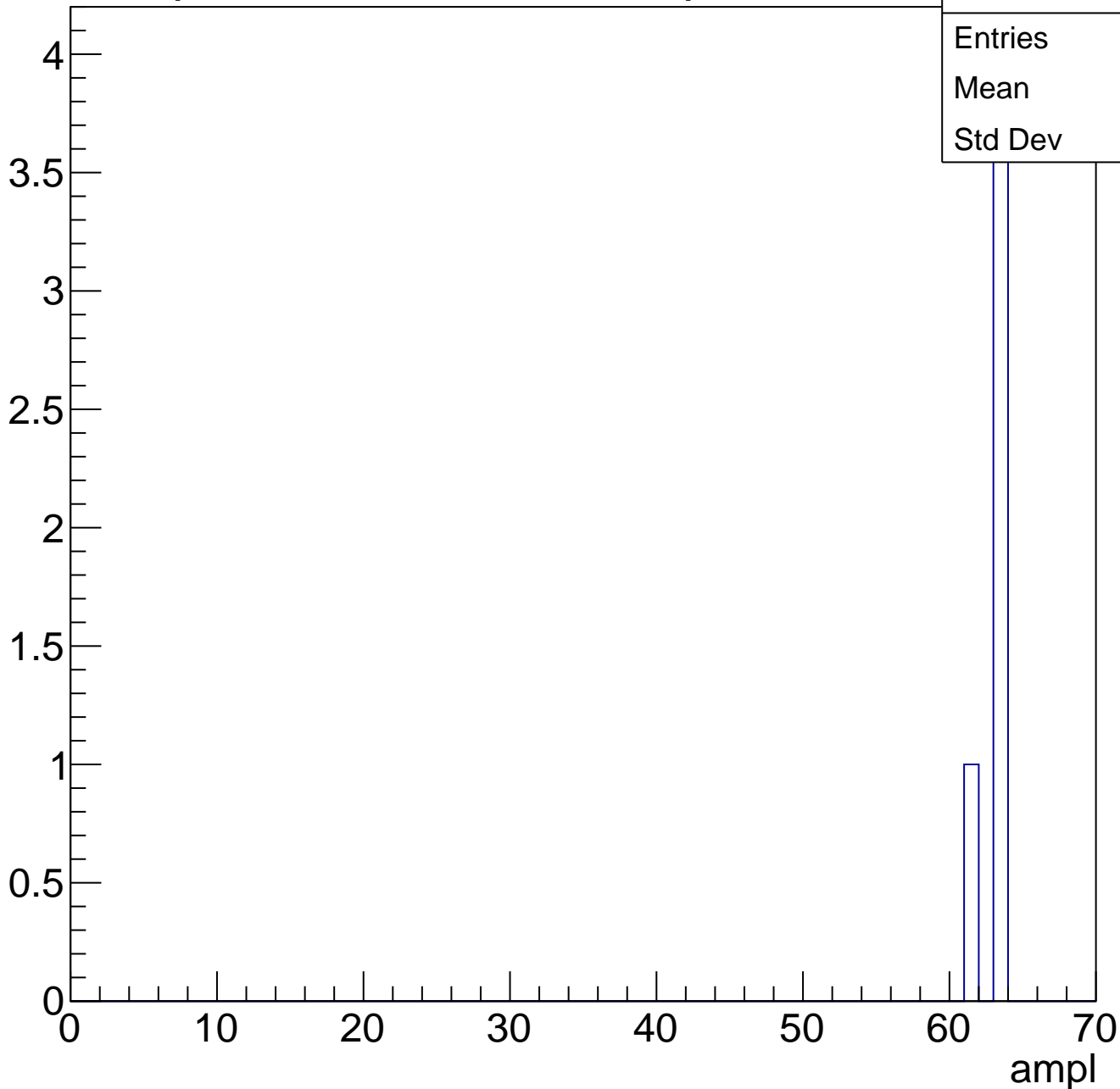
ampl



B1L001S, U24-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch32, adc0

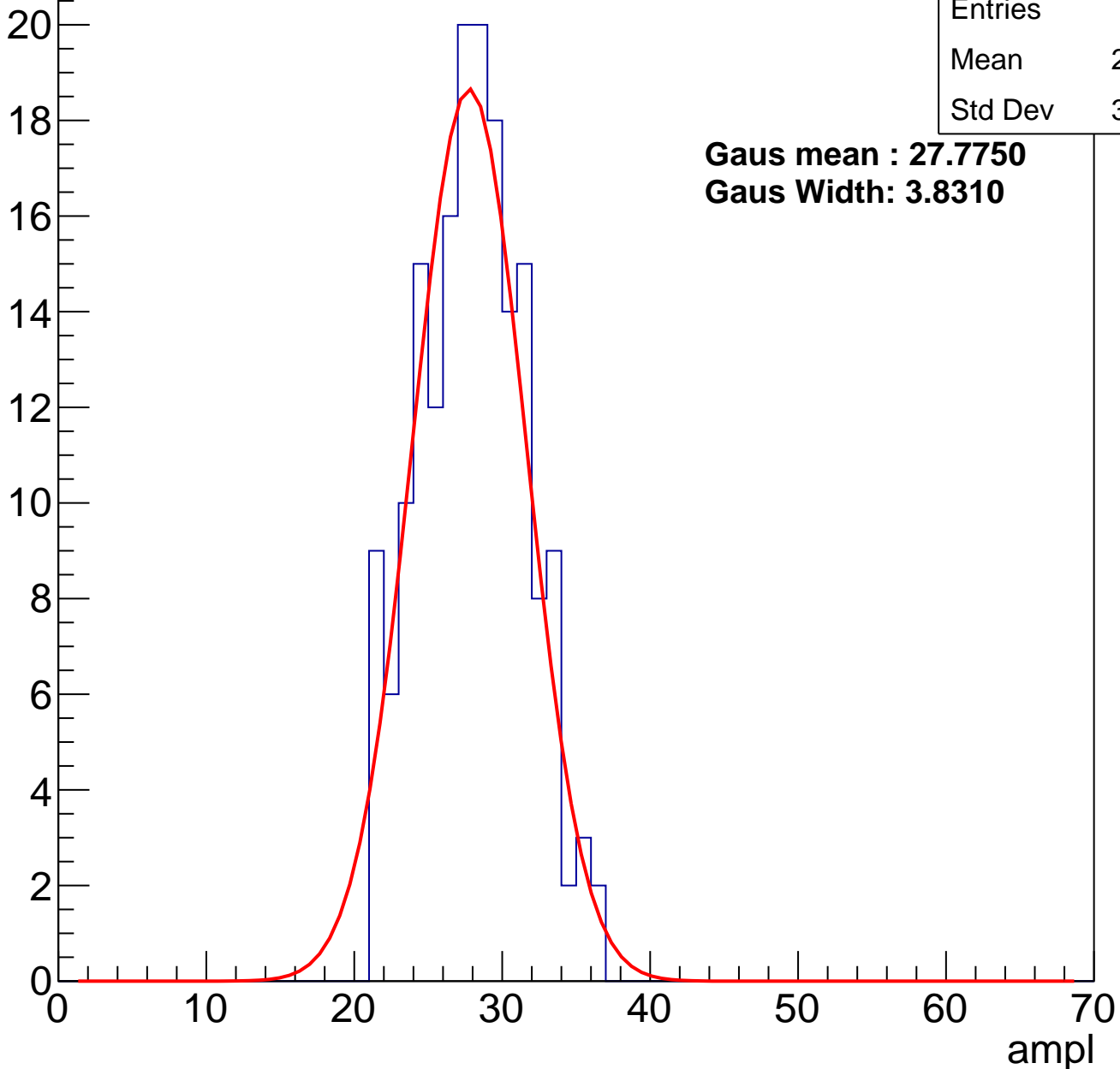
calib_packv5_042523_0143.root, FC#2, port C2

Entries	179
Mean	27.55
Std Dev	3.516

Gaus mean : 27.7750

Gaus Width: 3.8310

Entry



B1L001S, U24-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

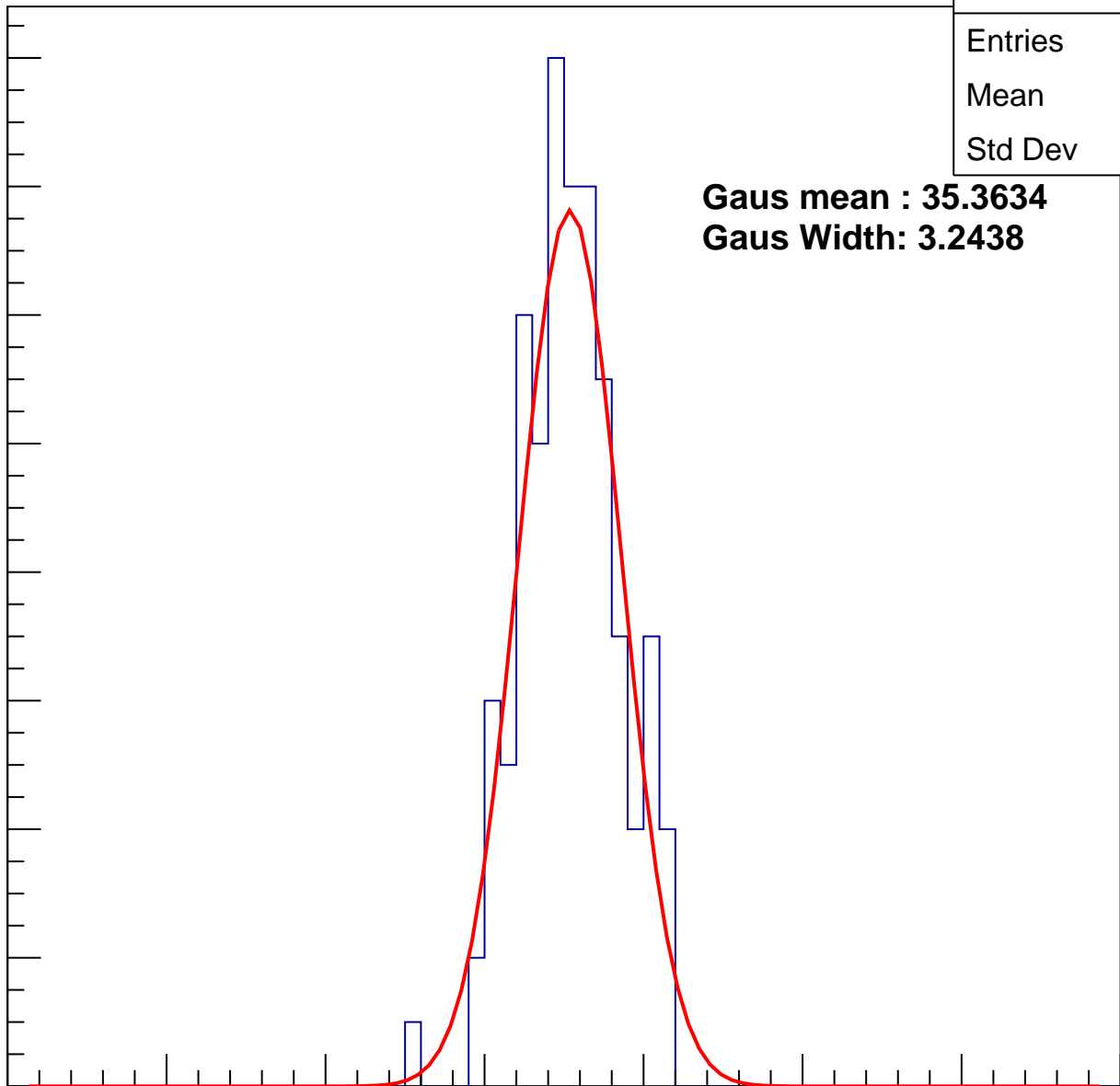
Entries	113
Mean	34.89
Std Dev	3.075

Gaus mean : 35.3634

Gaus Width: 3.2438

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch32, adc2

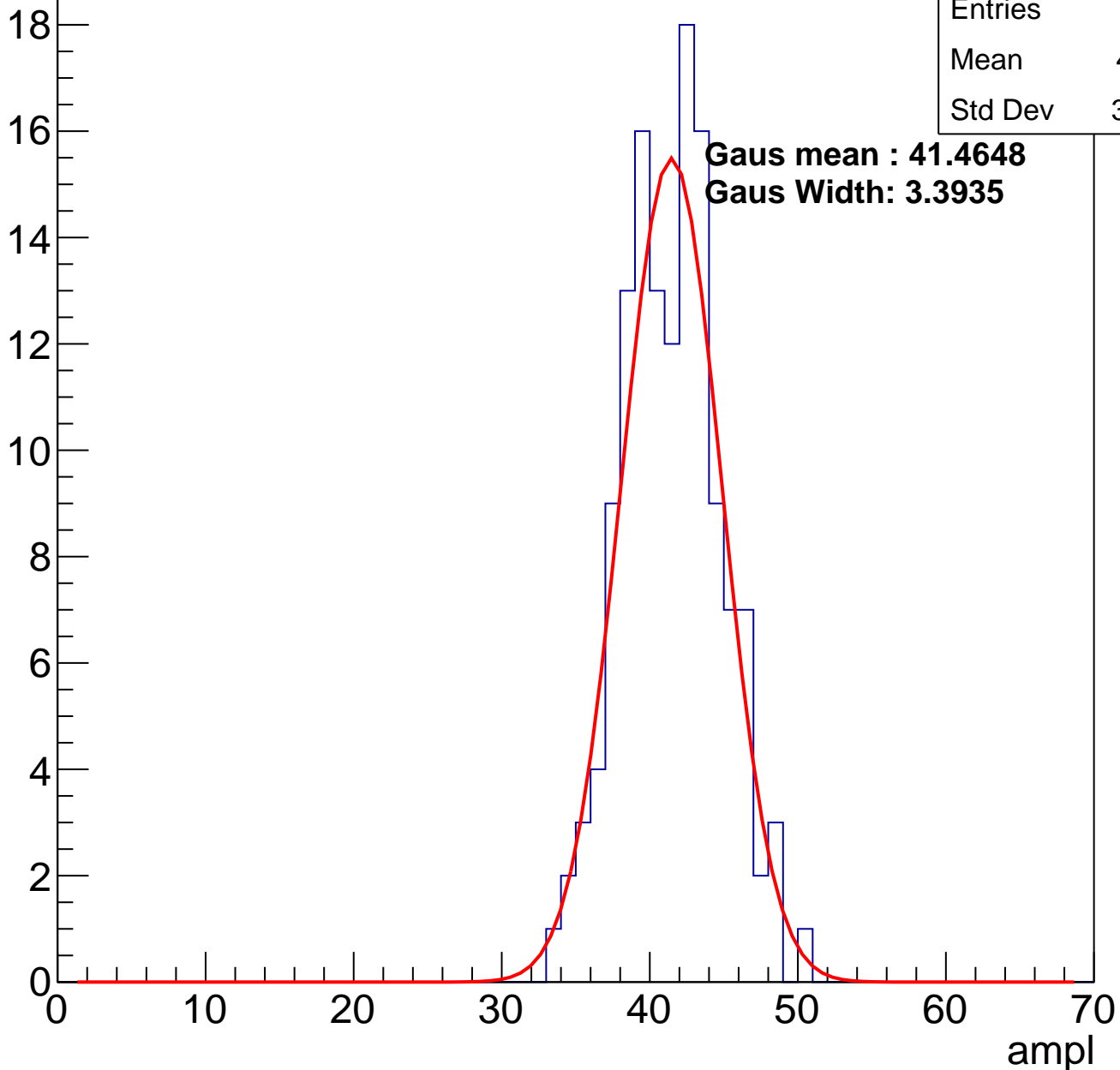
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	41.01
Std Dev	3.274

Gaus mean : 41.4648

Gaus Width: 3.3935

Entry



B1L001S, U24-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	47.46
Std Dev	2.967

Entry

14

12

10

8

6

4

2

0

0

10

20

30

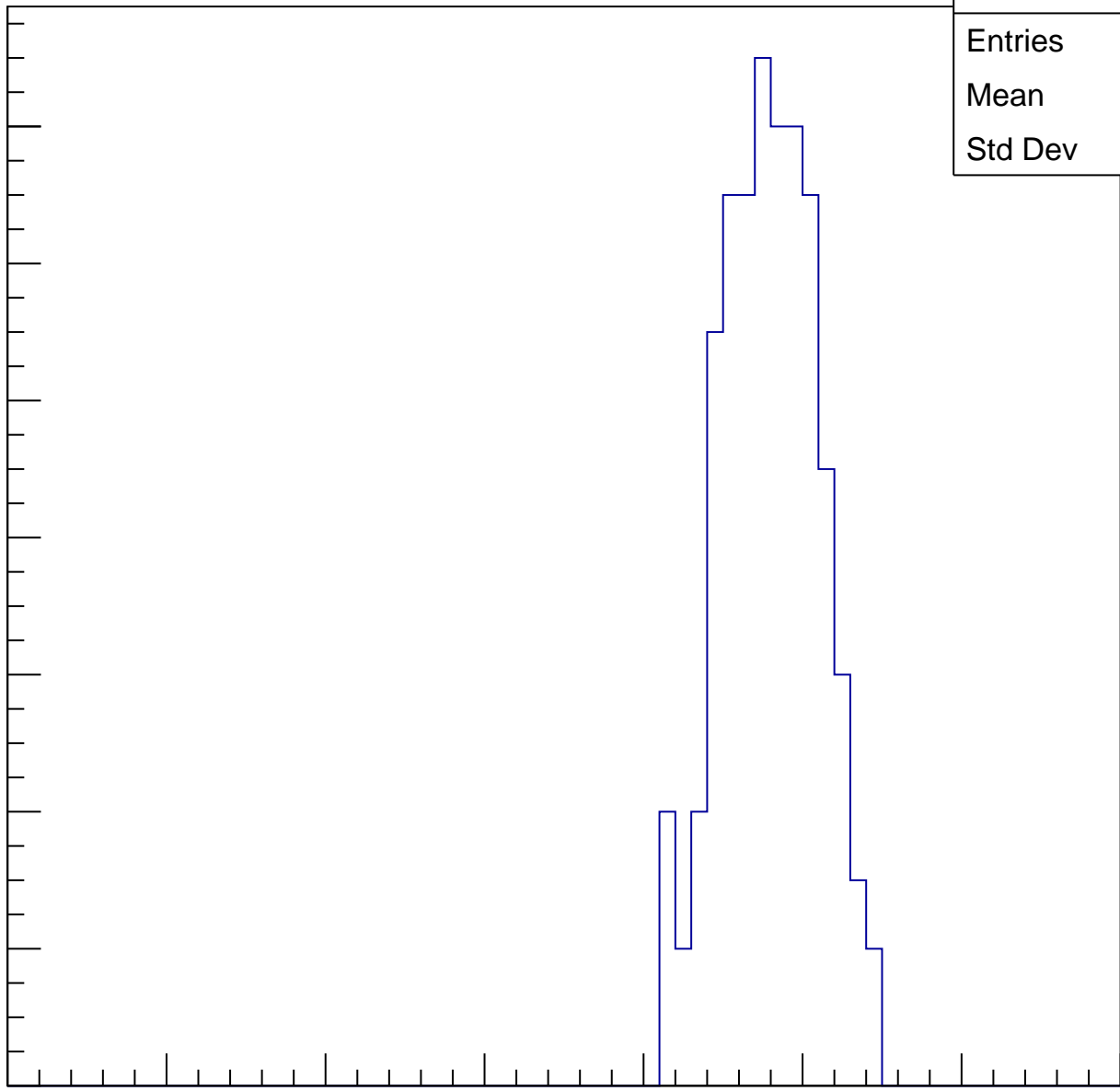
40

50

60

70

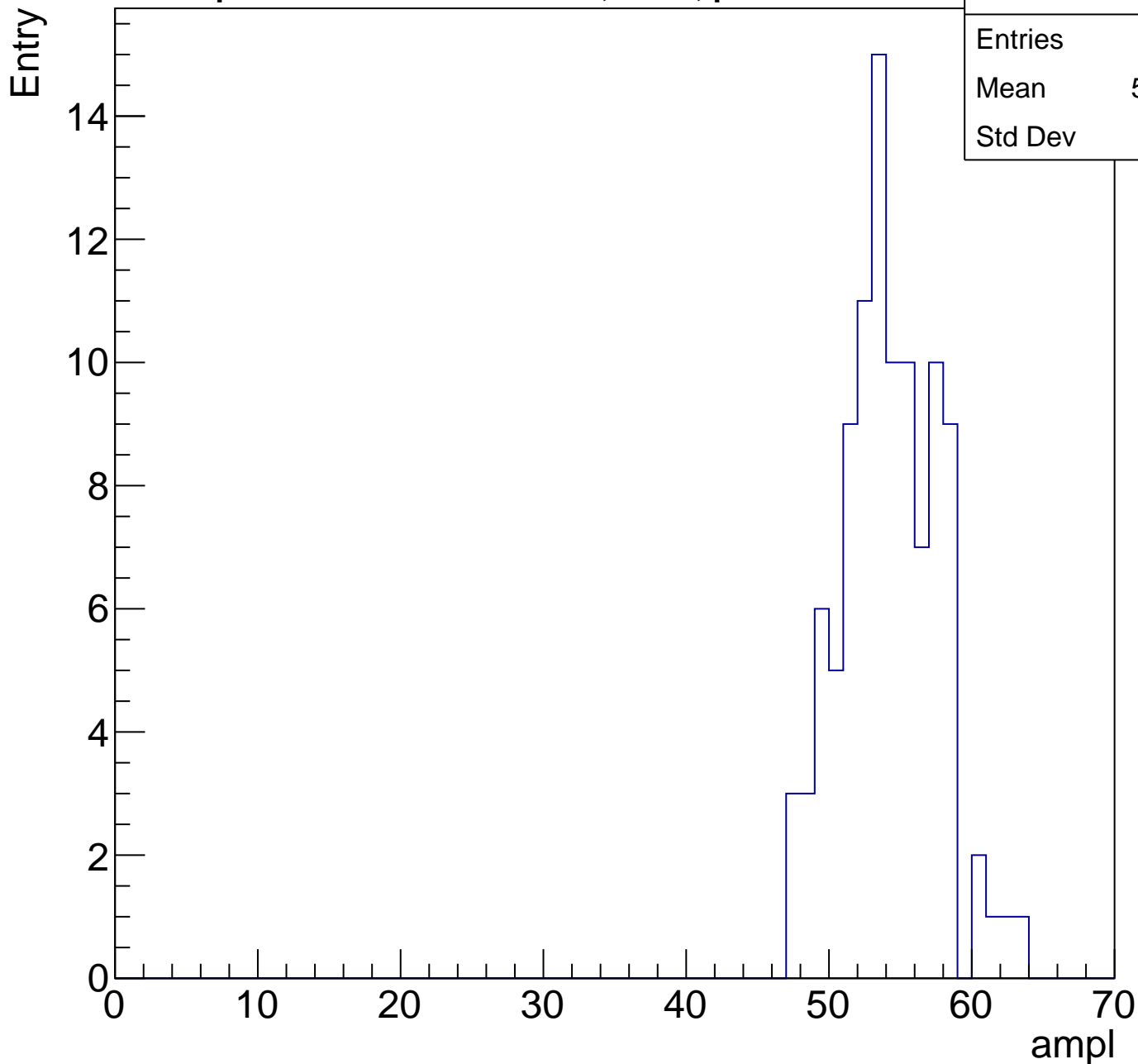
ampl



B1L001S, U24-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	53.74
Std Dev	3.35



B1L001S, U24-ch32, adc5

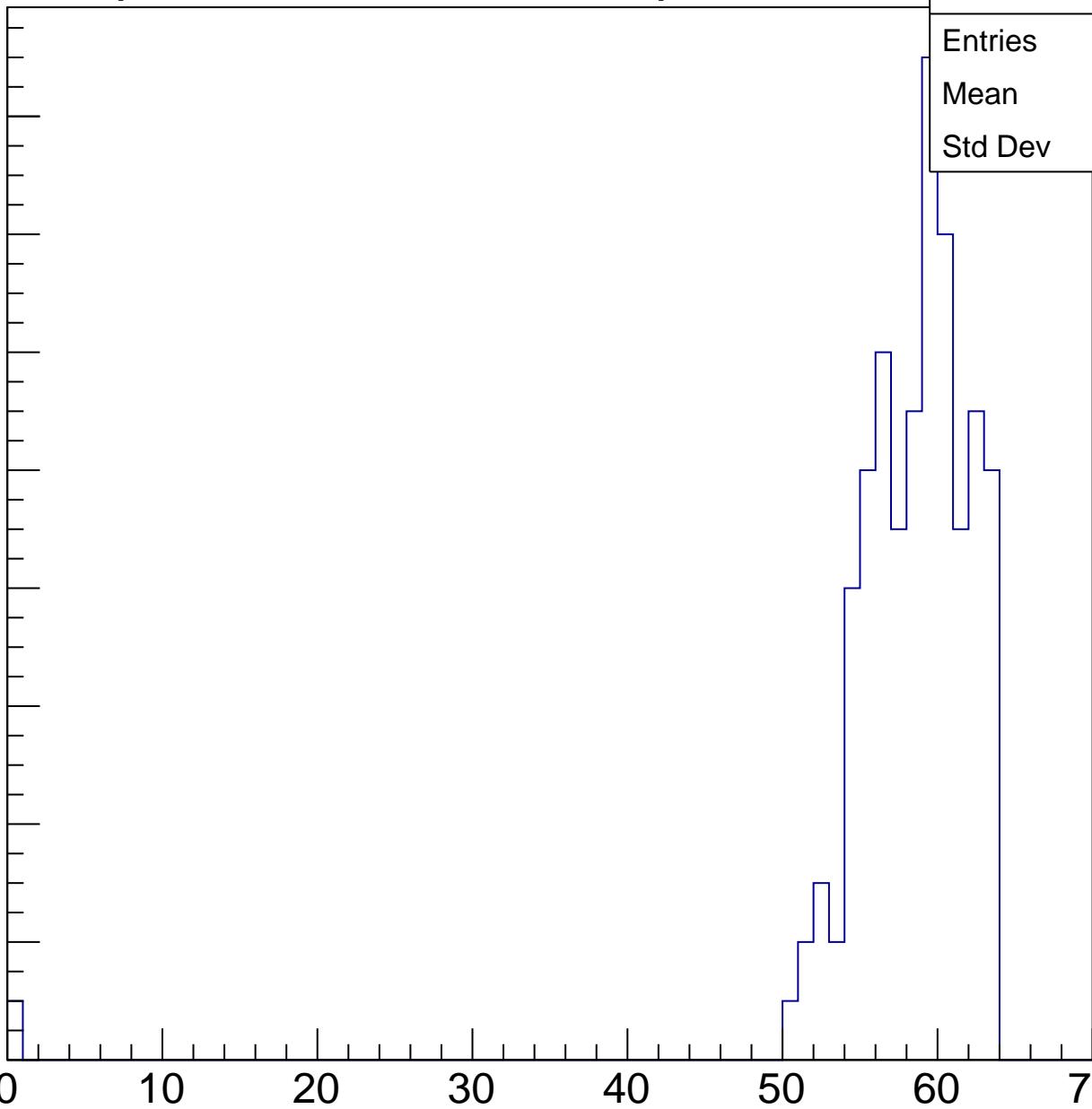
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	57.69
Std Dev	6.142

ampl

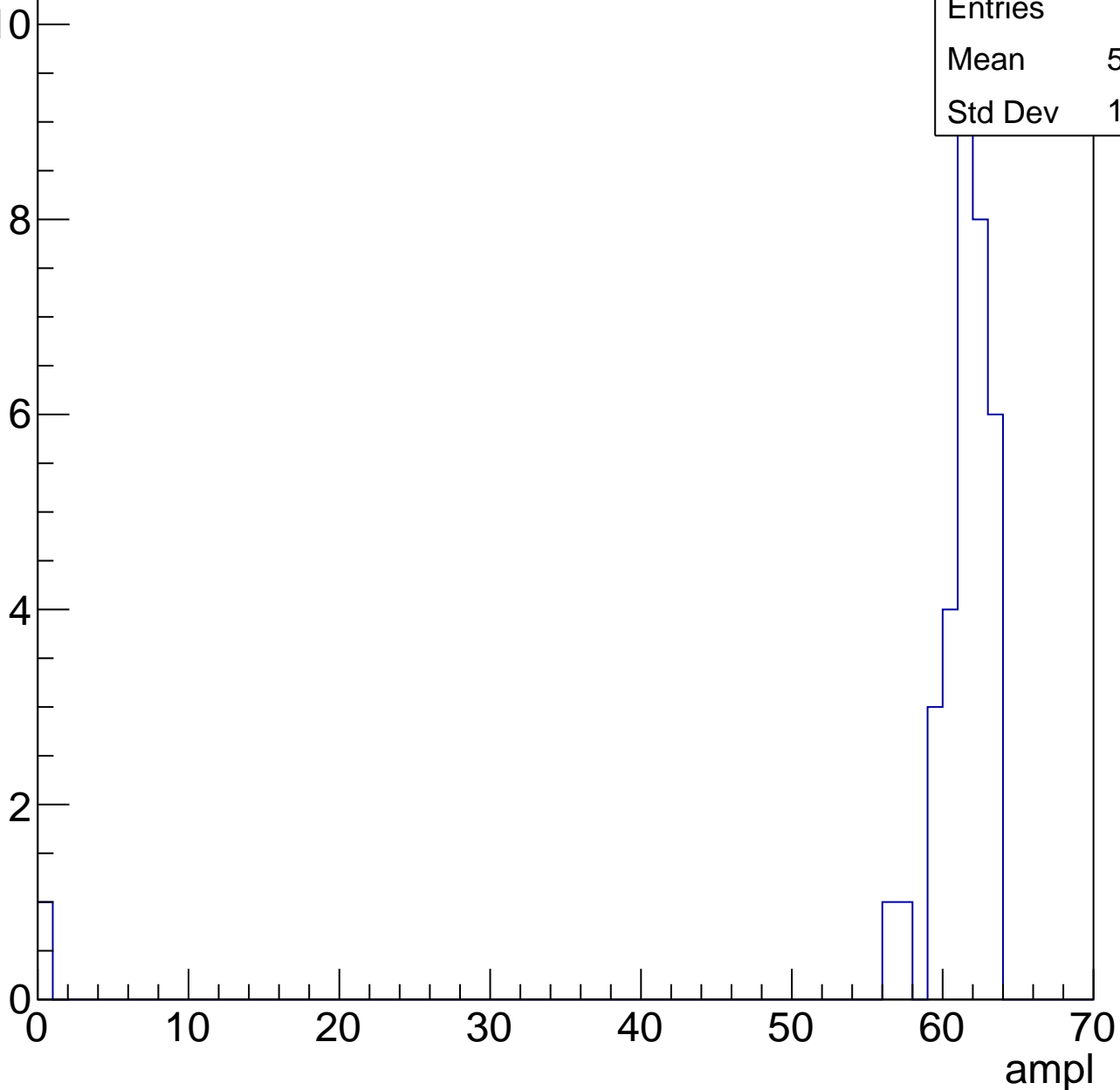


B1L001S, U24-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

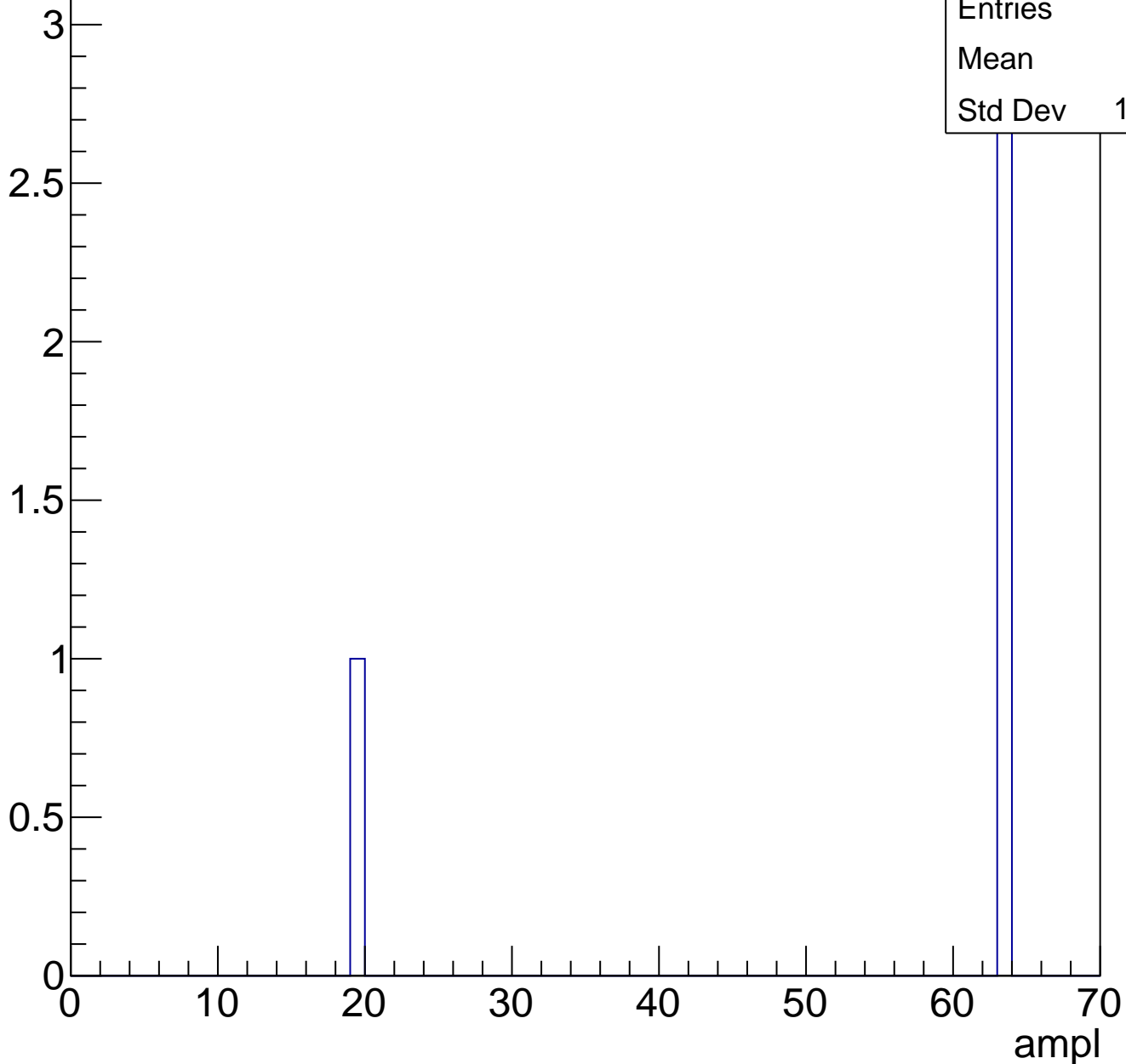
Entries	34
Mean	59.24
Std Dev	10.44



B1L001S, U24-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch33, adc0

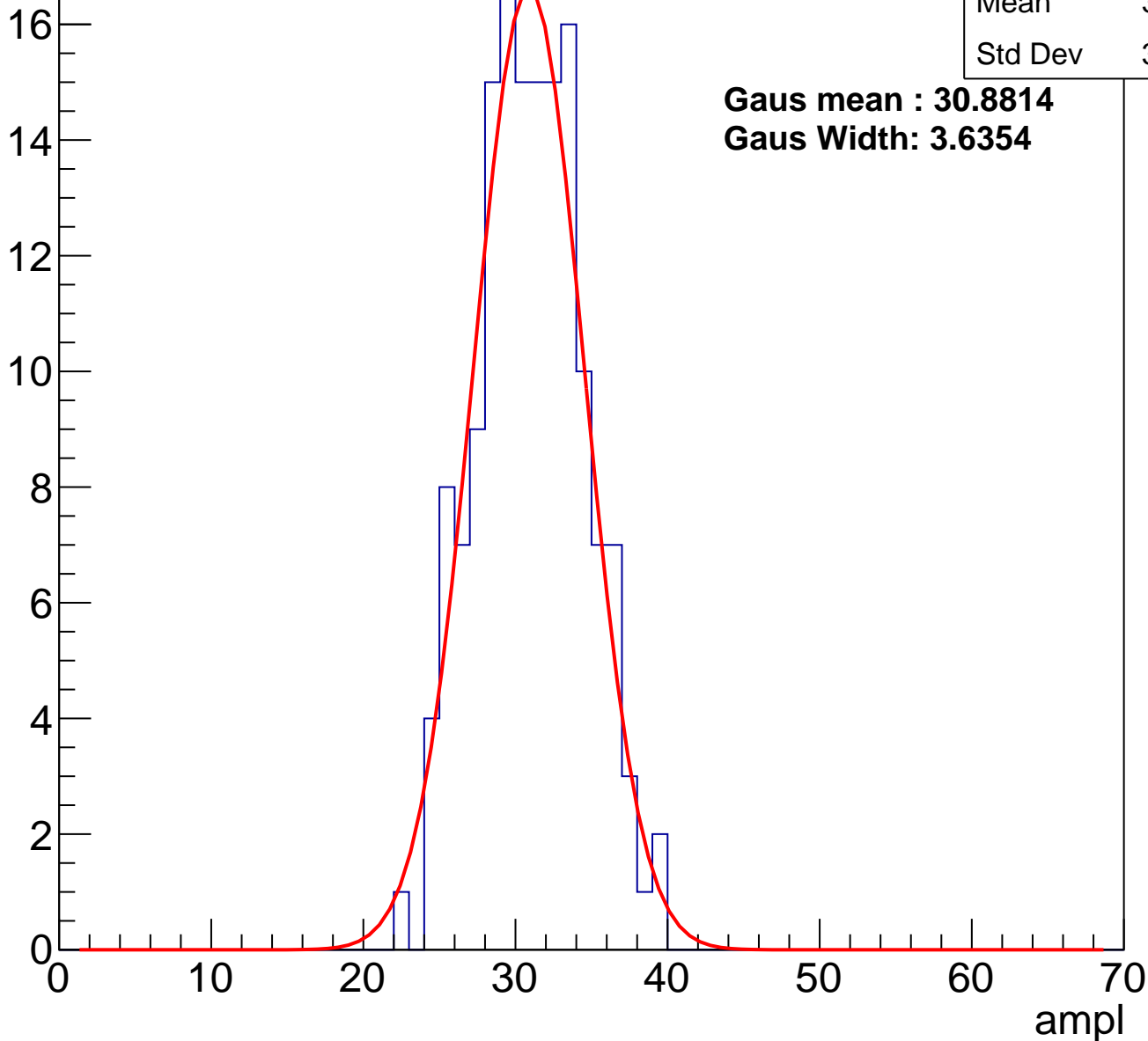
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	152
Mean	30.55
Std Dev	3.452

Gaus mean : 30.8814

Gaus Width: 3.6354

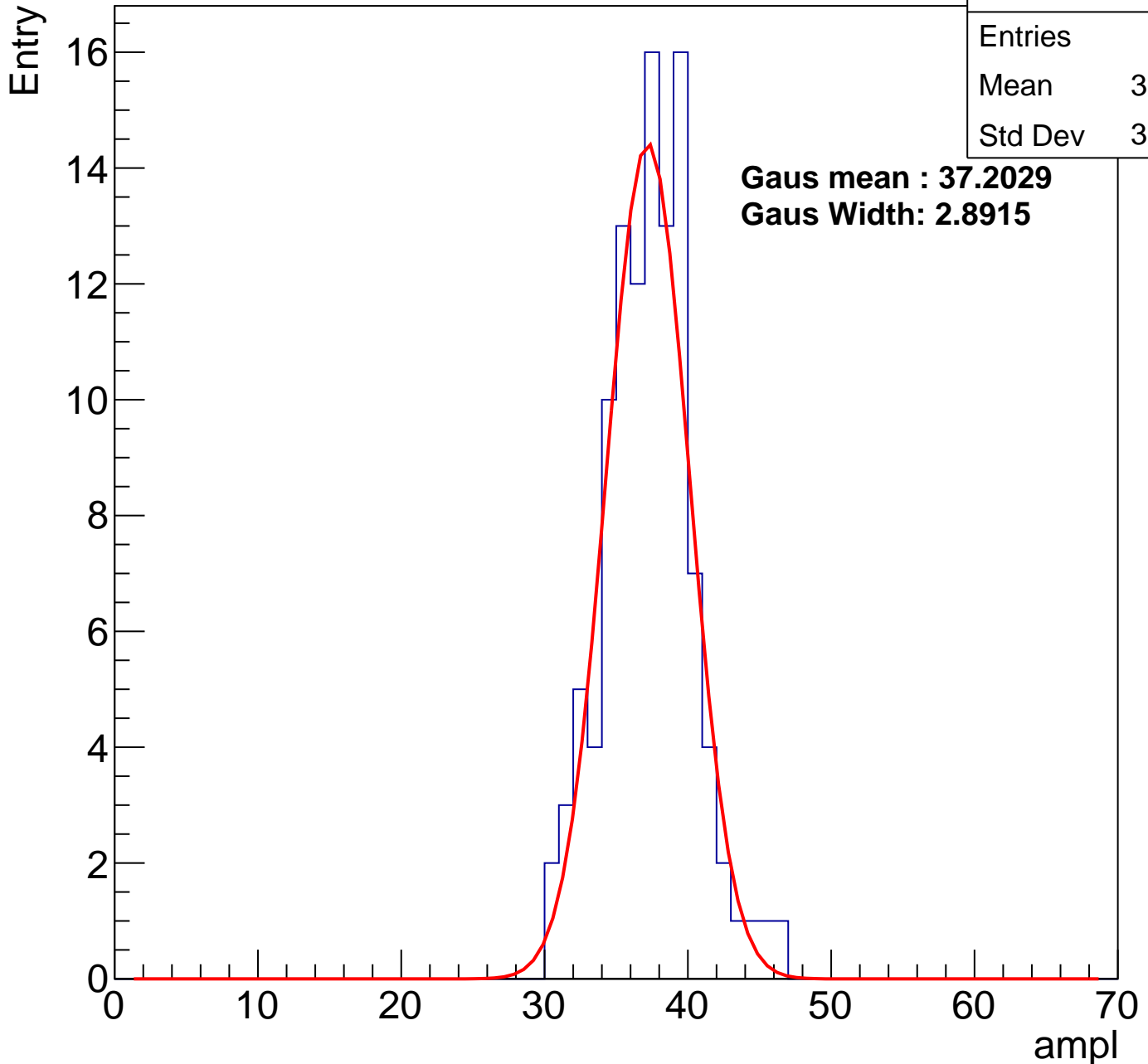


B1L001S, U24-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.83
Std Dev	3.046

Gaus mean : 37.2029
Gaus Width: 2.8915



B1L001S, U24-ch33, adc2

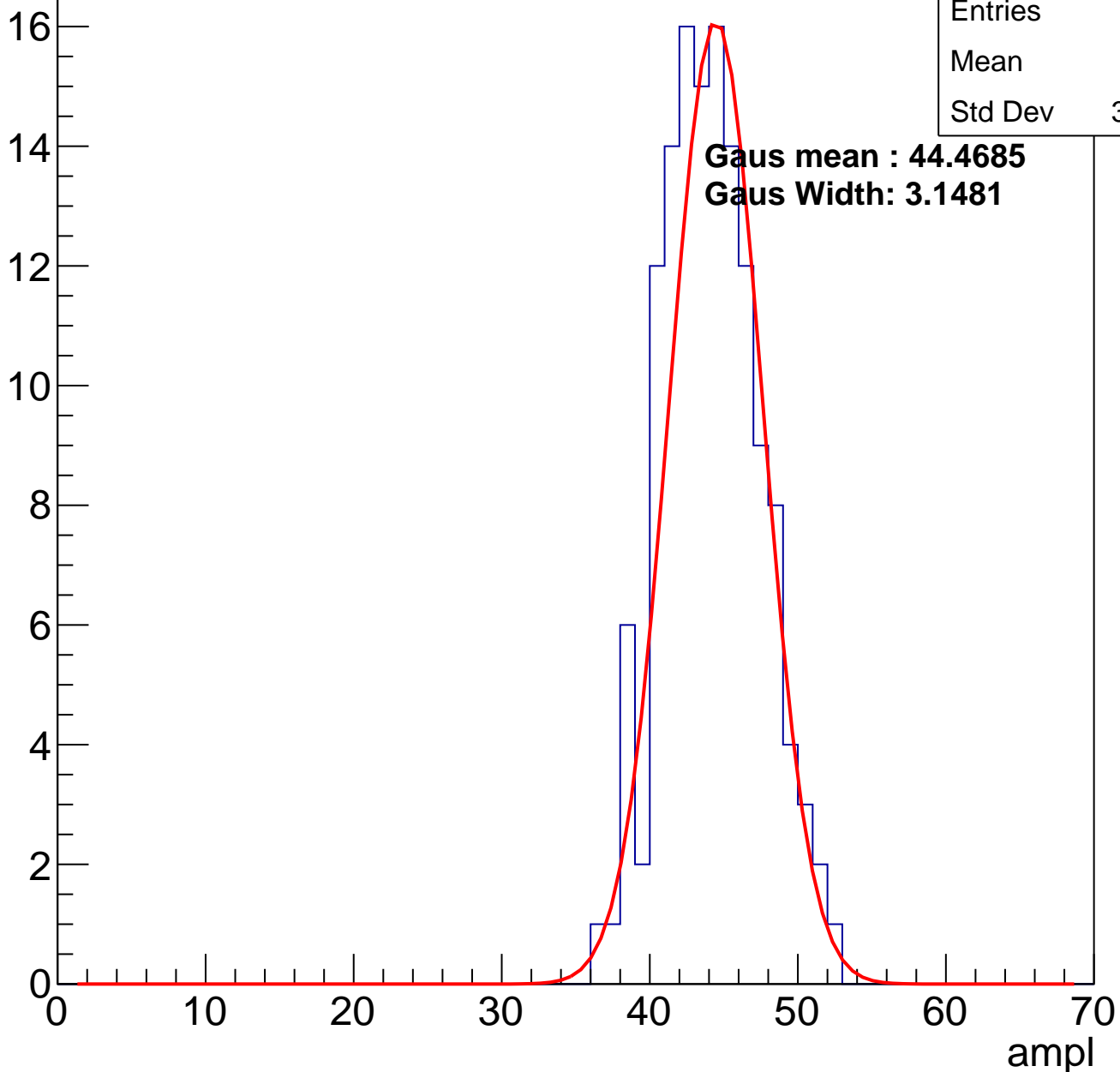
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.7
Std Dev	3.214

Entry

Gaus mean : 44.4685

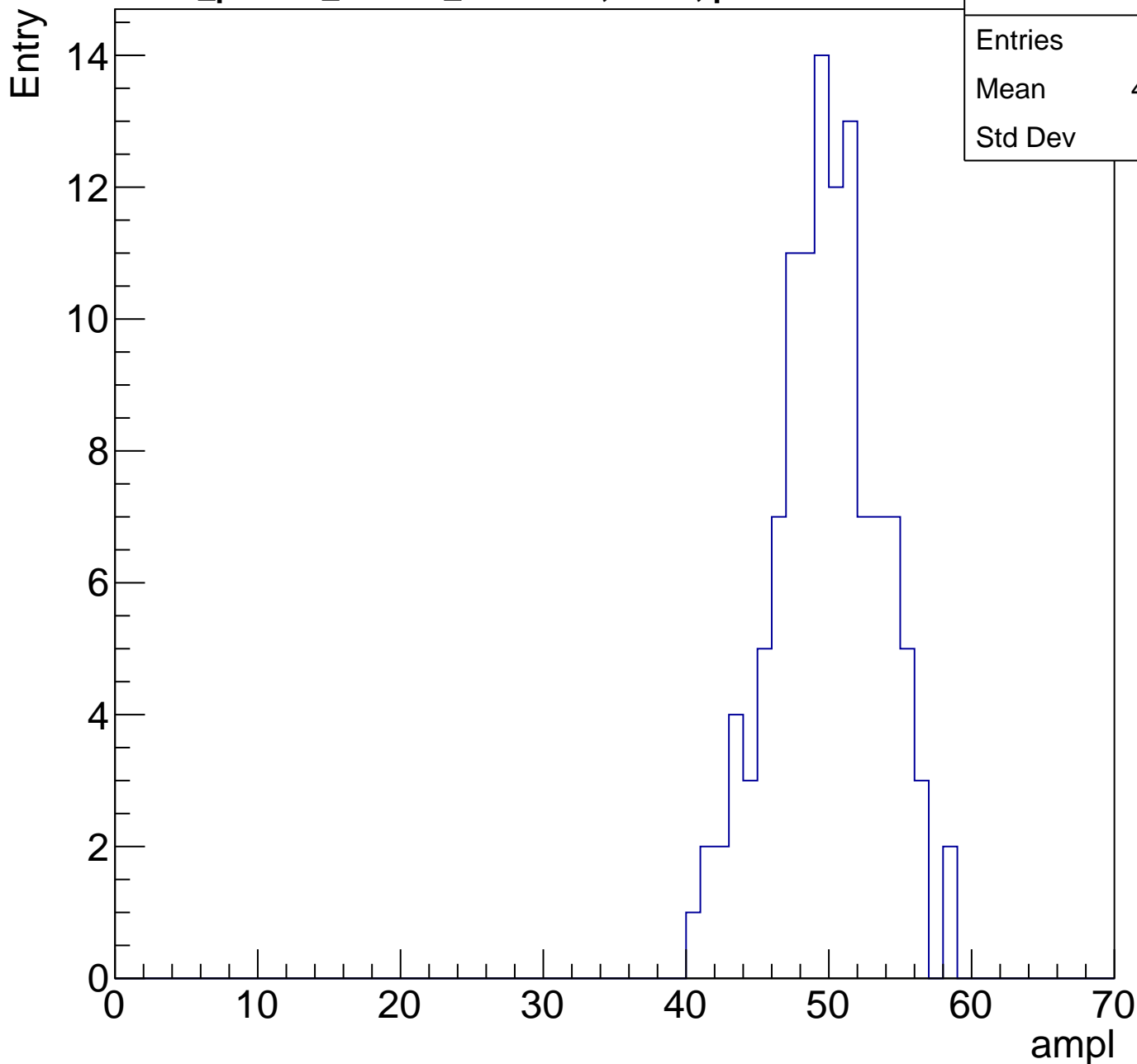
Gaus Width: 3.1481



B1L001S, U24-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	49.34
Std Dev	3.73



B1L001S, U24-ch33, adc4

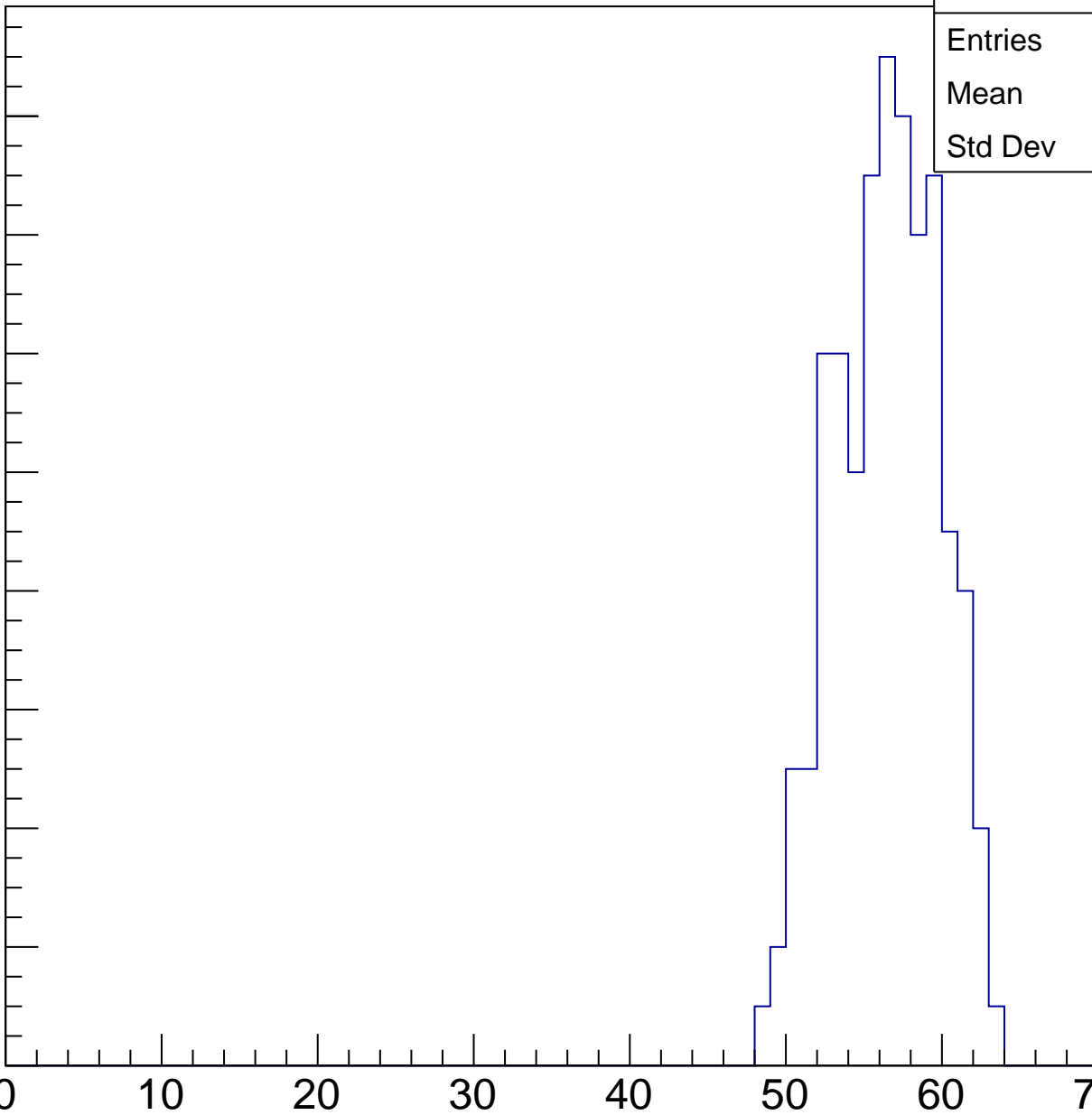
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	146
Mean	56
Std Dev	3.275

ampl



B1L001S, U24-ch33, adc5

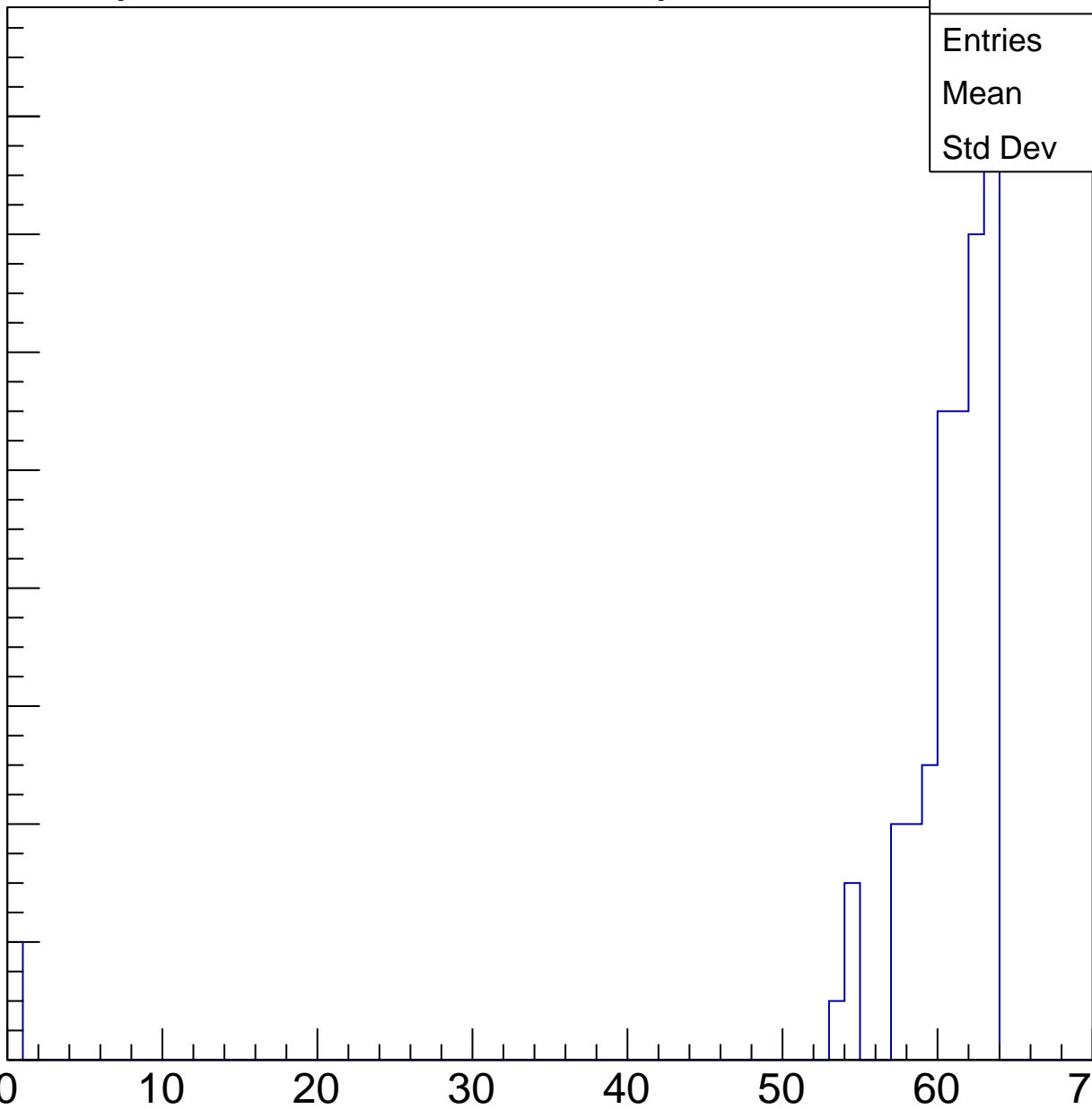
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	72
Mean	58.89
Std Dev	10.24

ampl



B1L001S, U24-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch33, adc7

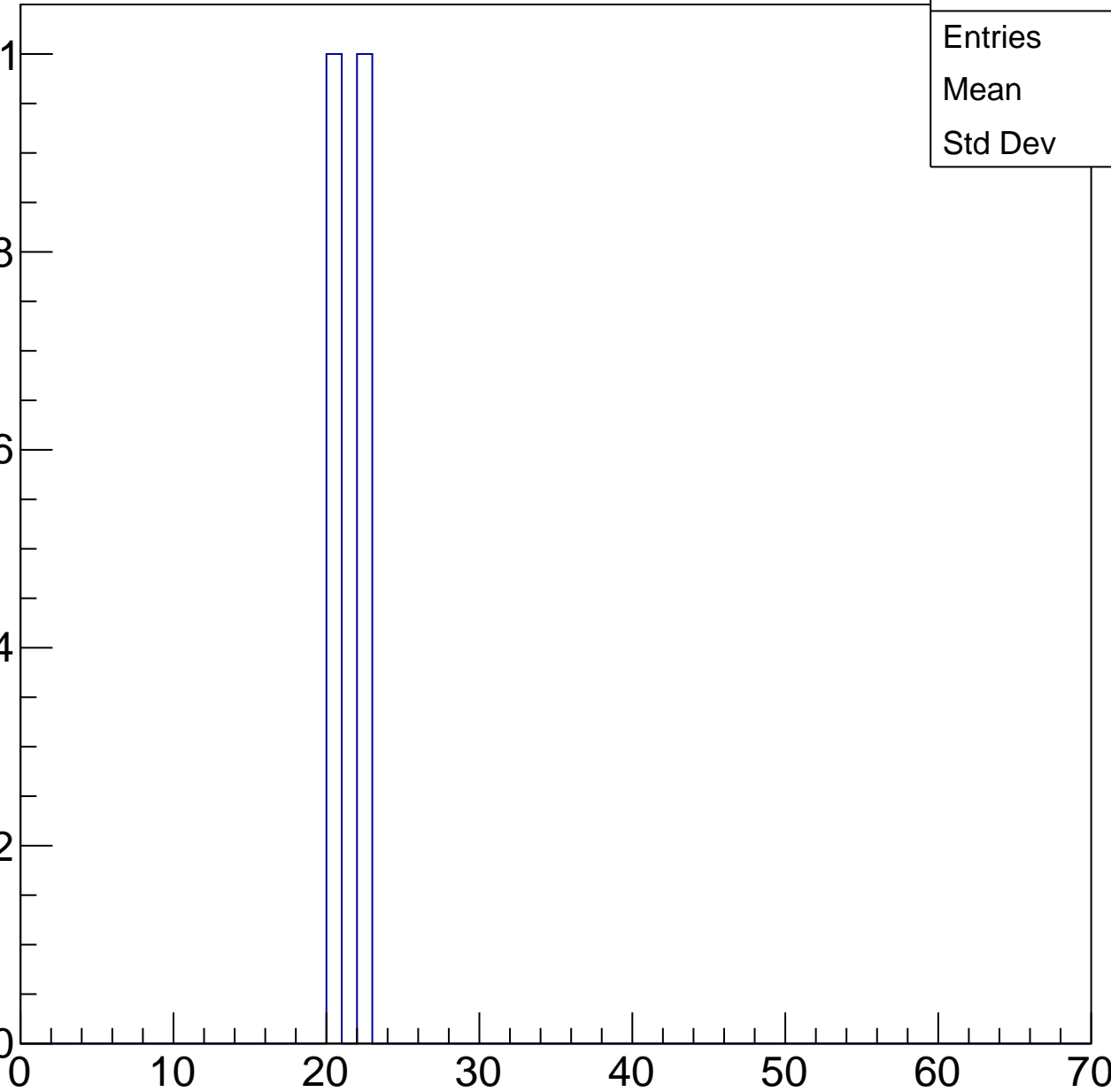
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	21
Std Dev	1

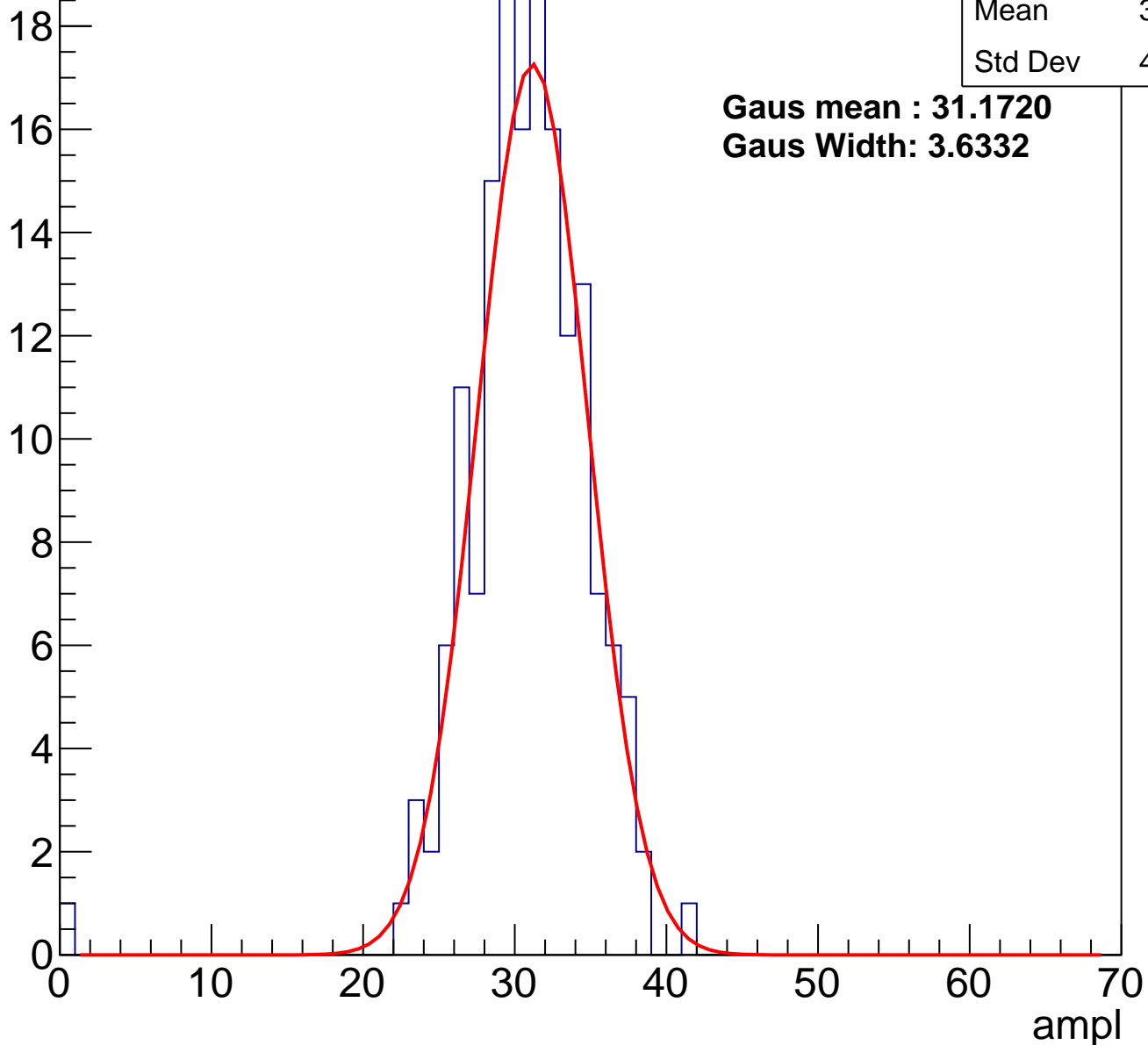
ampl



B1L001S, U24-ch34, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch34, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	38.13
Std Dev	3.333

Gaus mean : 38.6299

Gaus Width: 3.3316

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

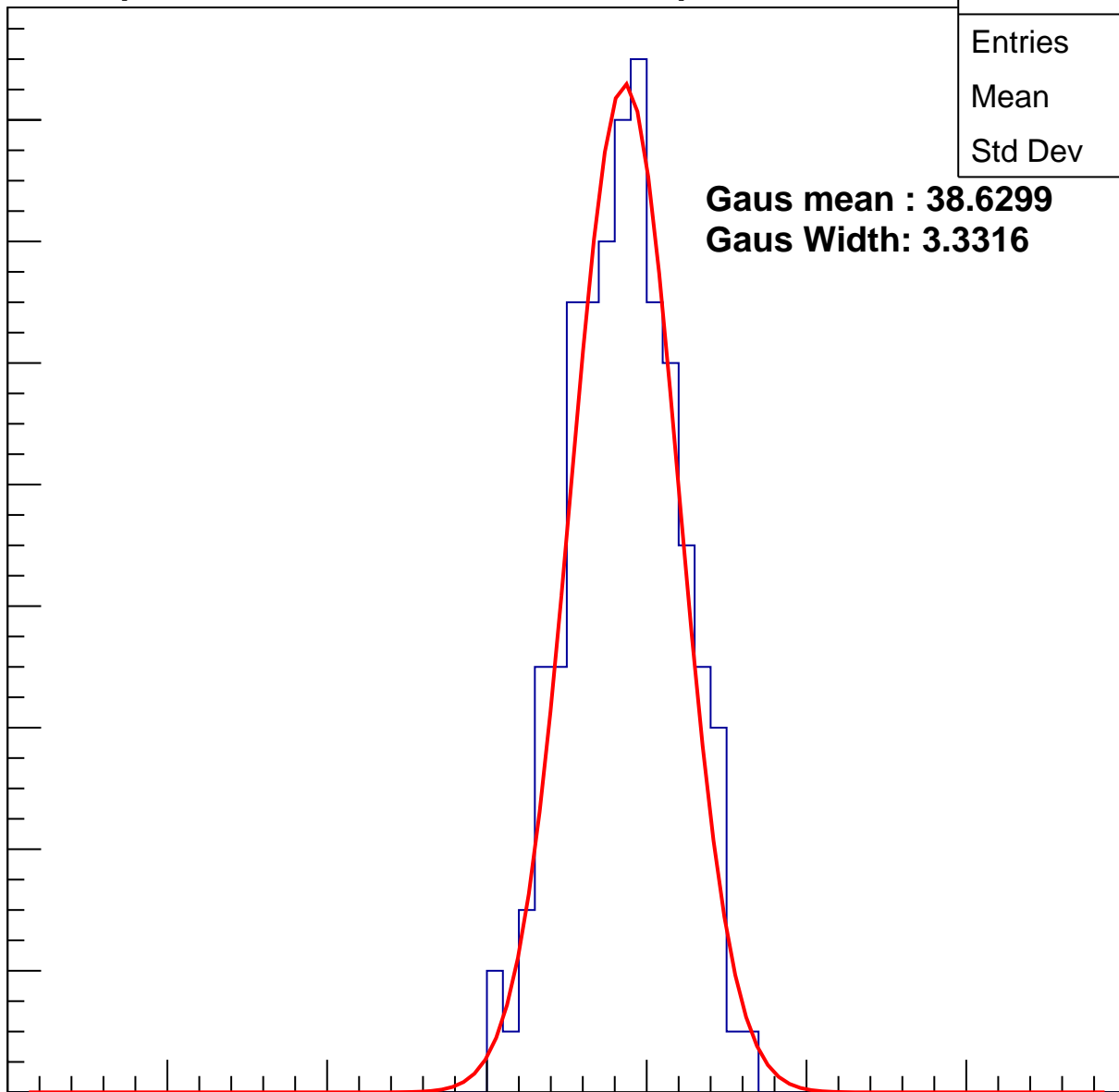
40

50

60

70

ampl



B1L001S, U24-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	44.26
Std Dev	3.242

Gaus mean : 44.9280

Gaus Width: 3.1675

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

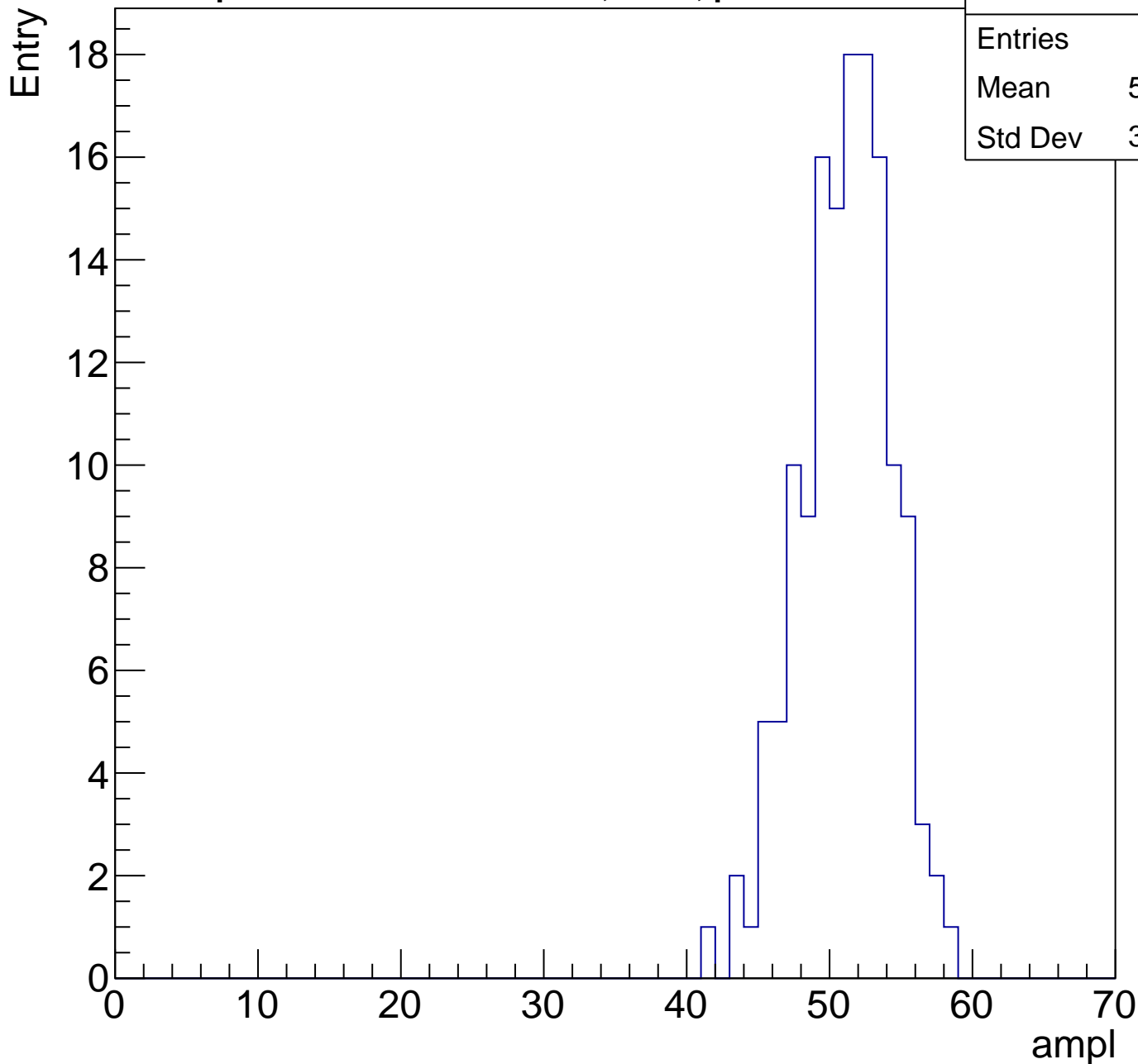
60

70

B1L001S, U24-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	50.63
Std Dev	3.145



B1L001S, U24-ch34, adc4

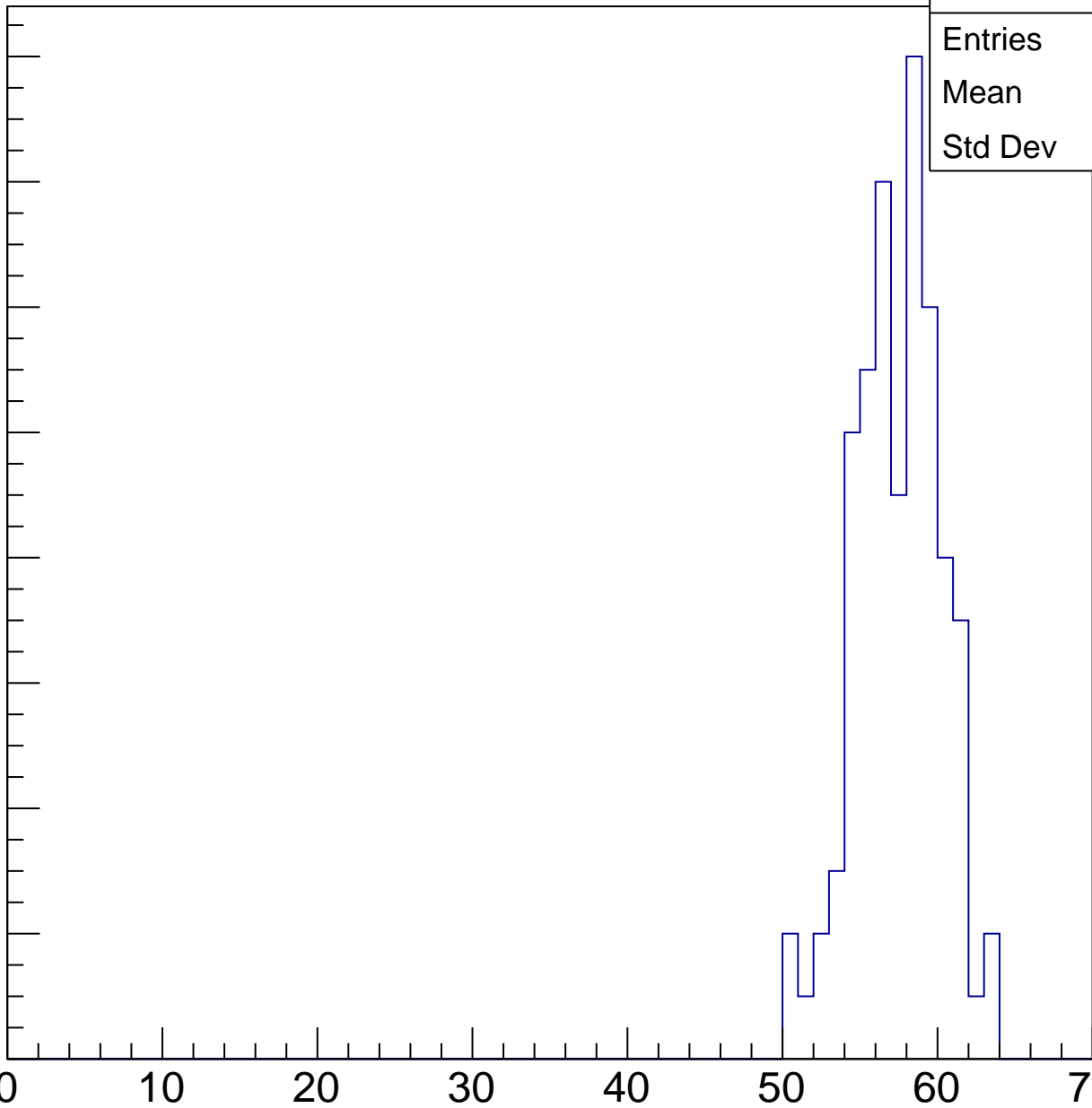
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	57.01
Std Dev	2.72

ampl



B1L001S, U24-ch34, adc5

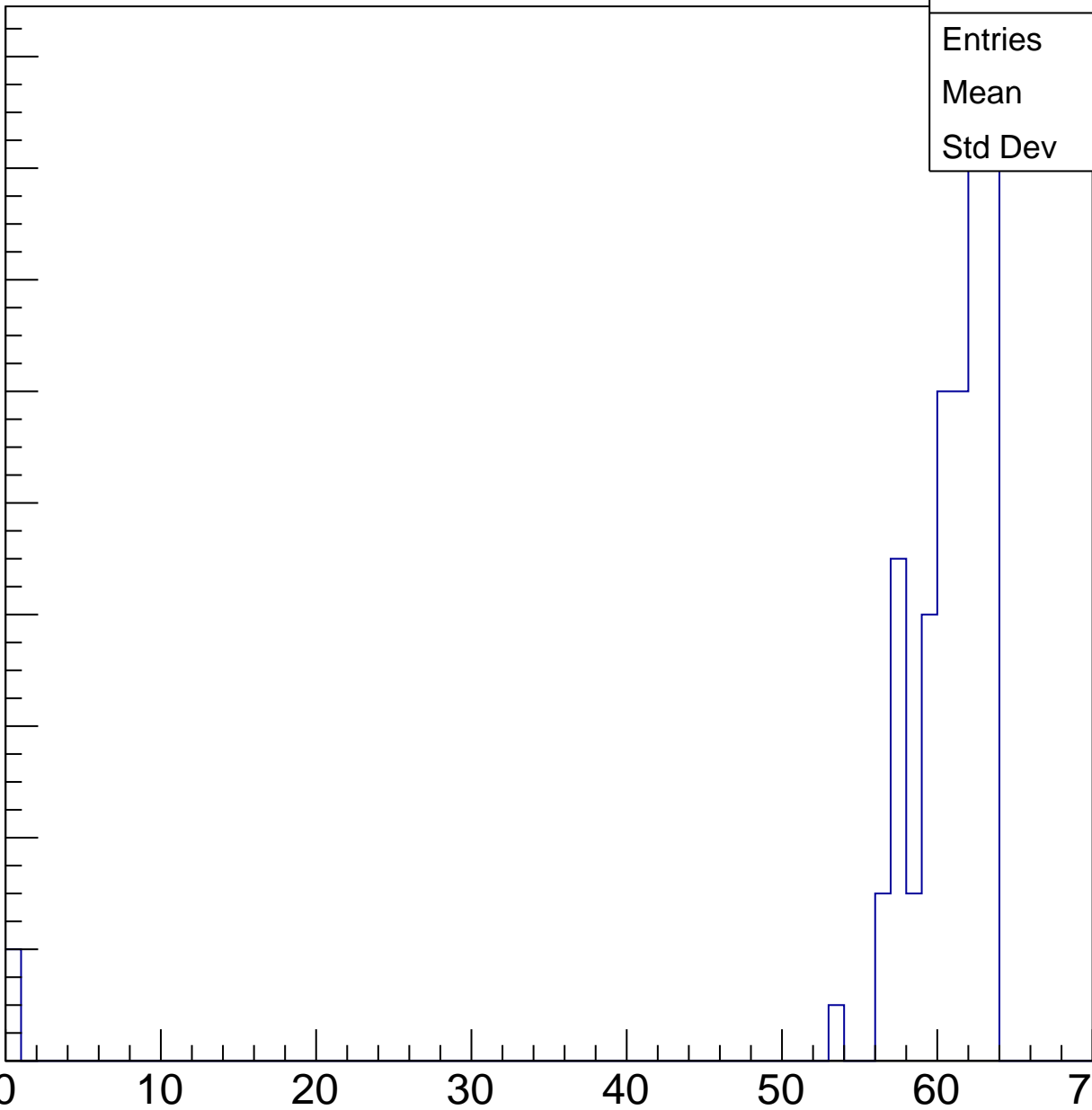
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.05
Std Dev	9.429

ampl



B1L001S, U24-ch34, adc6

calib_packv5_042523_0143.root, FC#2, port C2

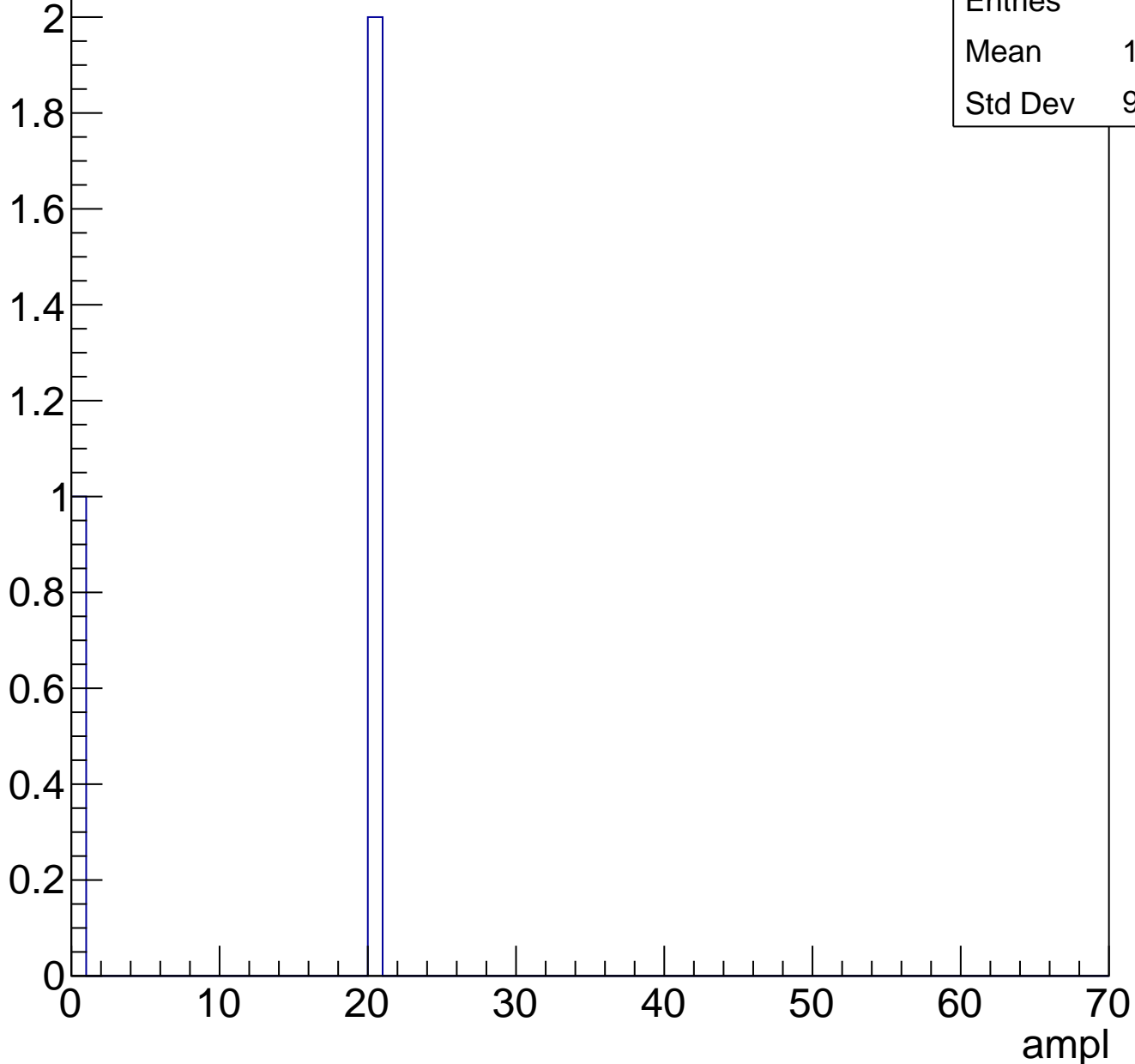
Entry



B1L001S, U24-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	13.33
Std Dev	9.428

B1L001S, U24-ch35, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	30.06
Std Dev	4.135

Gaus mean : 30.6171
Gaus Width: 2.7830

Entry

12

10

8

6

4

2

0

0

10

20

30

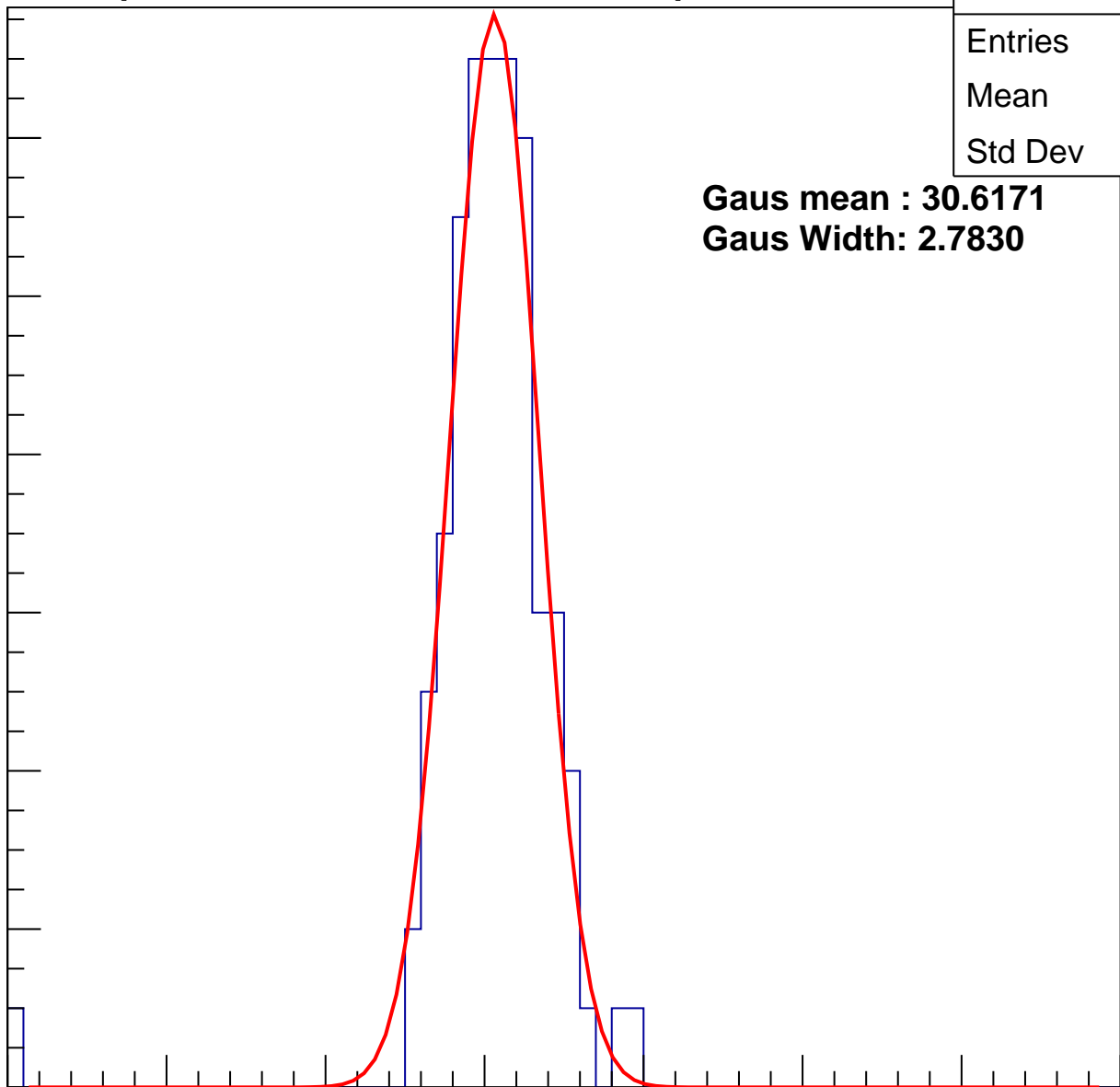
40

50

60

70

ampl



B1L001S, U24-ch35, adc1

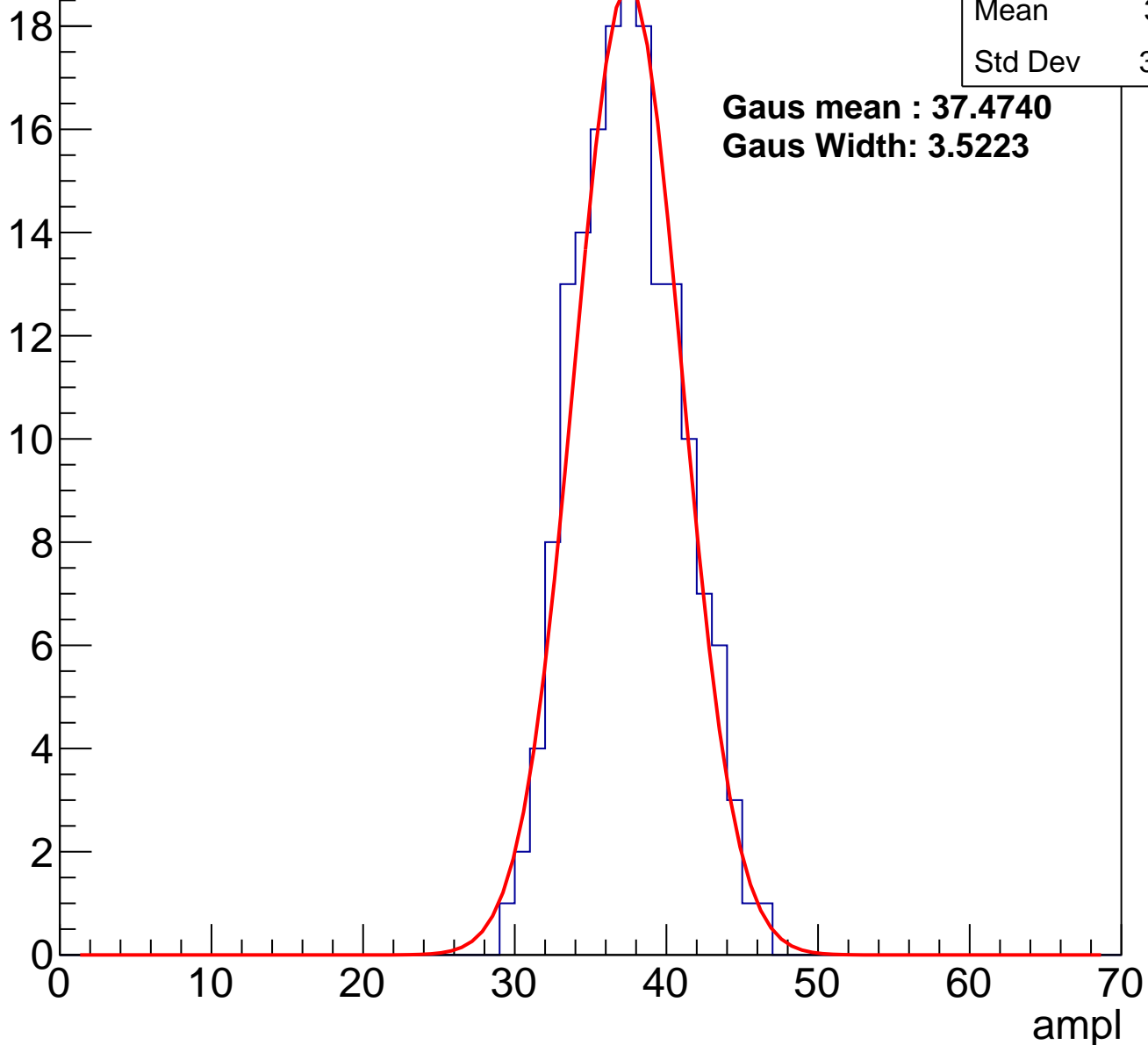
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	37.01
Std Dev	3.419

Gaus mean : 37.4740

Gaus Width: 3.5223

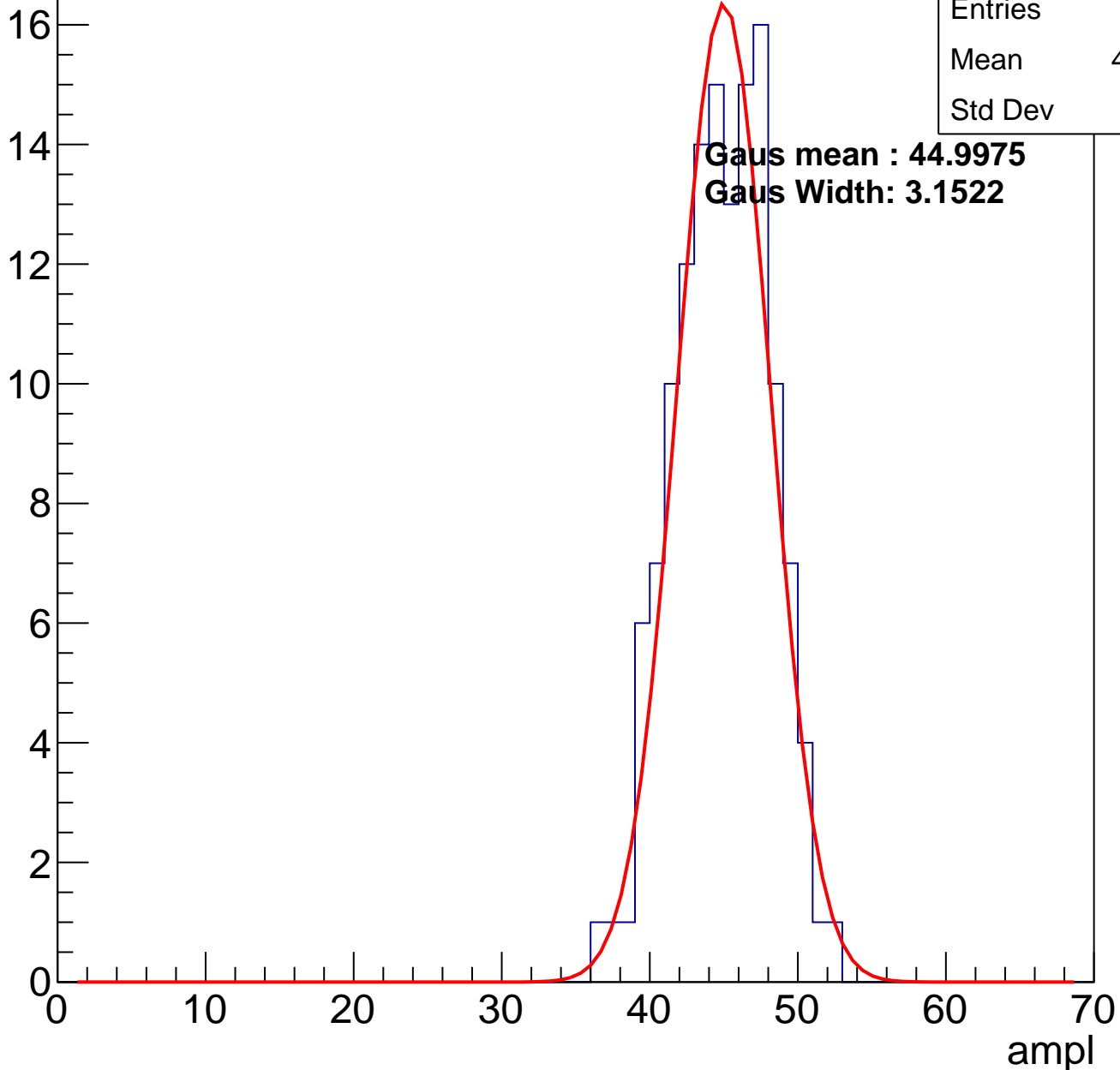
Entry



B1L001S, U24-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	134
Mean	44.43
Std Dev	3.17

B1L001S, U24-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	50.59
Std Dev	3.147

Entry

16
14
12
10
8
6
4
2
0

0

10

20

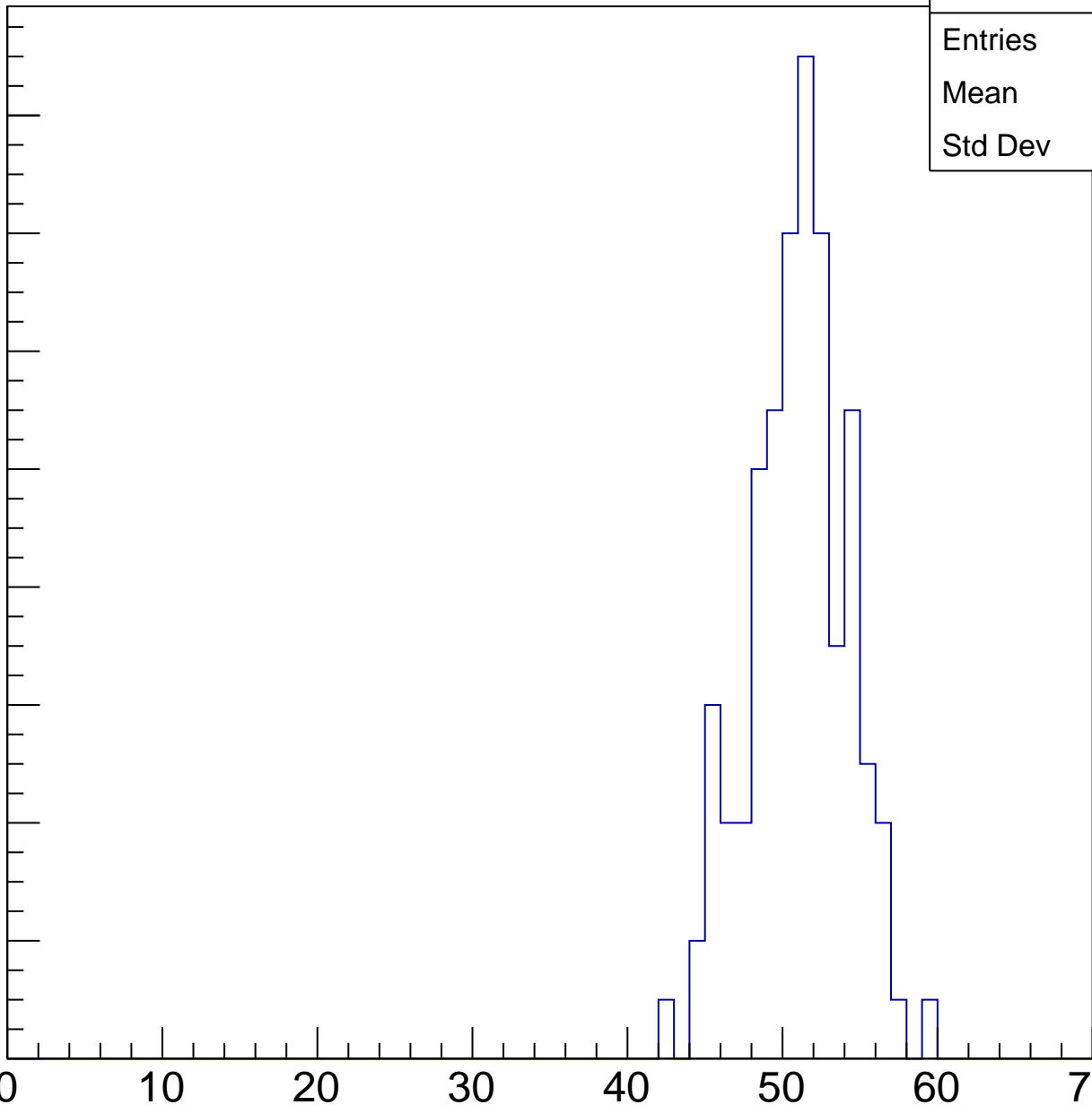
30

40

50

60

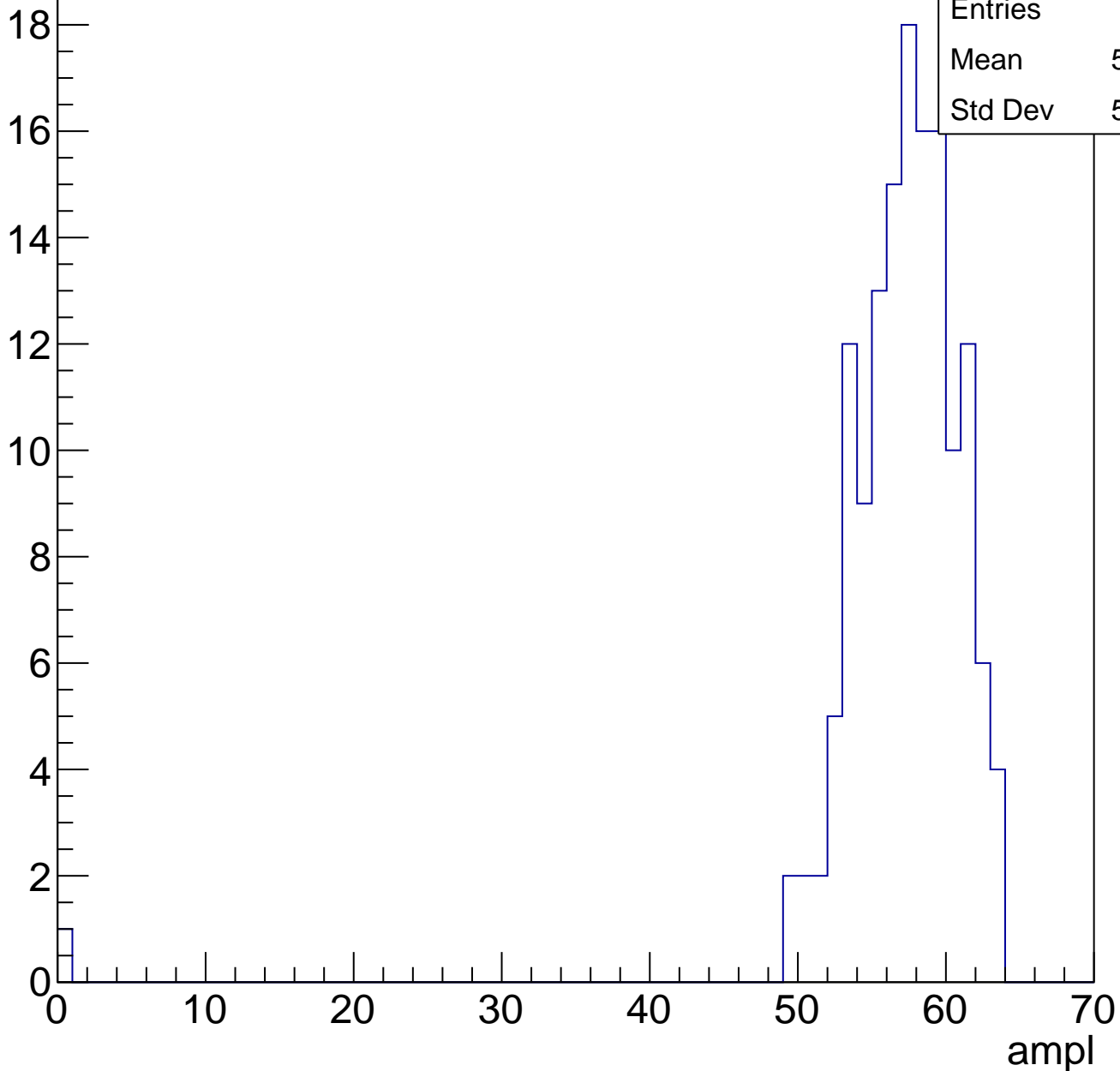
ampl



B1L001S, U24-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch35, adc5

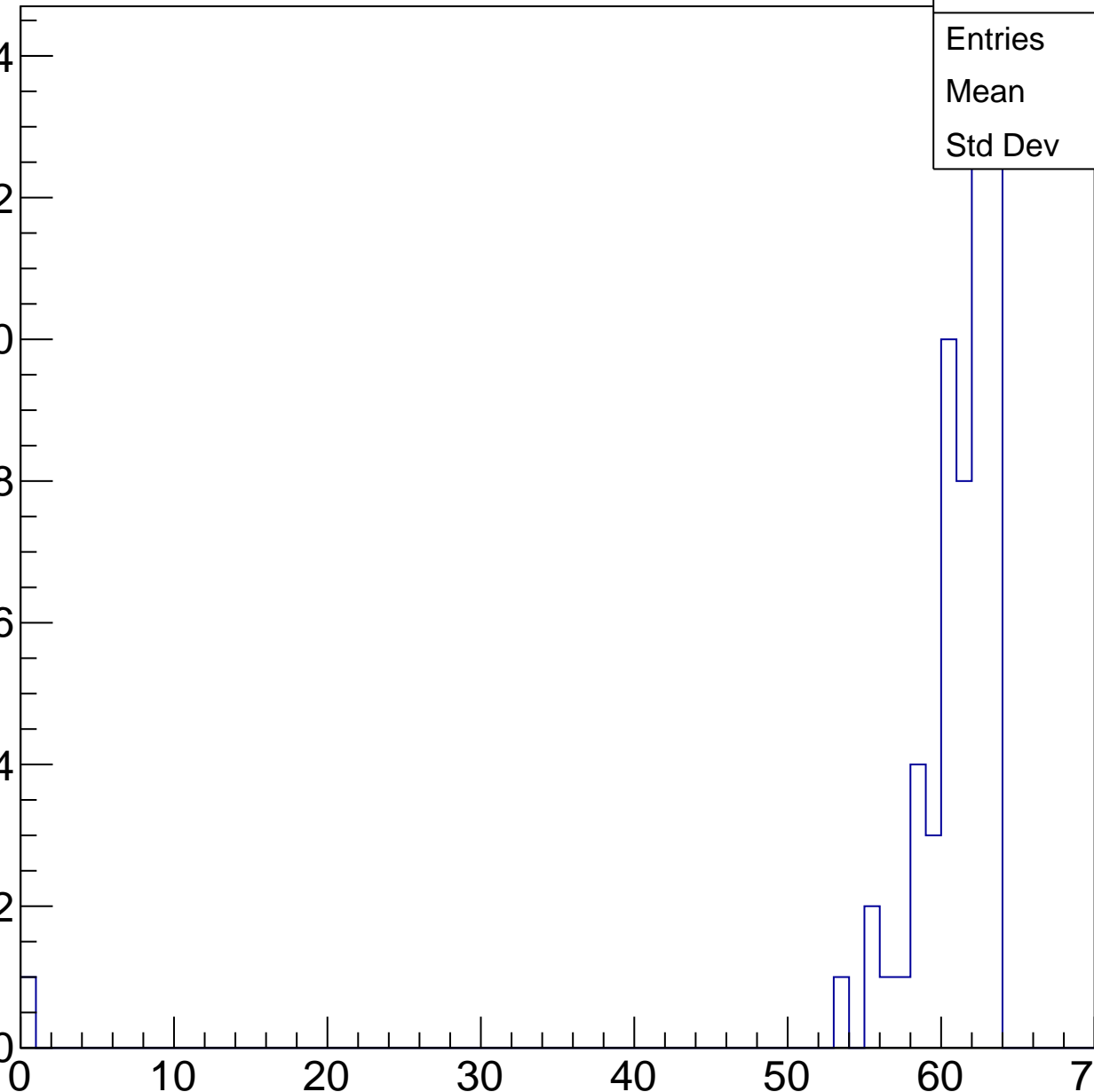
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.71
Std Dev	8.16

ampl



B1L001S, U24-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

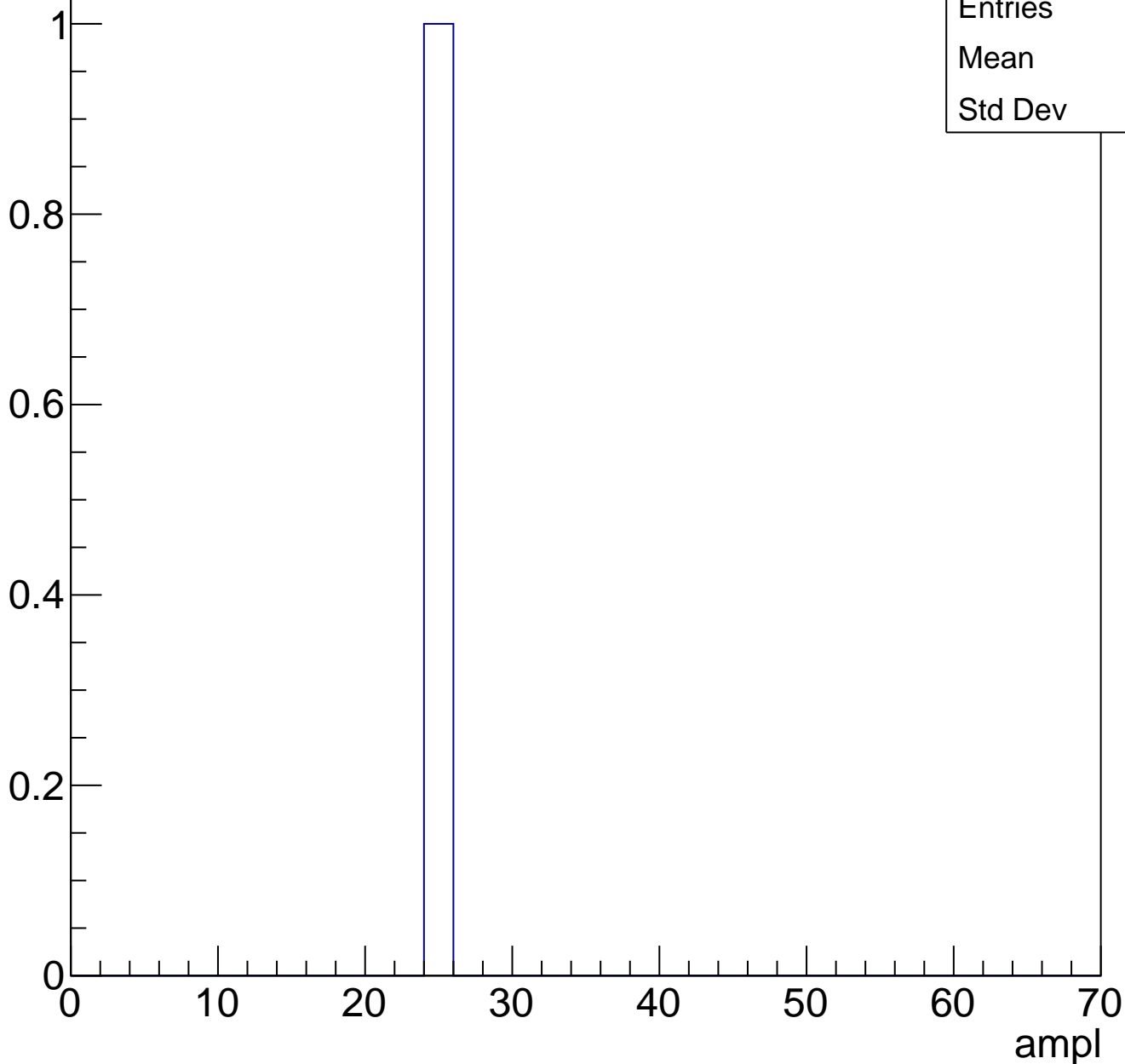
Entry



B1L001S, U24-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch36, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.99
Std Dev	3.472

Gaus mean : 30.3888

Gaus Width: 3.6296

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

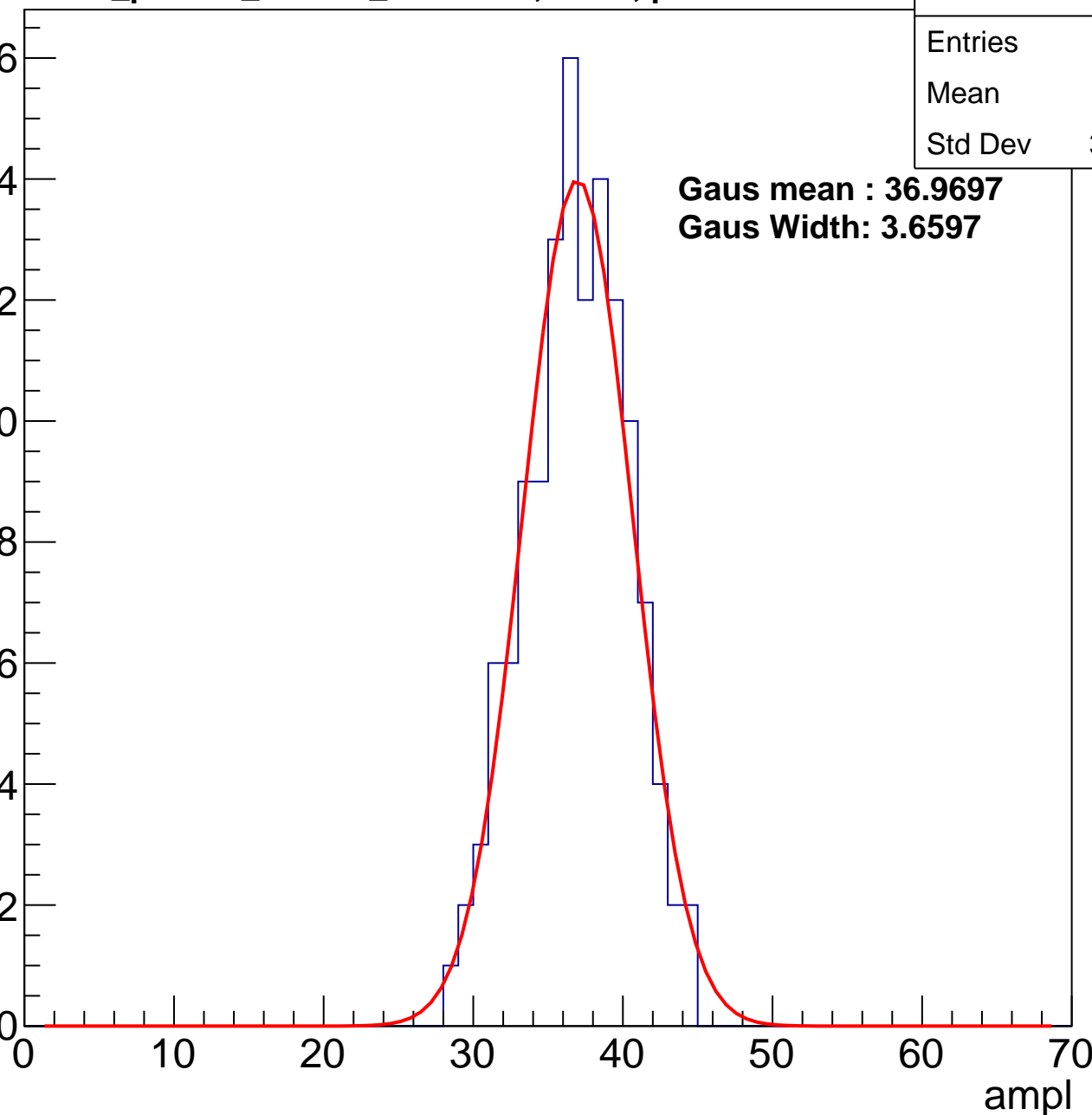
Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	36.41
Std Dev	3.429

Gaus mean : 36.9697

Gaus Width: 3.6597



ampl

B1L001S, U24-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	42.45
Std Dev	3.137

Gaus mean : 43.1019

Gaus Width: 3.1788

Entry

12

10

8

6

4

2

0

0

10

20

30

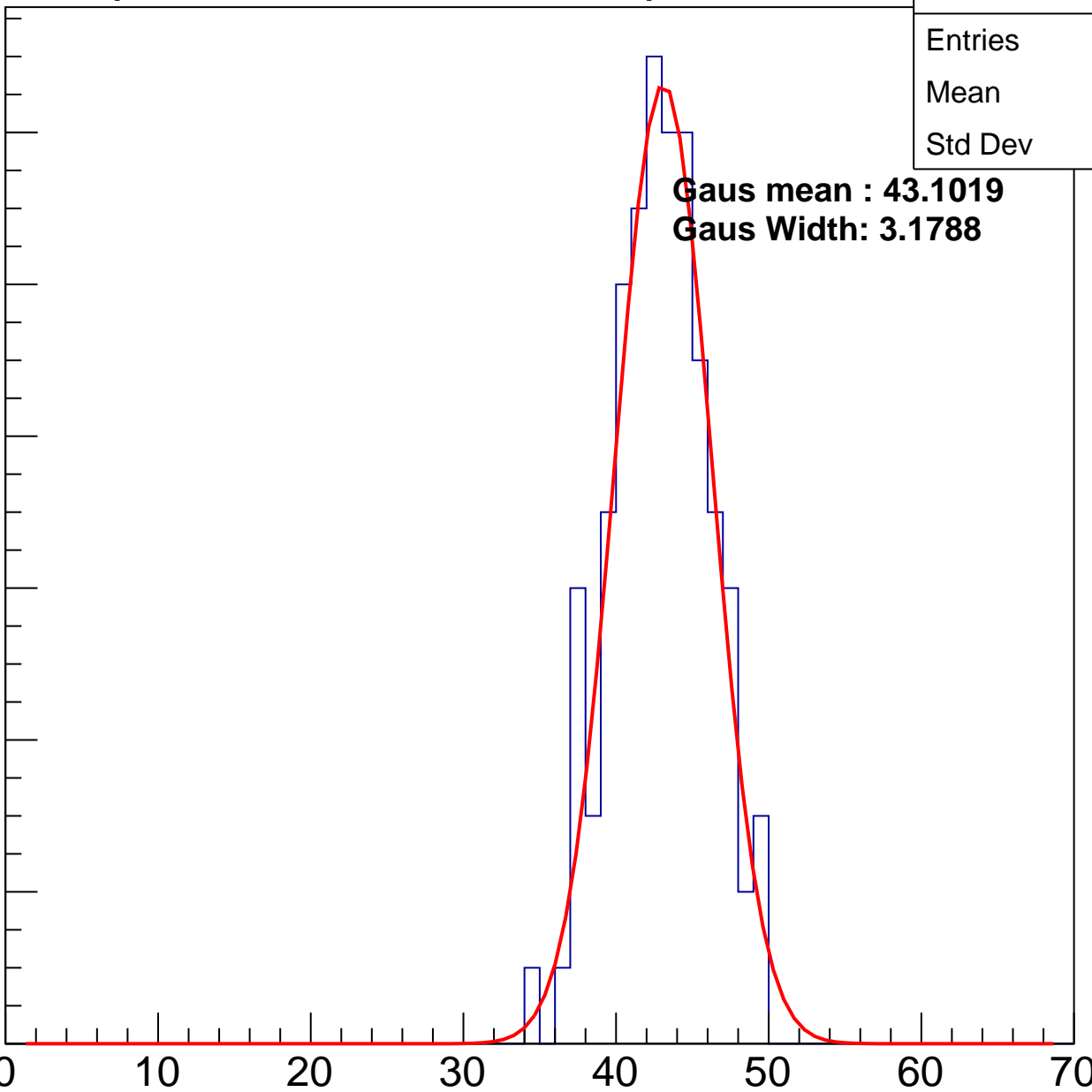
40

50

60

70

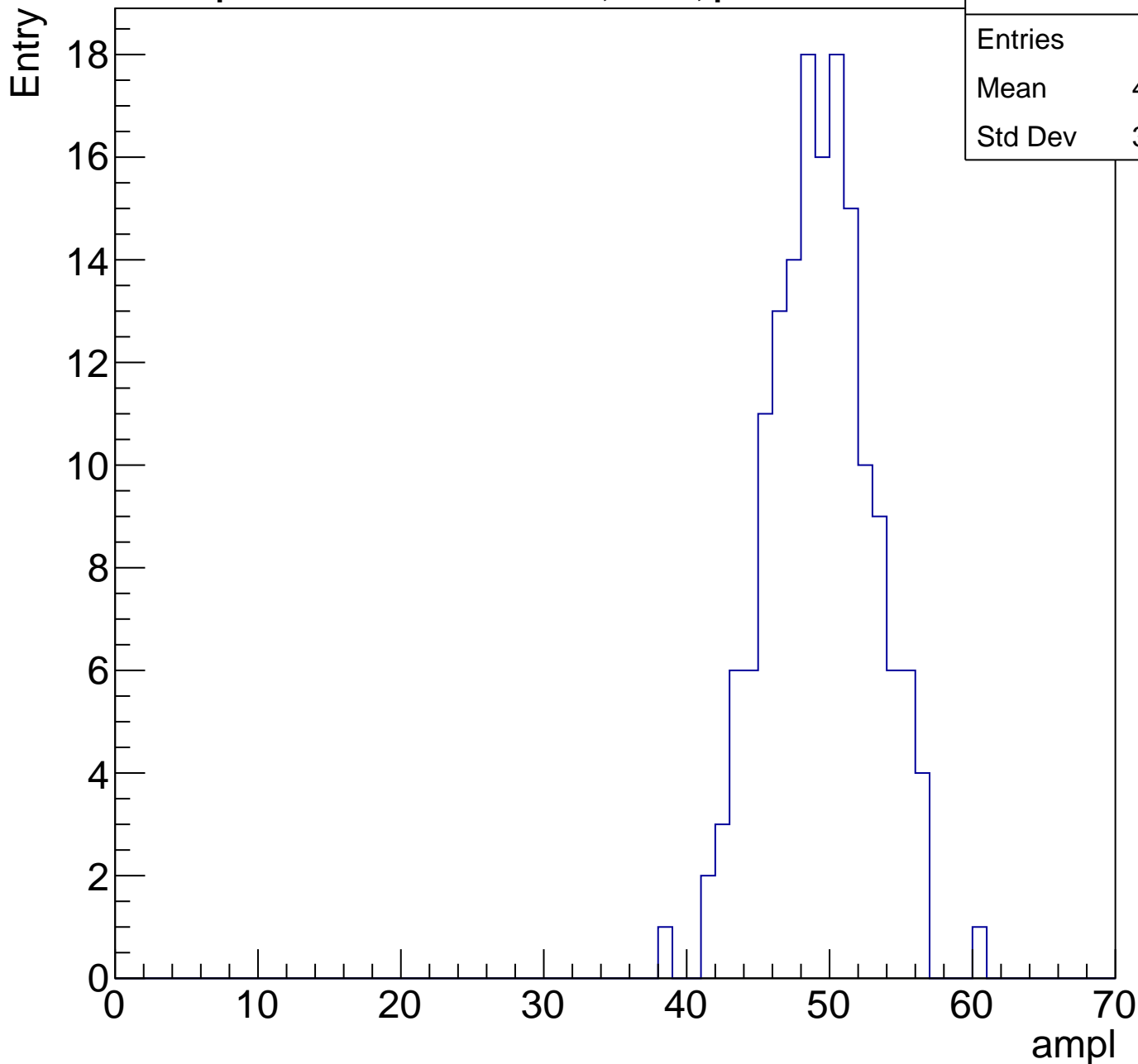
ampl



B1L001S, U24-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	48.85
Std Dev	3.652



B1L001S, U24-ch36, adc4

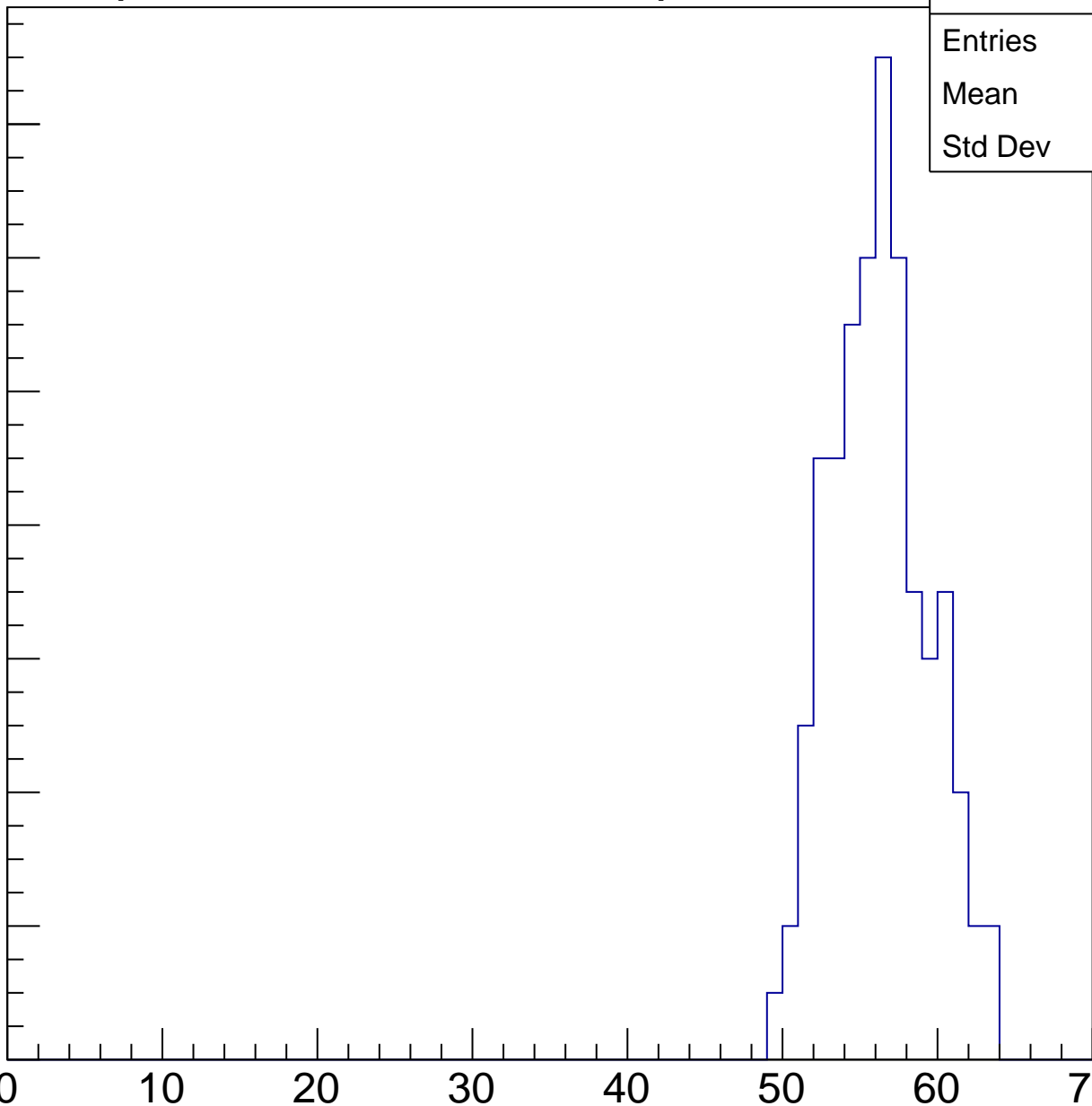
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	55.78
Std Dev	3.098

ampl



B1L001S, U24-ch36, adc5

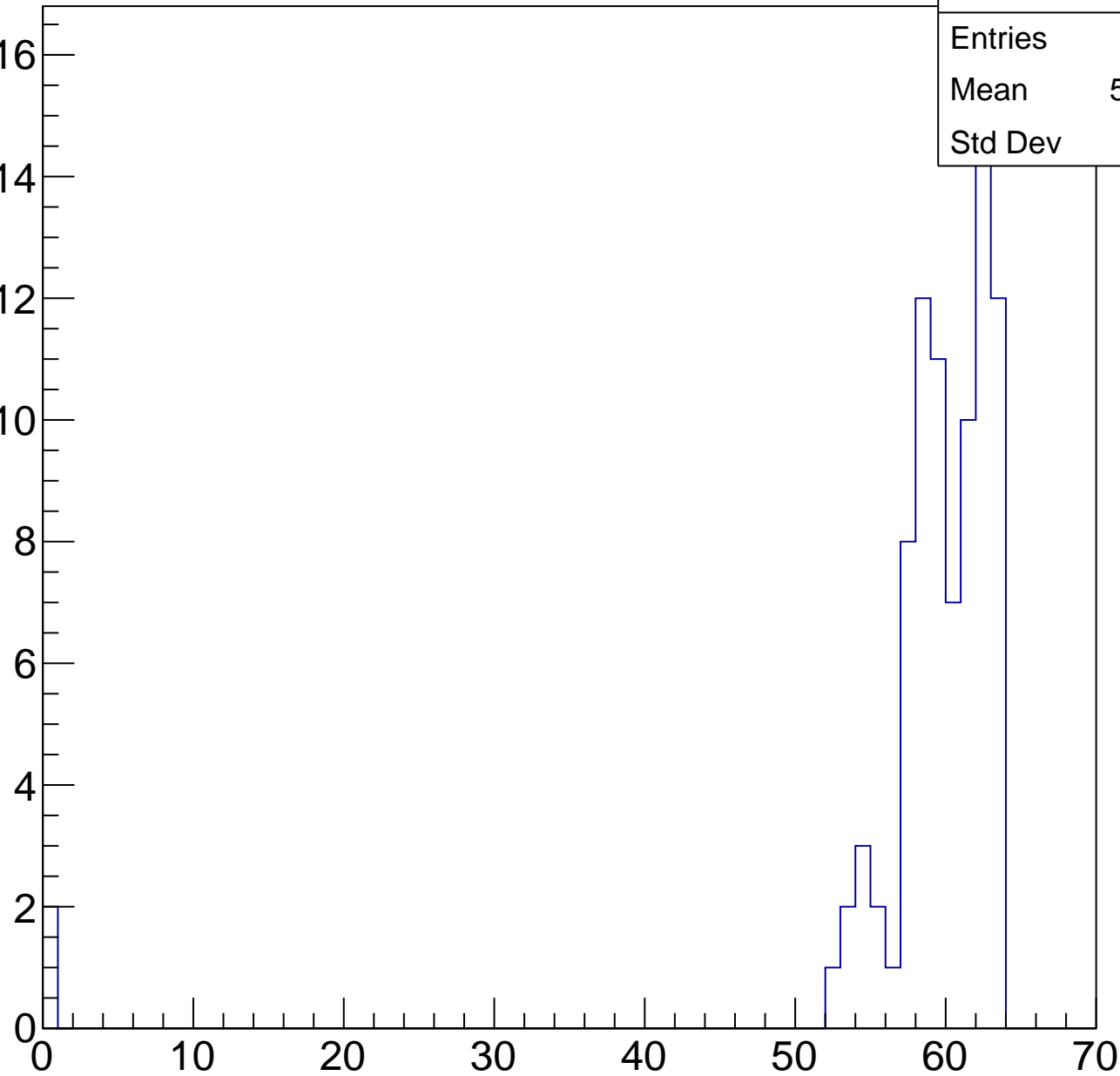
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.22
Std Dev	9.33

ampl

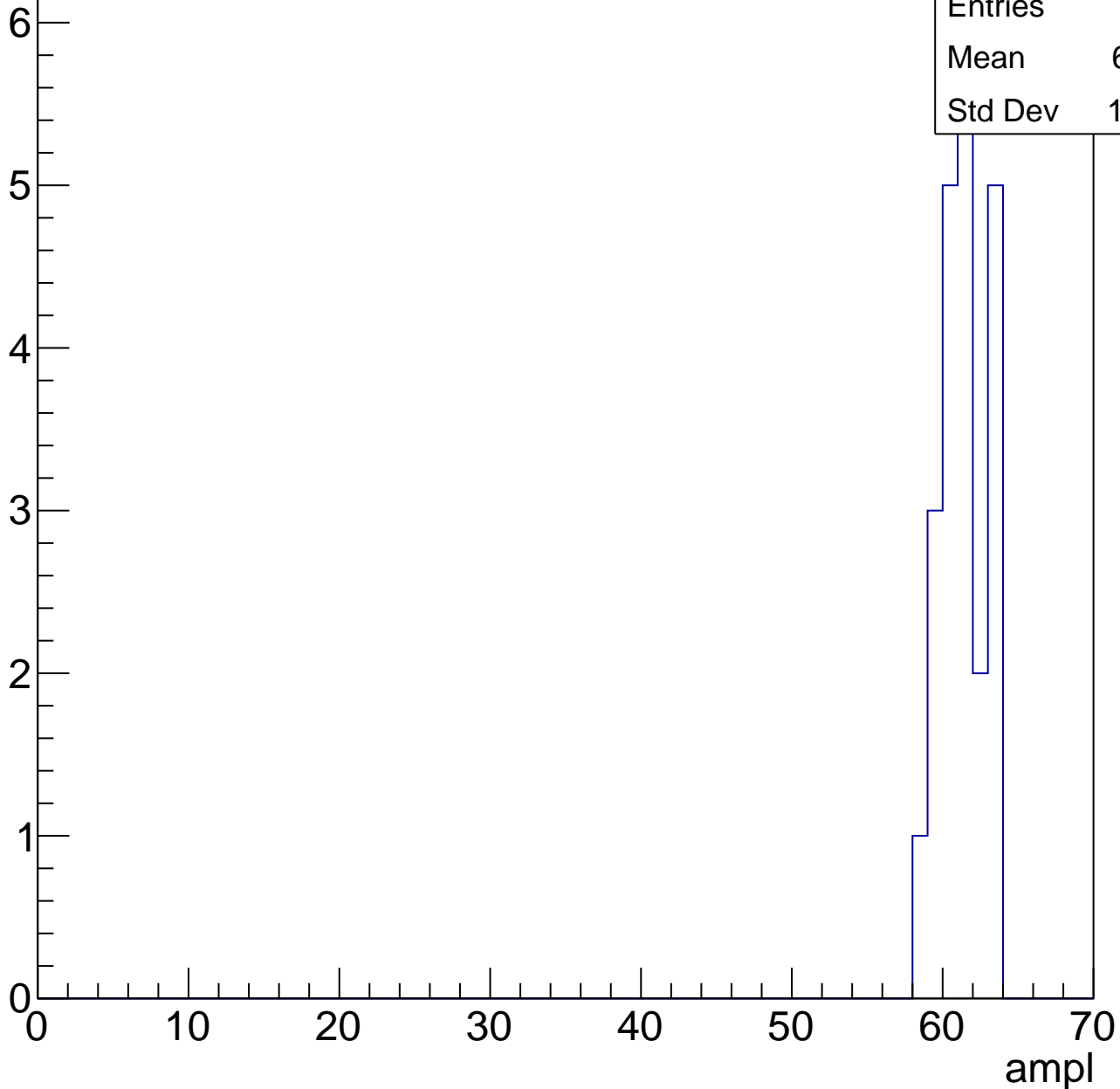


B1L001S, U24-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

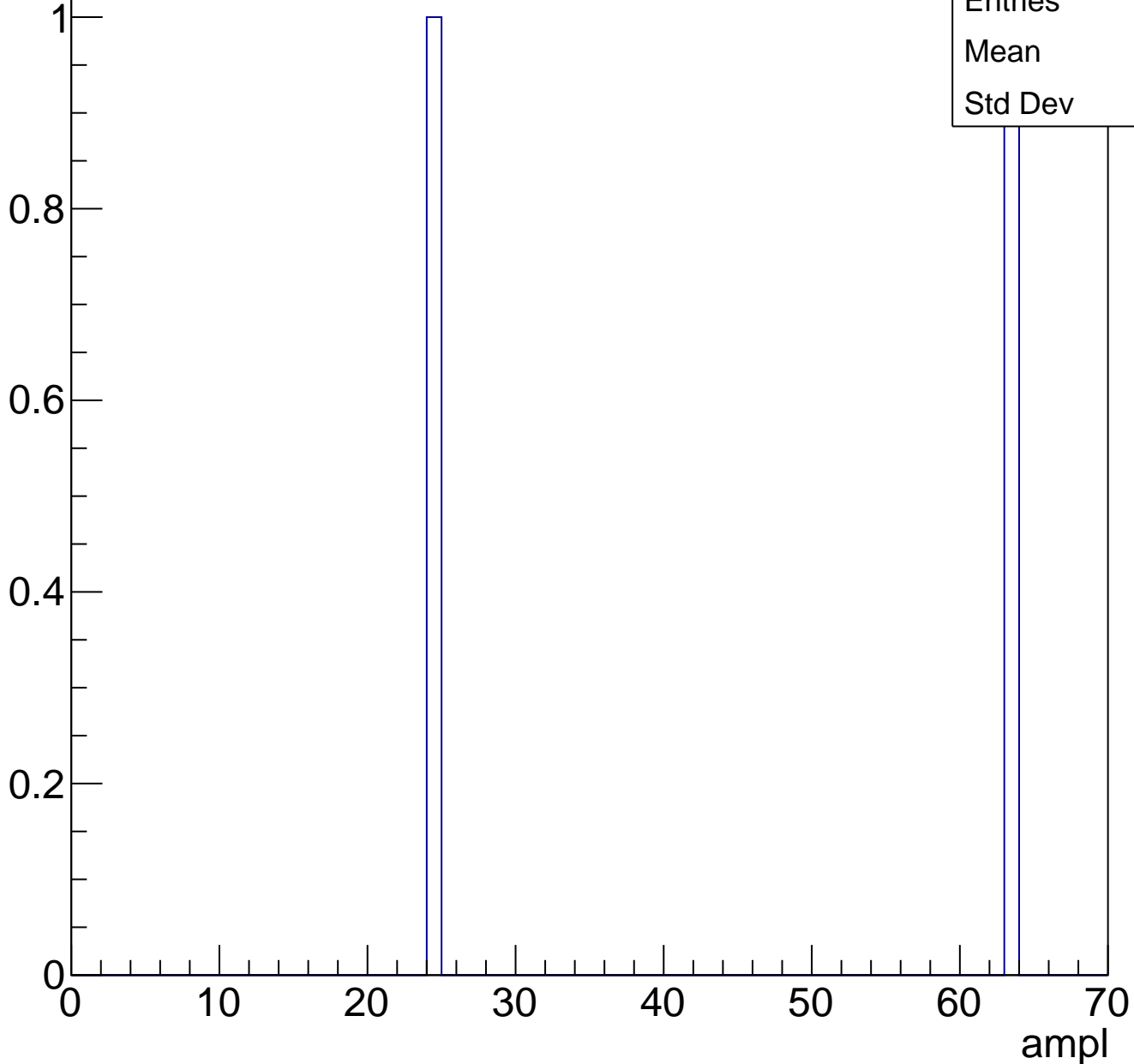
Entries	22
Mean	60.91
Std Dev	1.474



B1L001S, U24-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch37, adc0

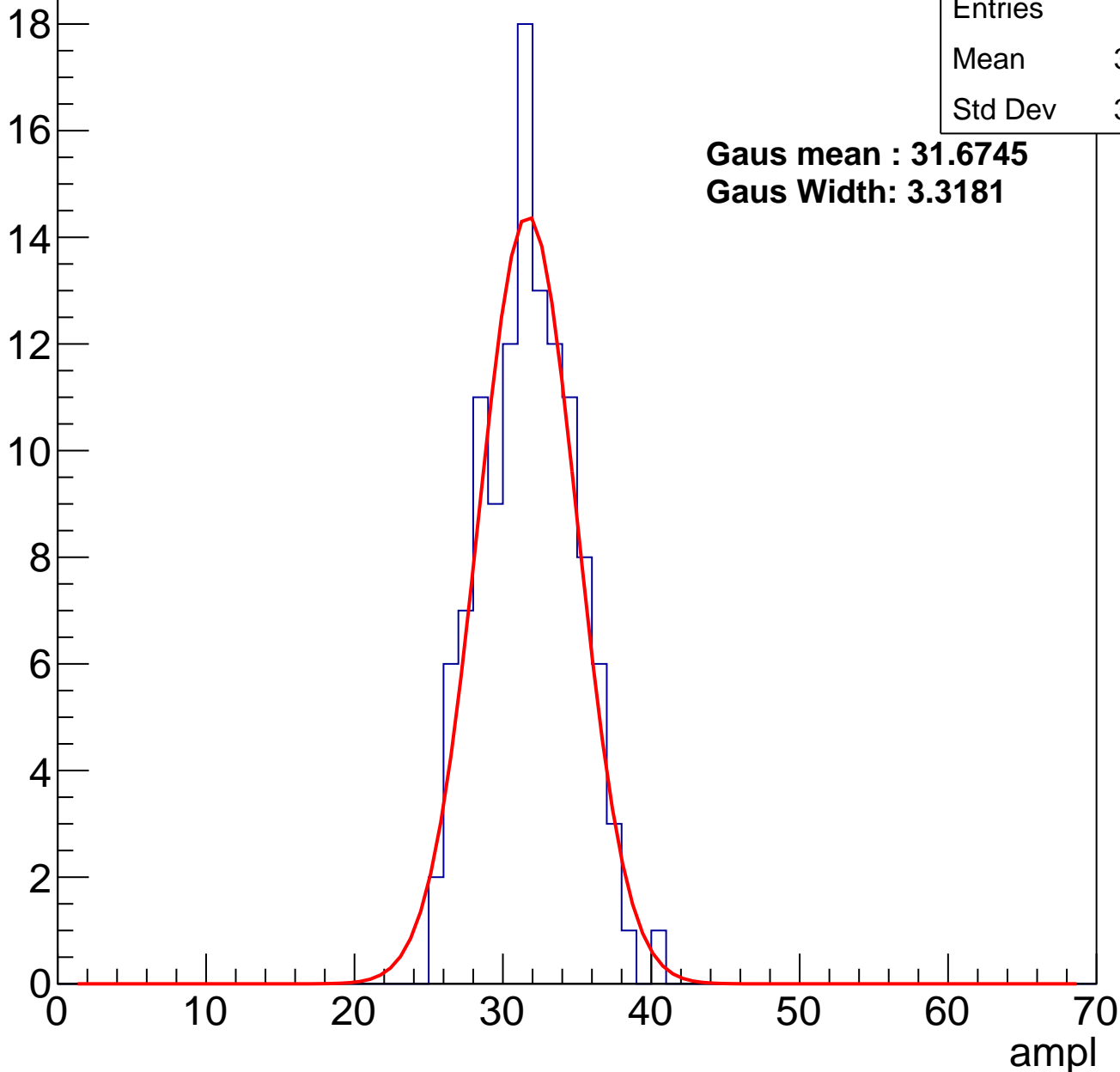
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	31.27
Std Dev	3.093

Gaus mean : 31.6745

Gaus Width: 3.3181

Entry

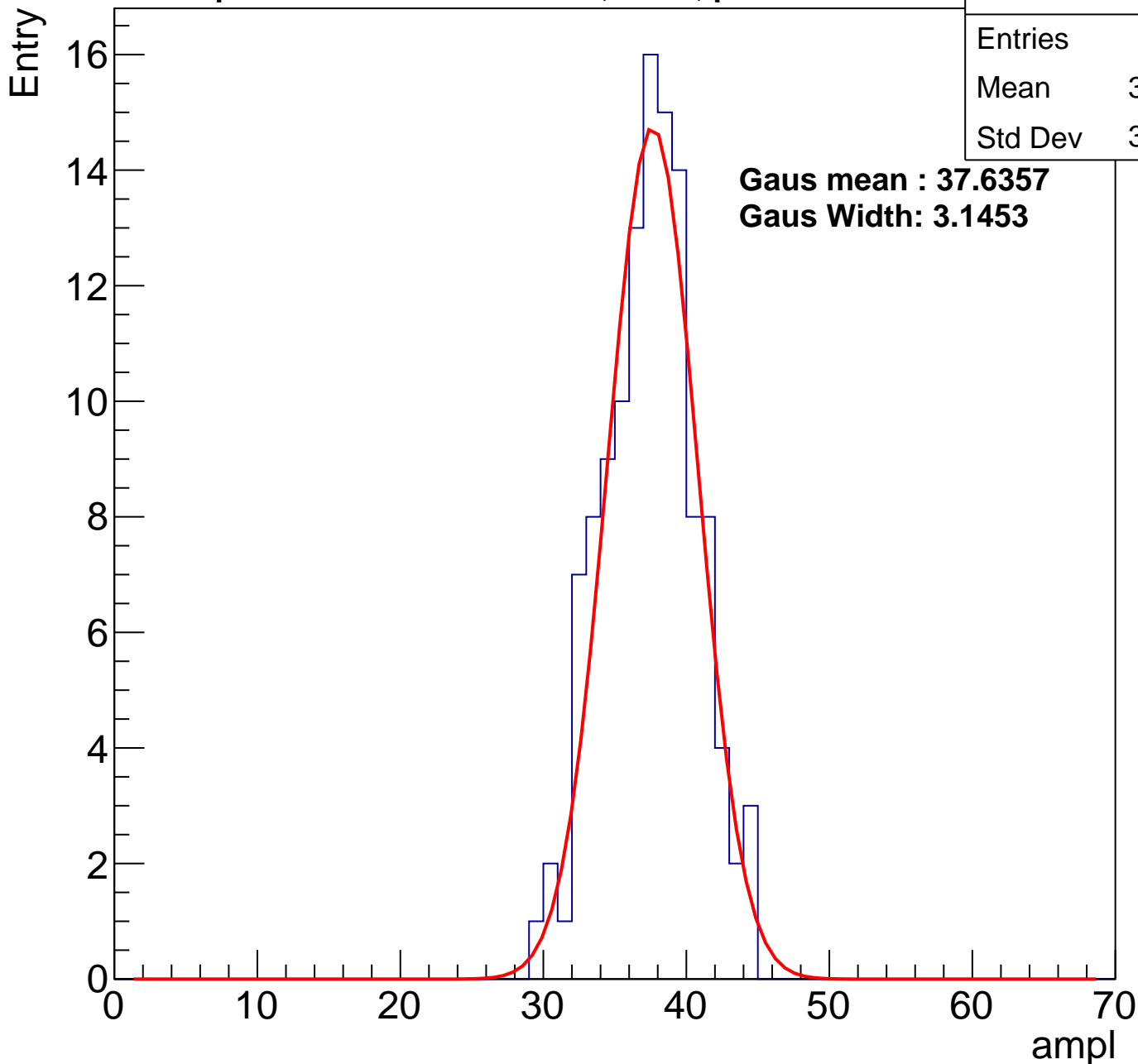


B1L001S, U24-ch37, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	36.98
Std Dev	3.182

Gaus mean : 37.6357
Gaus Width: 3.1453



B1L001S, U24-ch37, adc2

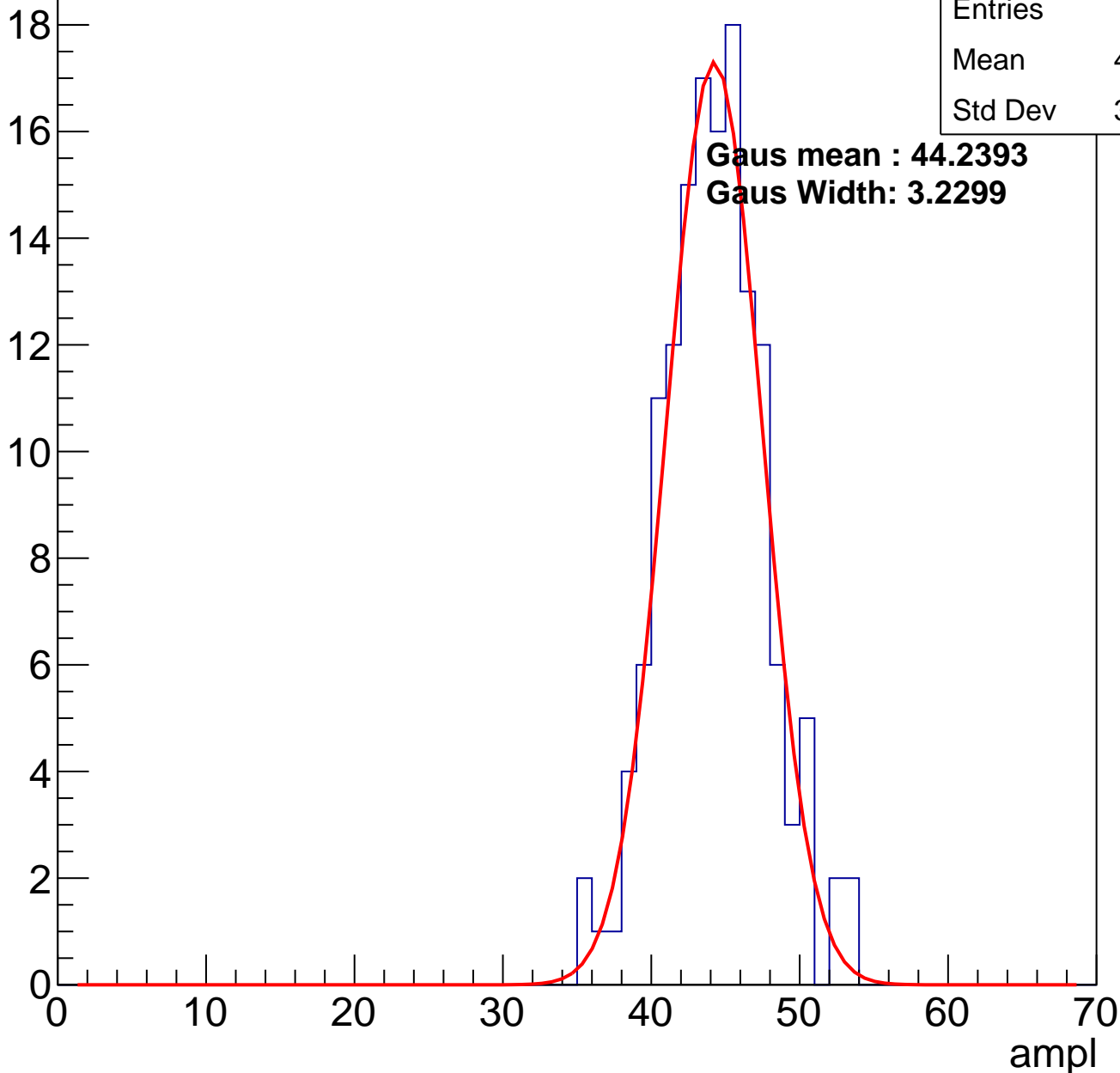
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	43.79
Std Dev	3.455

Gaus mean : 44.2393

Gaus Width: 3.2299

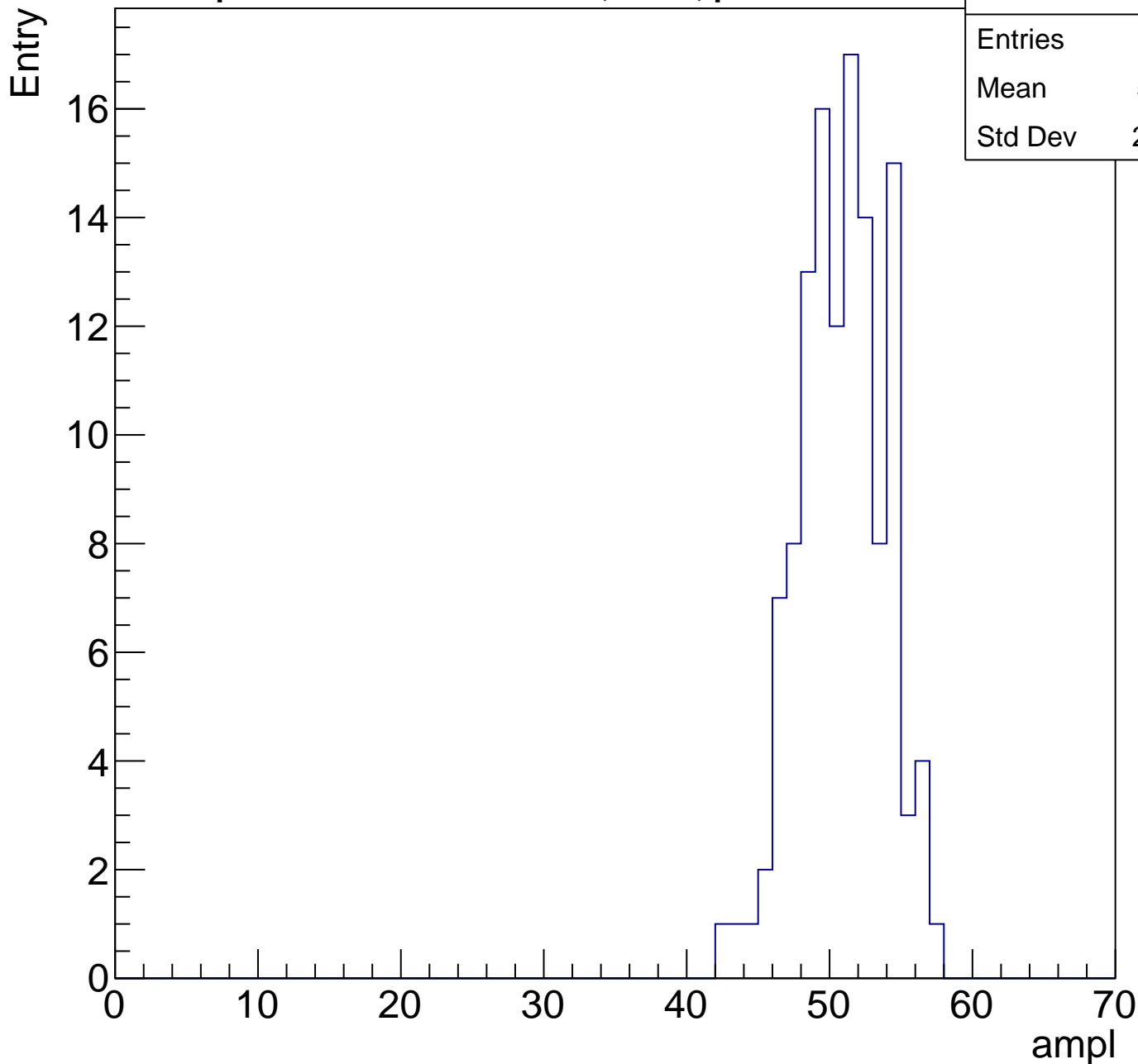
Entry



B1L001S, U24-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

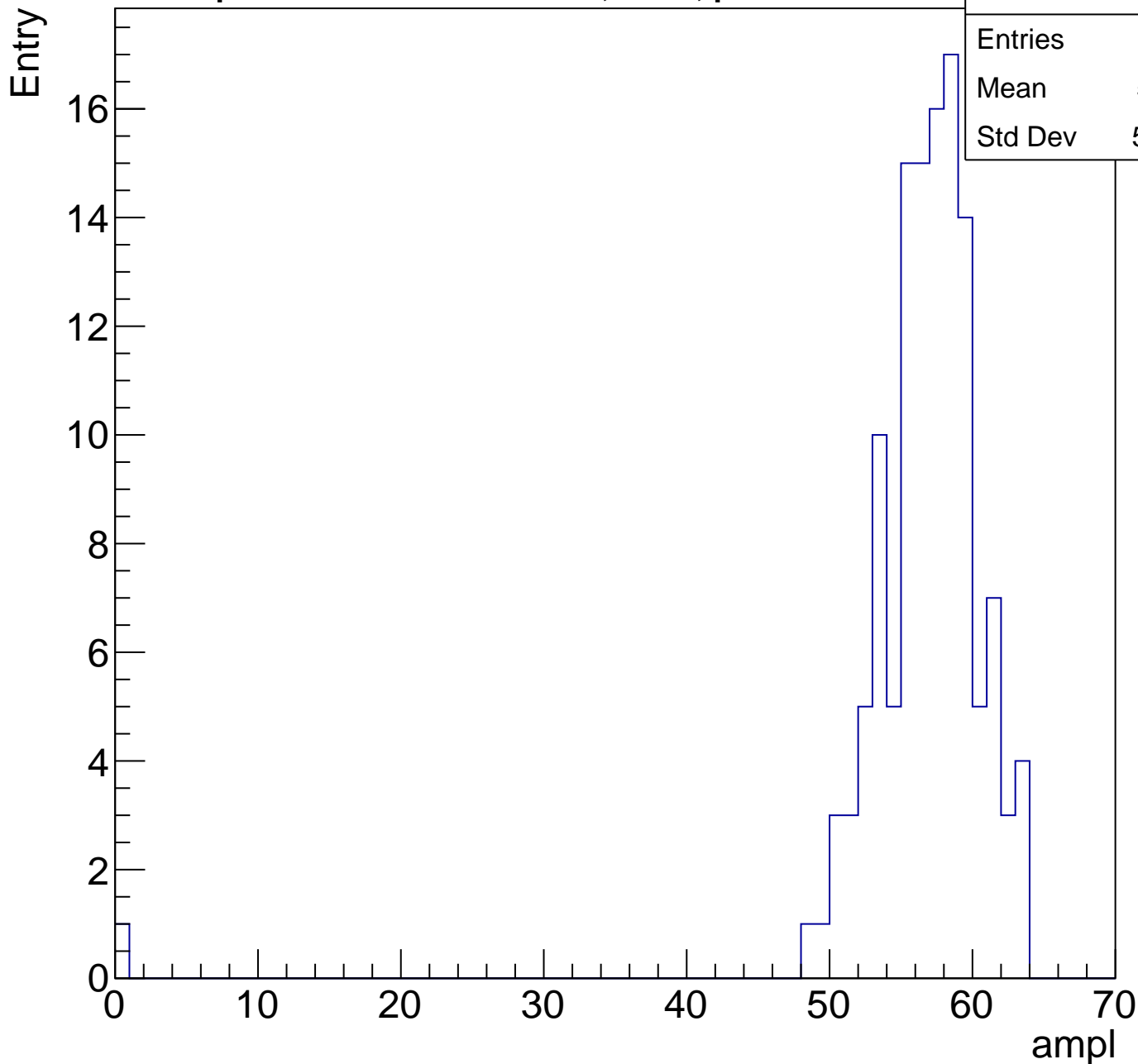
Entries	123
Mean	50.41
Std Dev	2.968



B1L001S, U24-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	56.11
Std Dev	5.936



B1L001S, U24-ch37, adc5

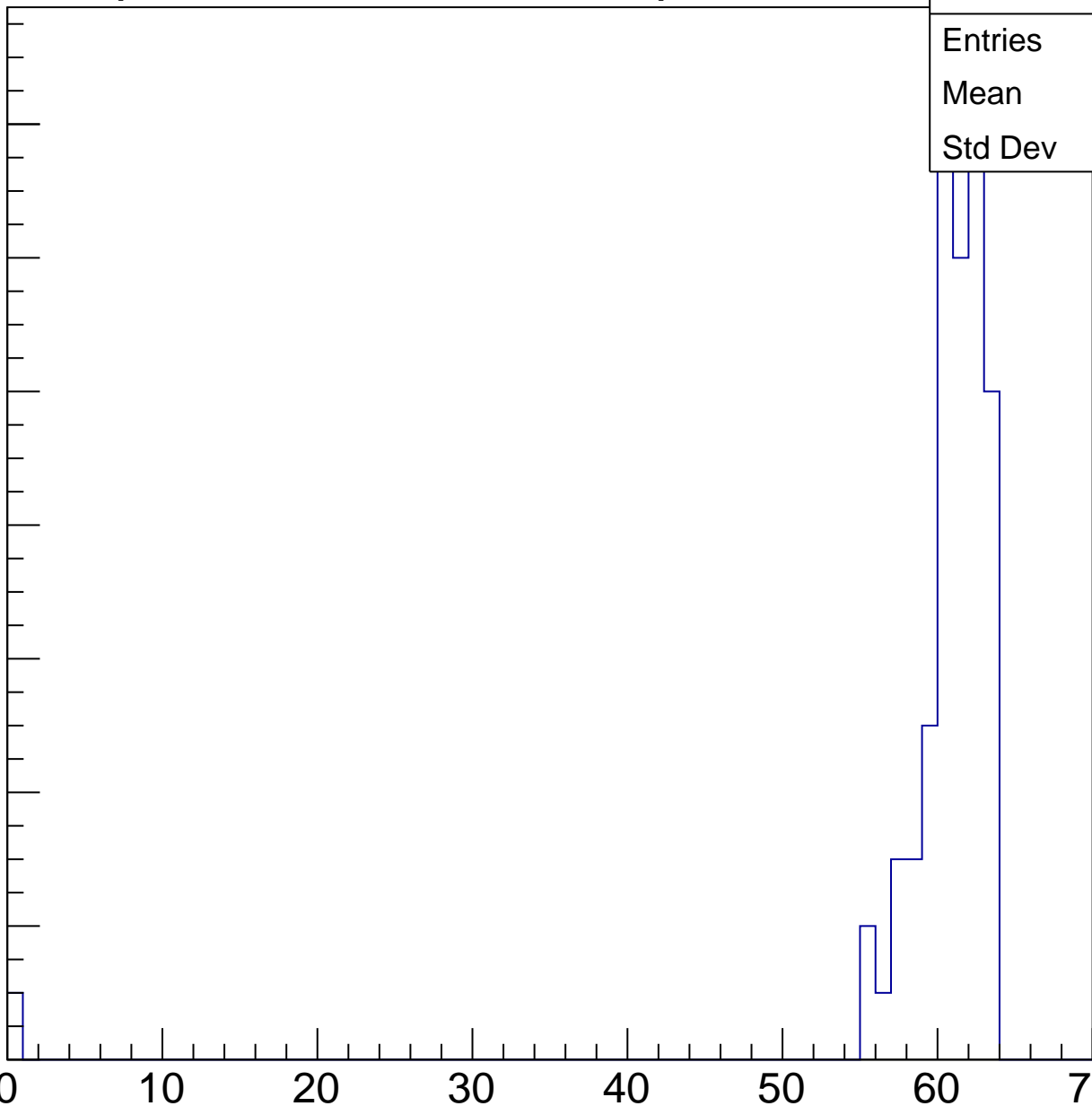
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.64
Std Dev	7.647

ampl

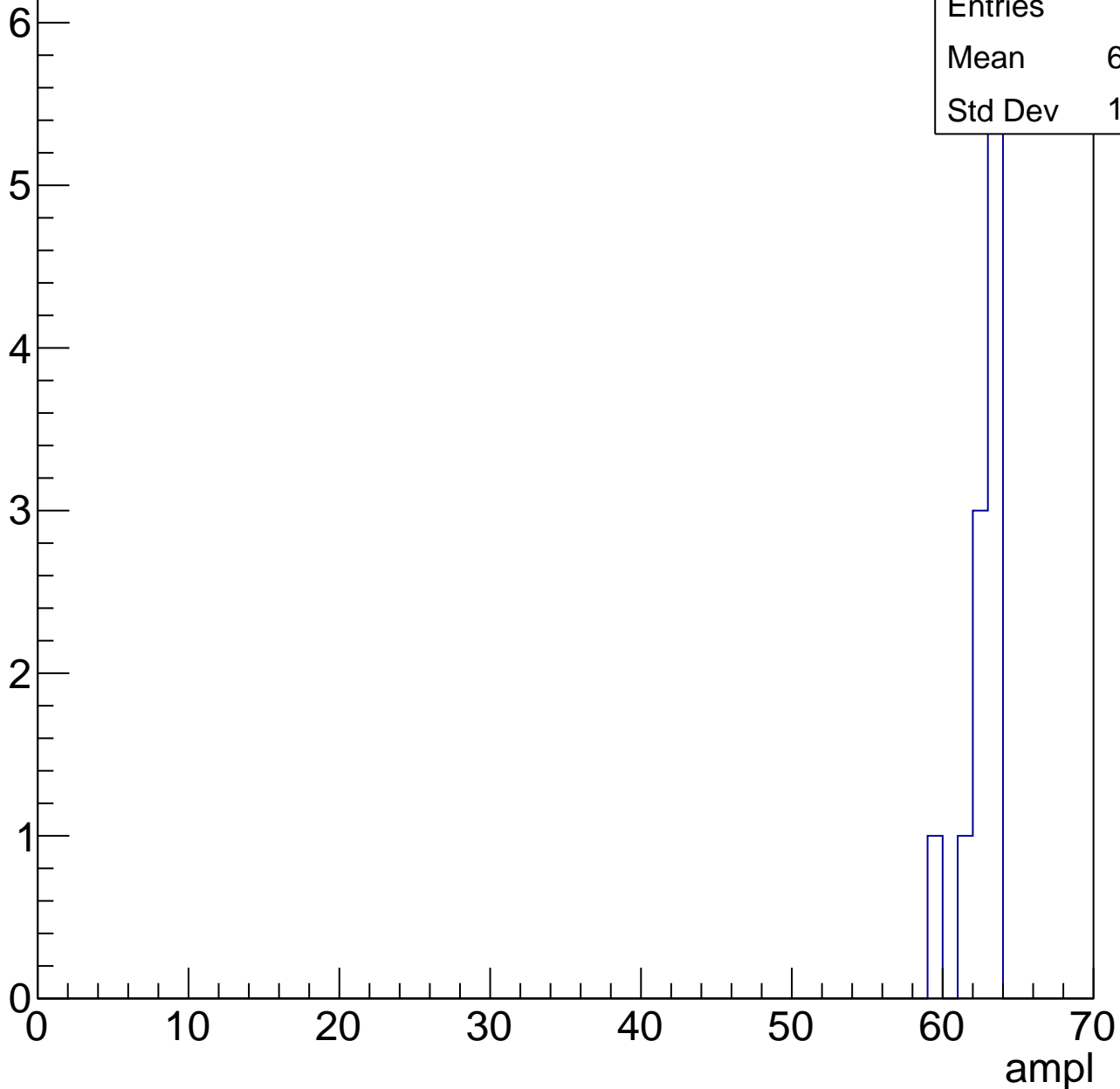


B1L001S, U24-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.18
Std Dev	1.192



B1L001S, U24-ch37, adc7

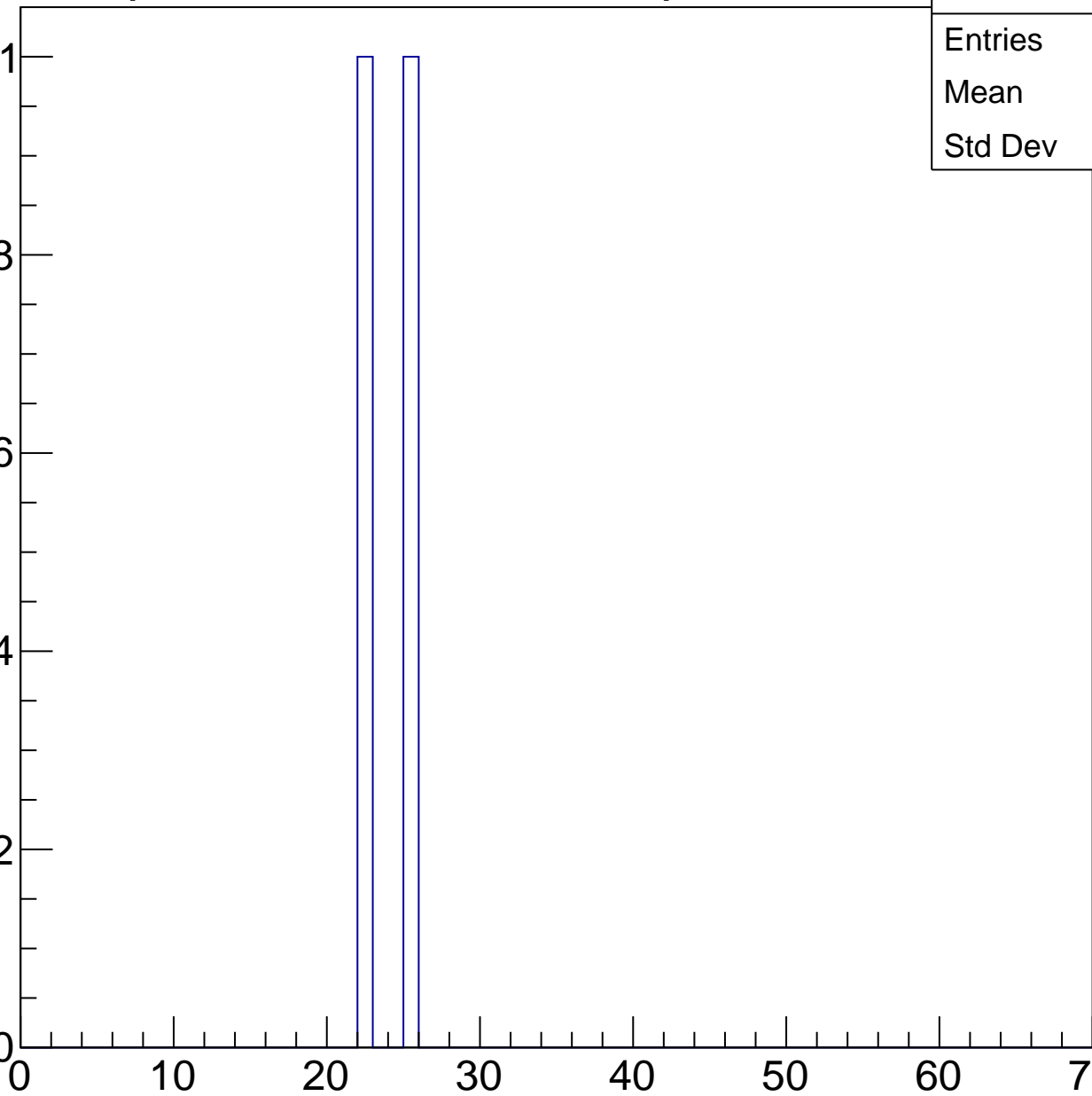
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23.5
Std Dev	1.5

ampl



B1L001S, U24-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	28.98
Std Dev	2.919

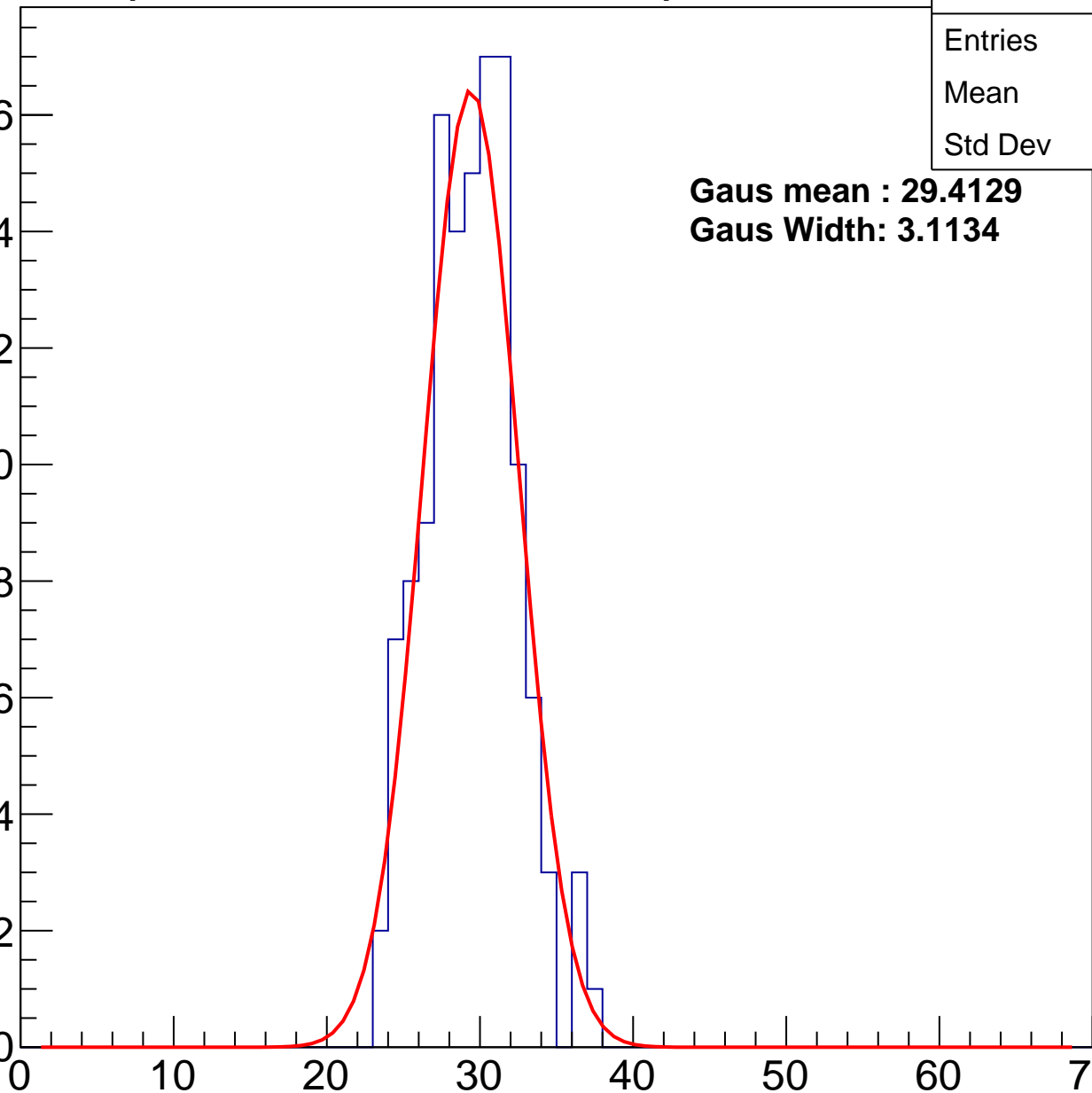
Gaus mean : 29.4129

Gaus Width: 3.1134

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch38, adc1

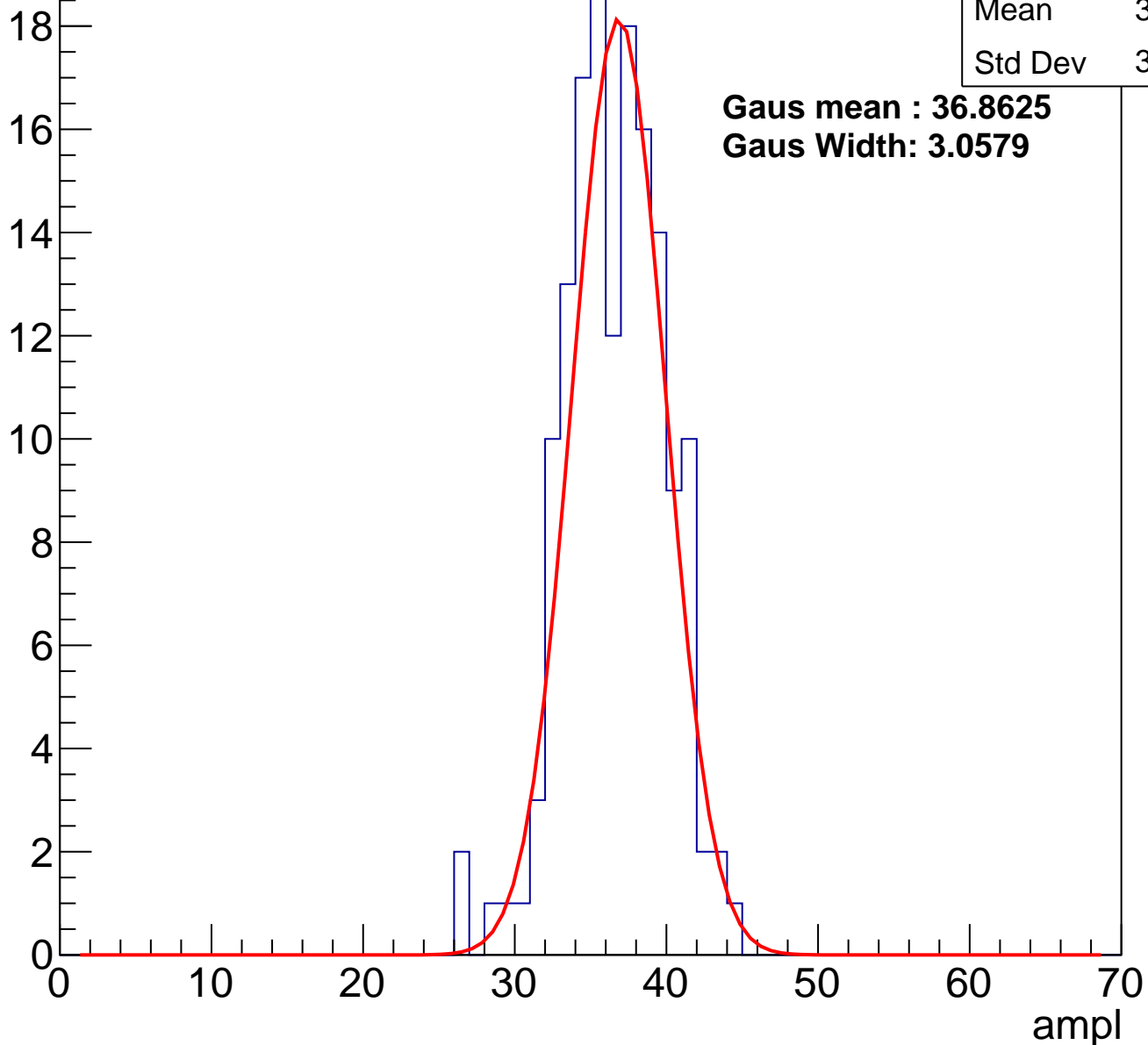
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	36.16
Std Dev	3.276

Gaus mean : 36.8625

Gaus Width: 3.0579

Entry



B1L001S, U24-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	42.91
Std Dev	3.383

Entry

16

14

12

10

8

6

4

2

0

Gaus mean : 43.7304

Gaus Width: 3.1217

0

10

20

30

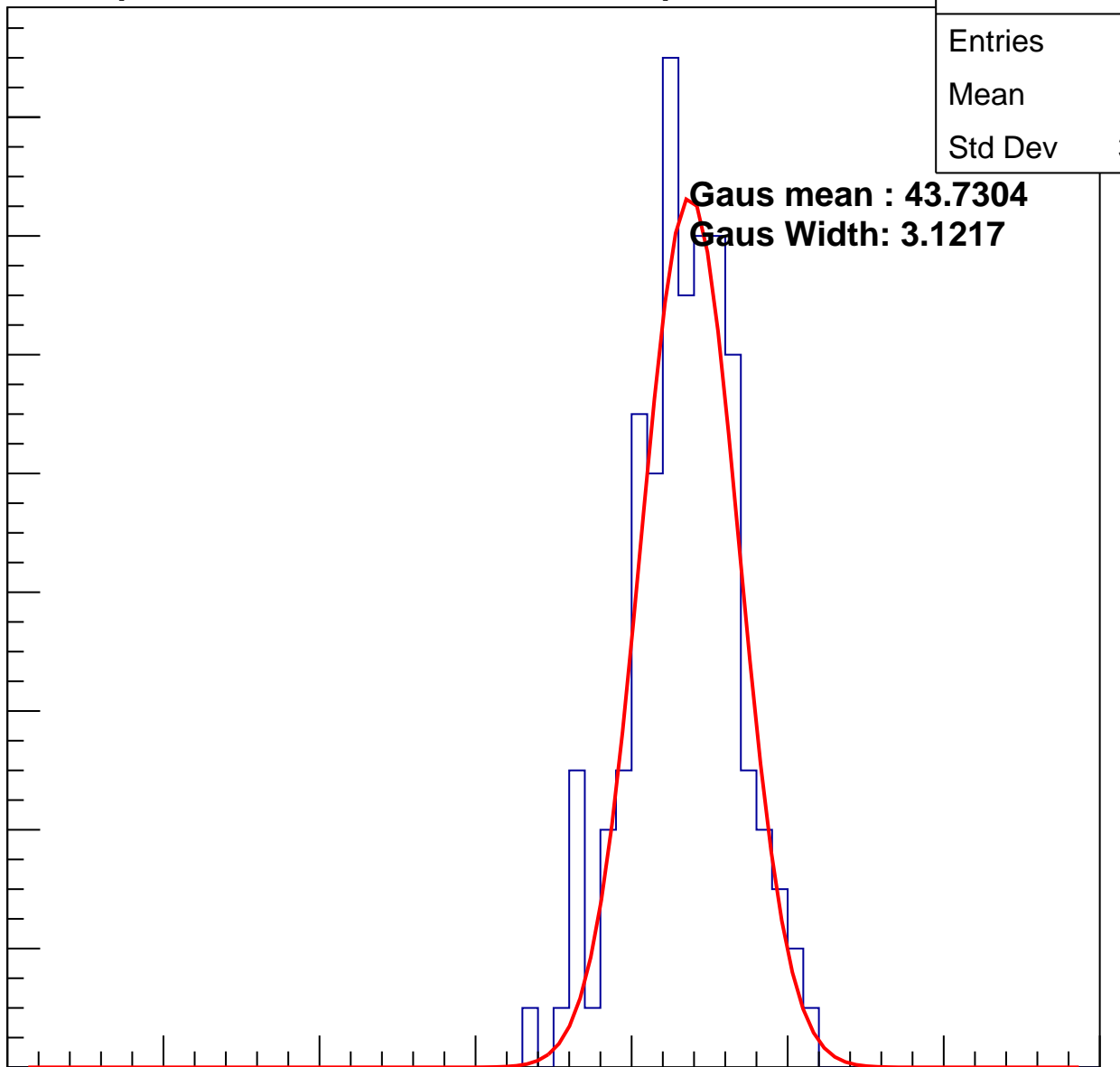
40

50

60

70

ampl



B1L001S, U24-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	49.53
Std Dev	3.461

Entry

16

14

12

10

8

6

4

2

0

0

10

20

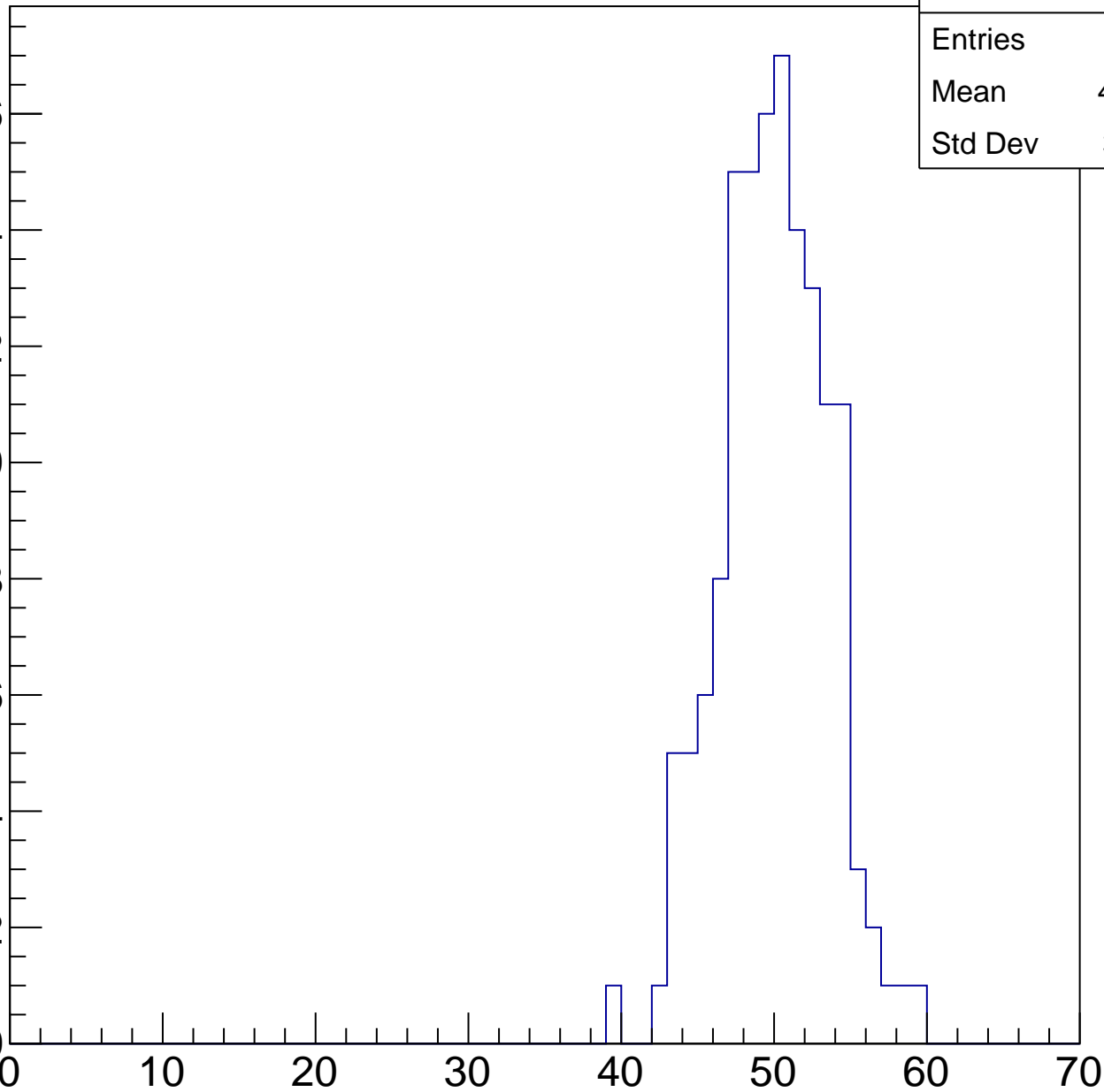
30

40

50

60

ampl

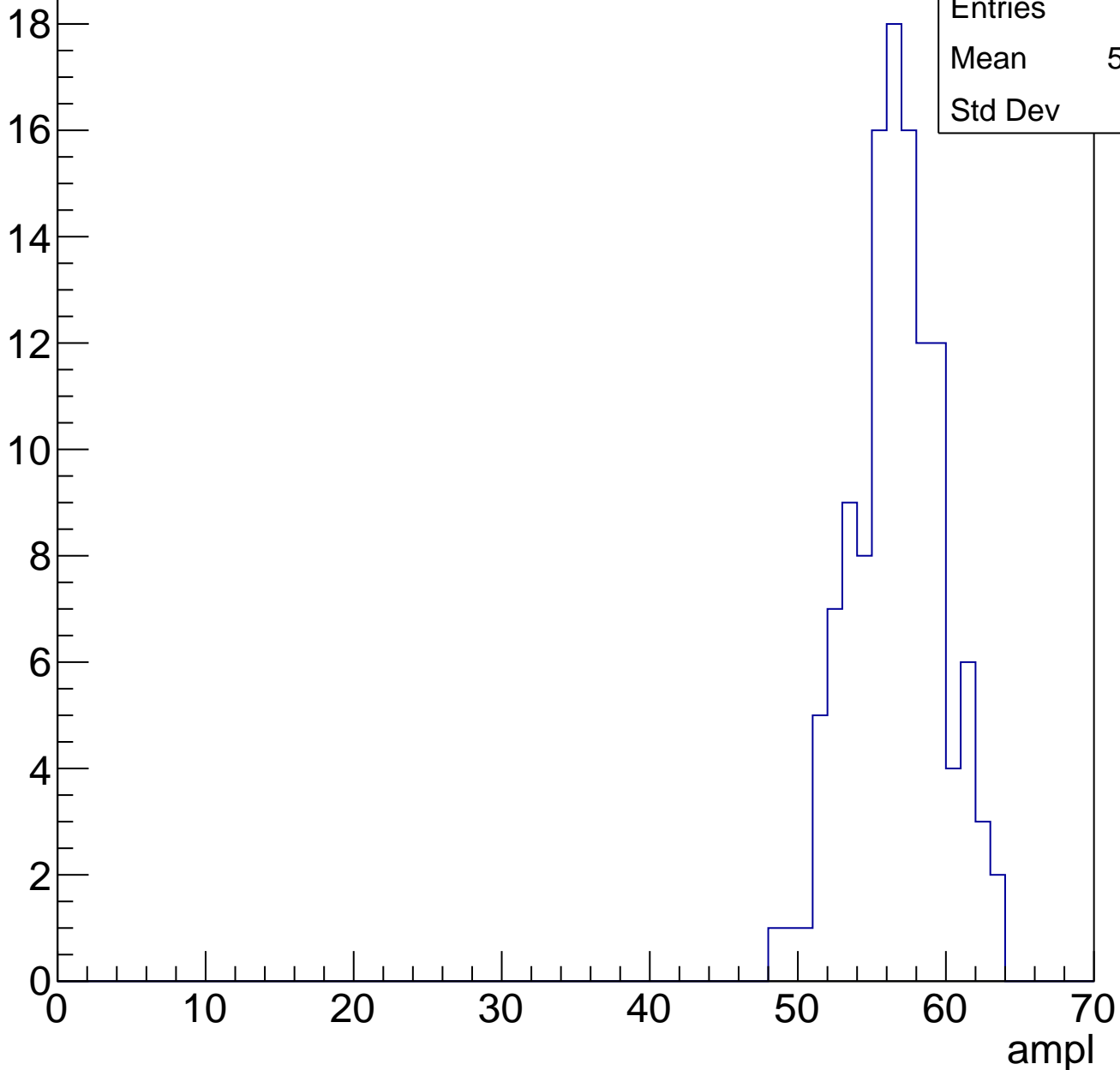


B1L001S, U24-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	56.17
Std Dev	3.02

Entry



B1L001S, U24-ch38, adc5

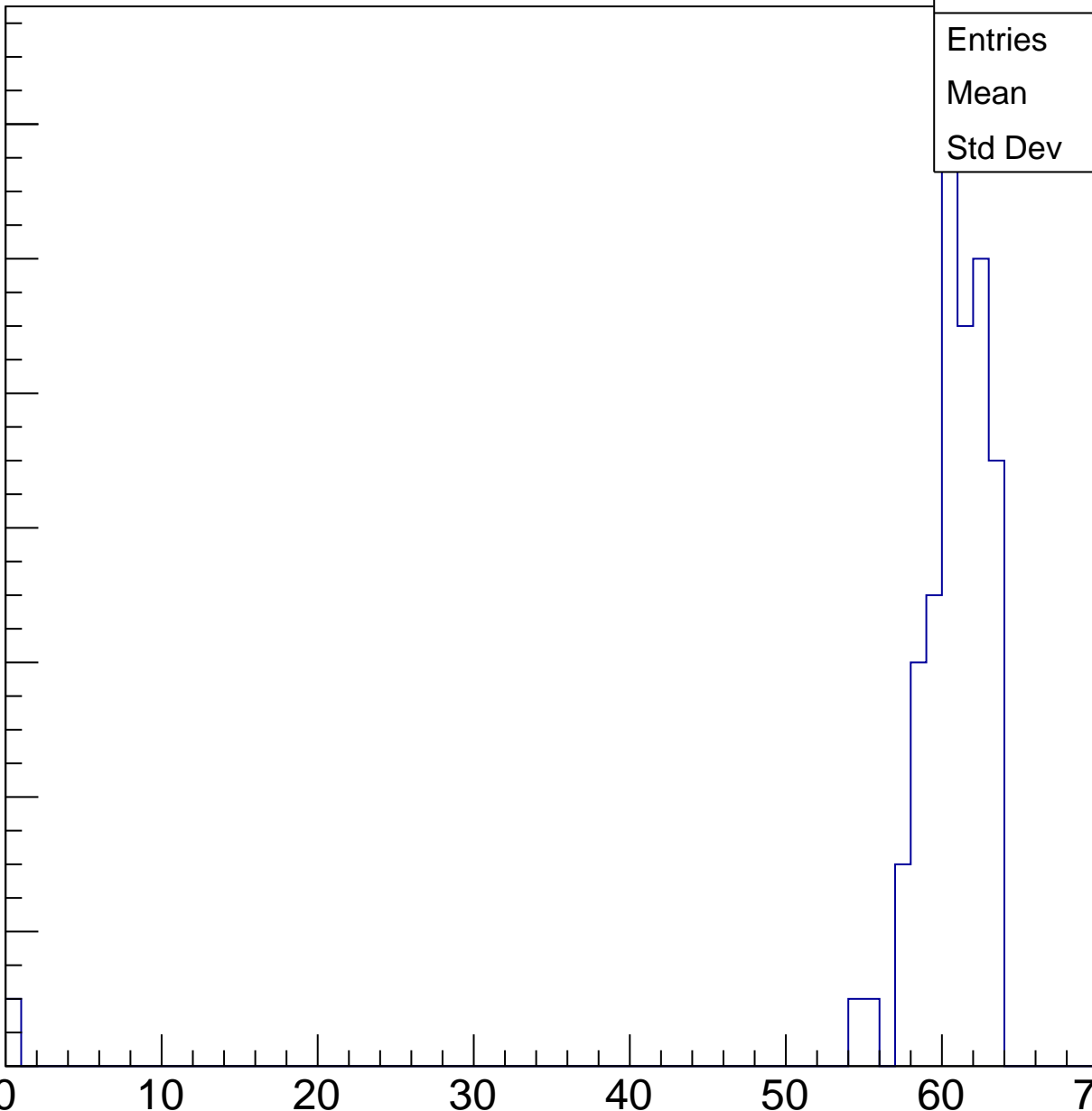
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.44
Std Dev	7.626

ampl

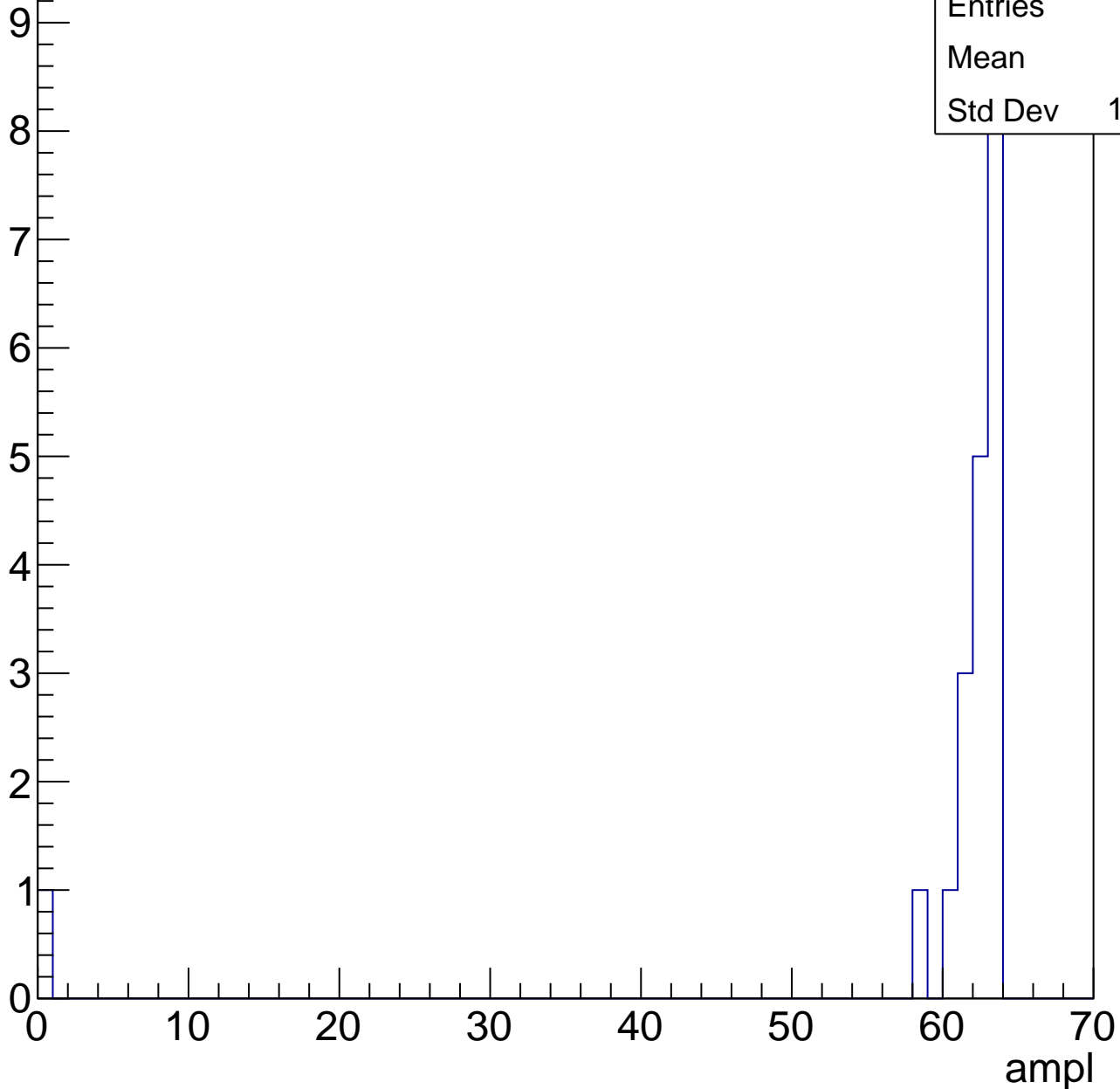


B1L001S, U24-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

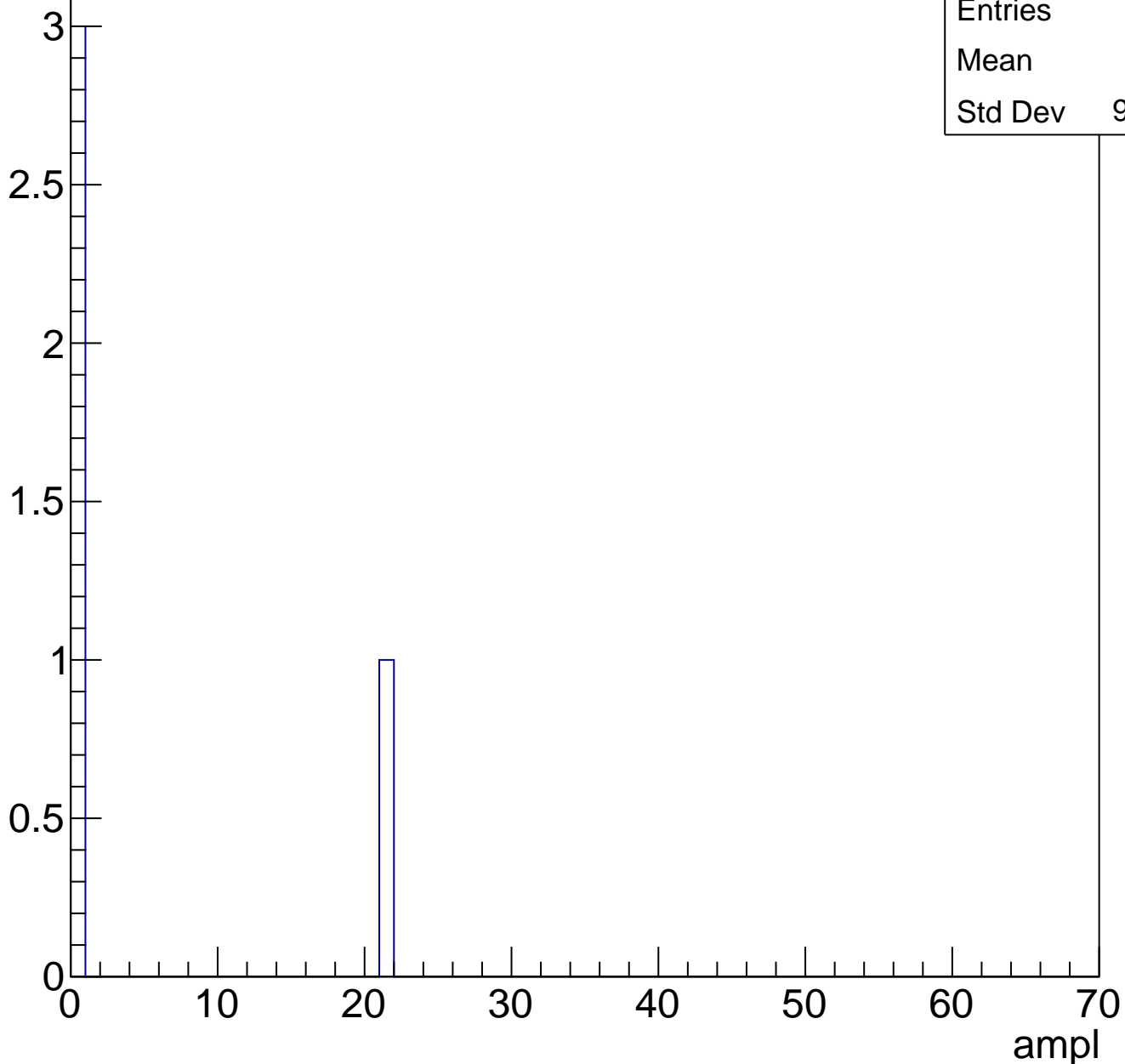
Entries	20
Mean	58.9
Std Dev	13.57



B1L001S, U24-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L001S, U24-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	30.11
Std Dev	3.031

Gaus mean : 30.8260

Gaus Width: 3.4969

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

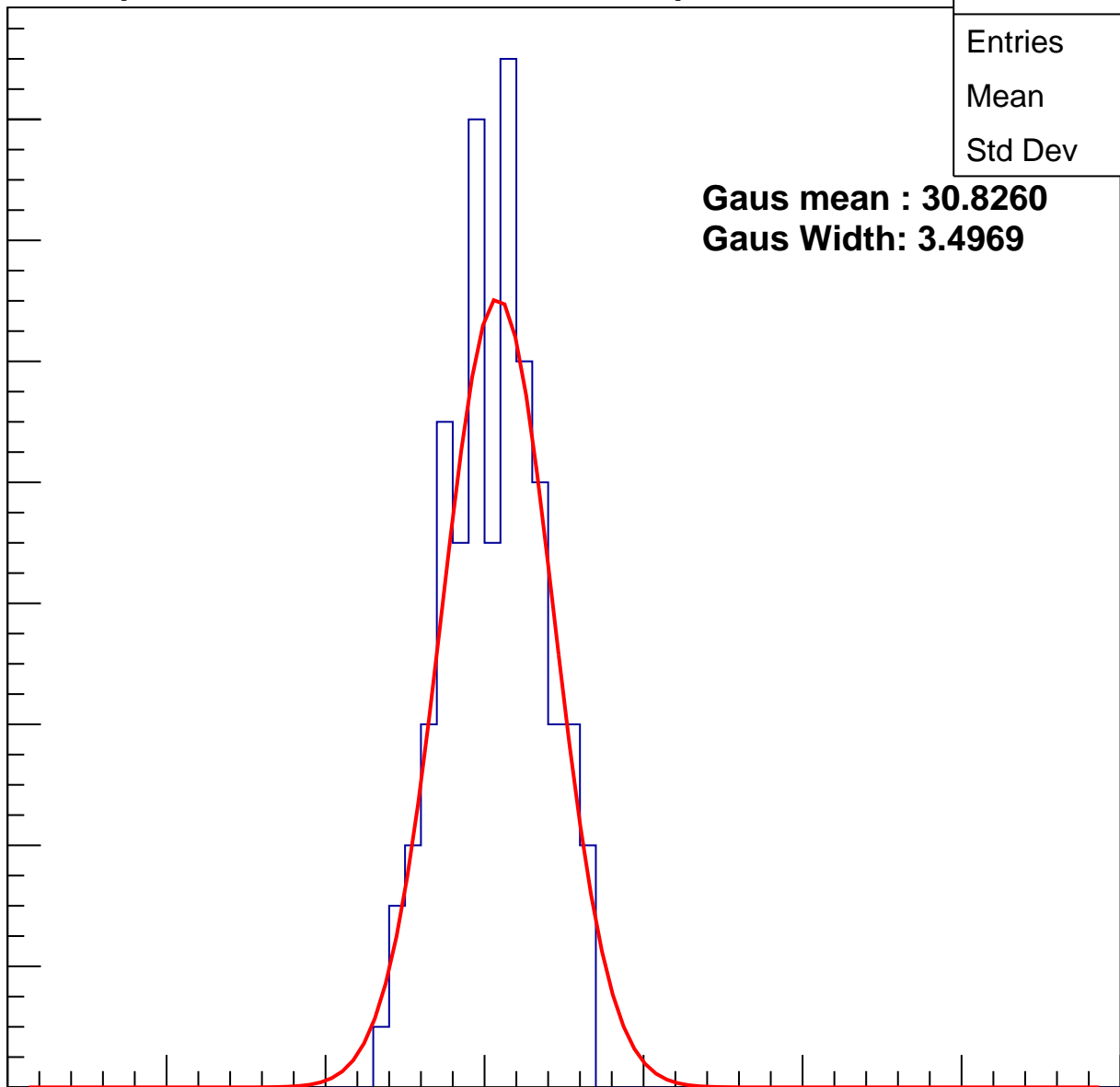
40

50

60

70

ampl



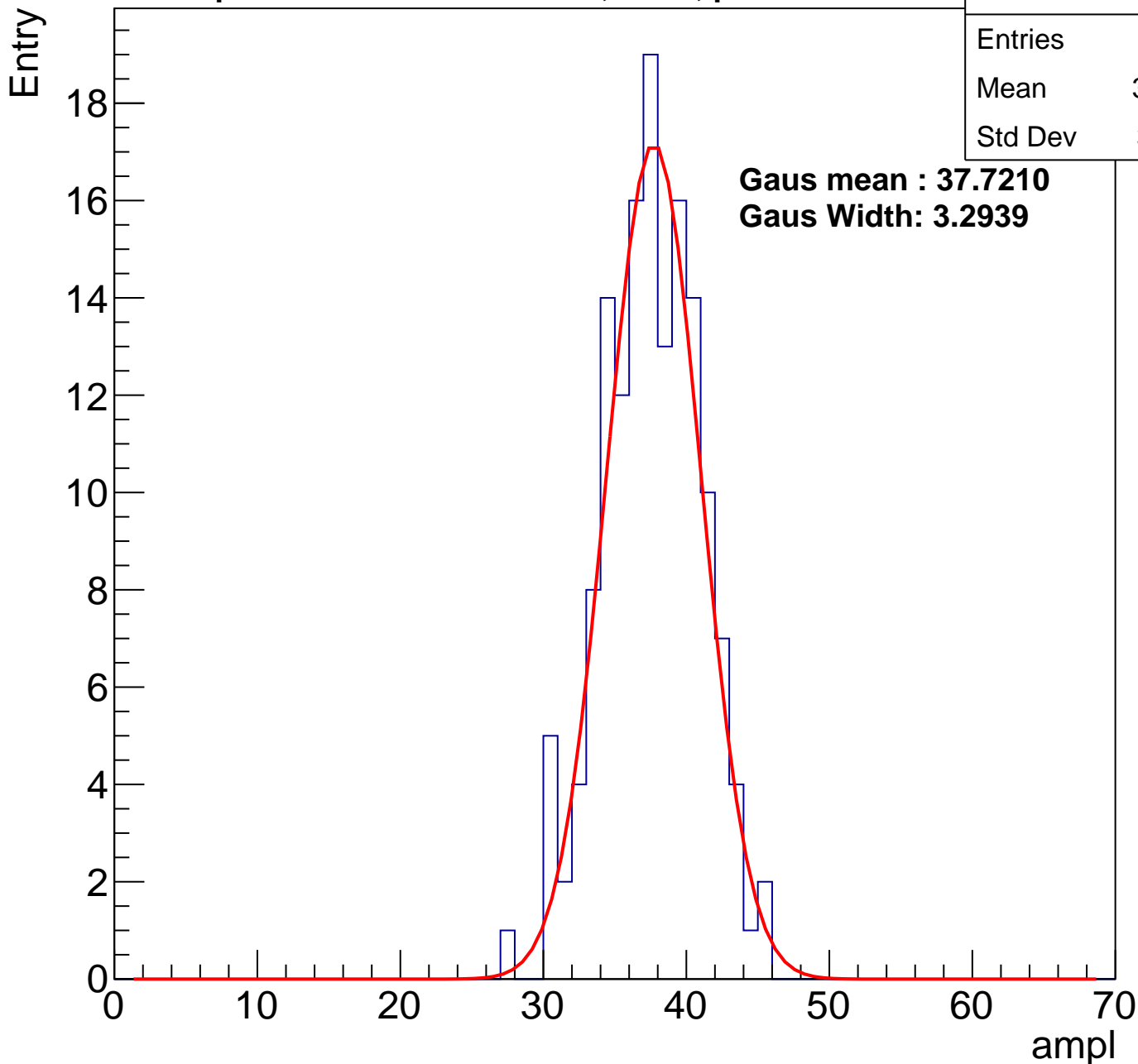
B1L001S, U24-ch39, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	37.12
Std Dev	3.381

Gaus mean : 37.7210

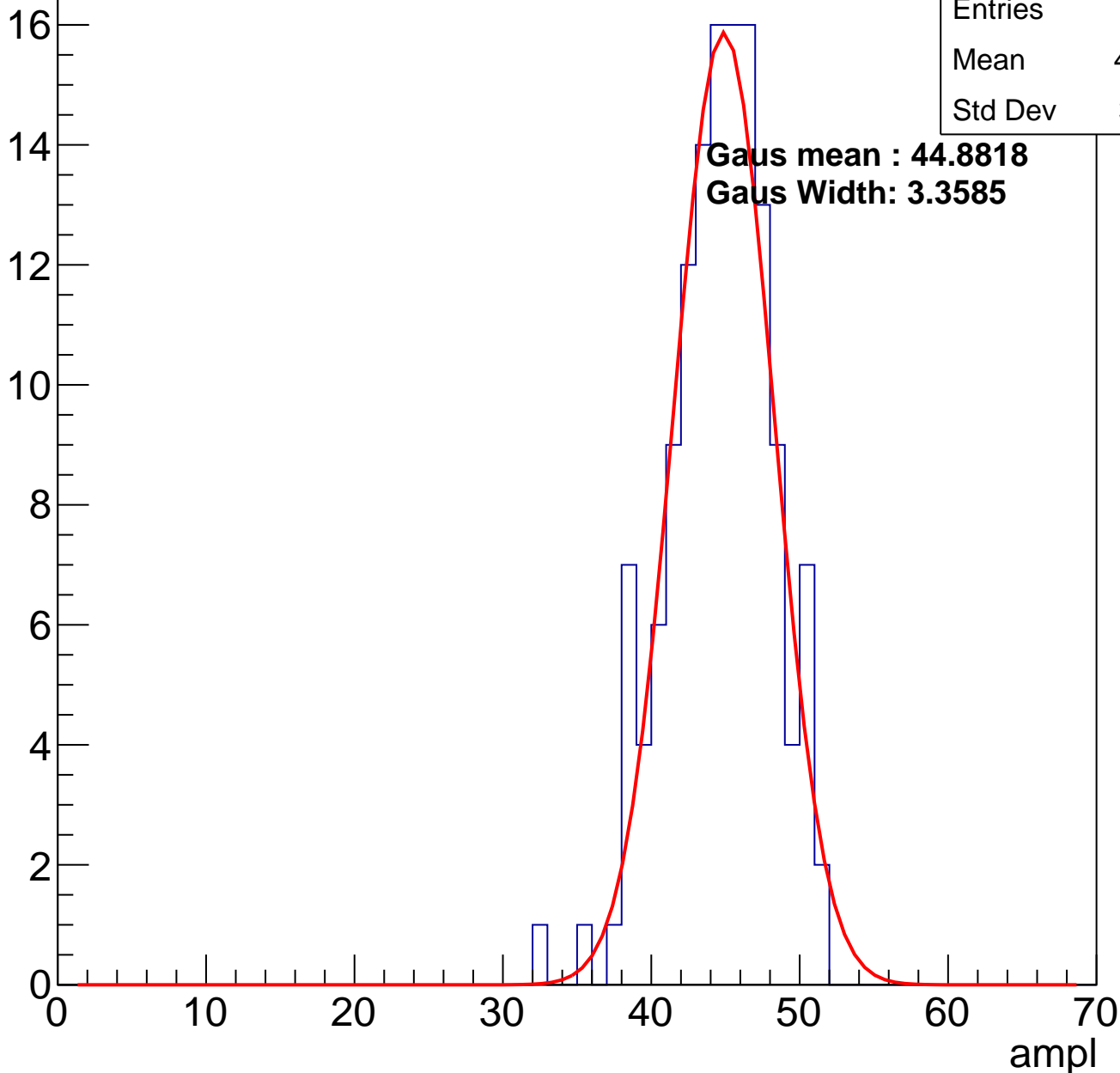
Gaus Width: 3.2939



B1L001S, U24-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

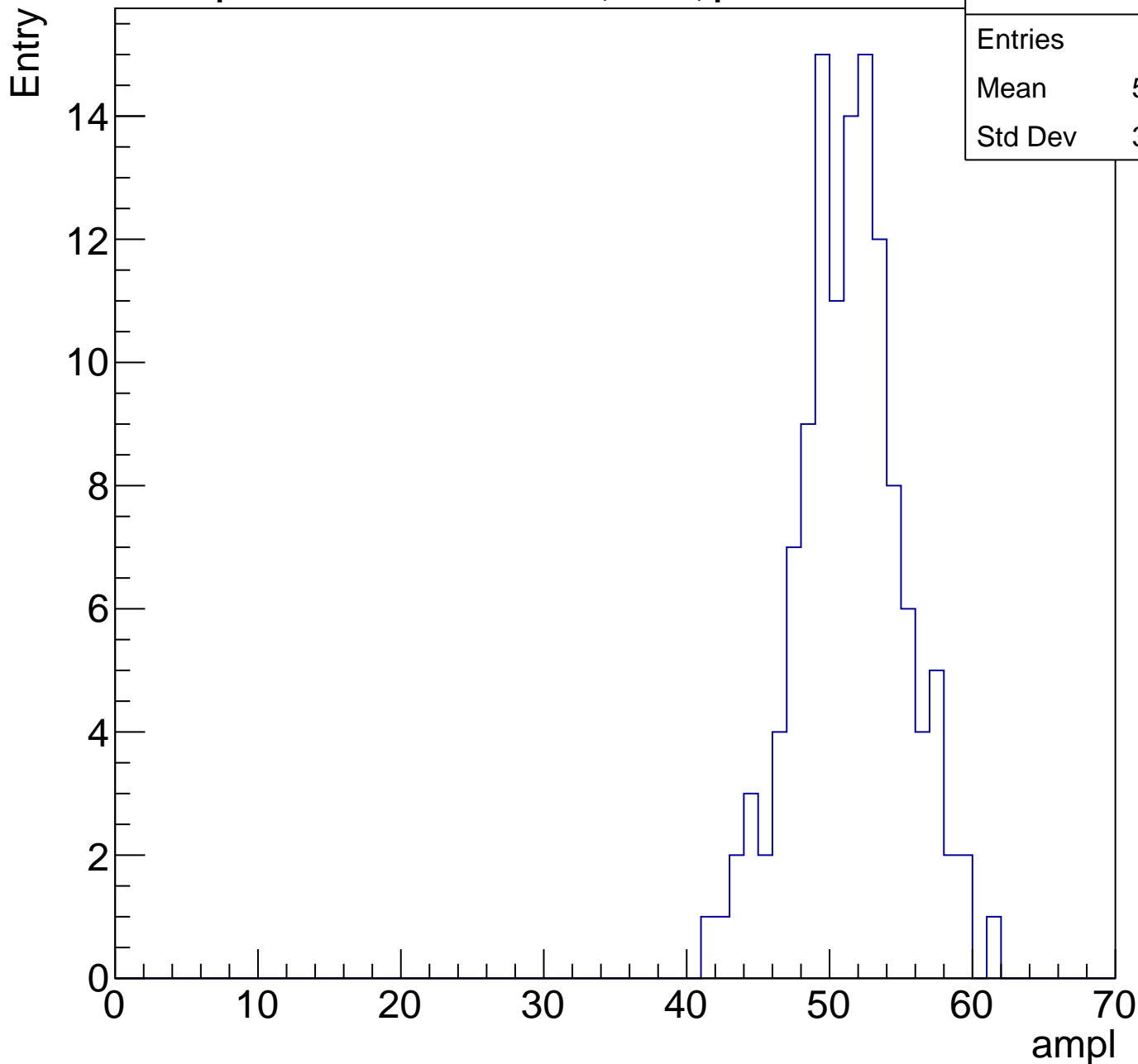
Entry



B1L001S, U24-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	50.94
Std Dev	3.734



B1L001S, U24-ch39, adc4

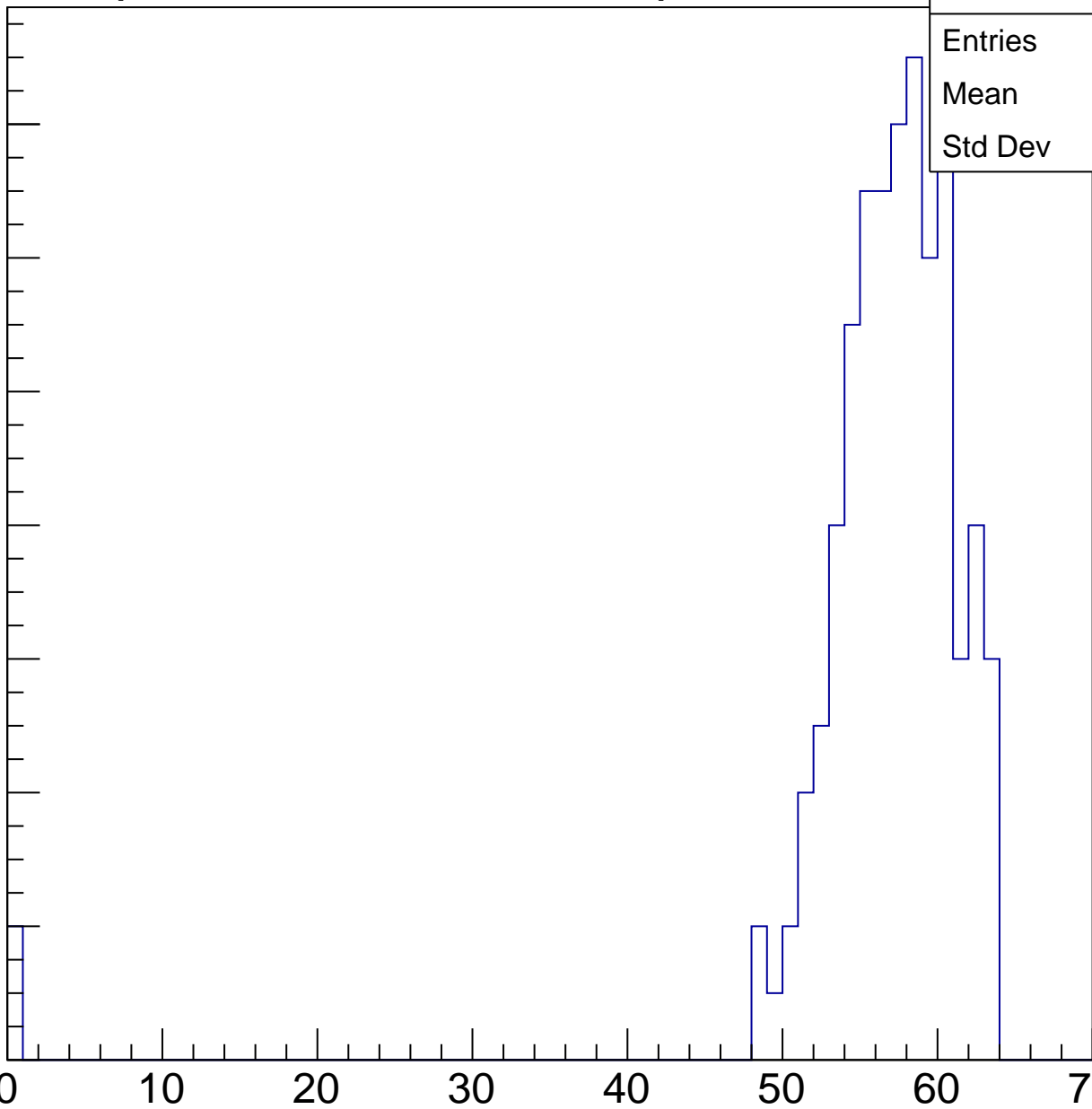
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	137
Mean	56.1
Std Dev	7.633

ampl



B1L001S, U24-ch39, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	53
Mean	60.51
Std Dev	2.271

B1L001S, U24-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch40, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.08
Std Dev	3.137

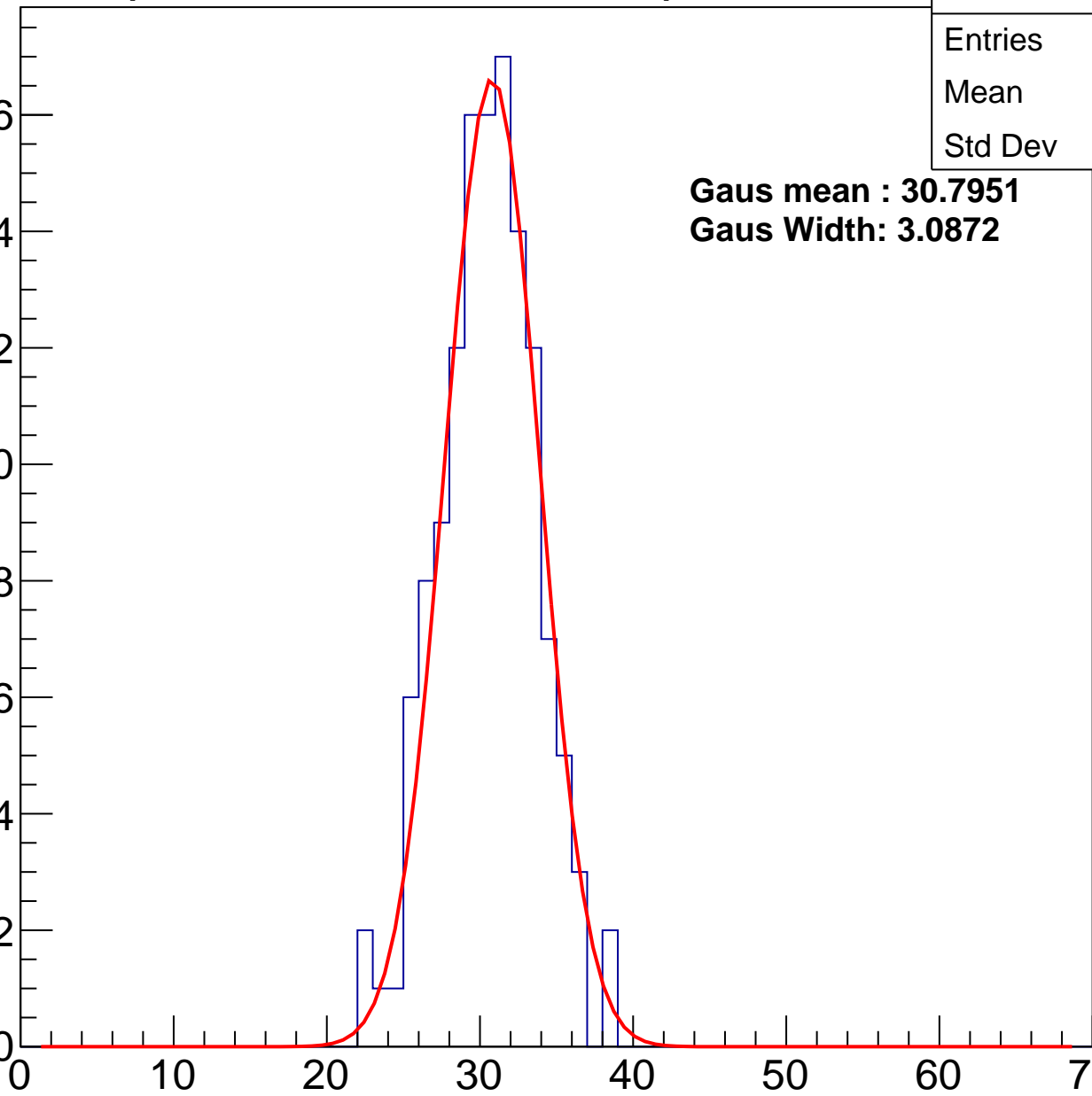
Gaus mean : 30.7951

Gaus Width: 3.0872

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch40, adc1

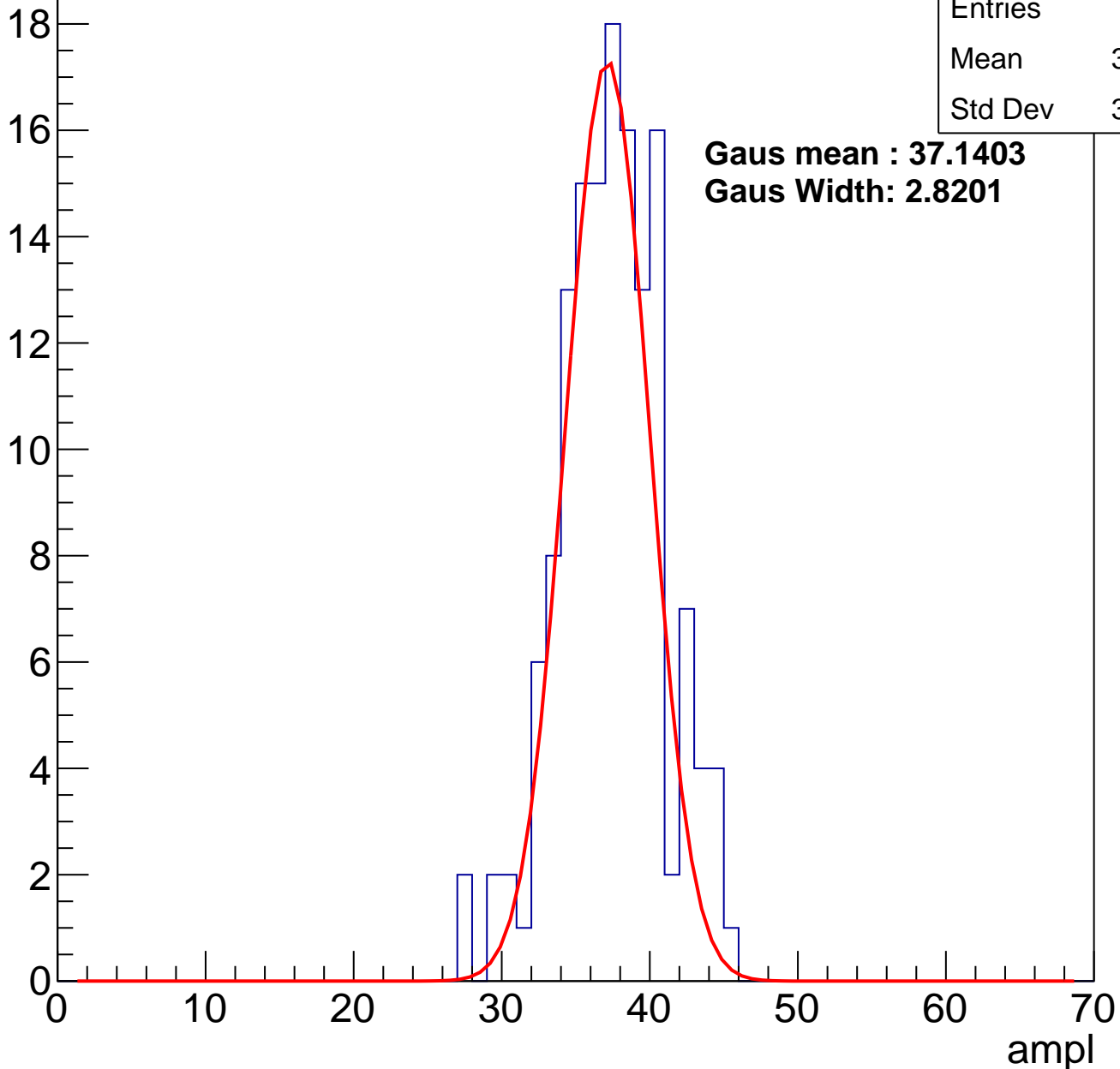
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.94
Std Dev	3.478

Gaus mean : 37.1403

Gaus Width: 2.8201

Entry



B1L001S, U24-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

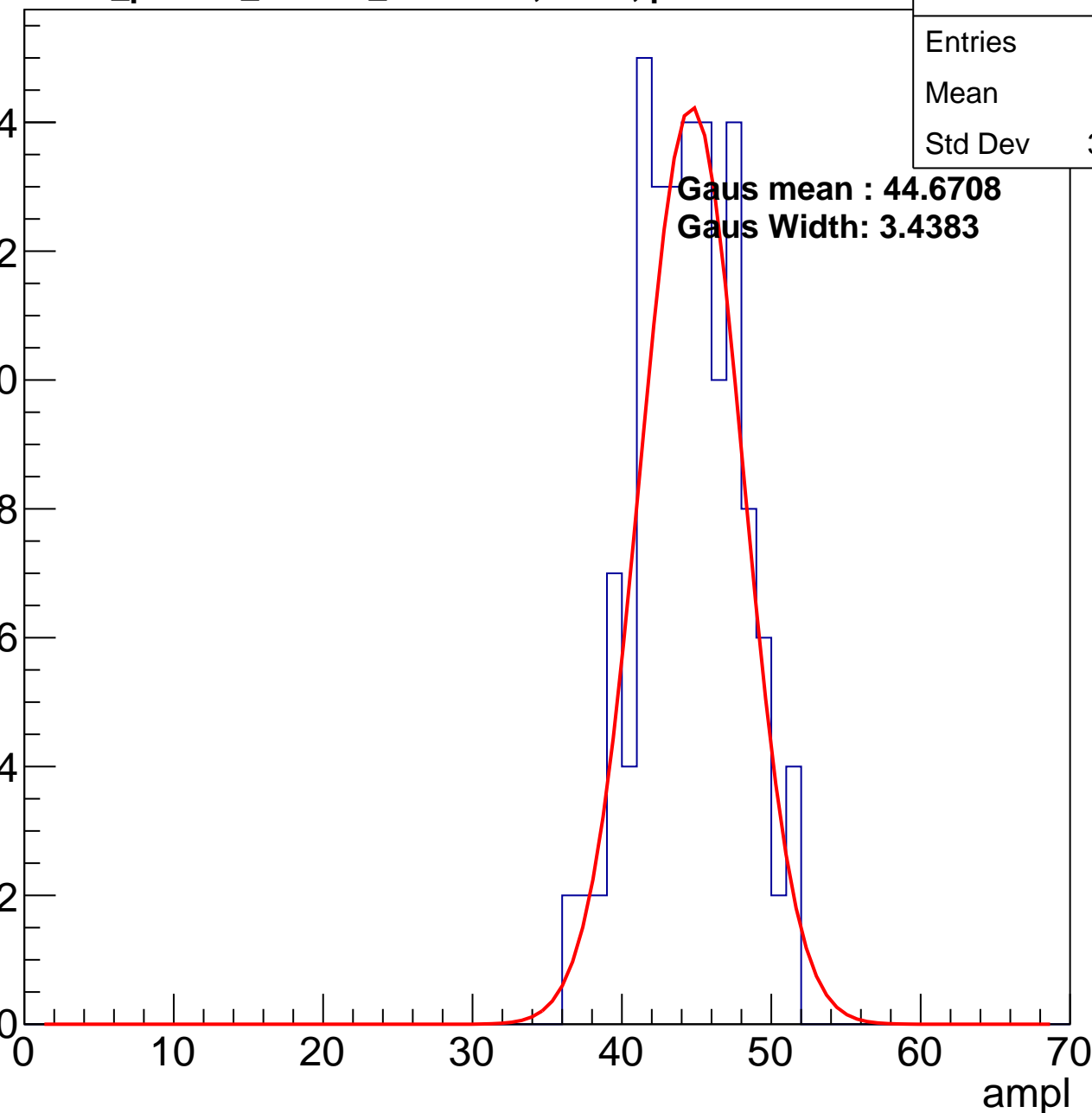
Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	44.01
Std Dev	3.355

Gaus mean : 44.6708

Gaus Width: 3.4383



B1L001S, U24-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

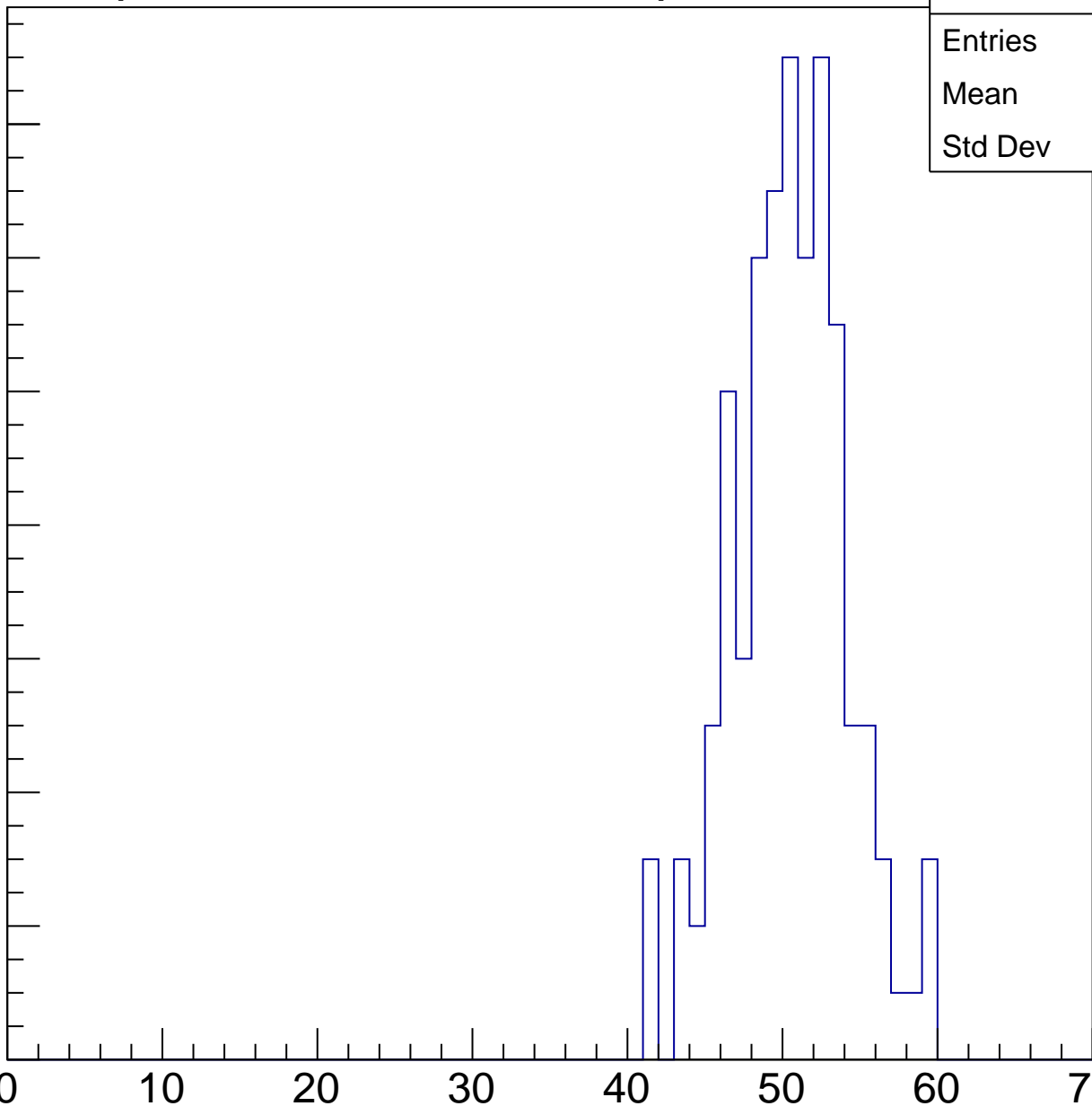
Entries	125
Mean	50
Std Dev	3.69

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch40, adc4

calib_packv5_042523_0143.root, FC#2, port C2

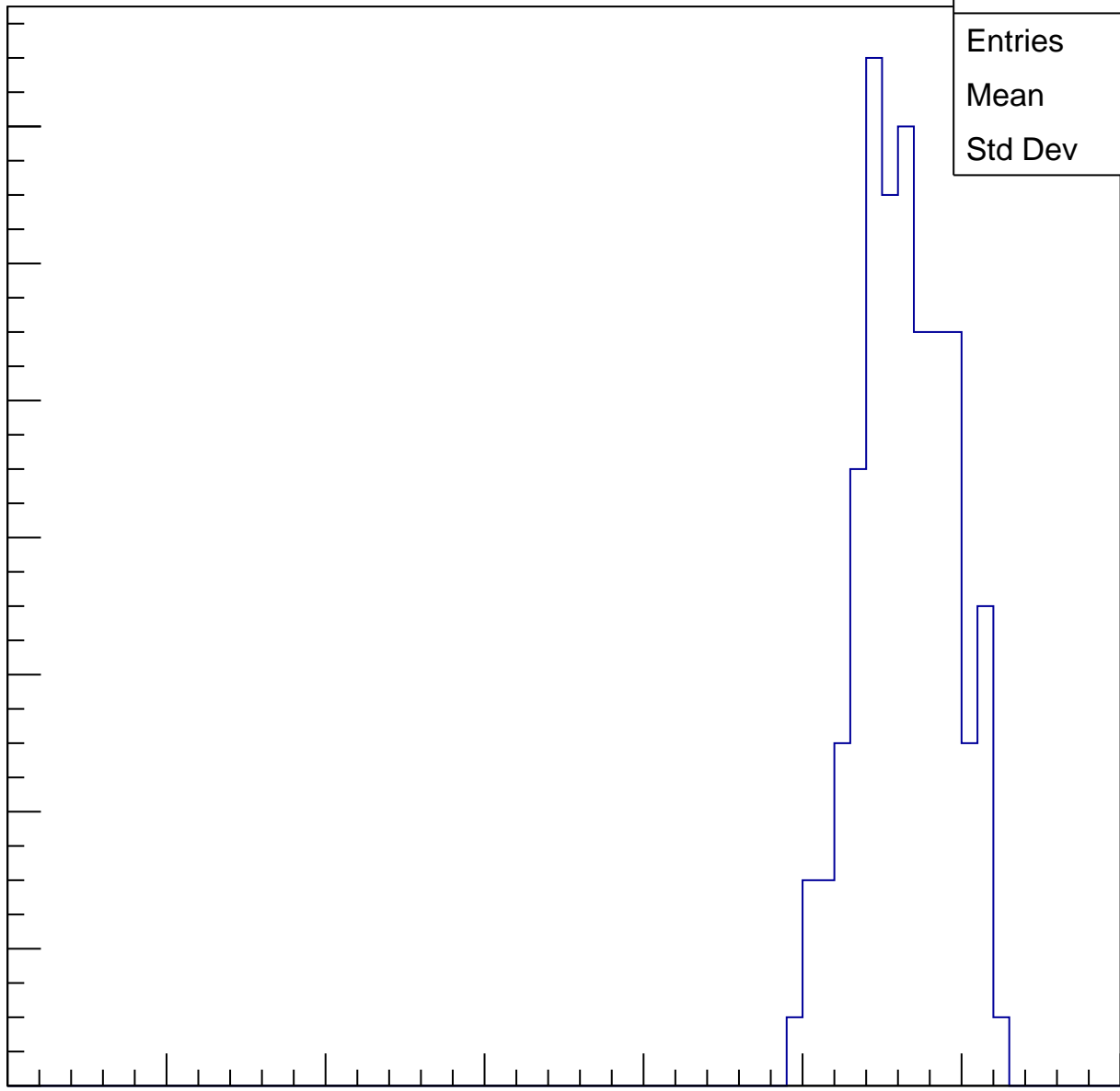
Entries	109
Mean	55.97
Std Dev	2.891

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch40, adc5

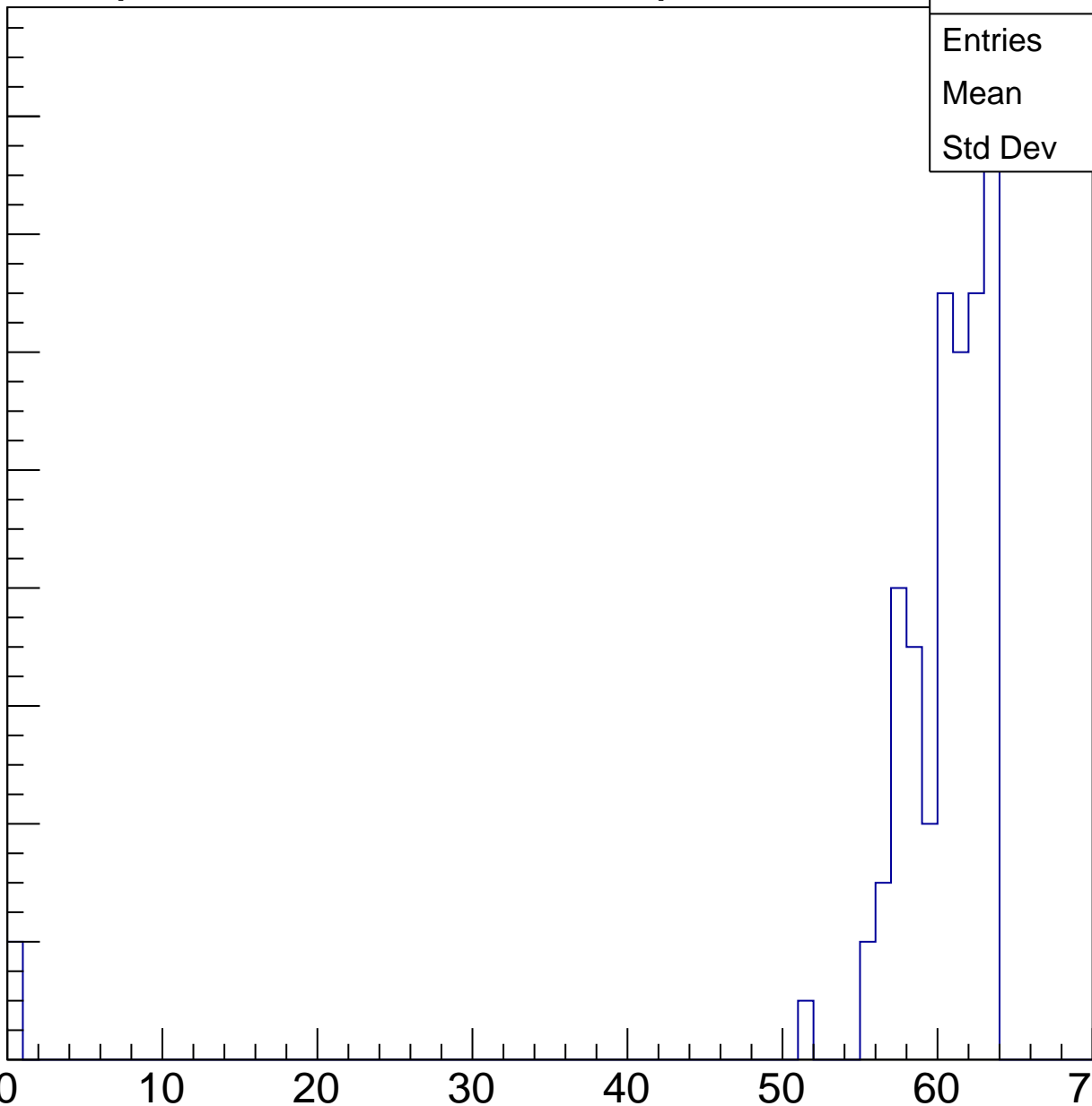
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	58.73
Std Dev	9.607

ampl

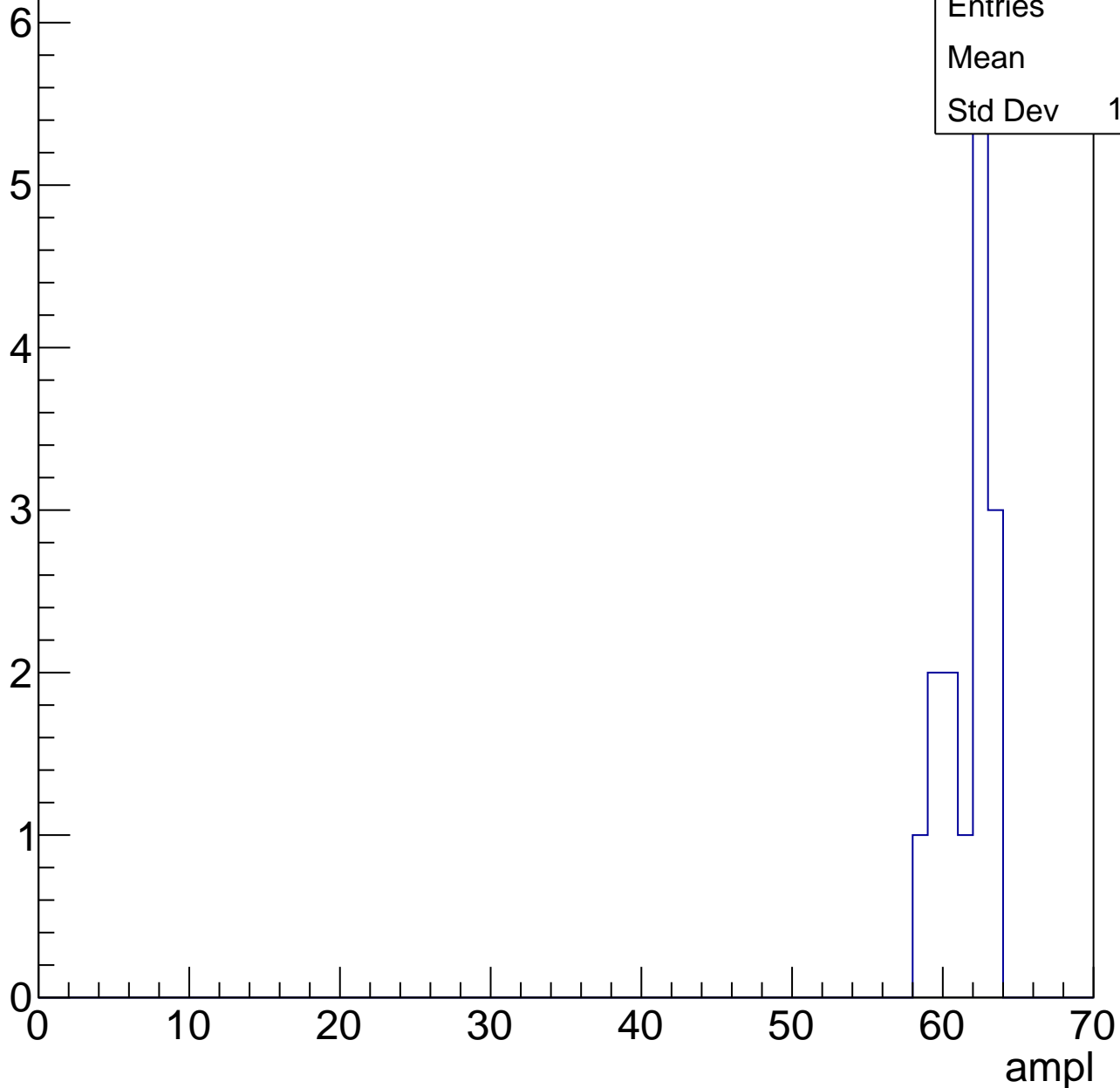


B1L001S, U24-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	61.2
Std Dev	1.558



B1L001S, U24-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U24-ch41, adc0

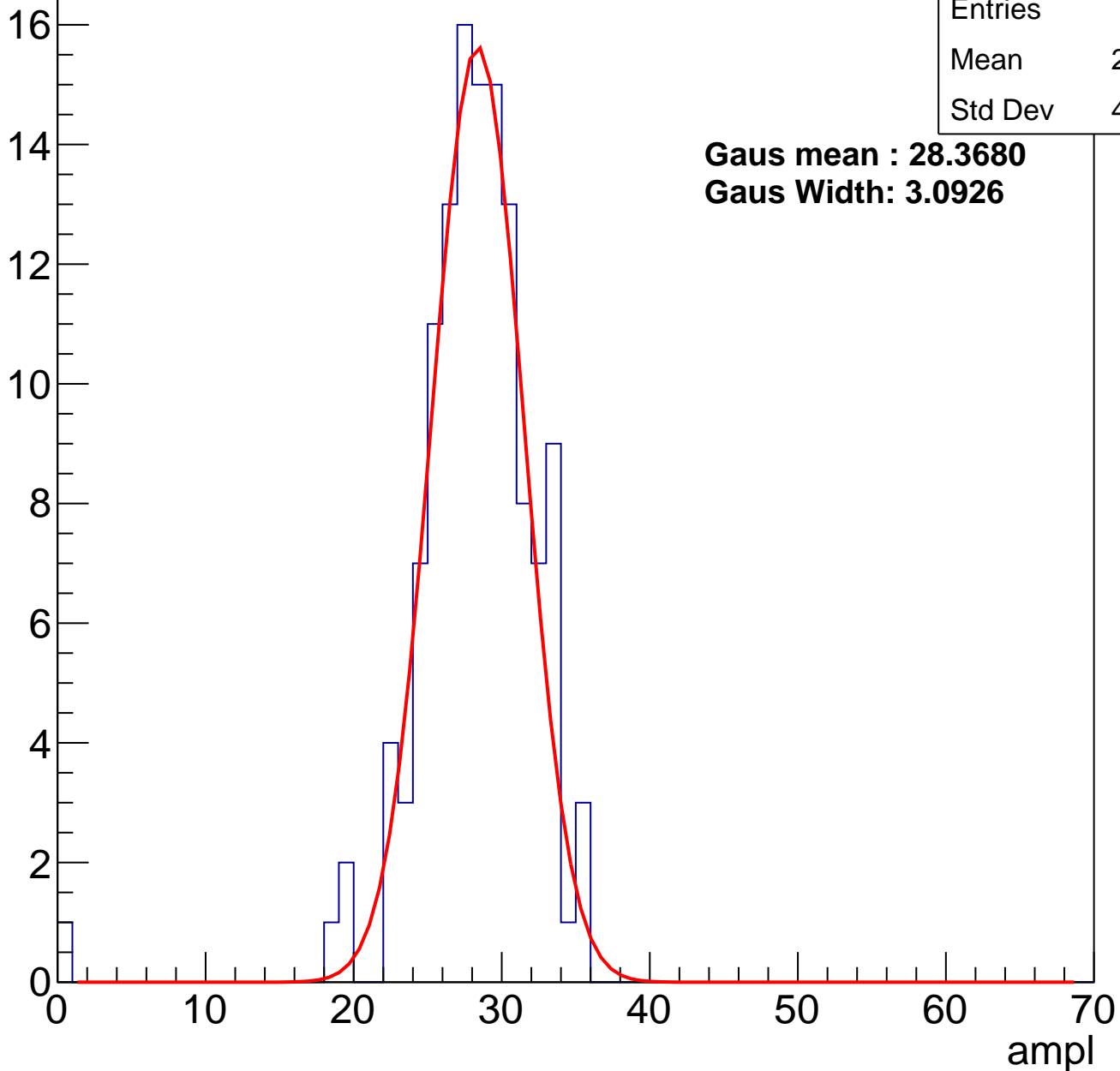
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	27.74
Std Dev	4.125

Gaus mean : 28.3680

Gaus Width: 3.0926

Entry



B1L001S, U24-ch41, adc1

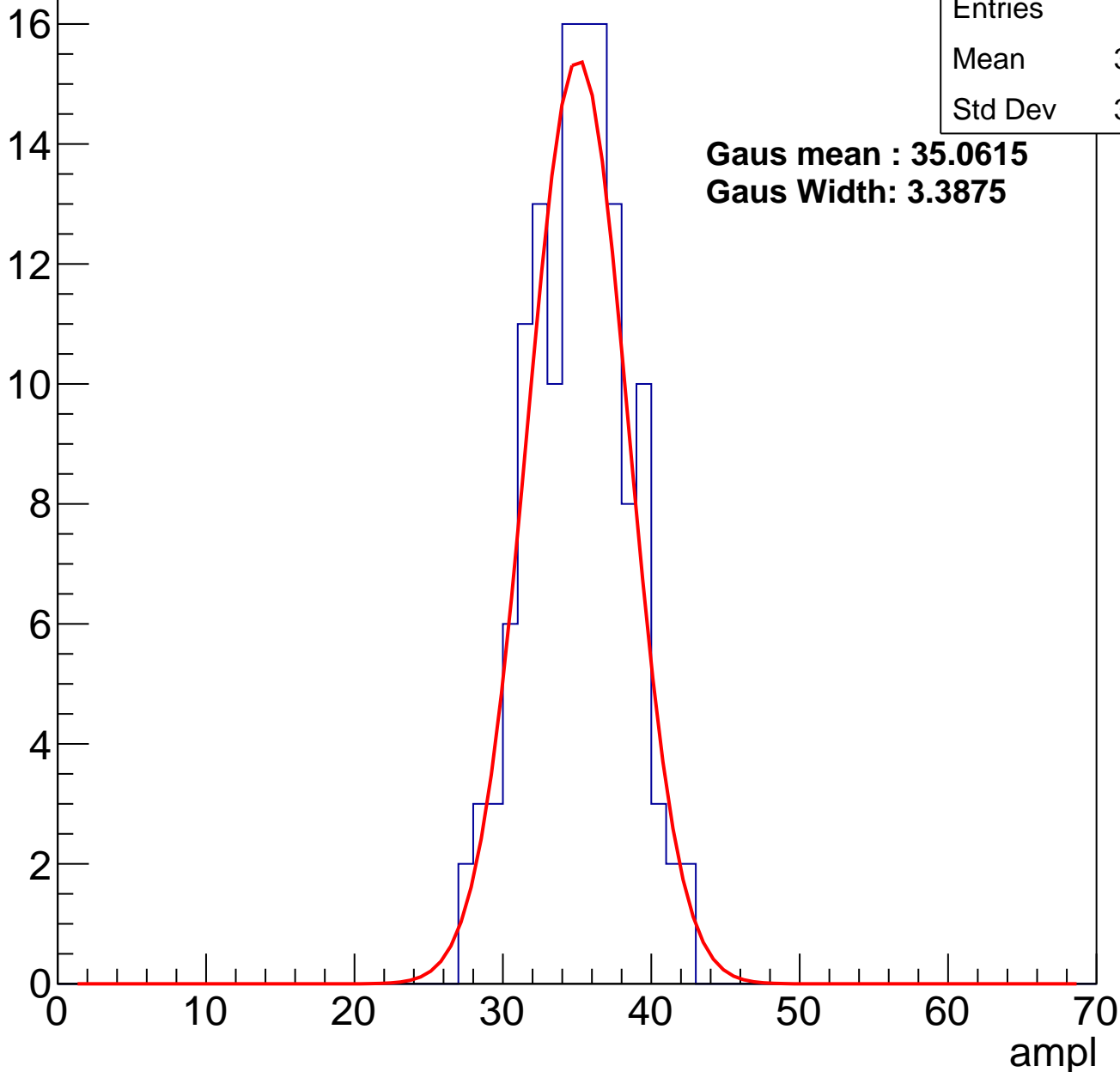
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	34.57
Std Dev	3.235

Gaus mean : 35.0615

Gaus Width: 3.3875

Entry



B1L001S, U24-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

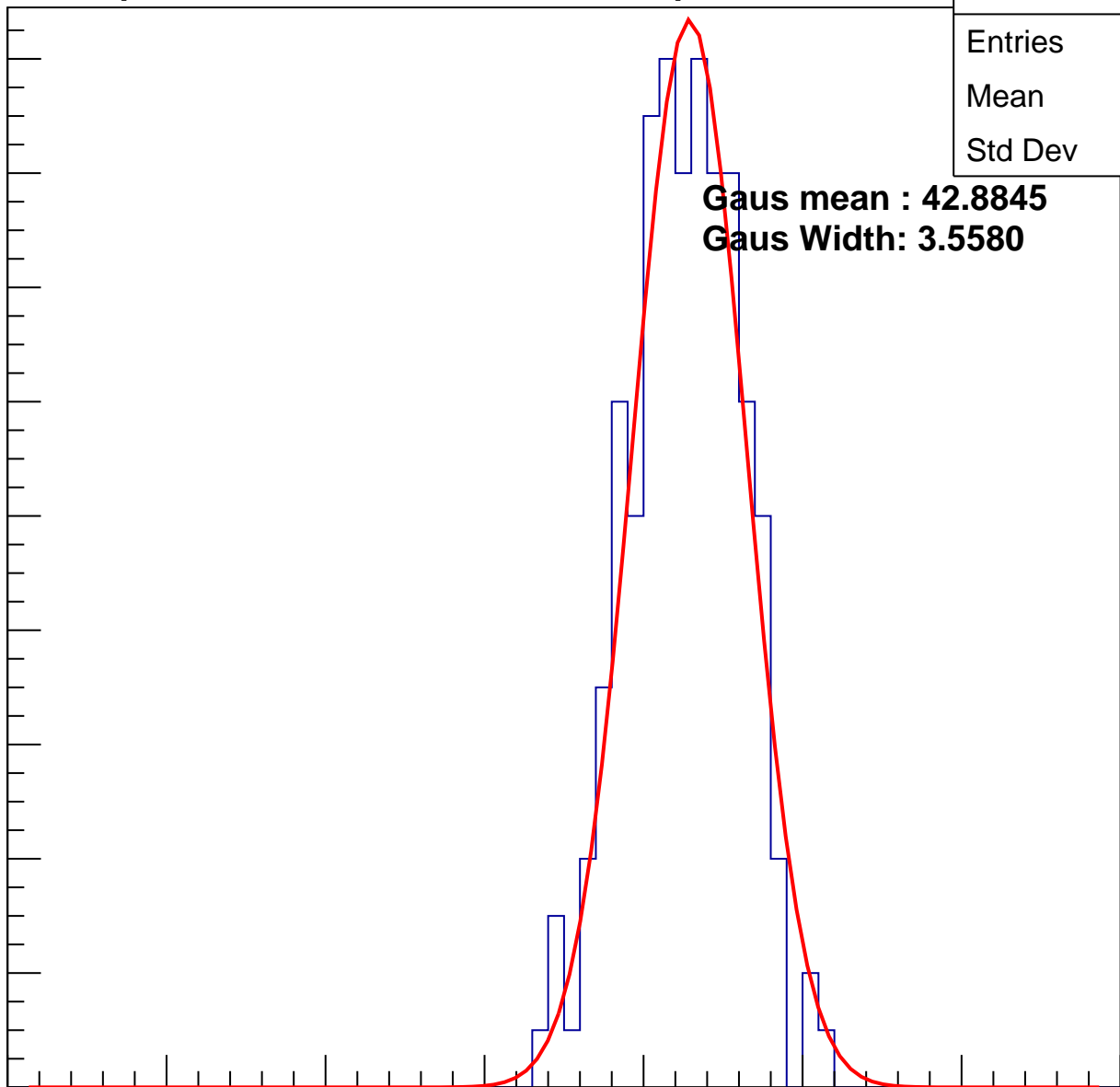
Entries	168
Mean	42.1
Std Dev	3.475

Gaus mean : 42.8845

Gaus Width: 3.5580

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch41, adc3

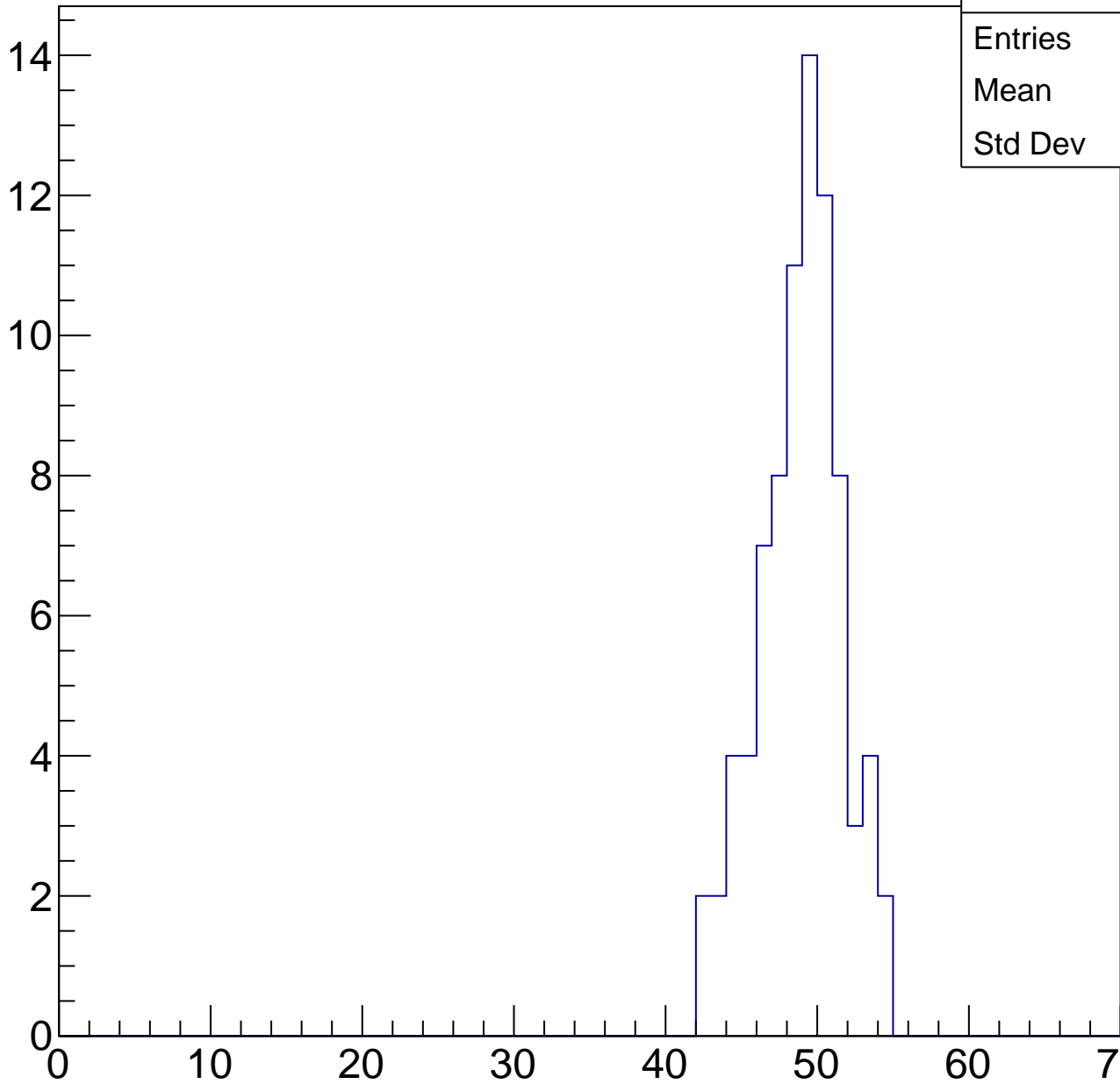
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	48.42
Std Dev	2.716

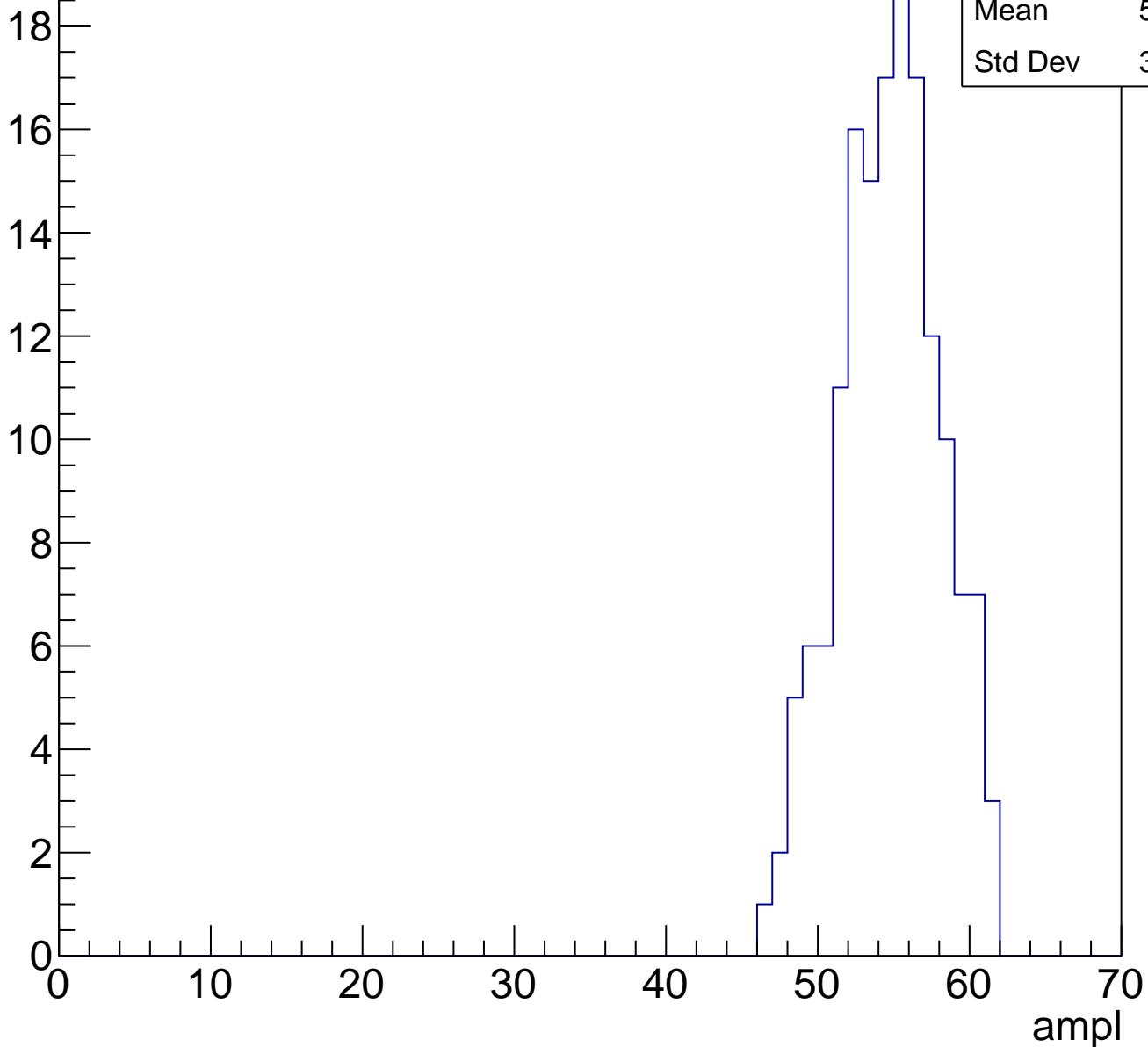
ampl



B1L001S, U24-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	154
Mean	54.27
Std Dev	3.307

B1L001S, U24-ch41, adc5

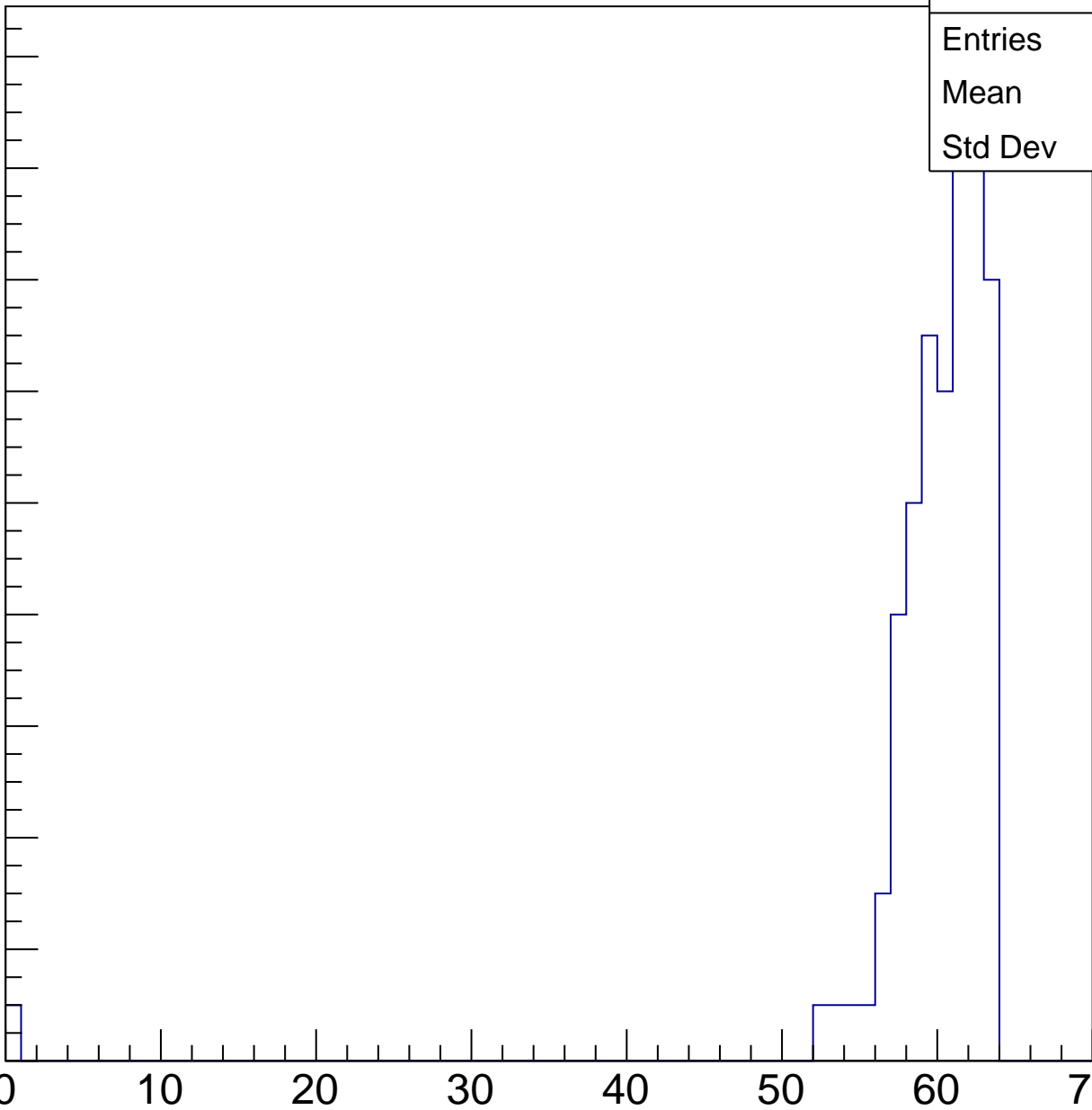
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.38
Std Dev	6.454

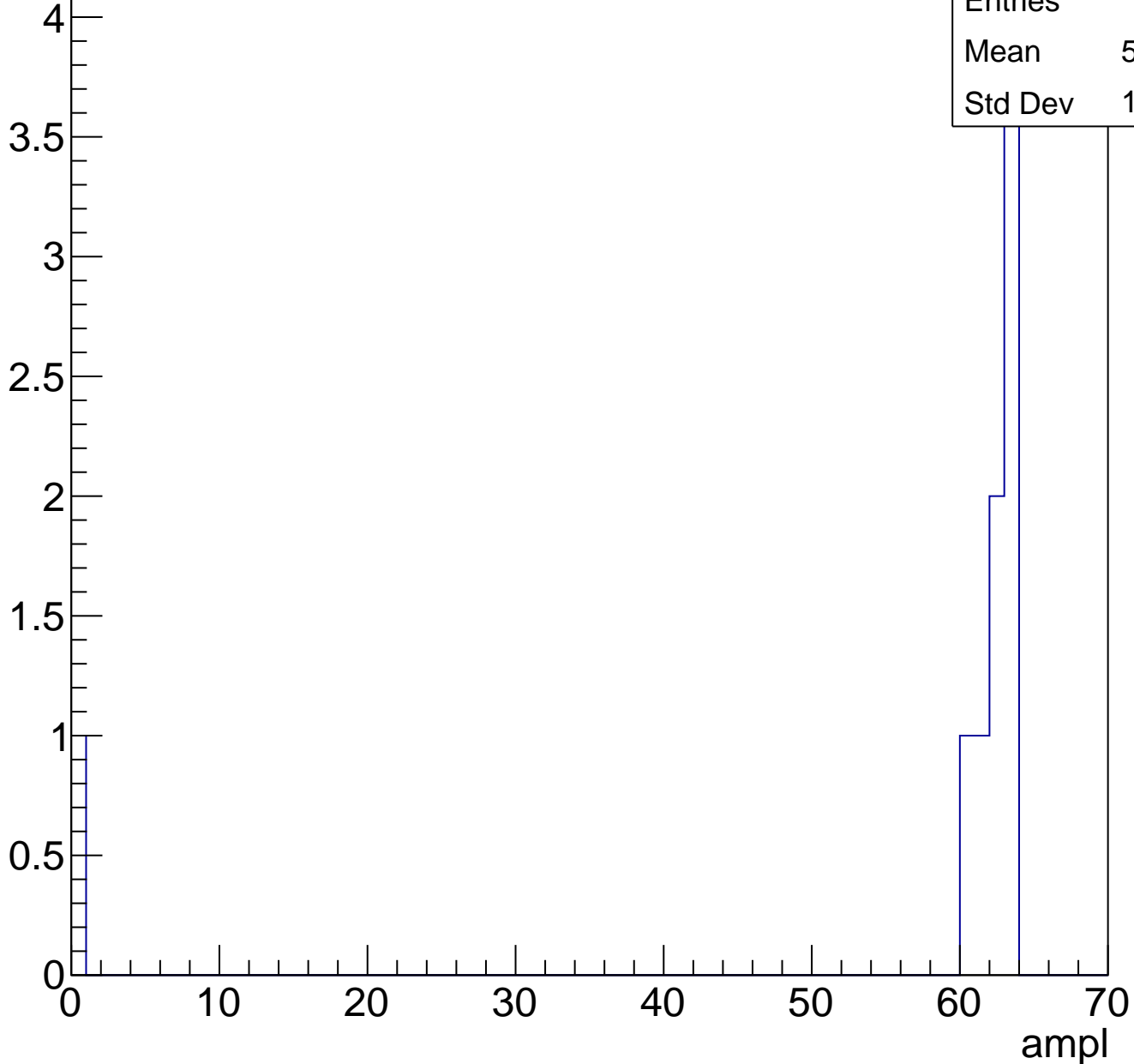
ampl



B1L001S, U24-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

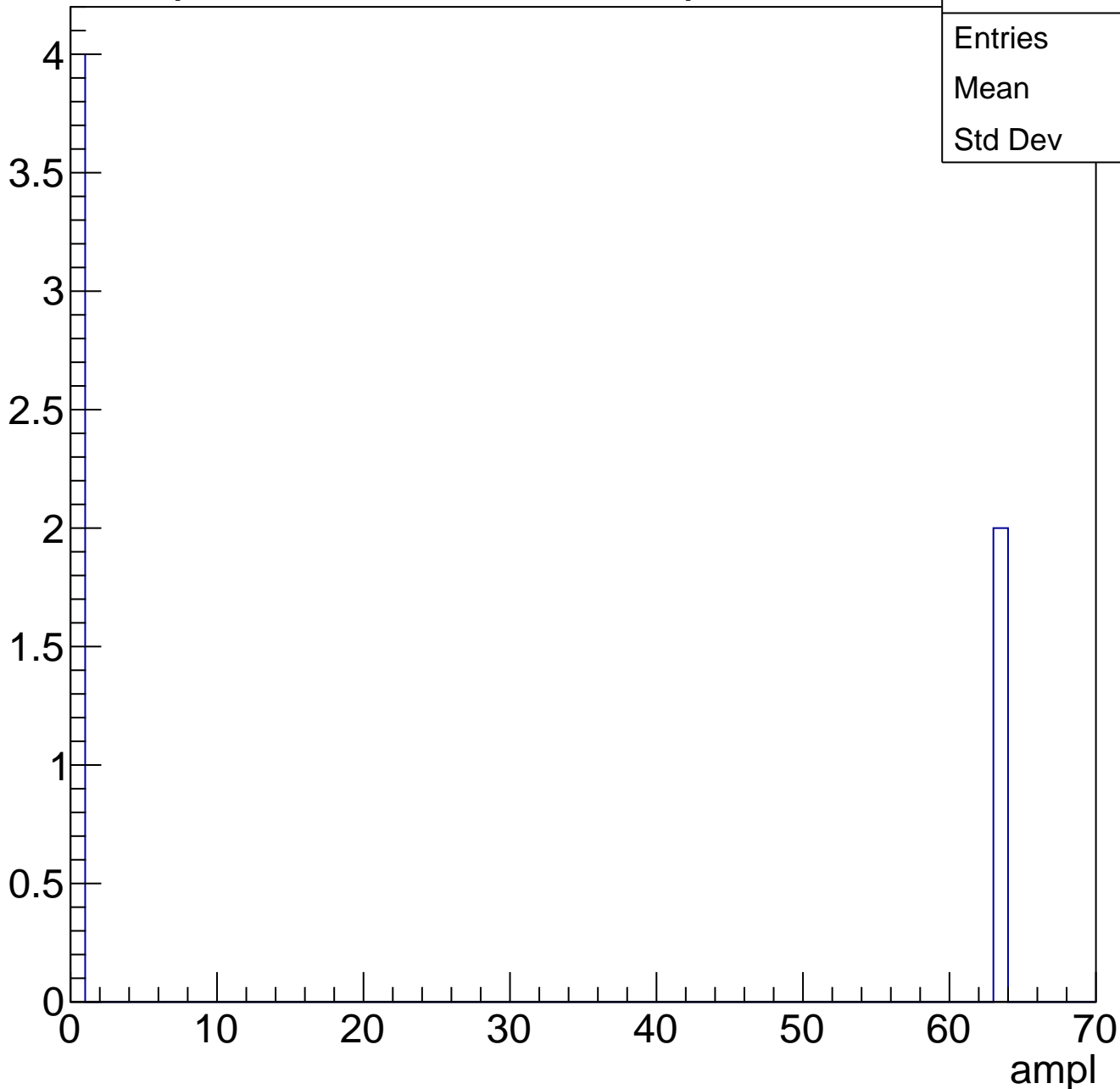
Entry



B1L001S, U24-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch42, adc0

calib_packv5_042523_0143.root, FC#2, port C2

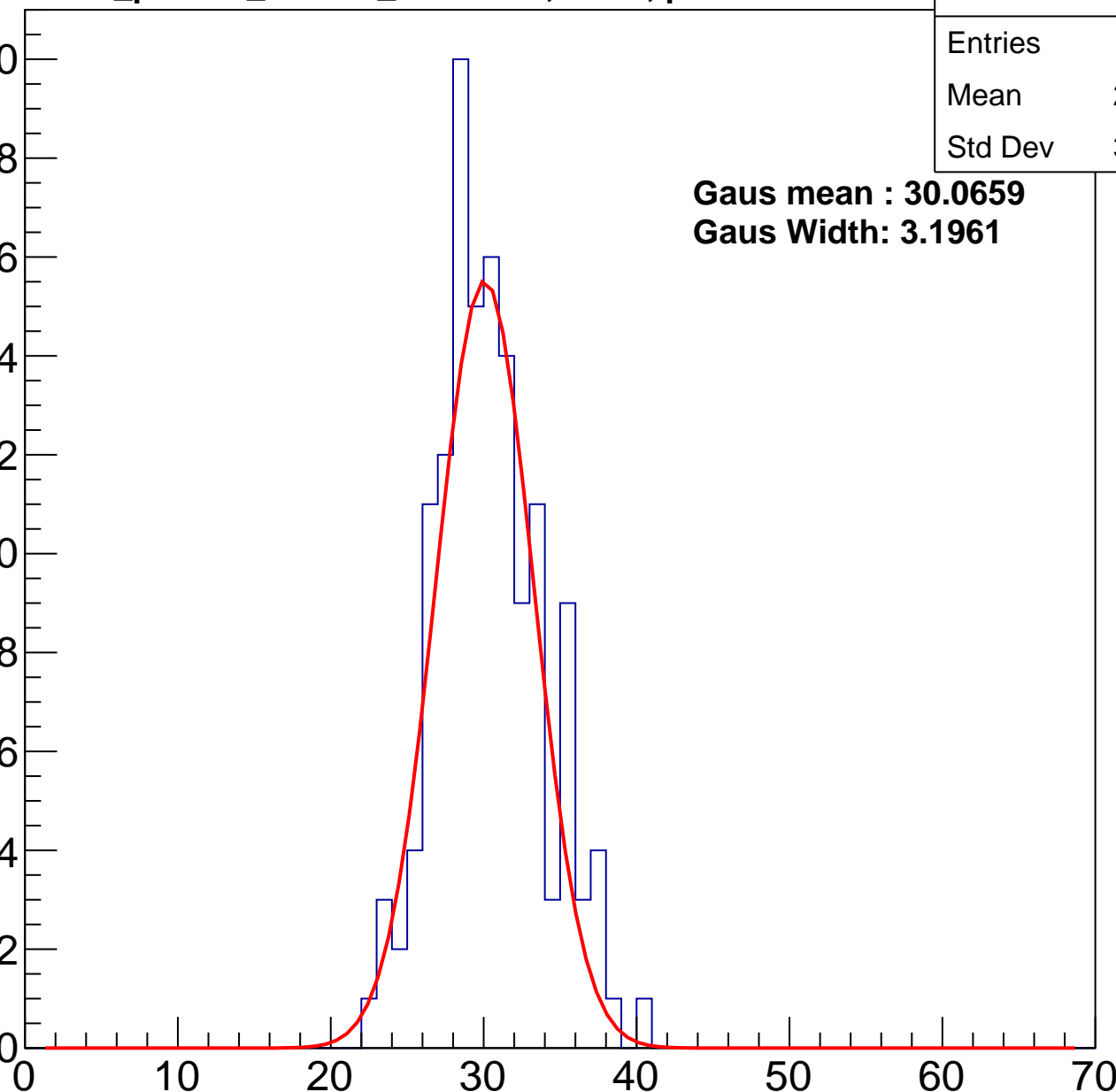
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	139
Mean	29.93
Std Dev	3.447

Gaus mean : 30.0659

Gaus Width: 3.1961



ampl

B1L001S, U24-ch42, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.1
Std Dev	3.248

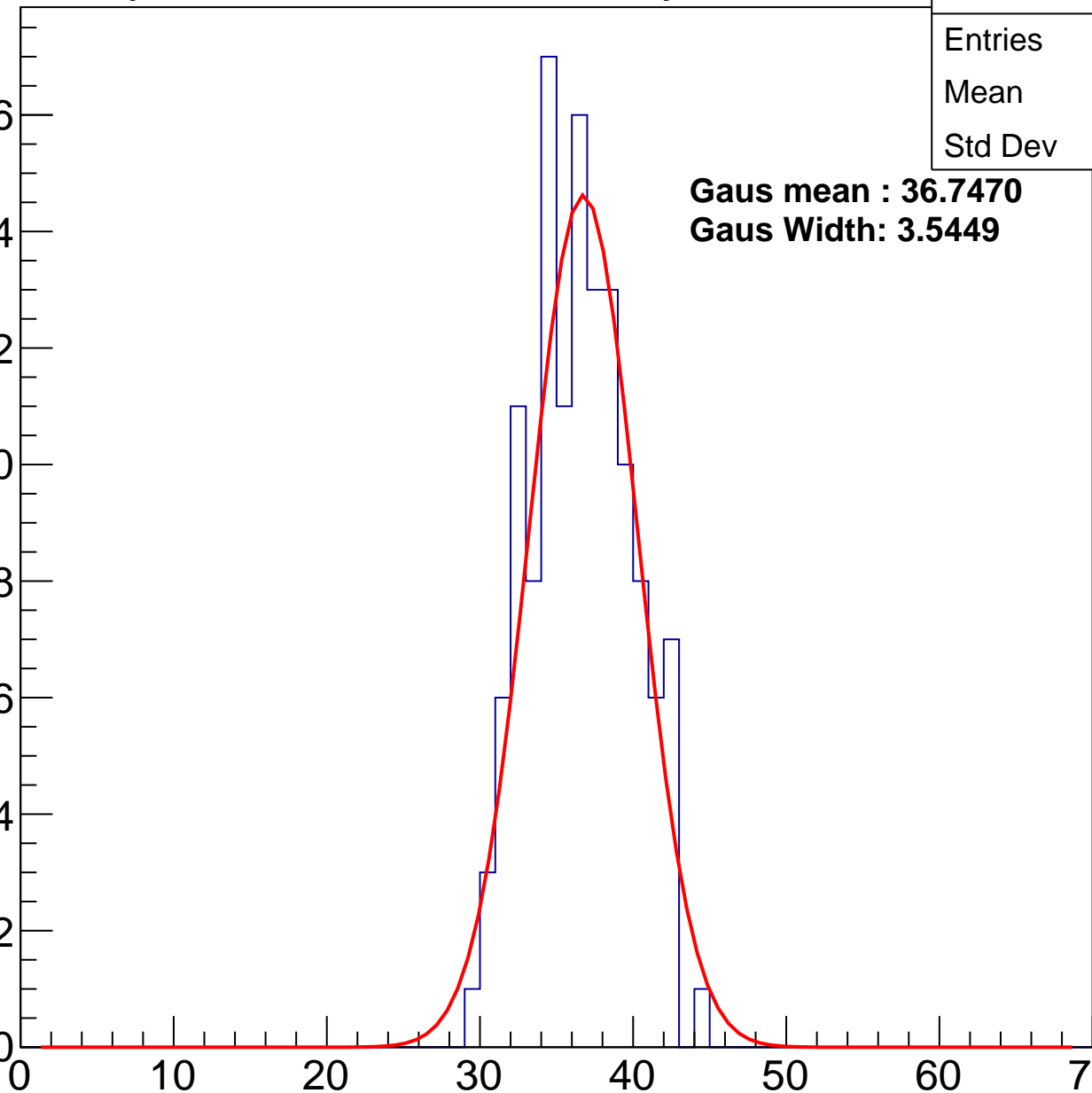
Gaus mean : 36.7470

Gaus Width: 3.5449

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

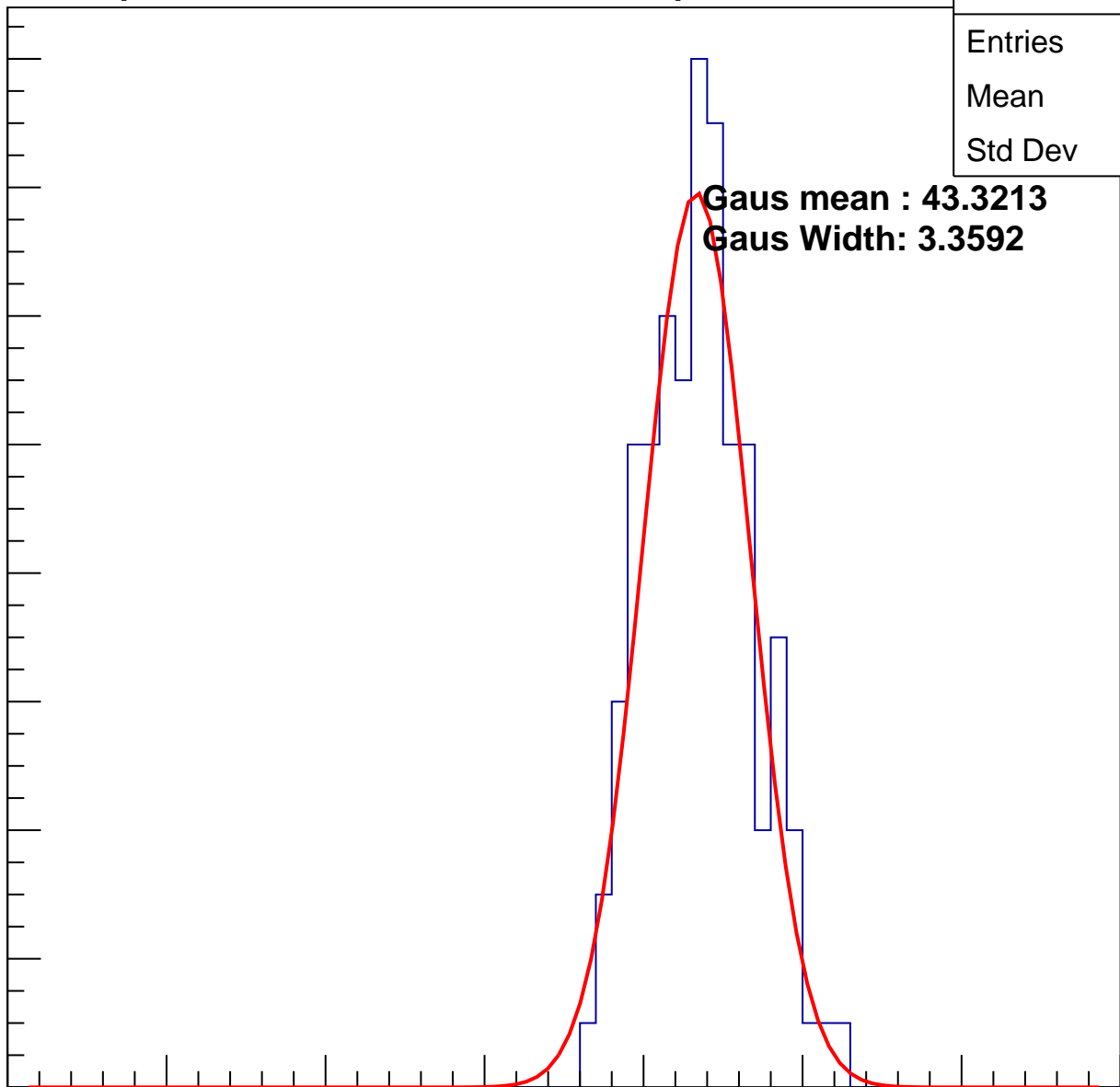
Entries	122
Mean	43.03
Std Dev	3.312

Gaus mean : 43.3213

Gaus Width: 3.3592

0 10 20 30 40 50 60 70

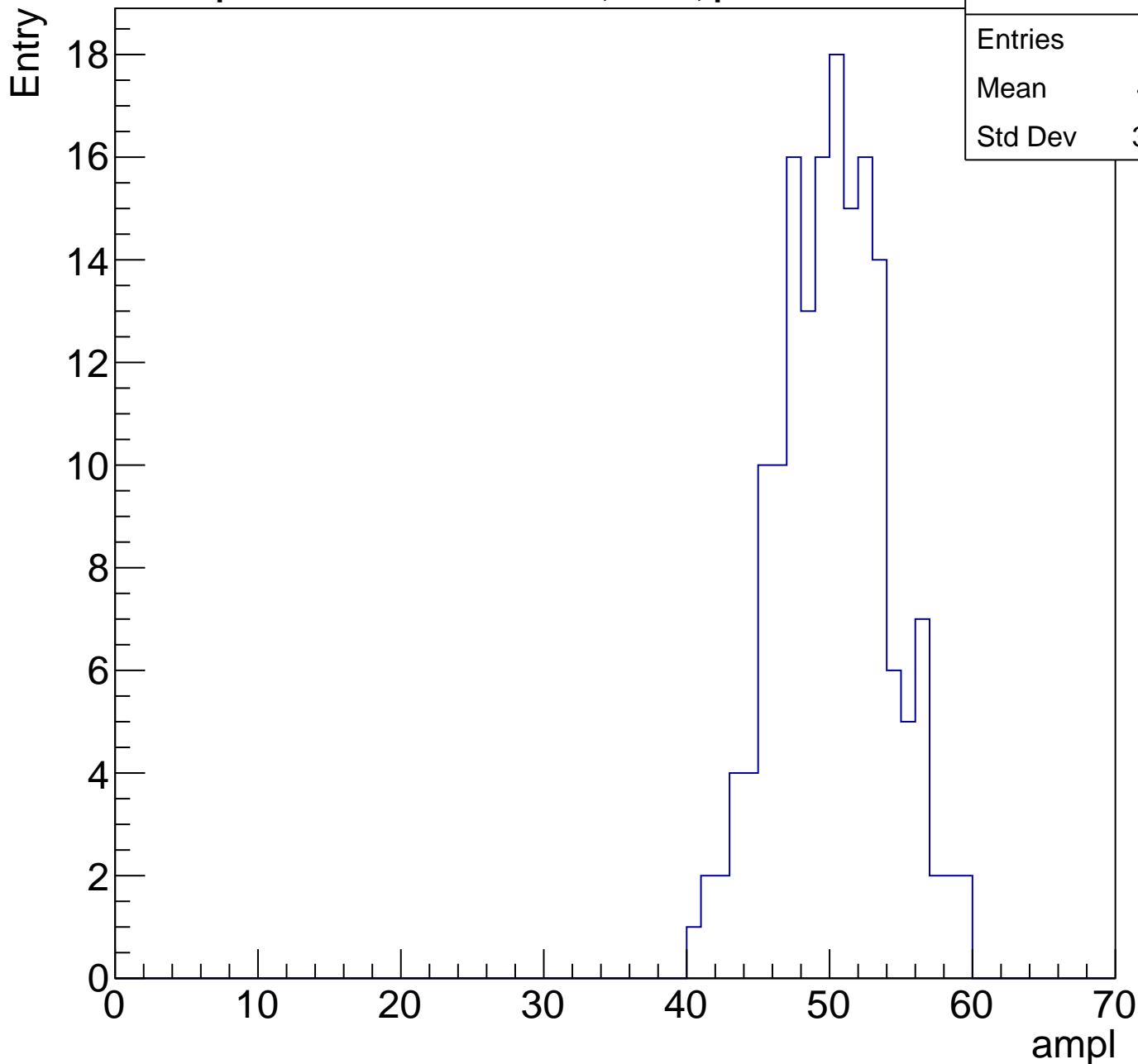
ampl



B1L001S, U24-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	49.71
Std Dev	3.817



B1L001S, U24-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

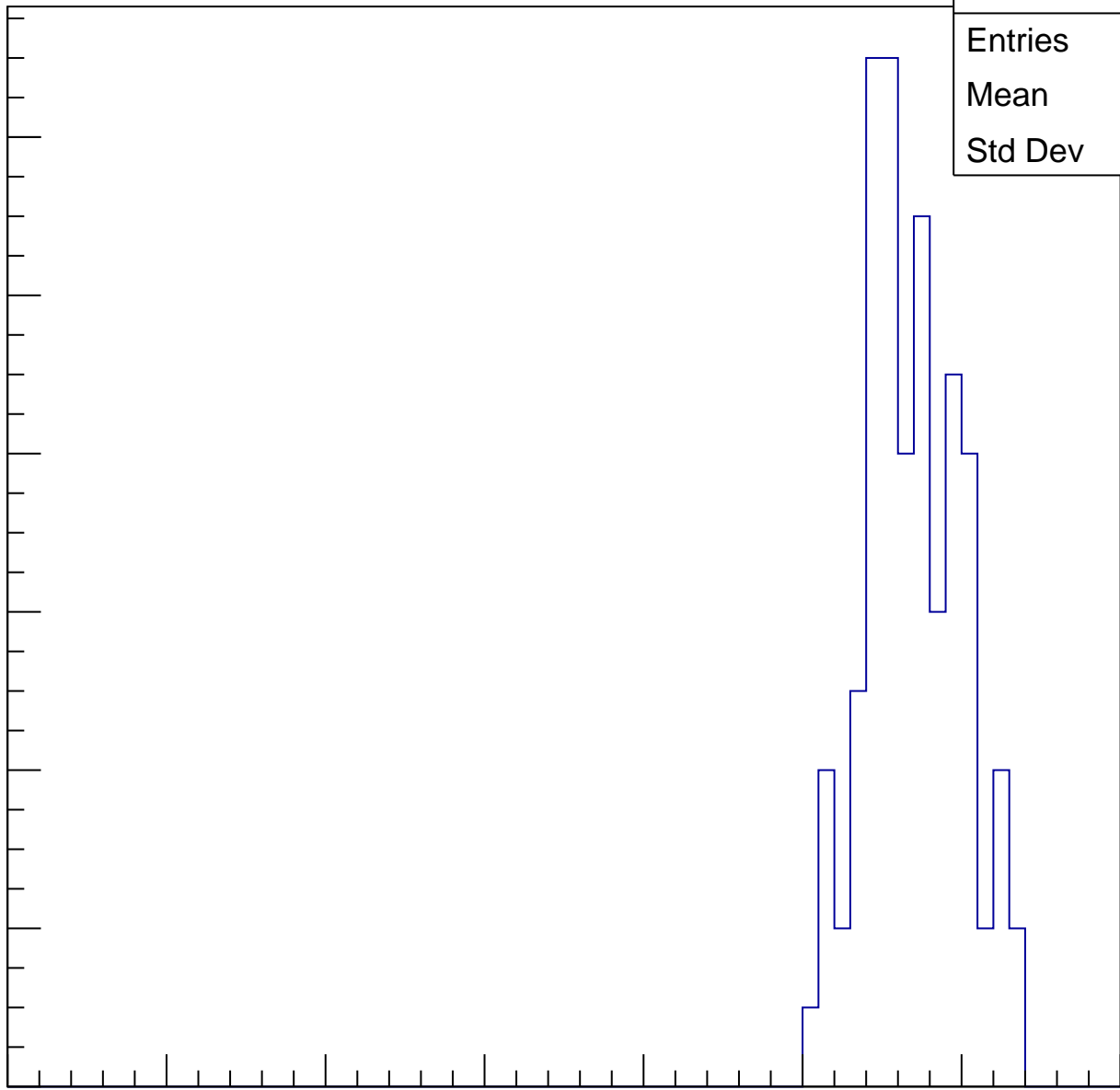
2

0

Entries	88
Mean	56.48
Std Dev	3.004

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch42, adc5

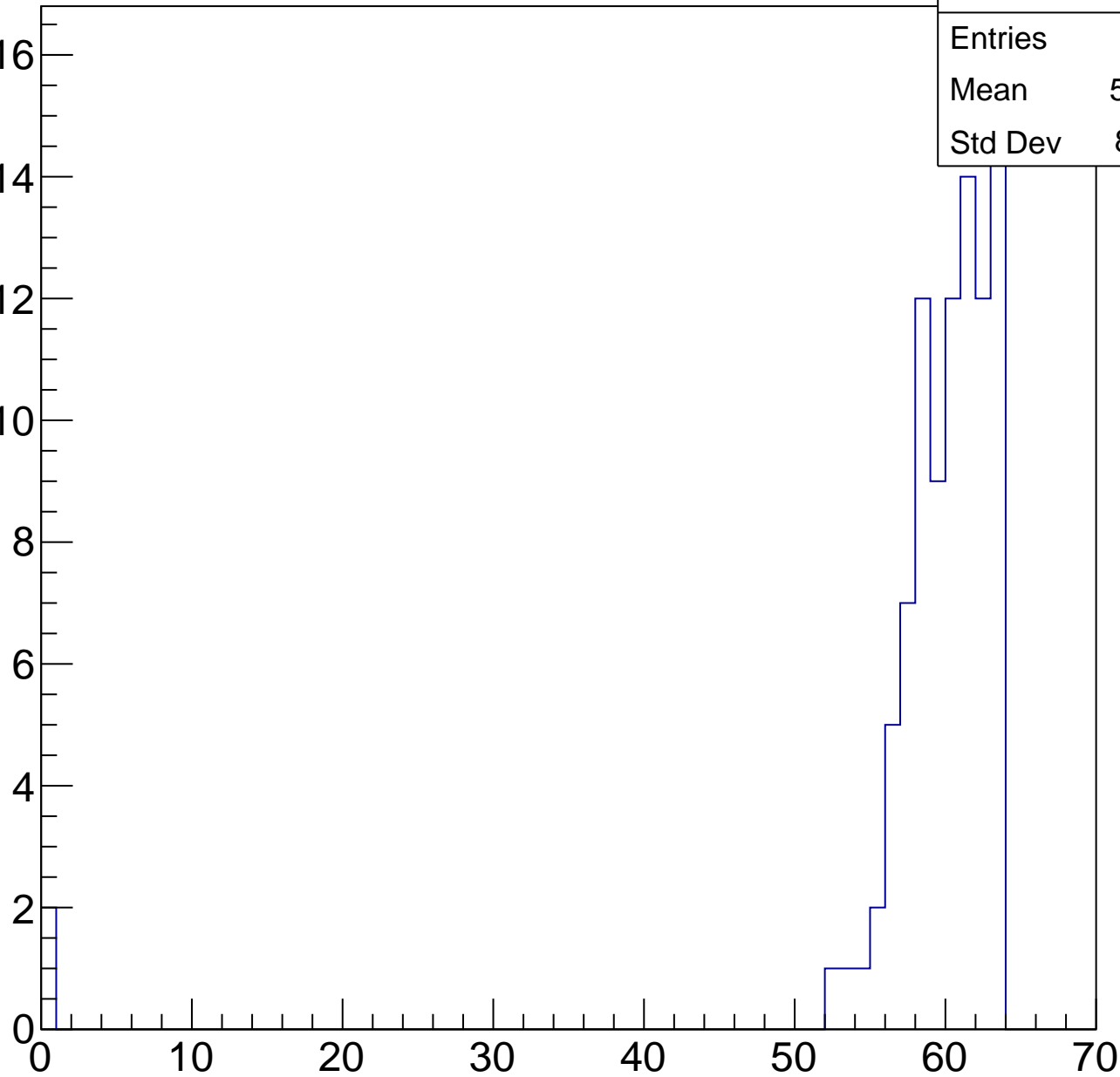
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.52
Std Dev	8.991

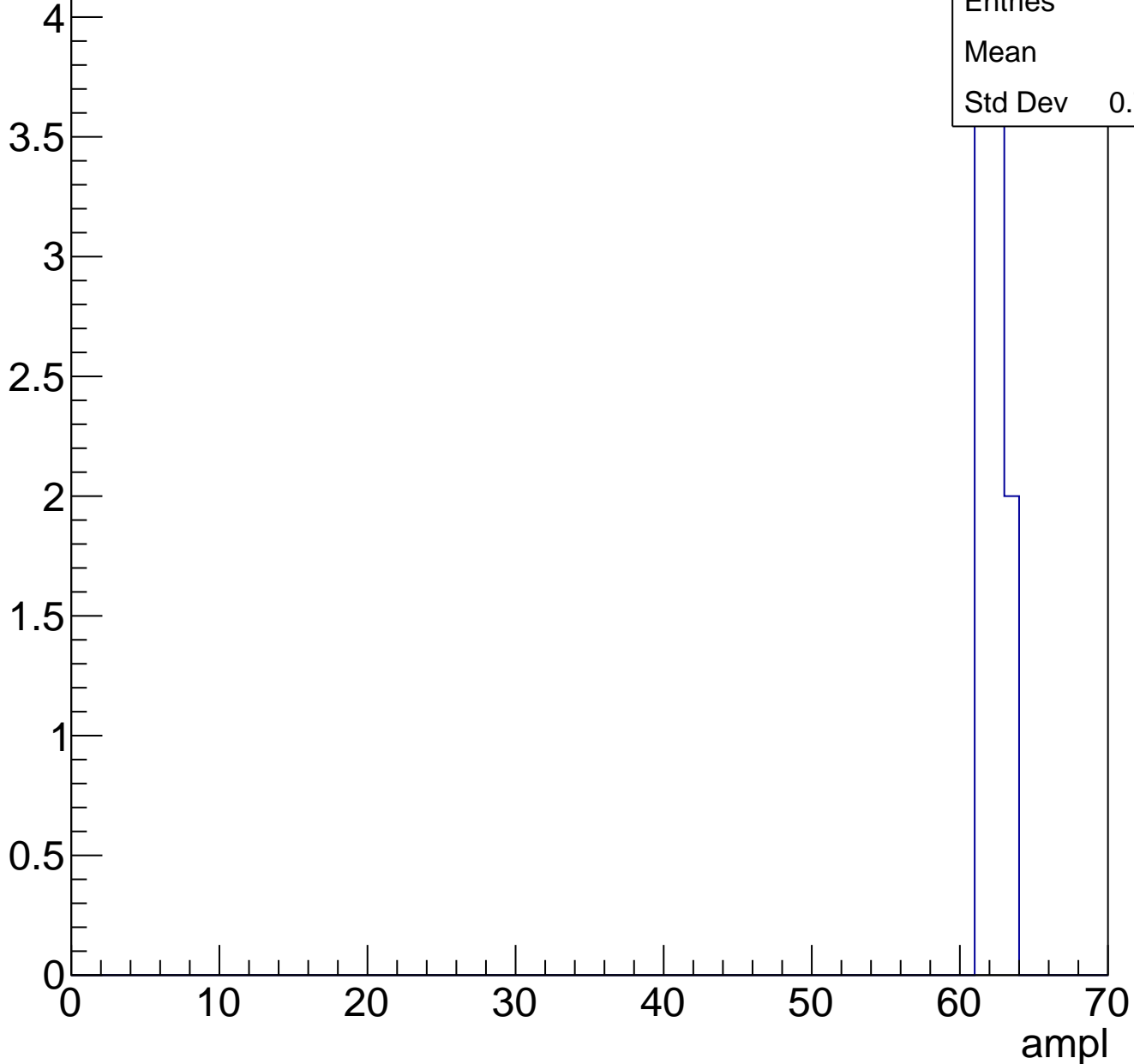
ampl



B1L001S, U24-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U24-ch43, adc0

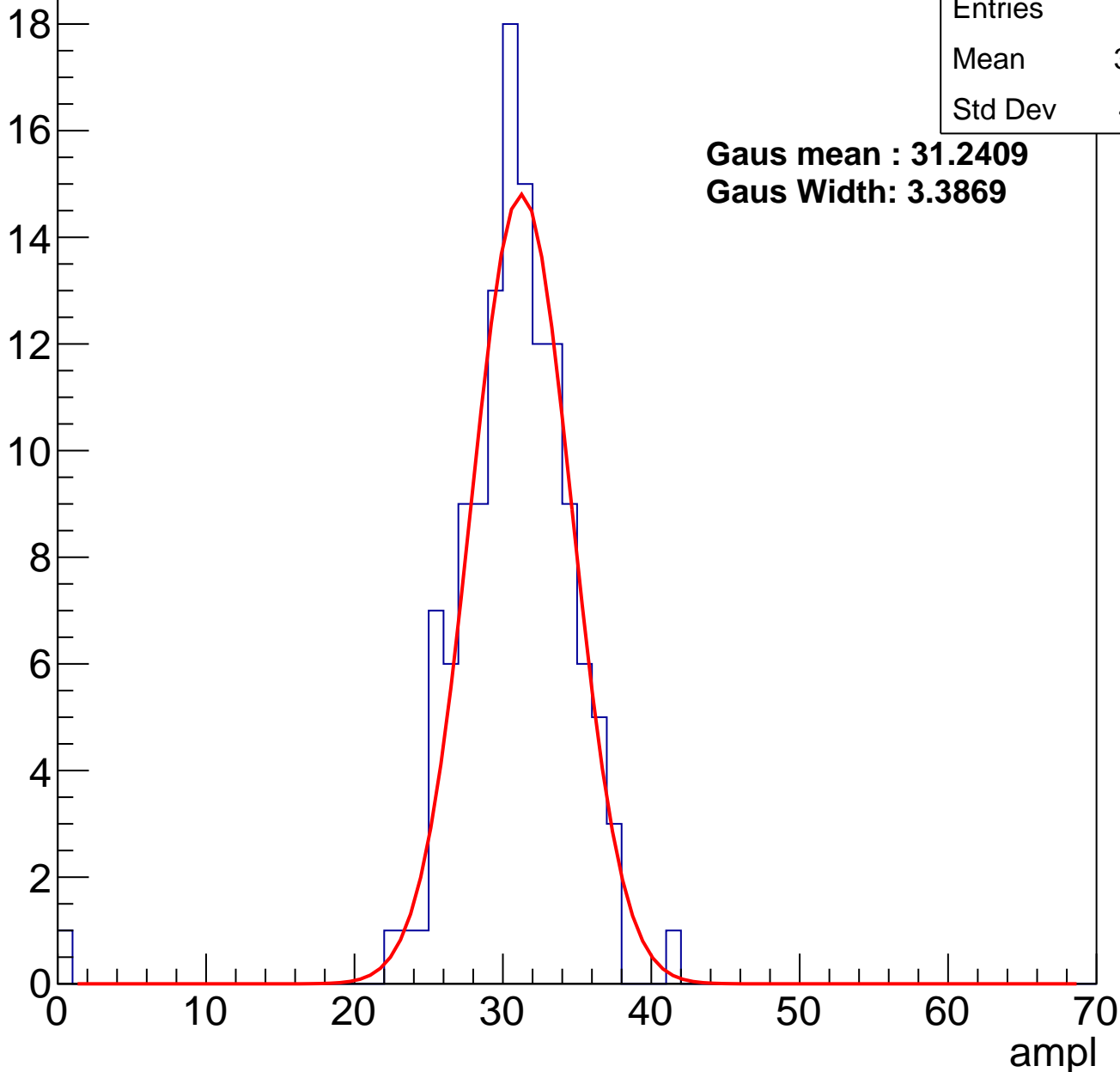
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	129
Mean	30.27
Std Dev	4.271

Gaus mean : 31.2409

Gaus Width: 3.3869

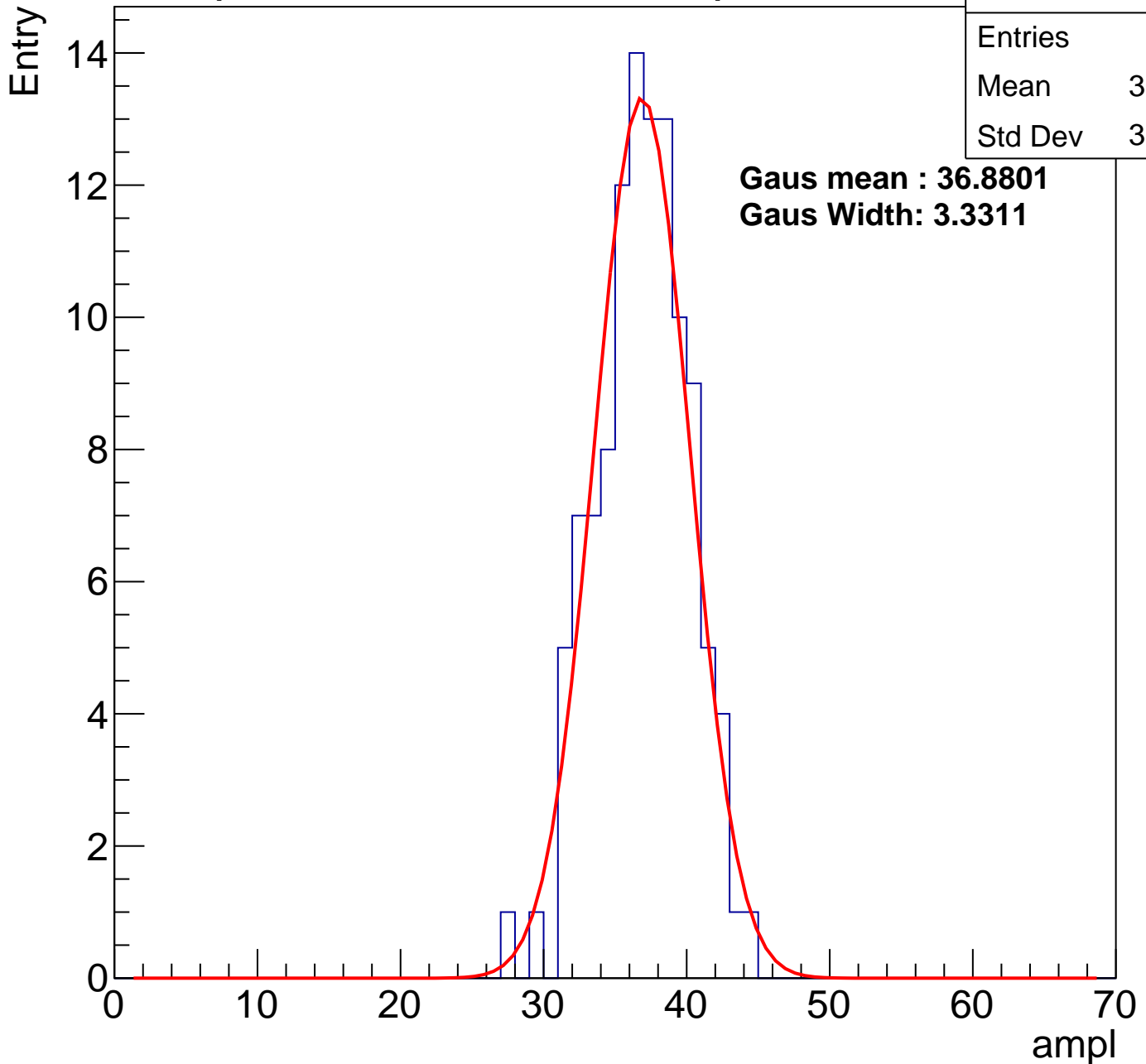


B1L001S, U24-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.46
Std Dev	3.193

Gaus mean : 36.8801
Gaus Width: 3.3311



B1L001S, U24-ch43, adc2

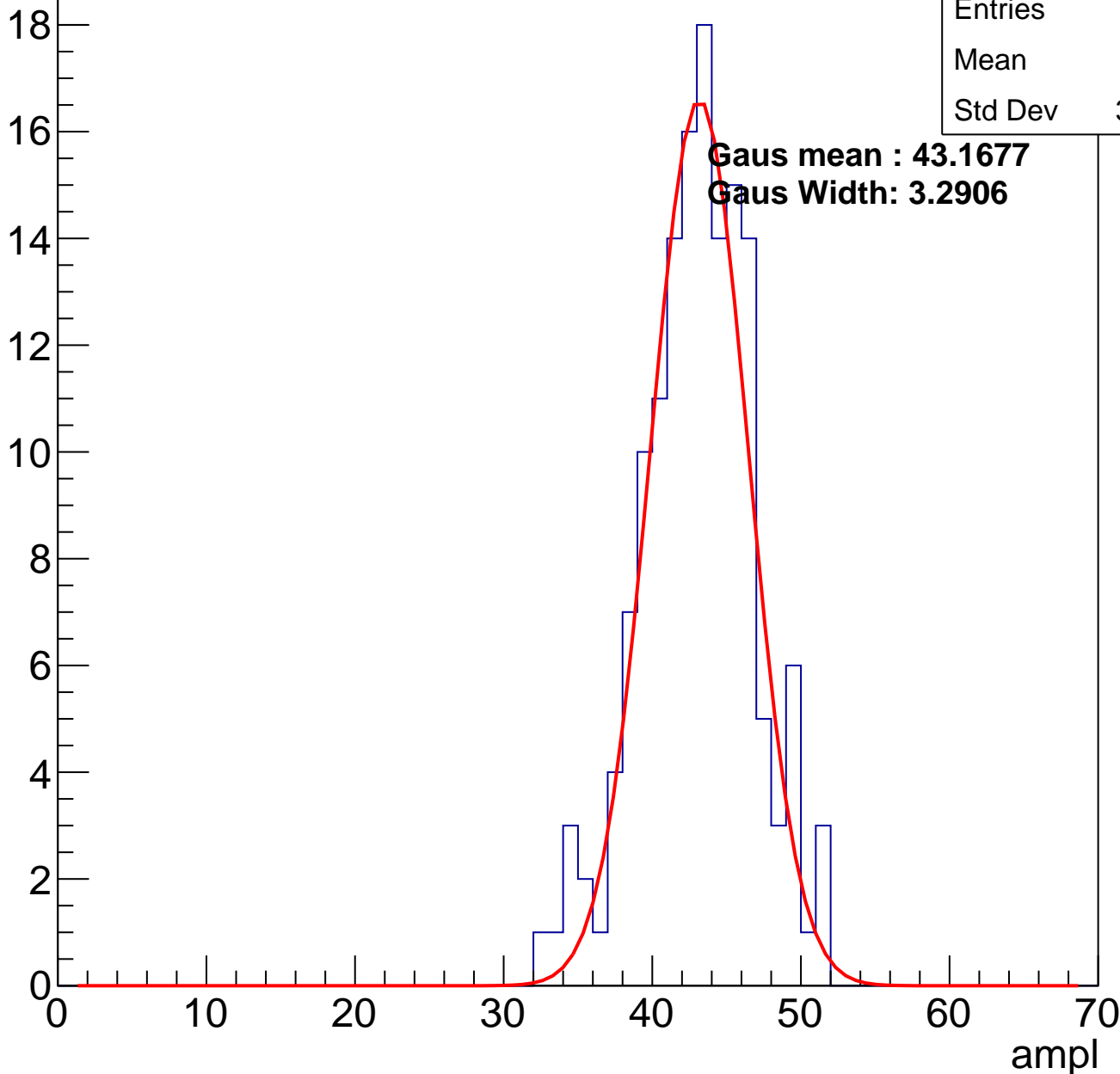
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	42.6
Std Dev	3.736

Gaus mean : 43.1677

Gaus Width: 3.2906

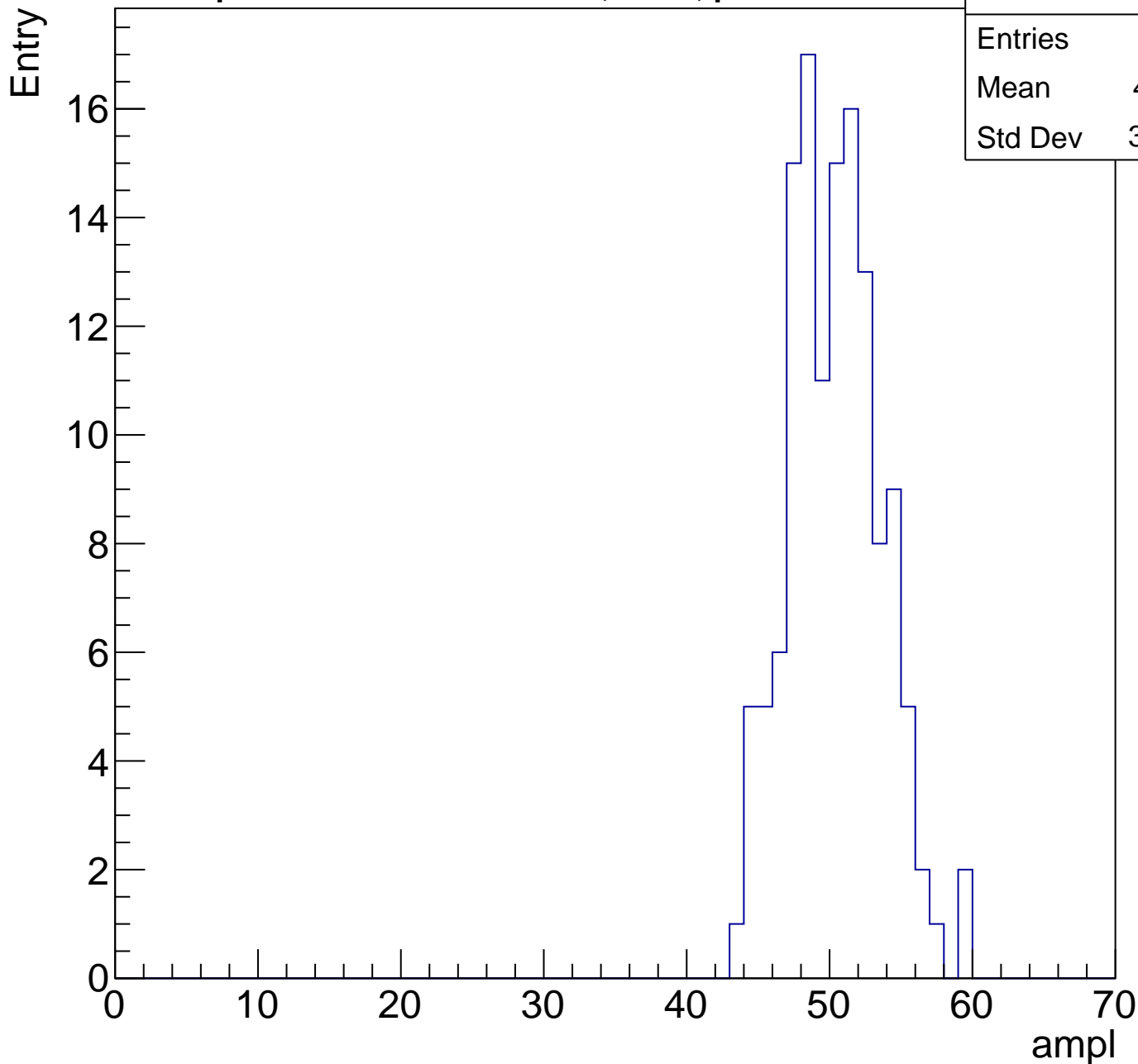
Entry



B1L001S, U24-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	49.91
Std Dev	3.216



B1L001S, U24-ch43, adc4

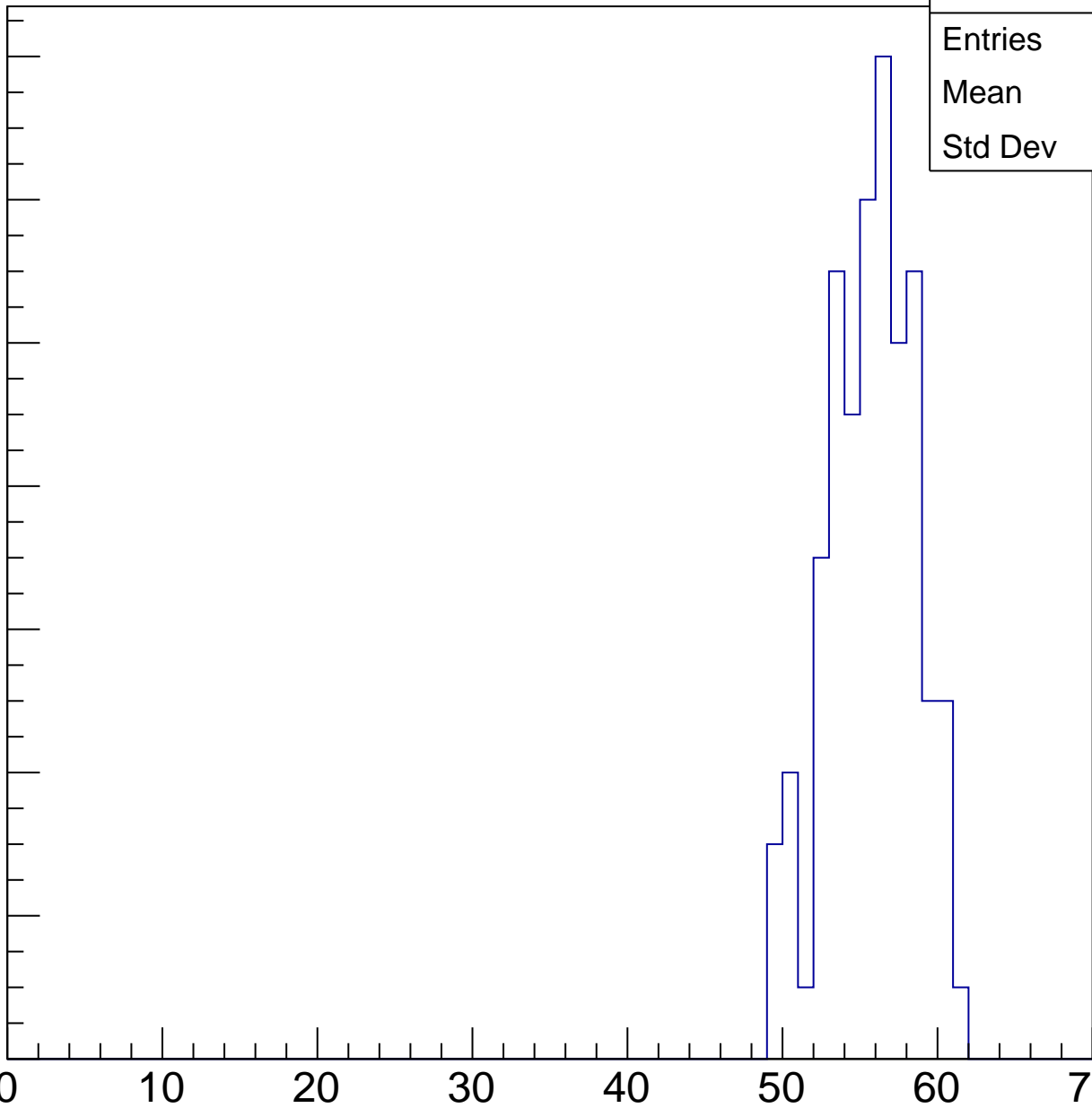
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	55.26
Std Dev	2.797

ampl



B1L001S, U24-ch43, adc5

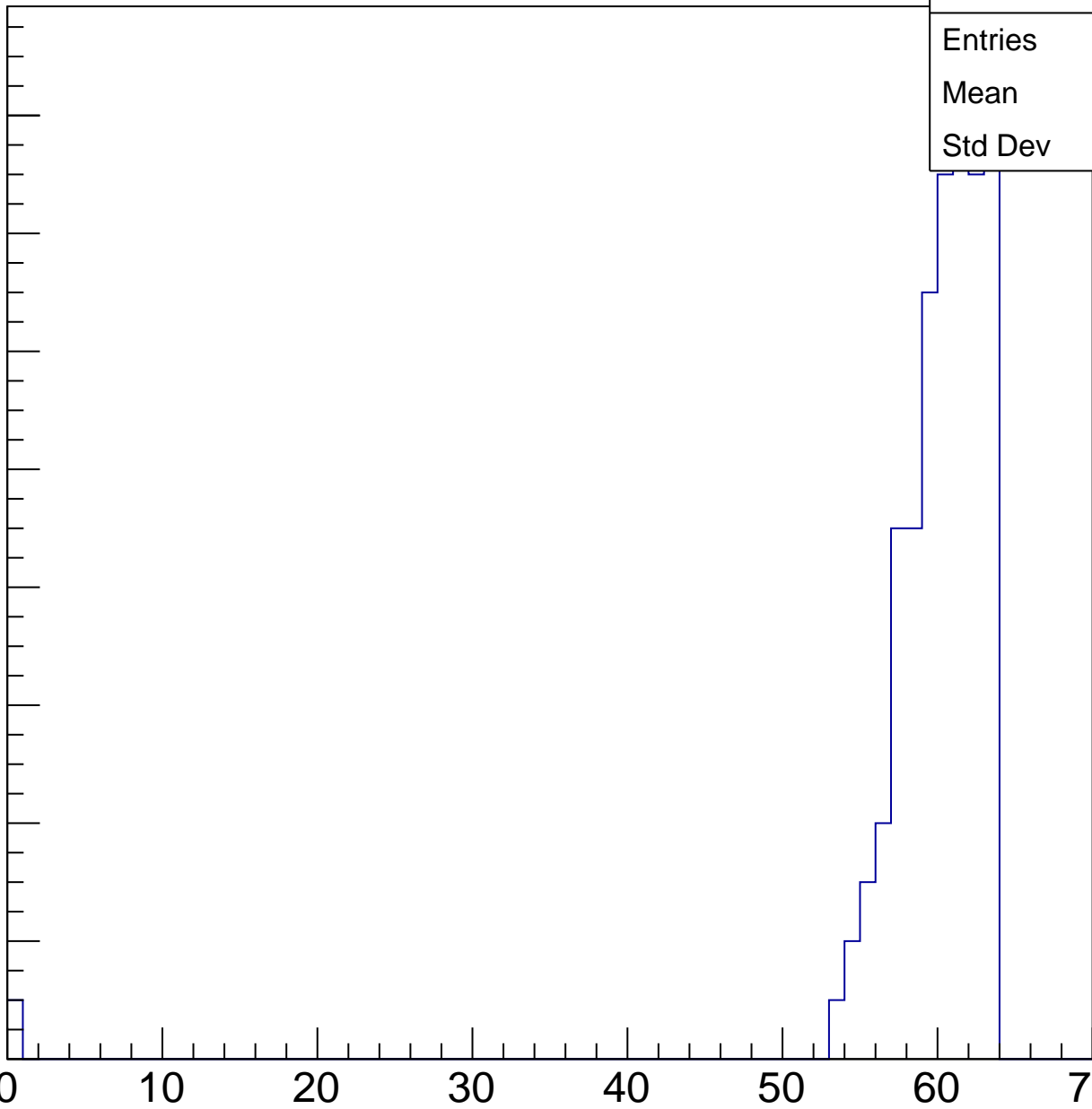
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	59.3
Std Dev	6.295

ampl

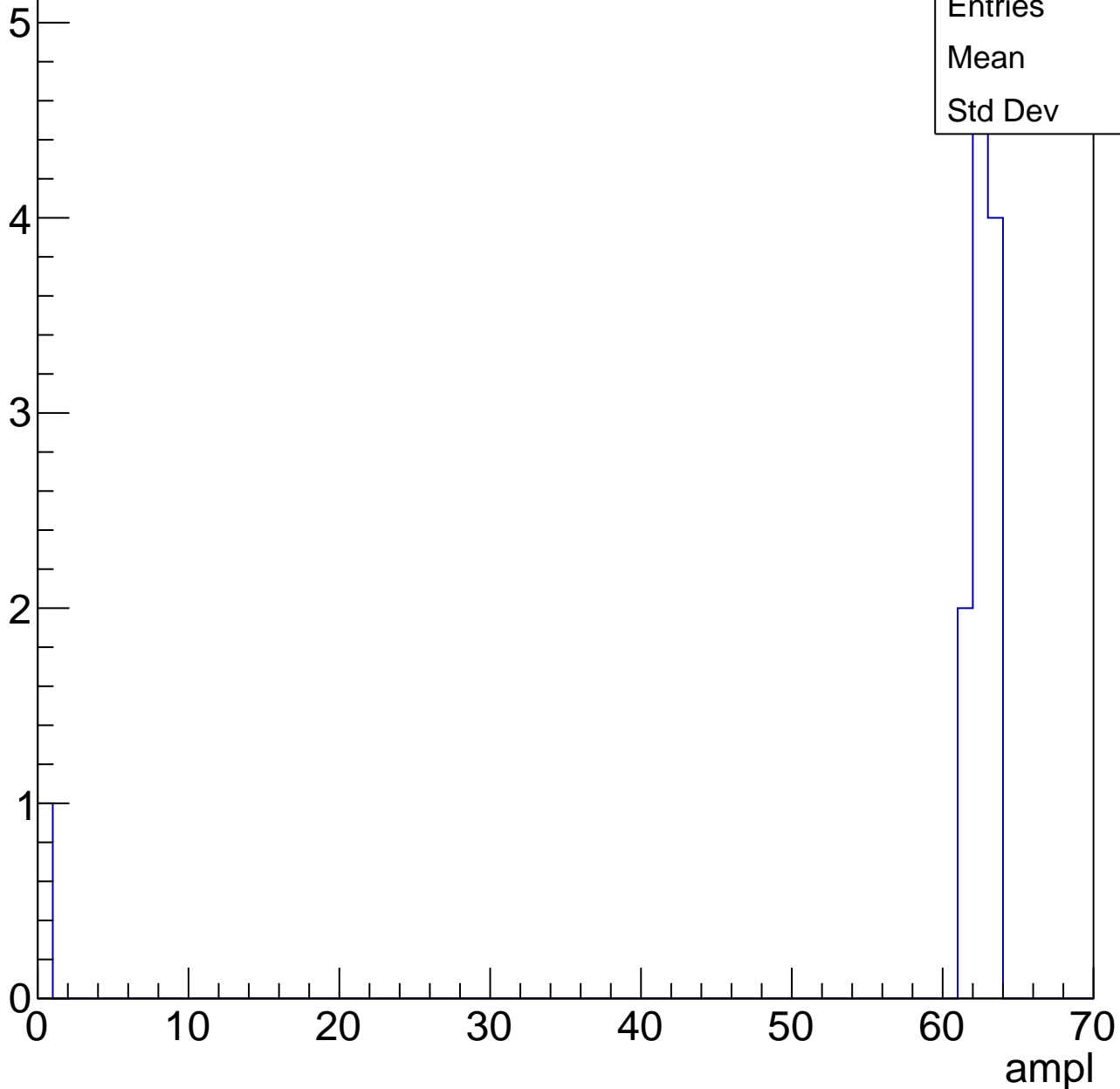


B1L001S, U24-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	57
Std Dev	17.2



B1L001S, U24-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch44, adc0

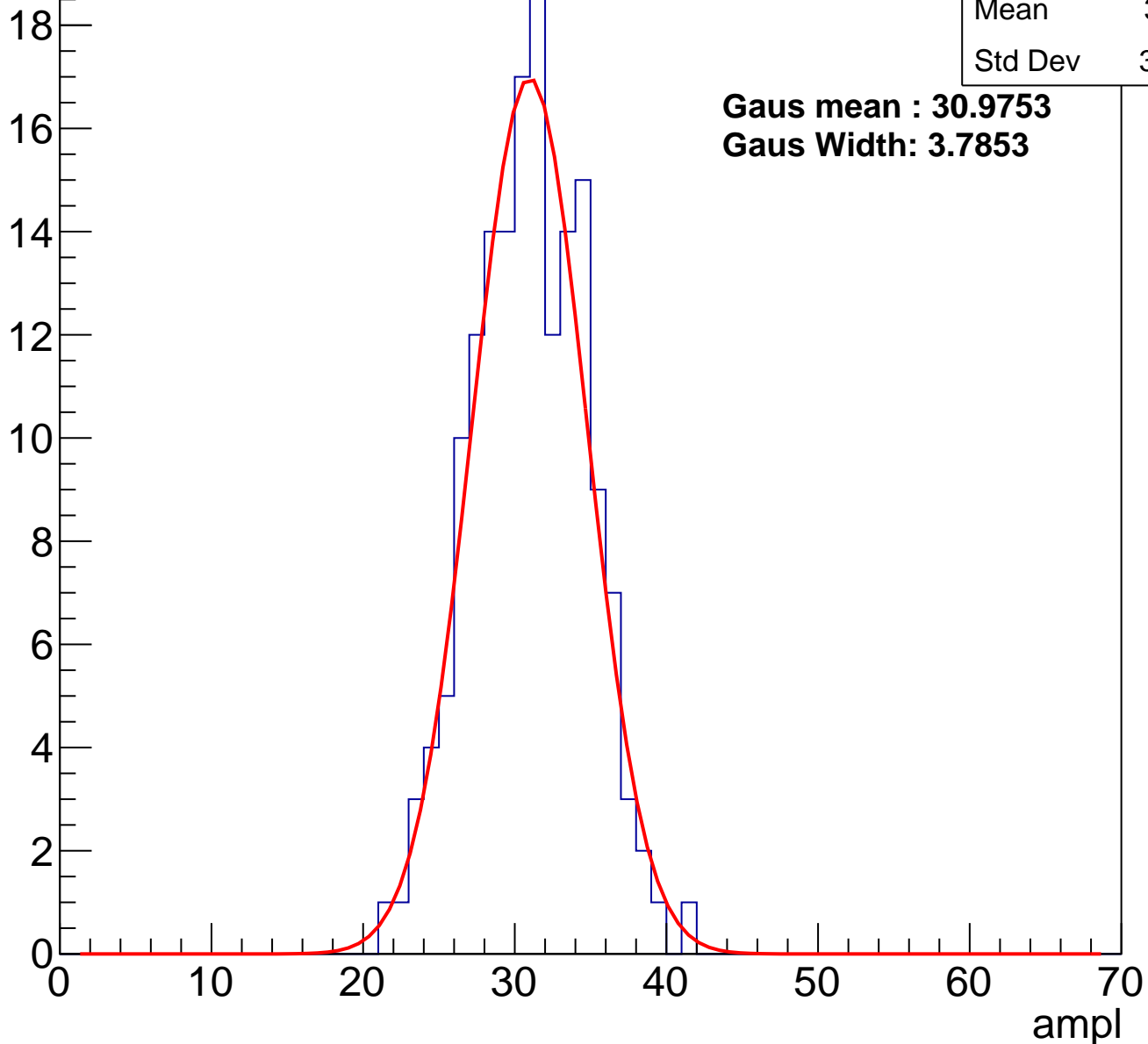
calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	30.51
Std Dev	3.698

Gaus mean : 30.9753

Gaus Width: 3.7853

Entry



B1L001S, U24-ch44, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	37.93
Std Dev	3.558

Gaus mean : 38.4856

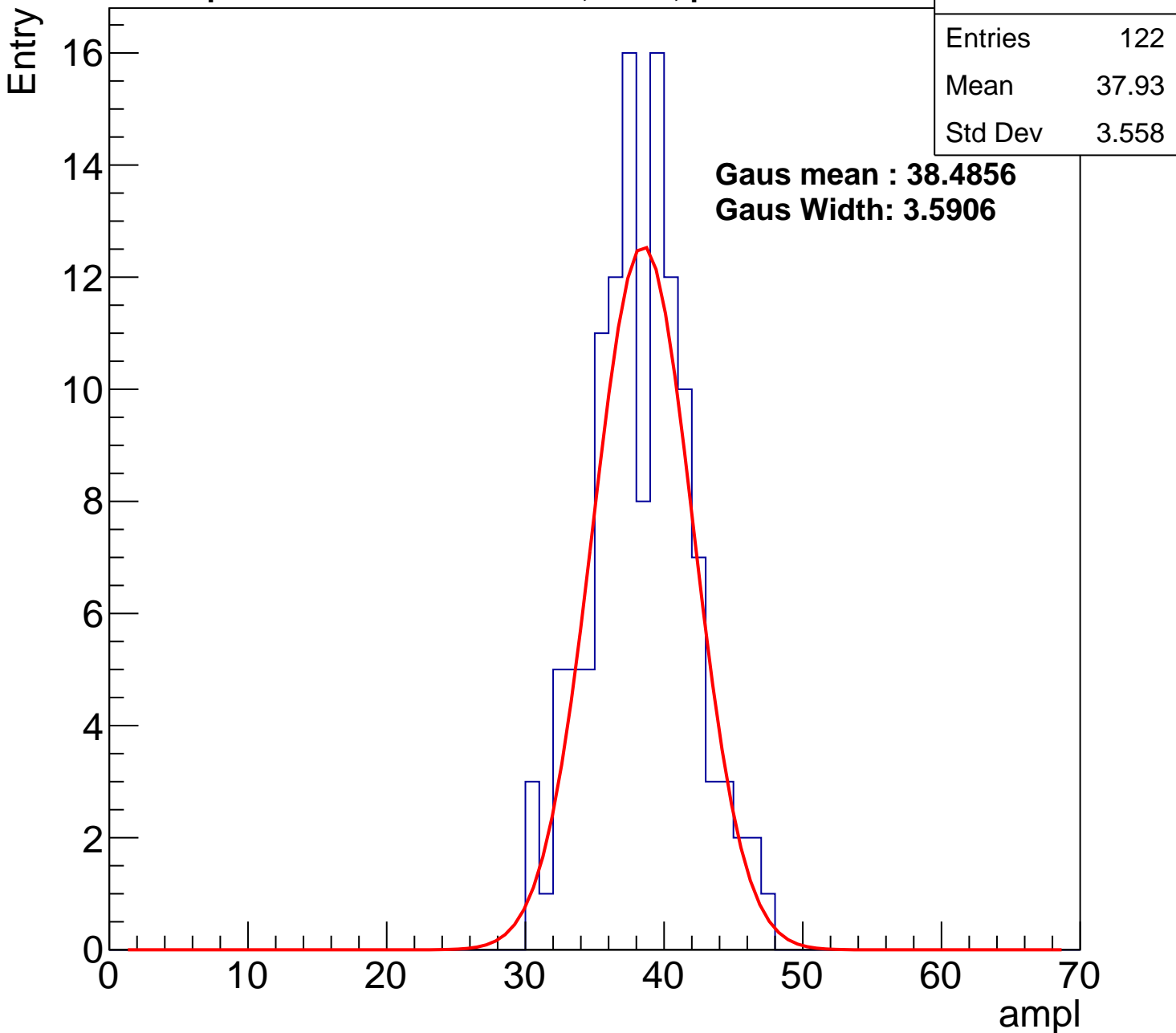
Gaus Width: 3.5906

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

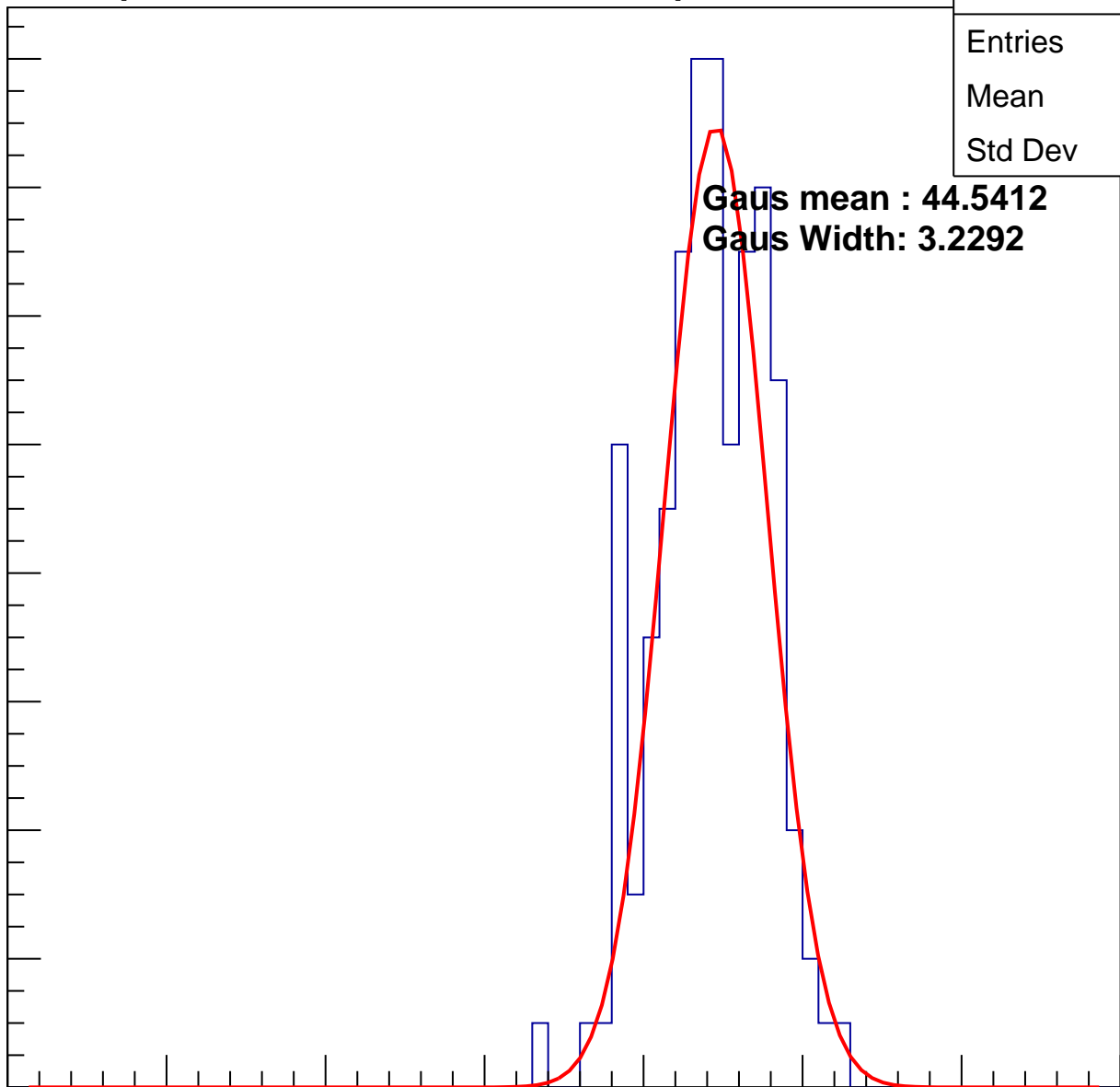
Entries	133
Mean	43.78
Std Dev	3.439

Gaus mean : 44.5412

Gaus Width: 3.2292

0 10 20 30 40 50 60 70

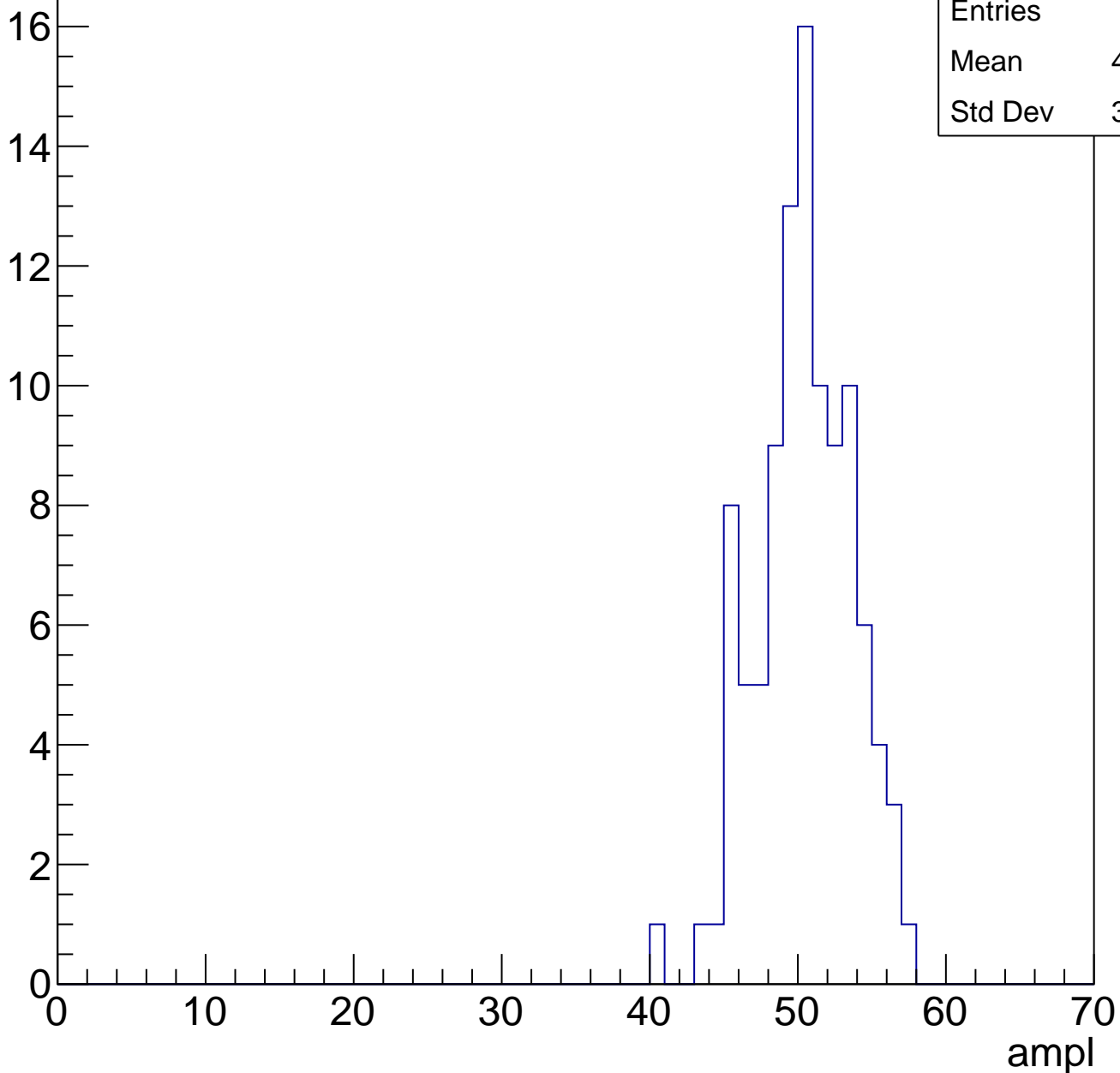
ampl



B1L001S, U24-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

145

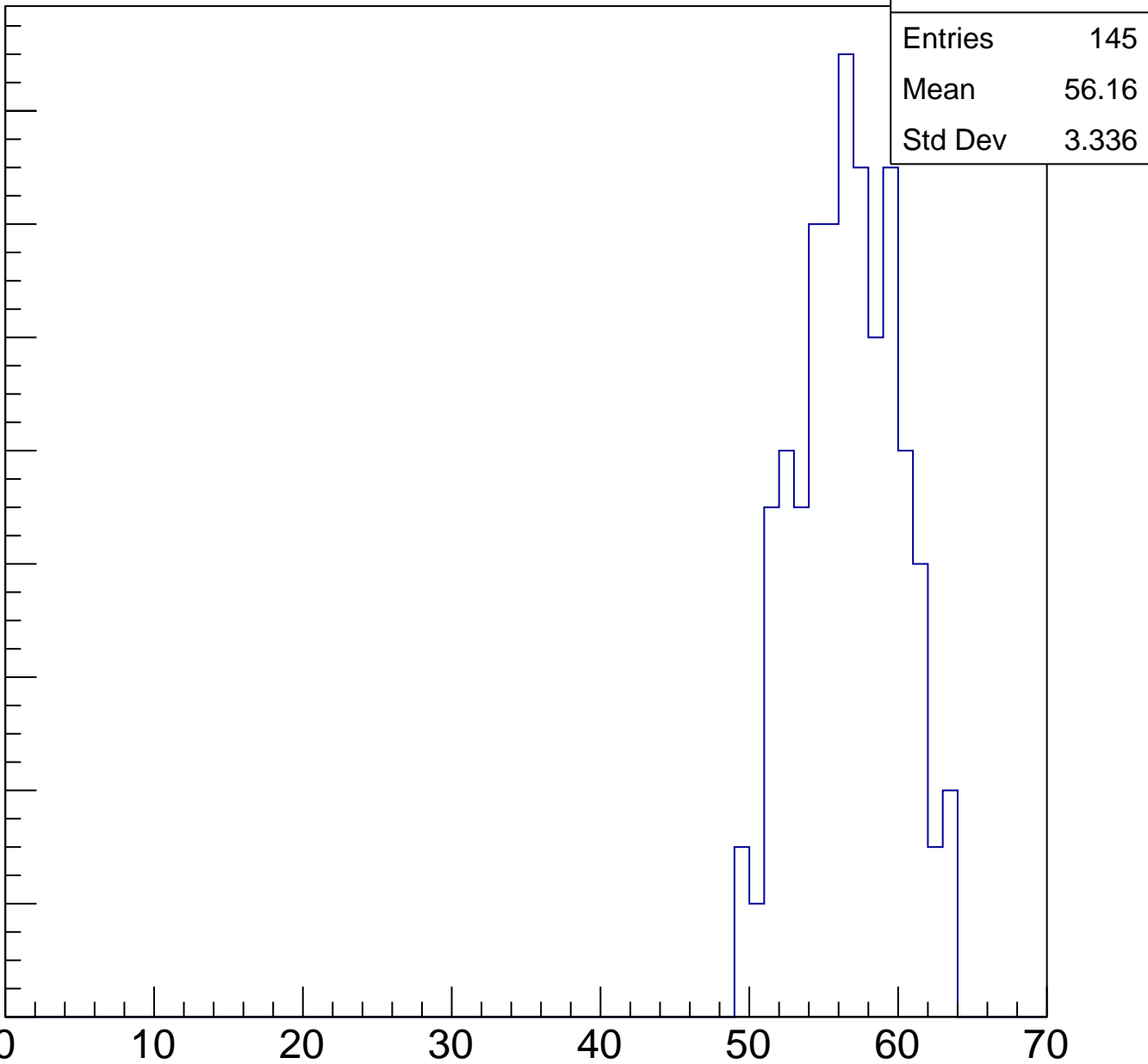
Mean

56.16

Std Dev

3.336

ampl



B1L001S, U24-ch44, adc5

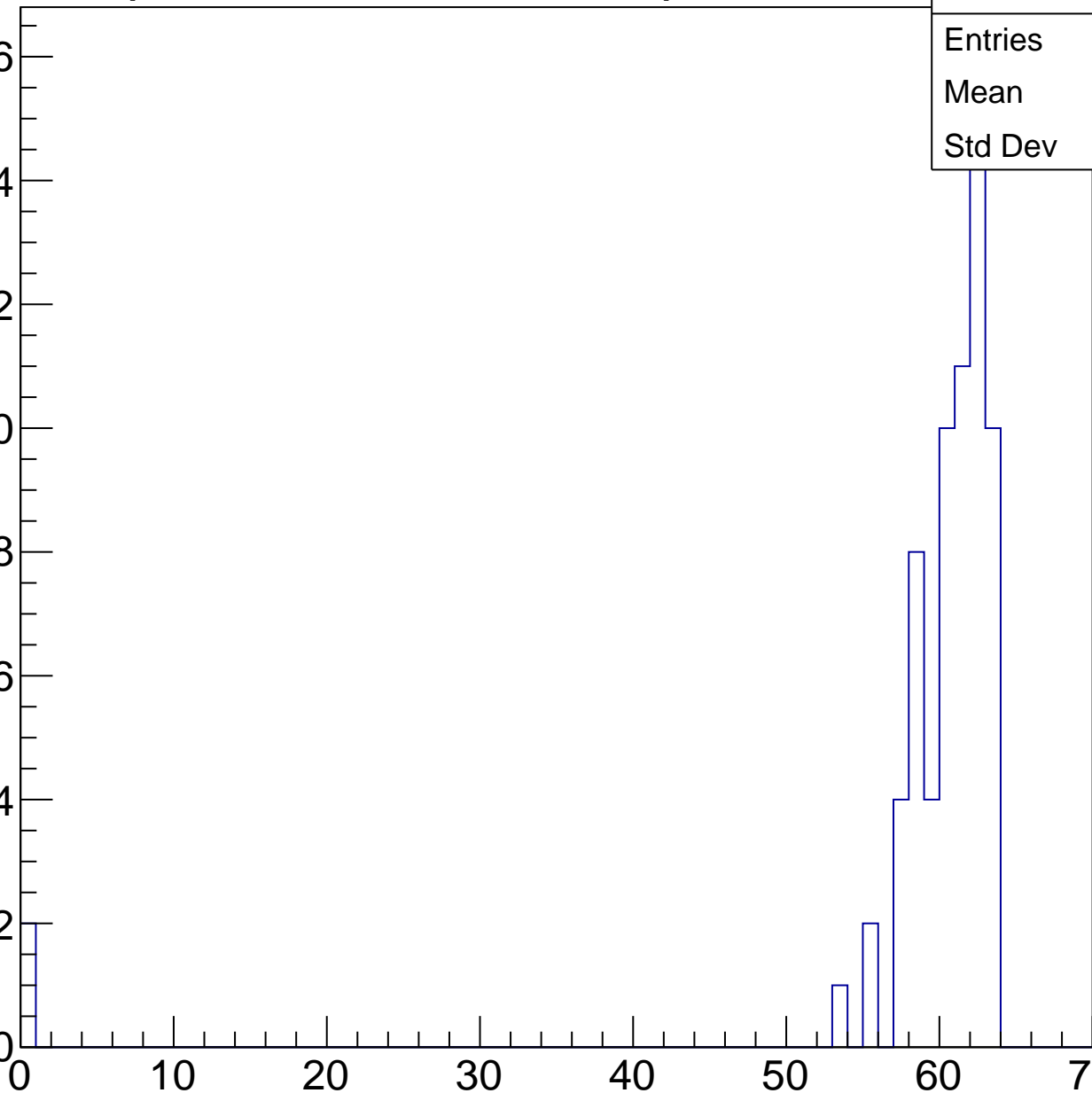
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	58.59
Std Dev	10.43

ampl

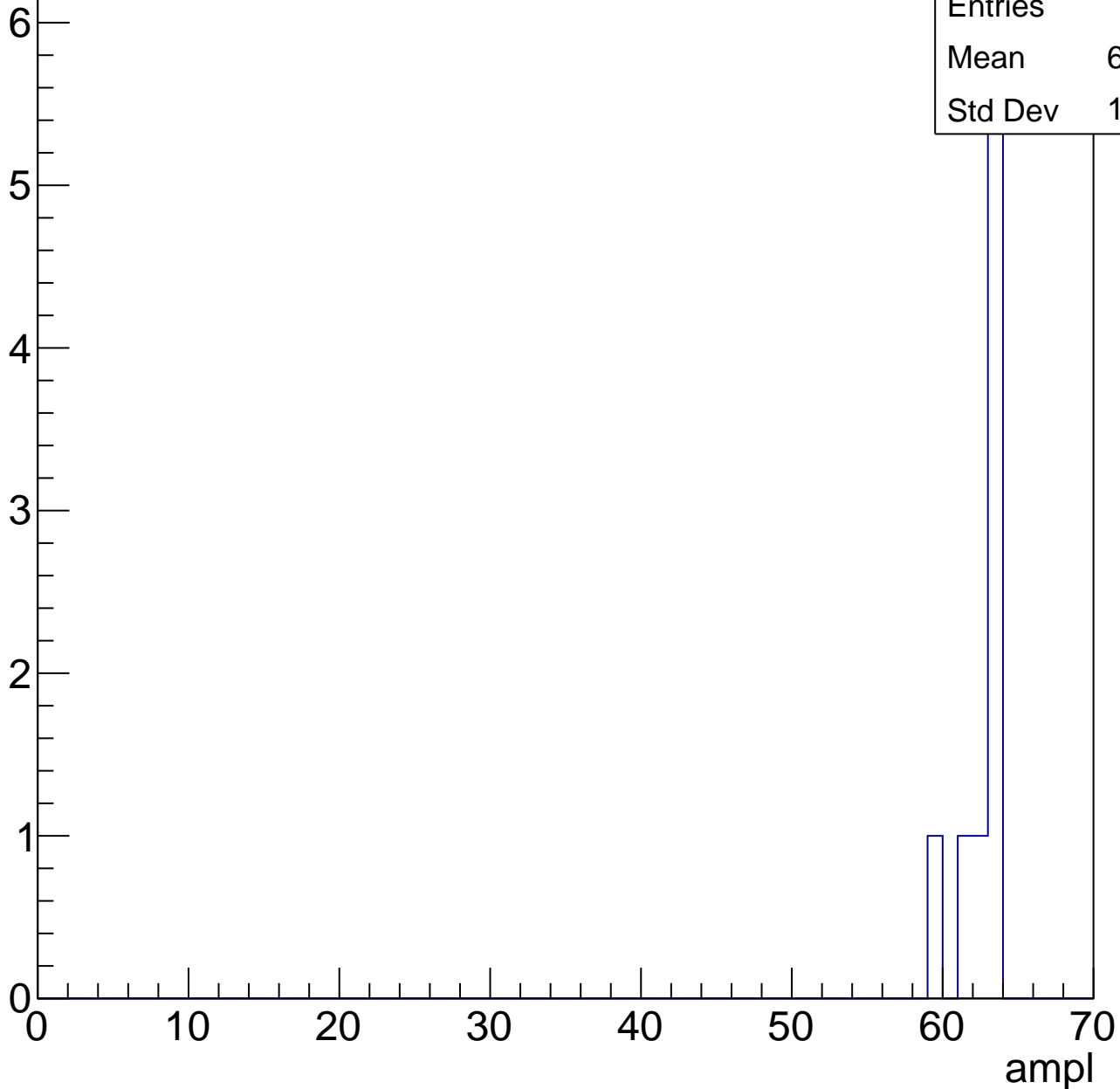


B1L001S, U24-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	62.22
Std Dev	1.315



B1L001S, U24-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



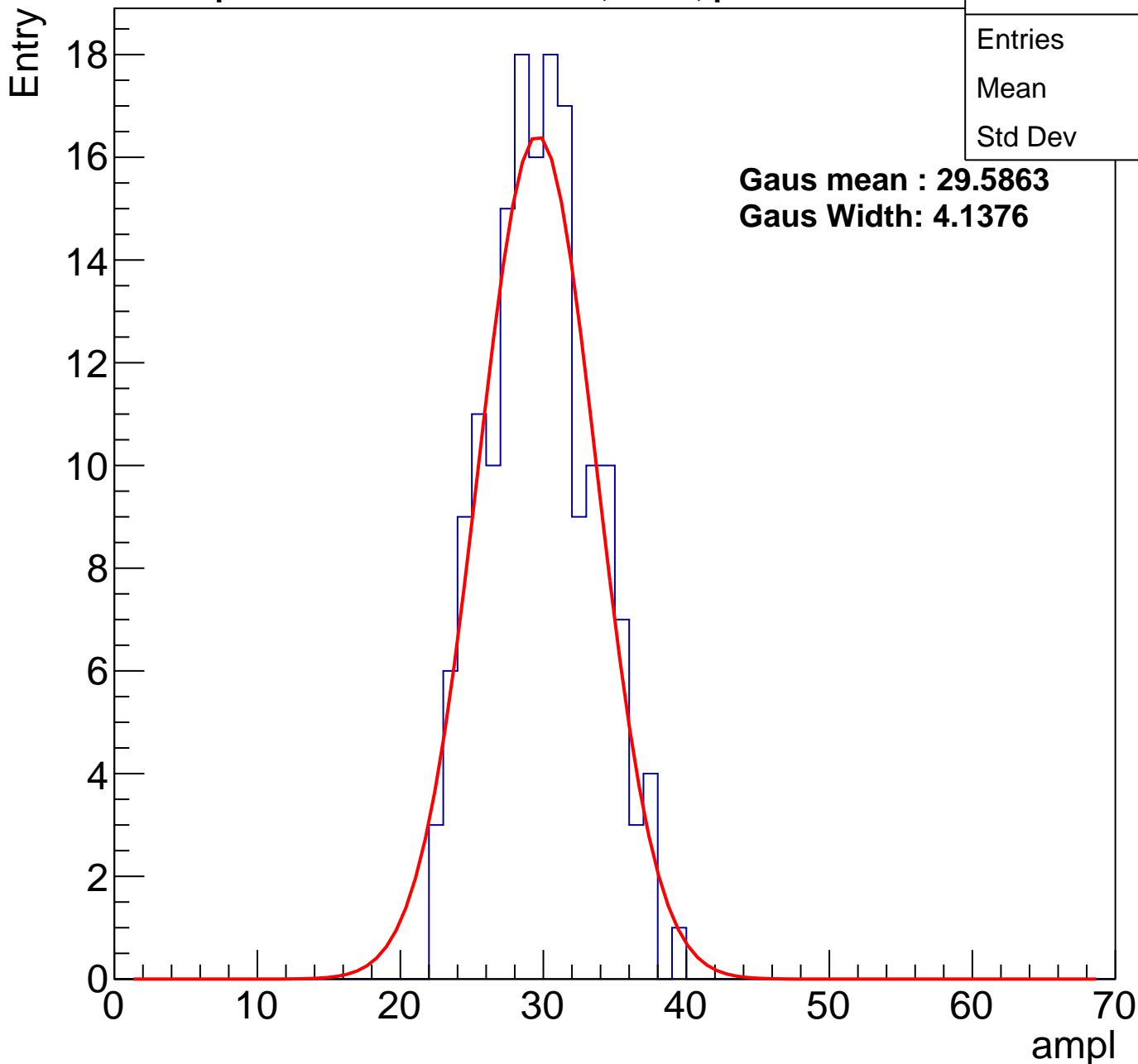
B1L001S, U24-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	29.3
Std Dev	3.67

Gaus mean : 29.5863

Gaus Width: 4.1376



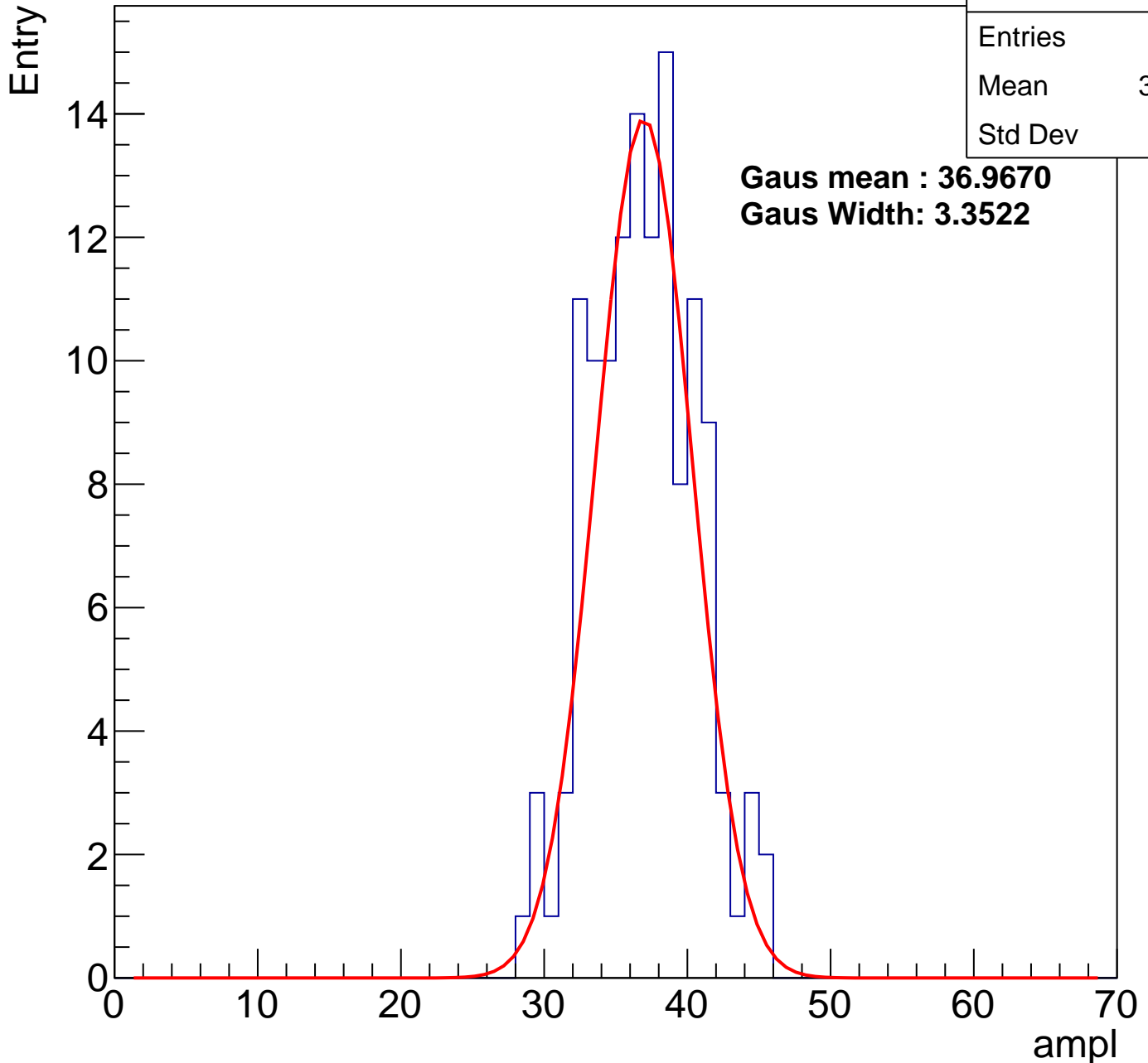
B1L001S, U24-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.51
Std Dev	3.59

Gaus mean : 36.9670

Gaus Width: 3.3522



B1L001S, U24-ch45, adc2

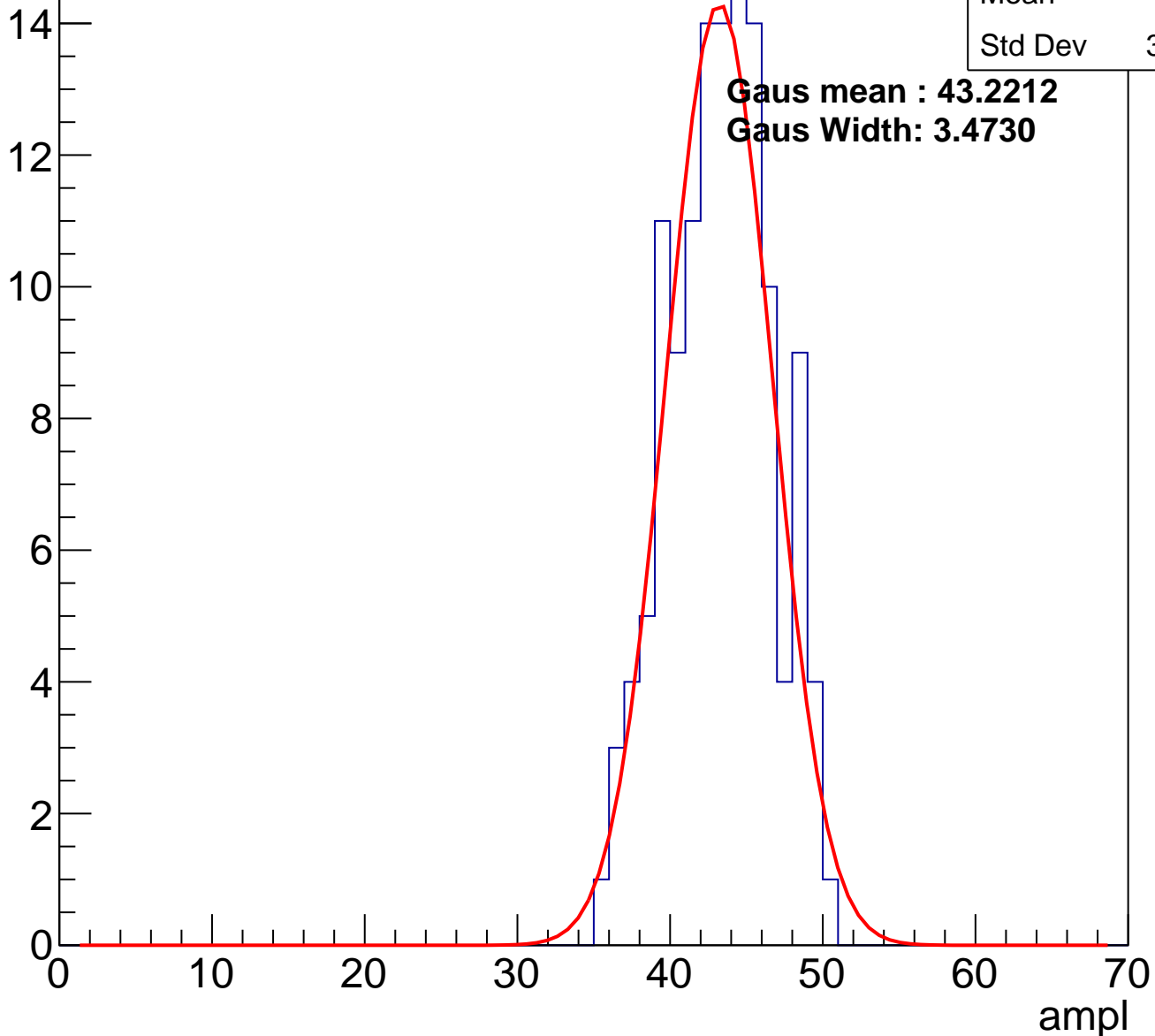
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.84
Std Dev	3.335

Gaus mean : 43.2212

Gaus Width: 3.4730

Entry



B1L001S, U24-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

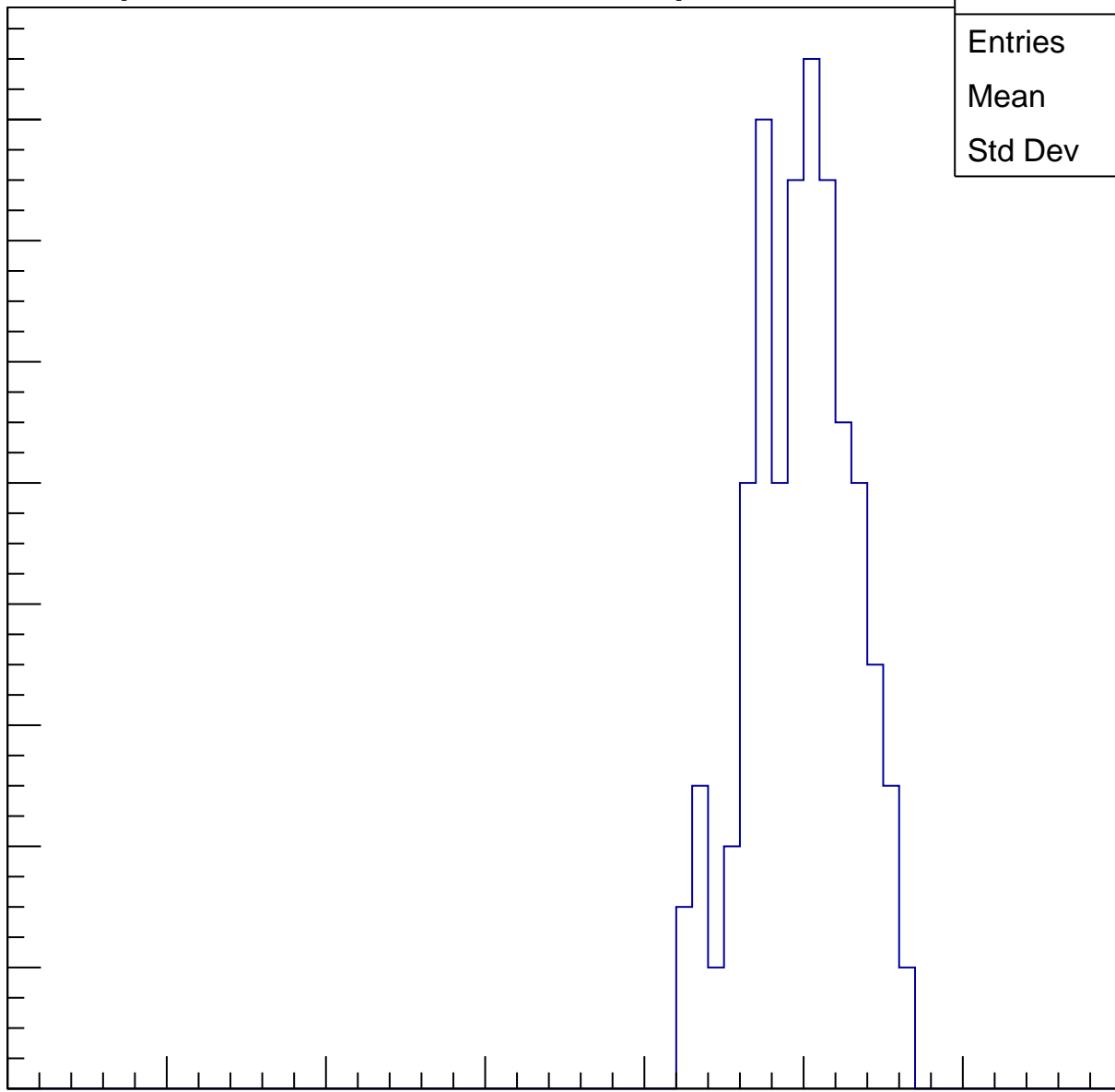
Entries	132
Mean	49.38
Std Dev	3.228

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

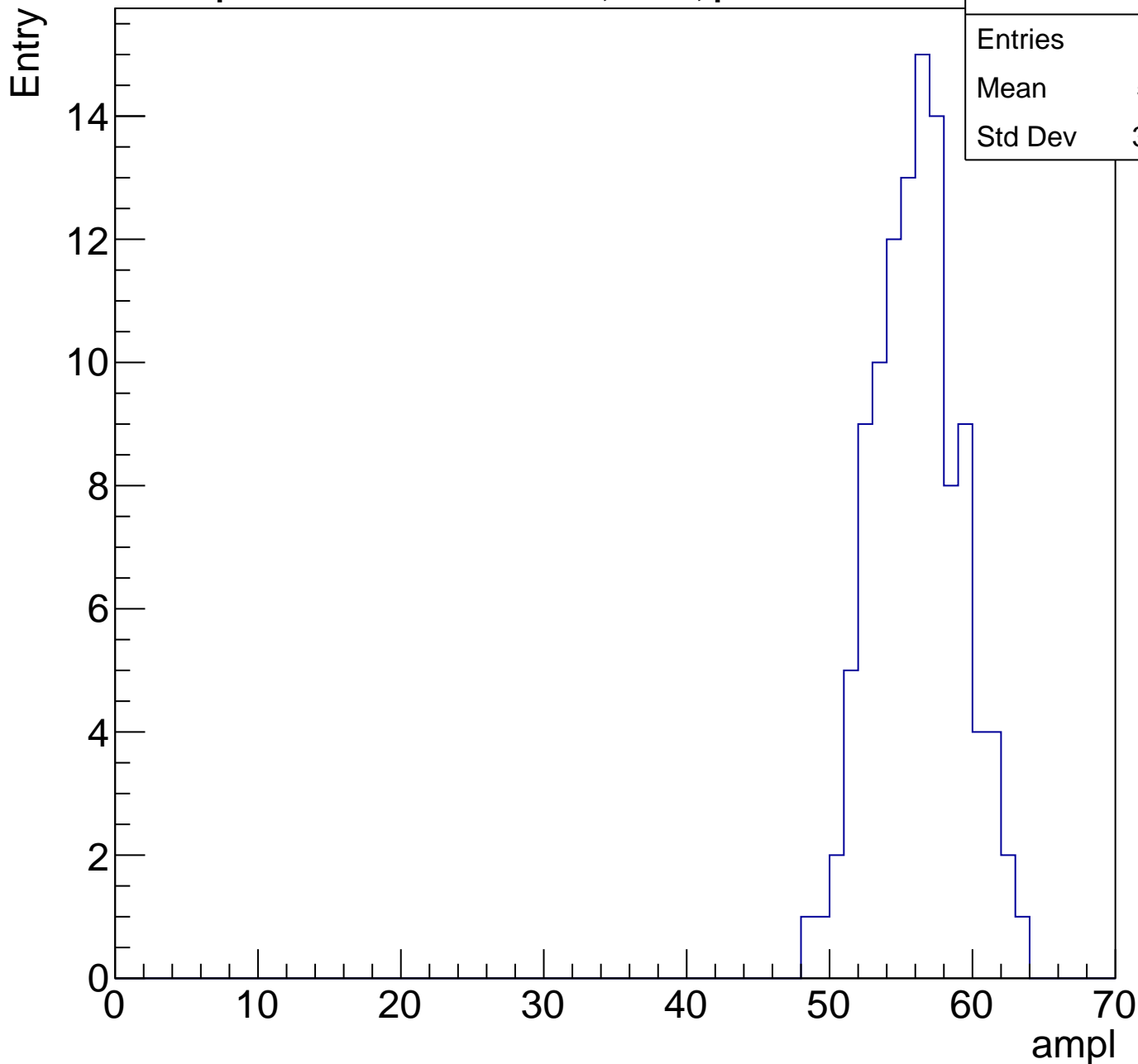
ampl



B1L001S, U24-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	55.61
Std Dev	3.015



B1L001S, U24-ch45, adc5

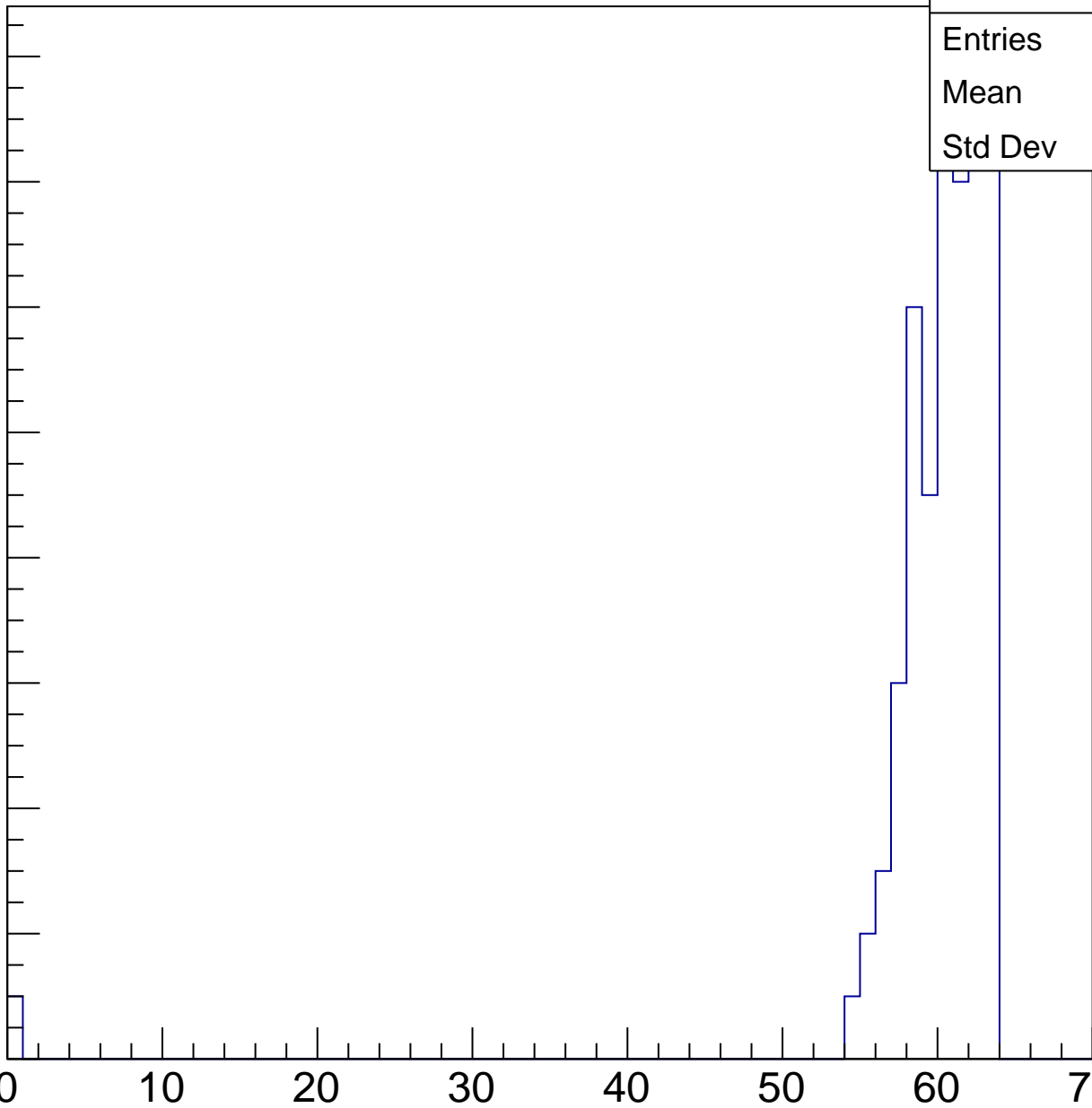
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.51
Std Dev	6.521

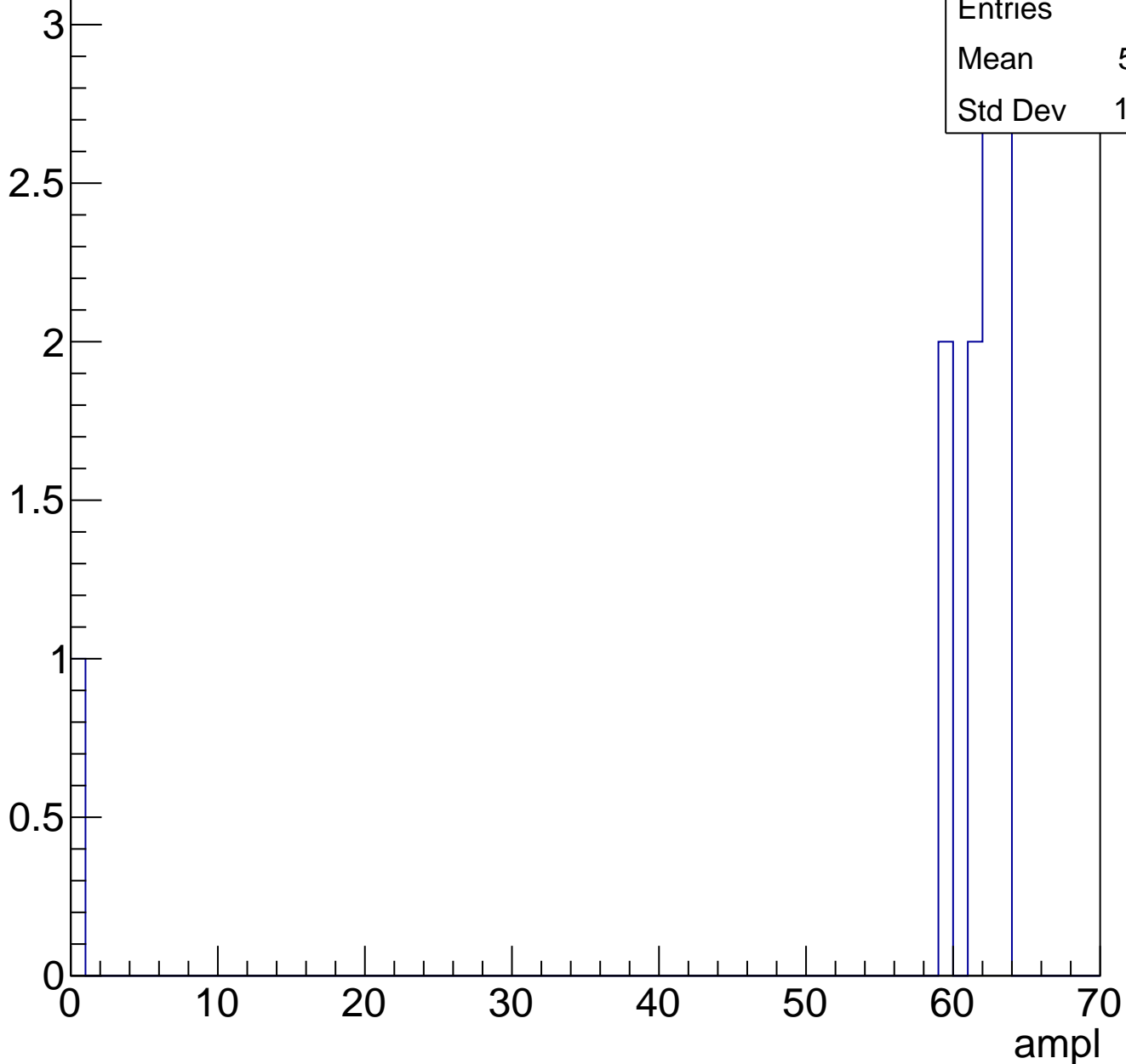
ampl



B1L001S, U24-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch46, adc0

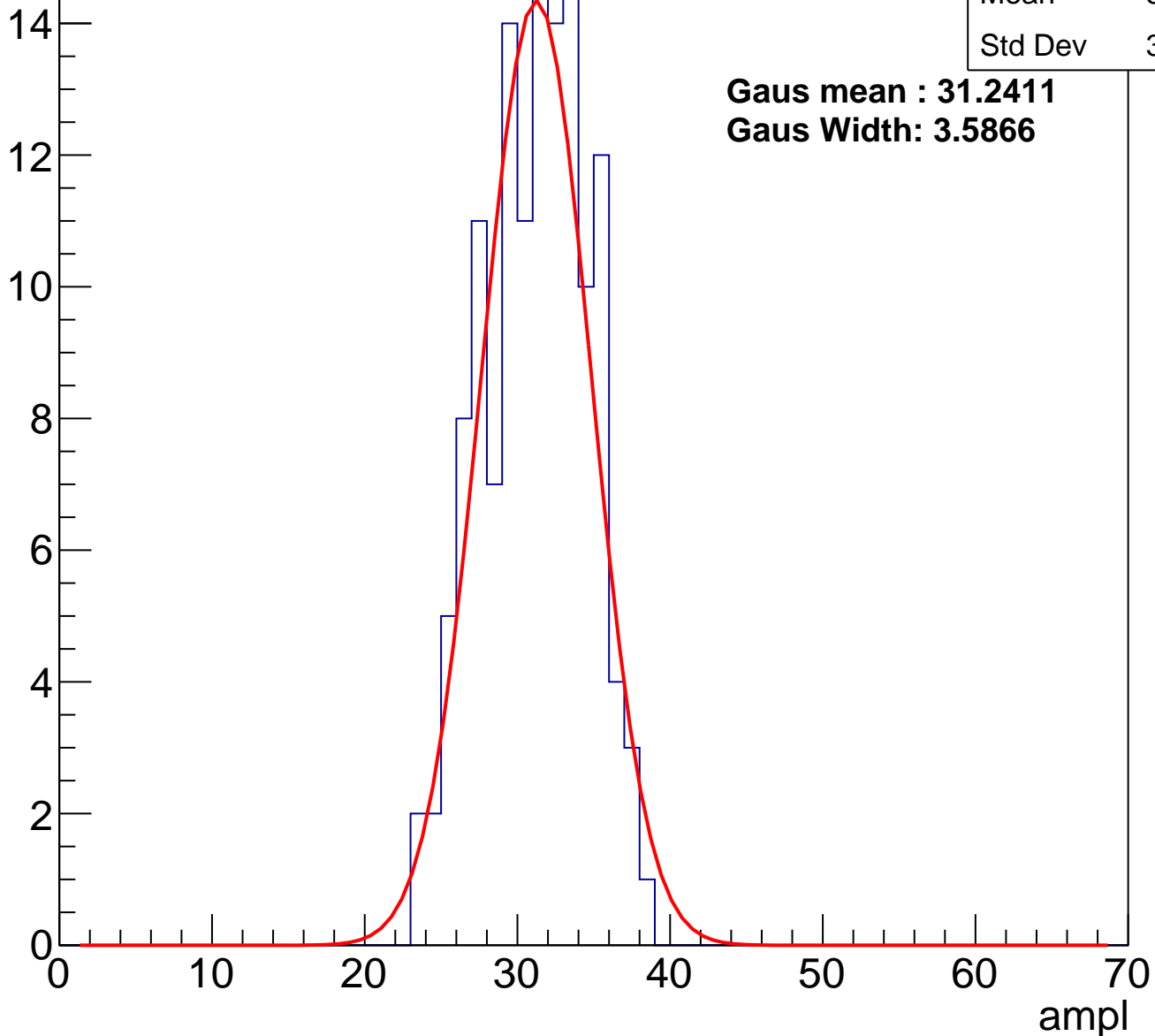
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.72
Std Dev	3.362

Gaus mean : 31.2411

Gaus Width: 3.5866

Entry



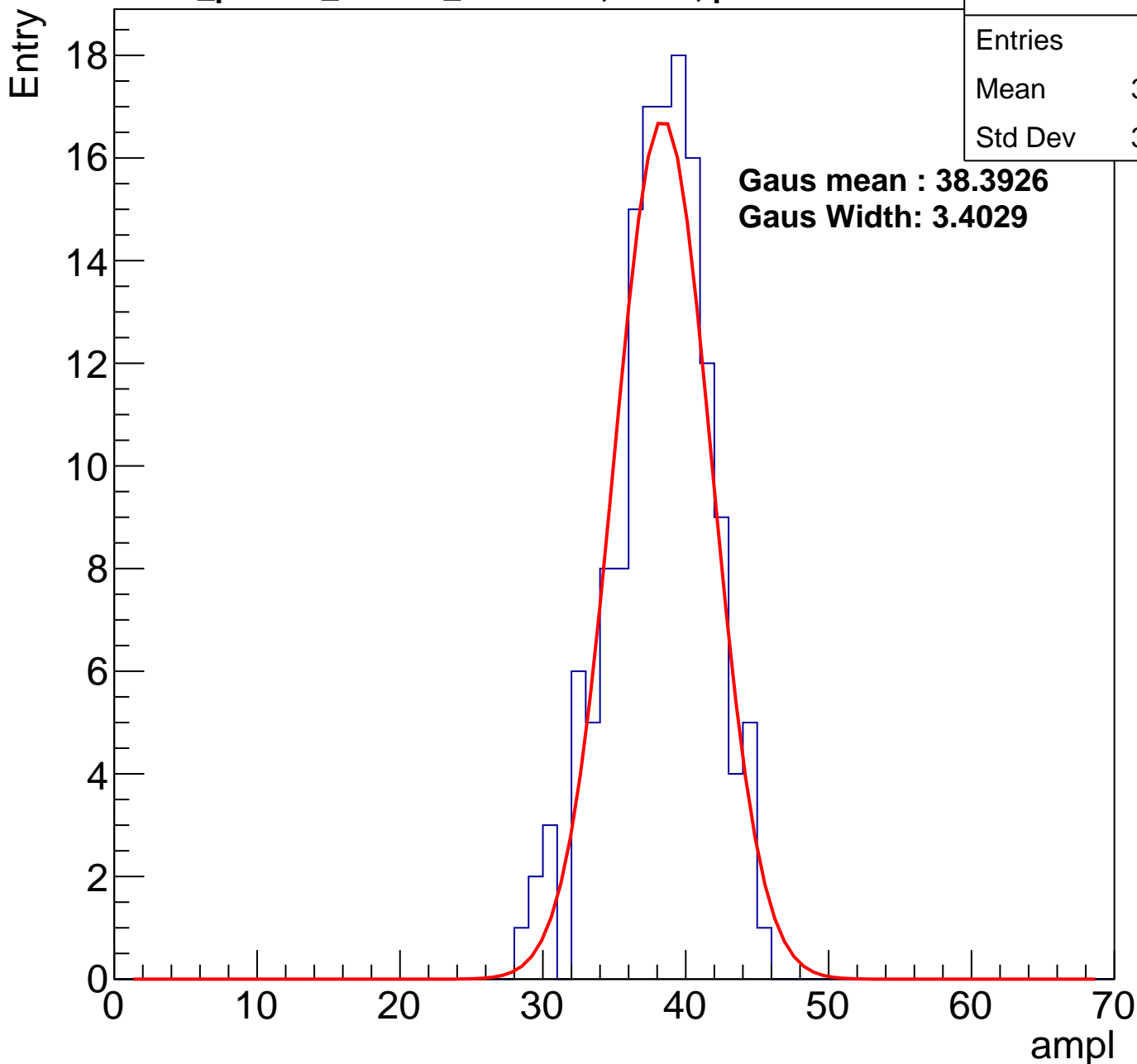
B1L001S, U24-ch46, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.75
Std Dev	3.426

Gaus mean : 38.3926

Gaus Width: 3.4029



B1L001S, U24-ch46, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

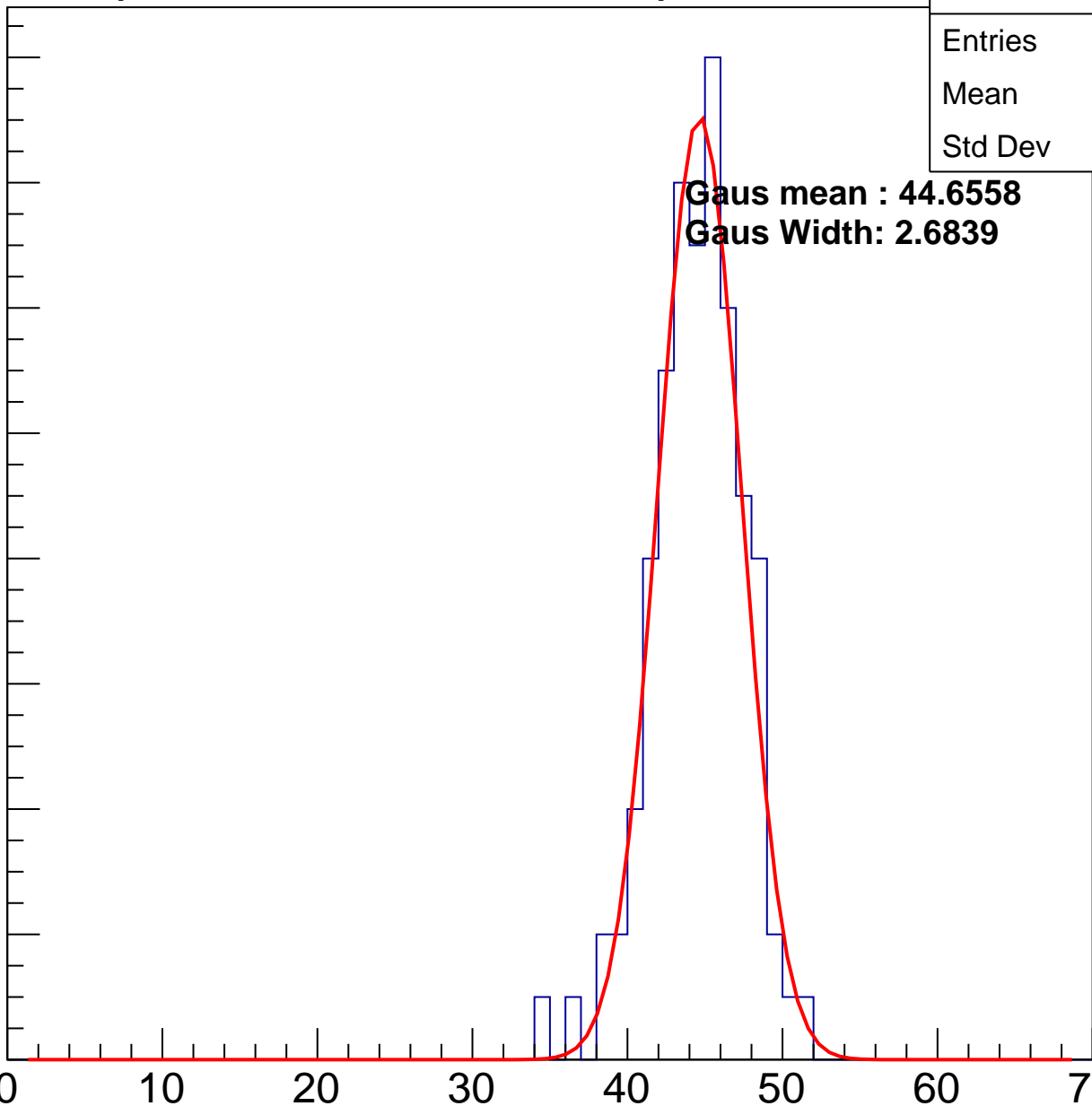
16
14
12
10
8
6
4
2
0

Entries	105
Mean	44.06
Std Dev	2.908

Gaus mean : 44.6558

Gaus Width: 2.6839

ampl

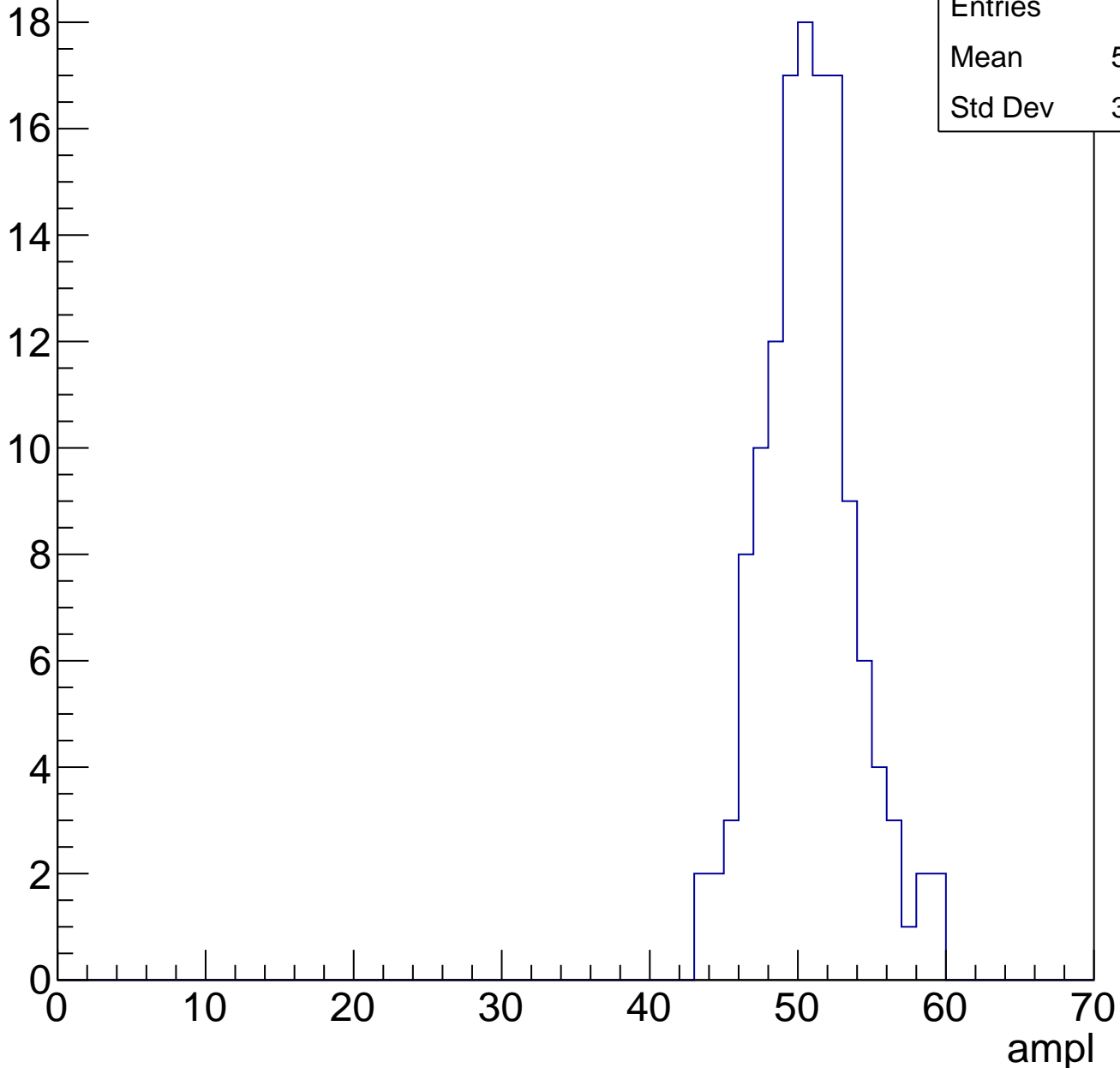


B1L001S, U24-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	50.28
Std Dev	3.158

Entry



B1L001S, U24-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

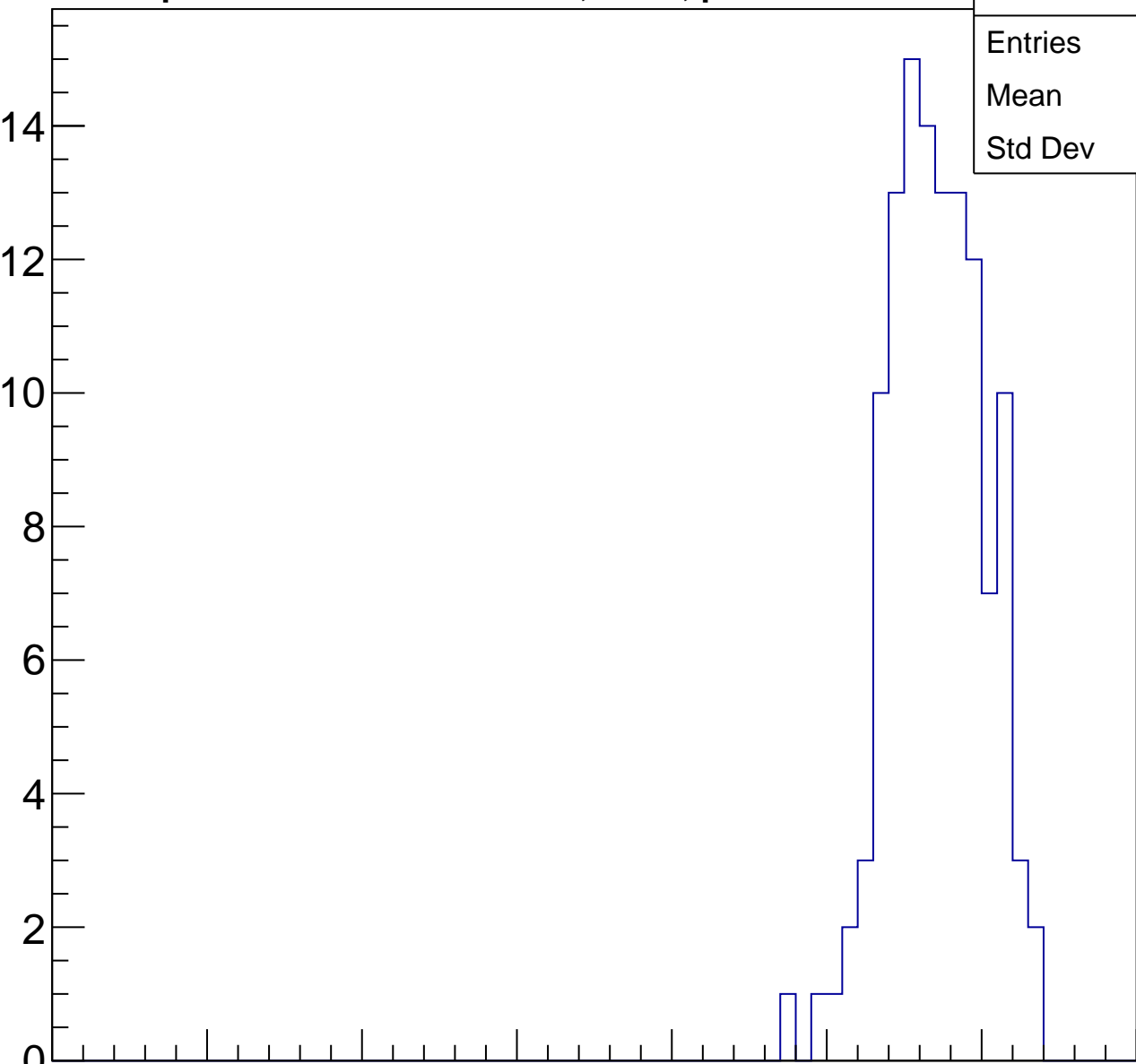
Entries	120
Mean	56.58
Std Dev	3.057

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch46, adc5

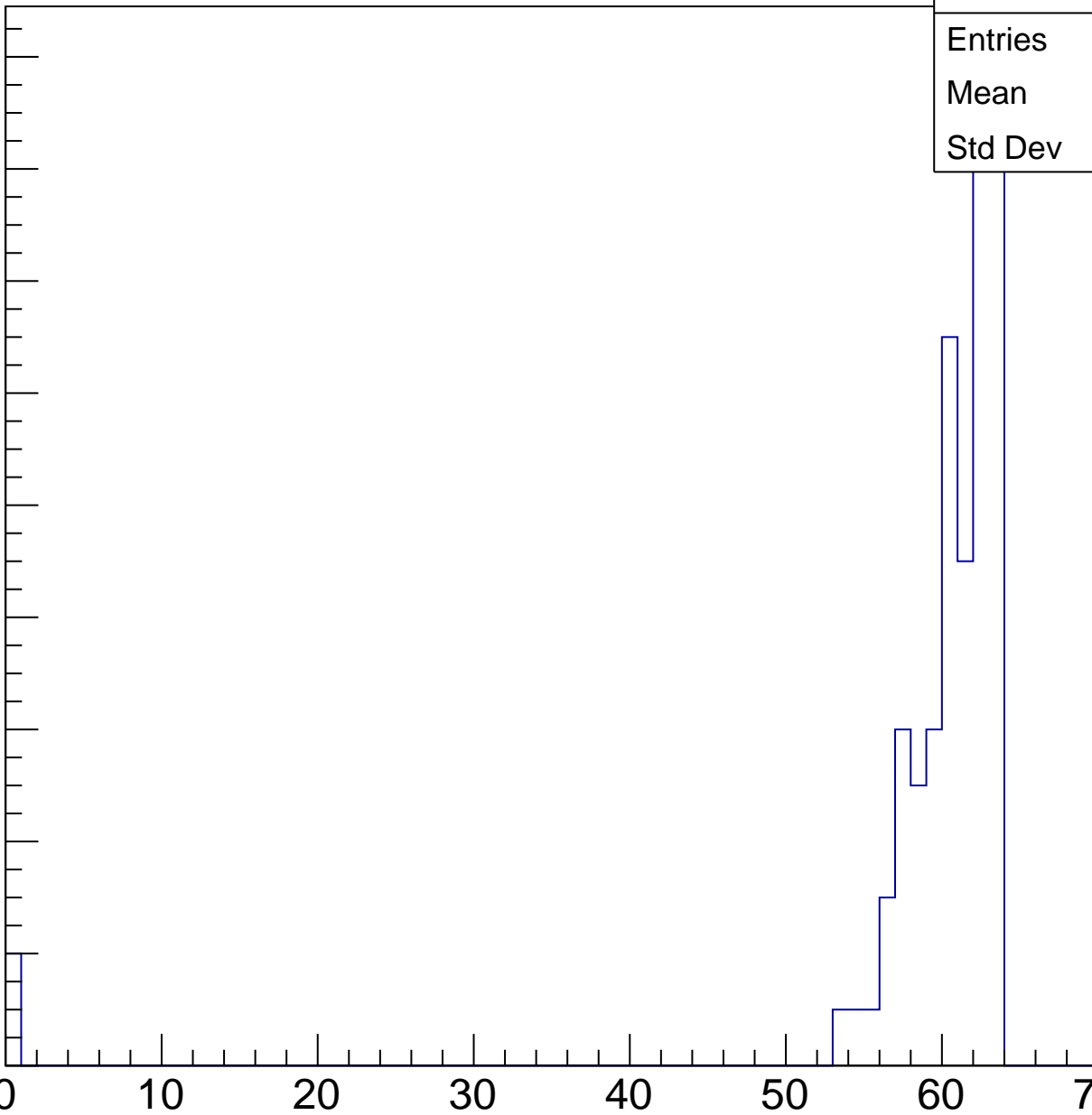
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.9
Std Dev	9.671

ampl



B1L001S, U24-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

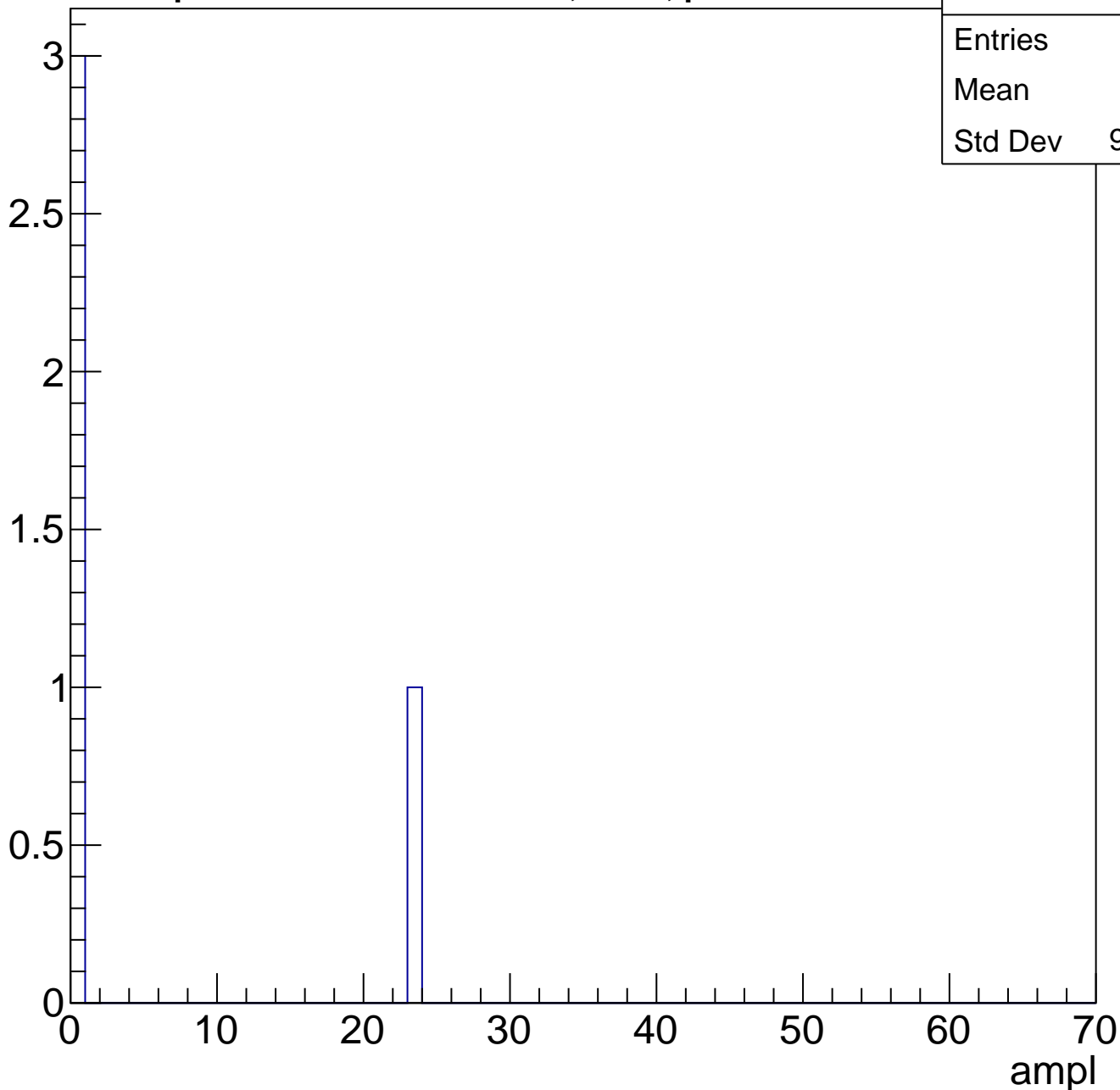
Entry



B1L001S, U24-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch47, adc0

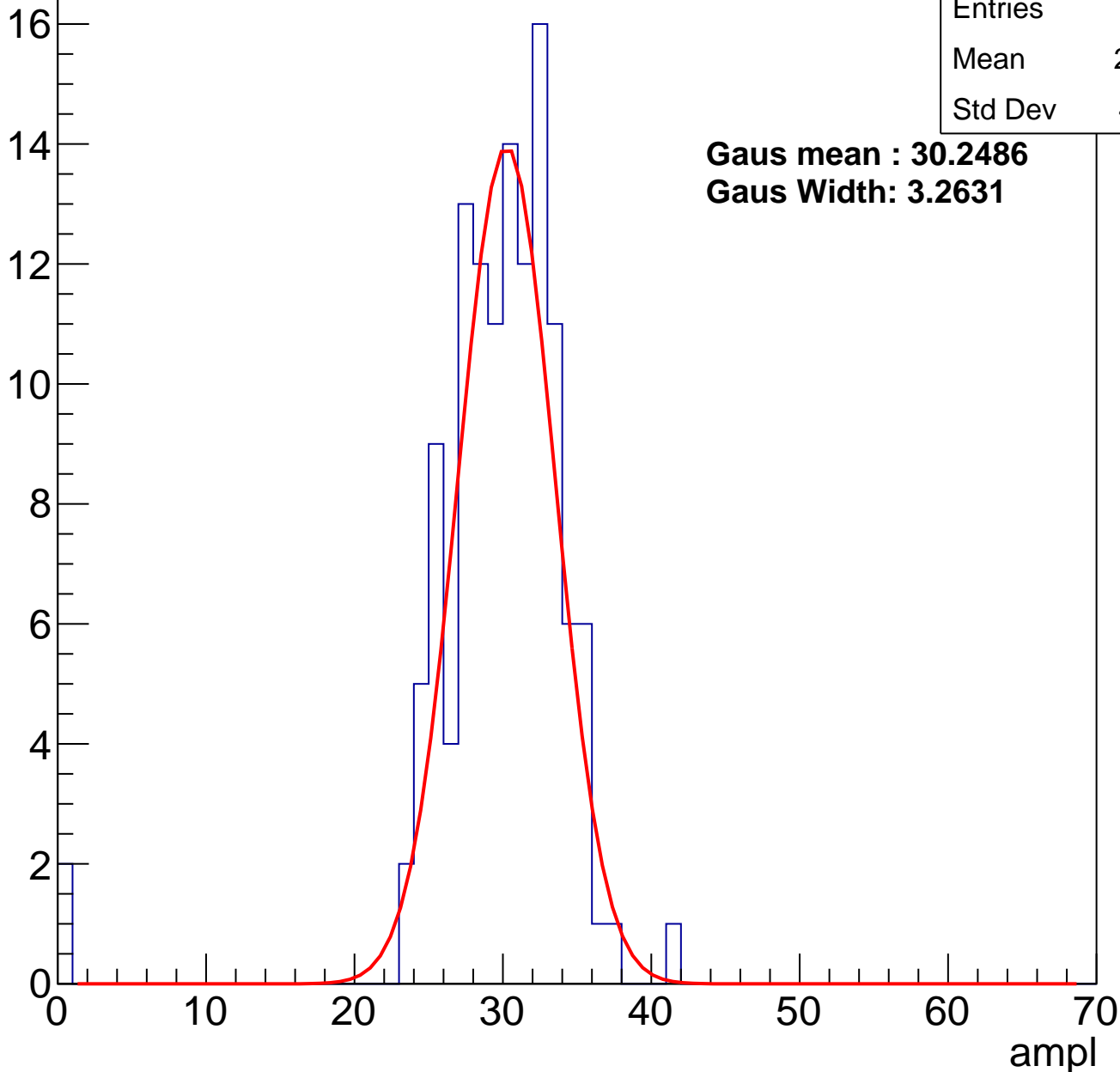
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.33
Std Dev	4.971

Gaus mean : 30.2486

Gaus Width: 3.2631

Entry



B1L001S, U24-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

147

Mean

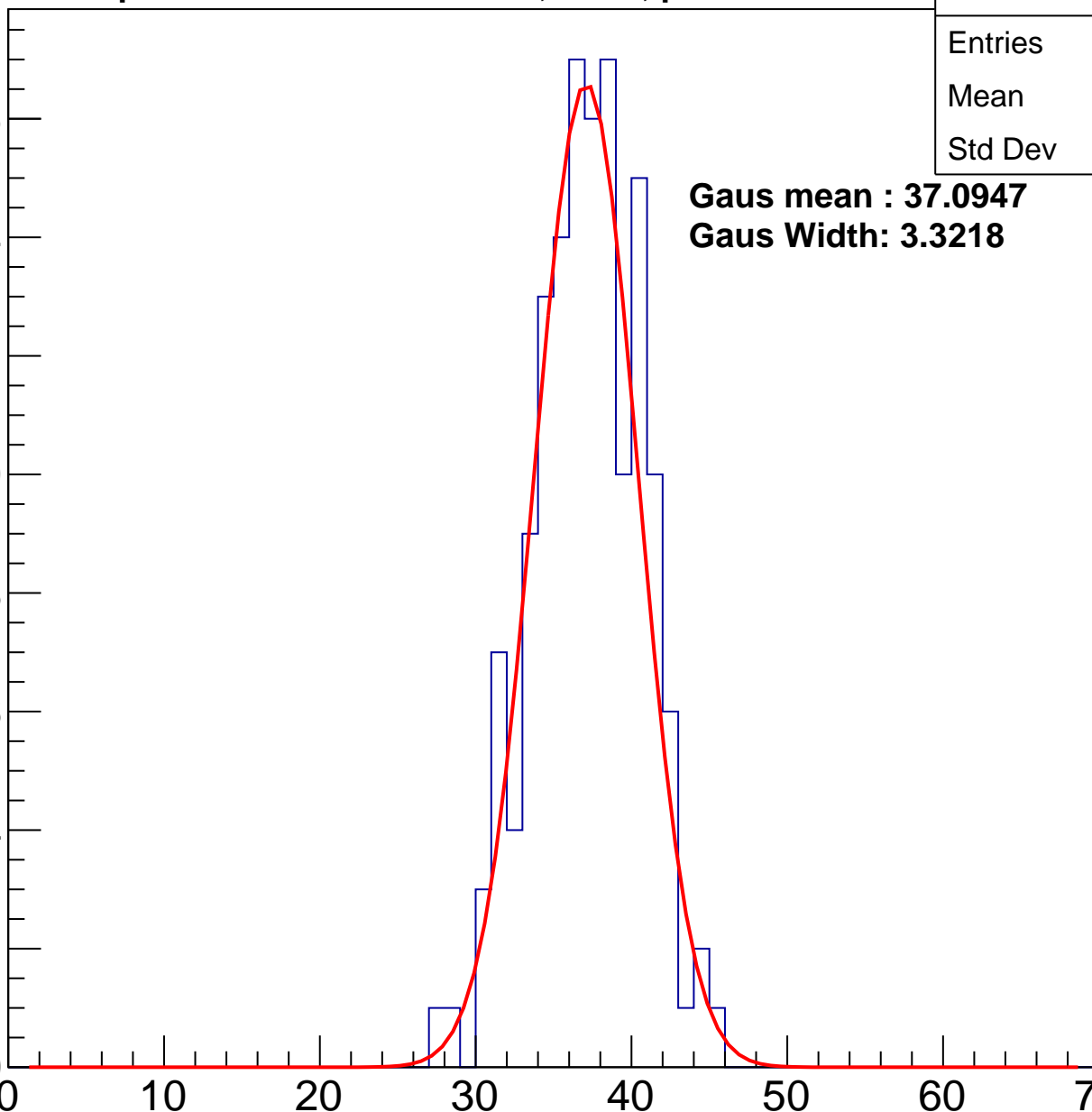
36.71

Std Dev

3.403

Gaus mean : 37.0947

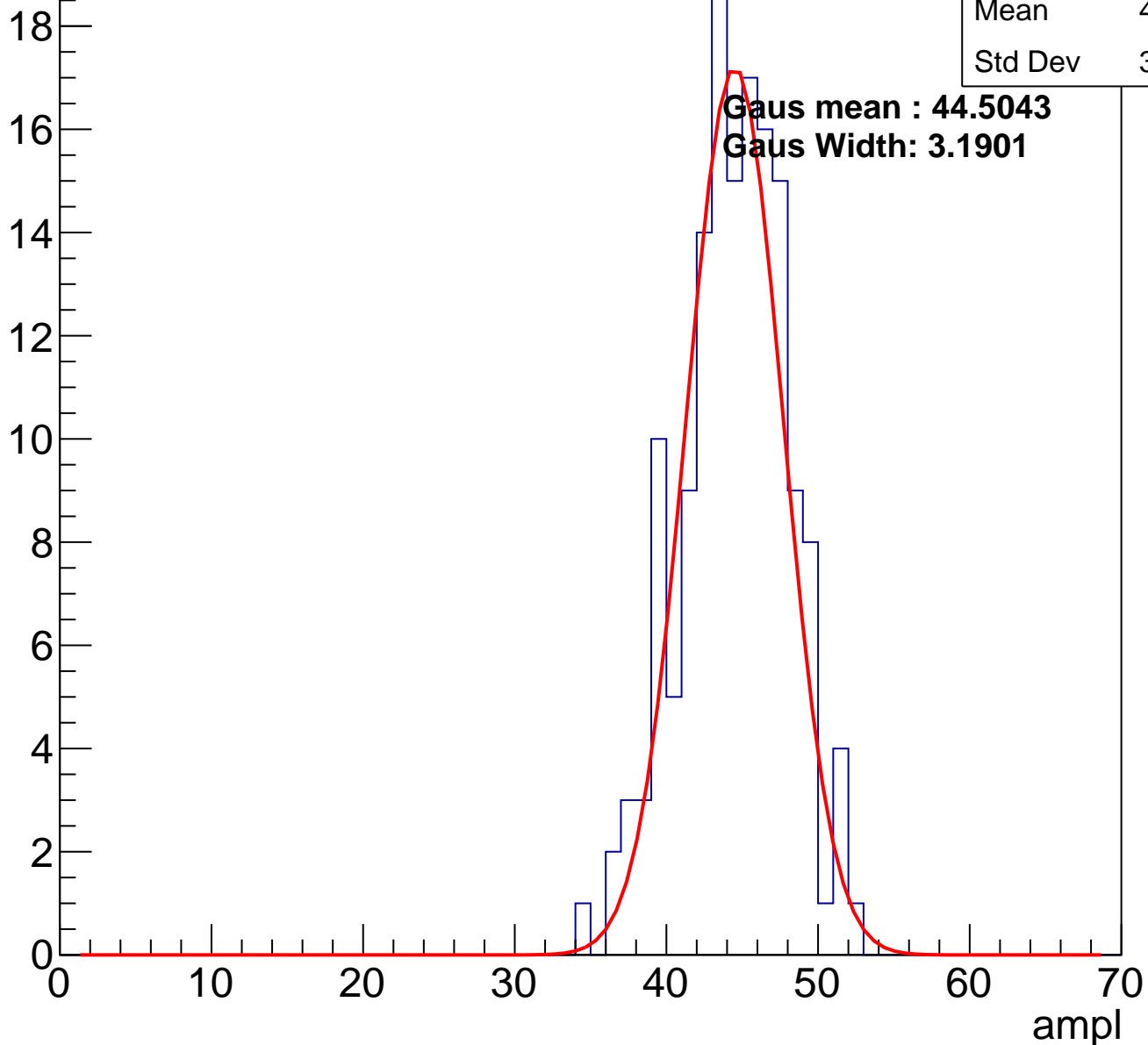
Gaus Width: 3.3218



B1L001S, U24-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch47, adc3

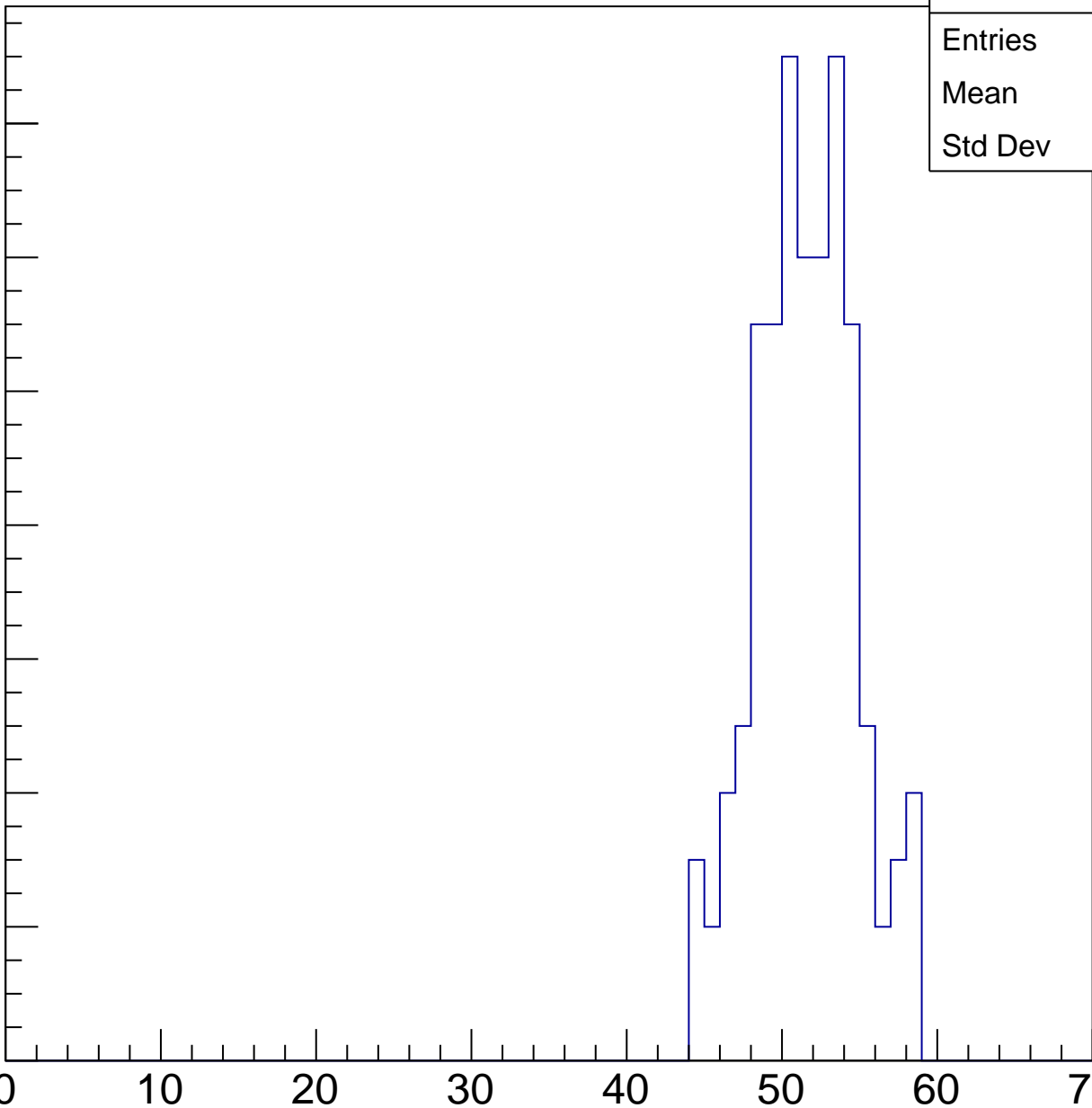
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	51.07
Std Dev	3.178

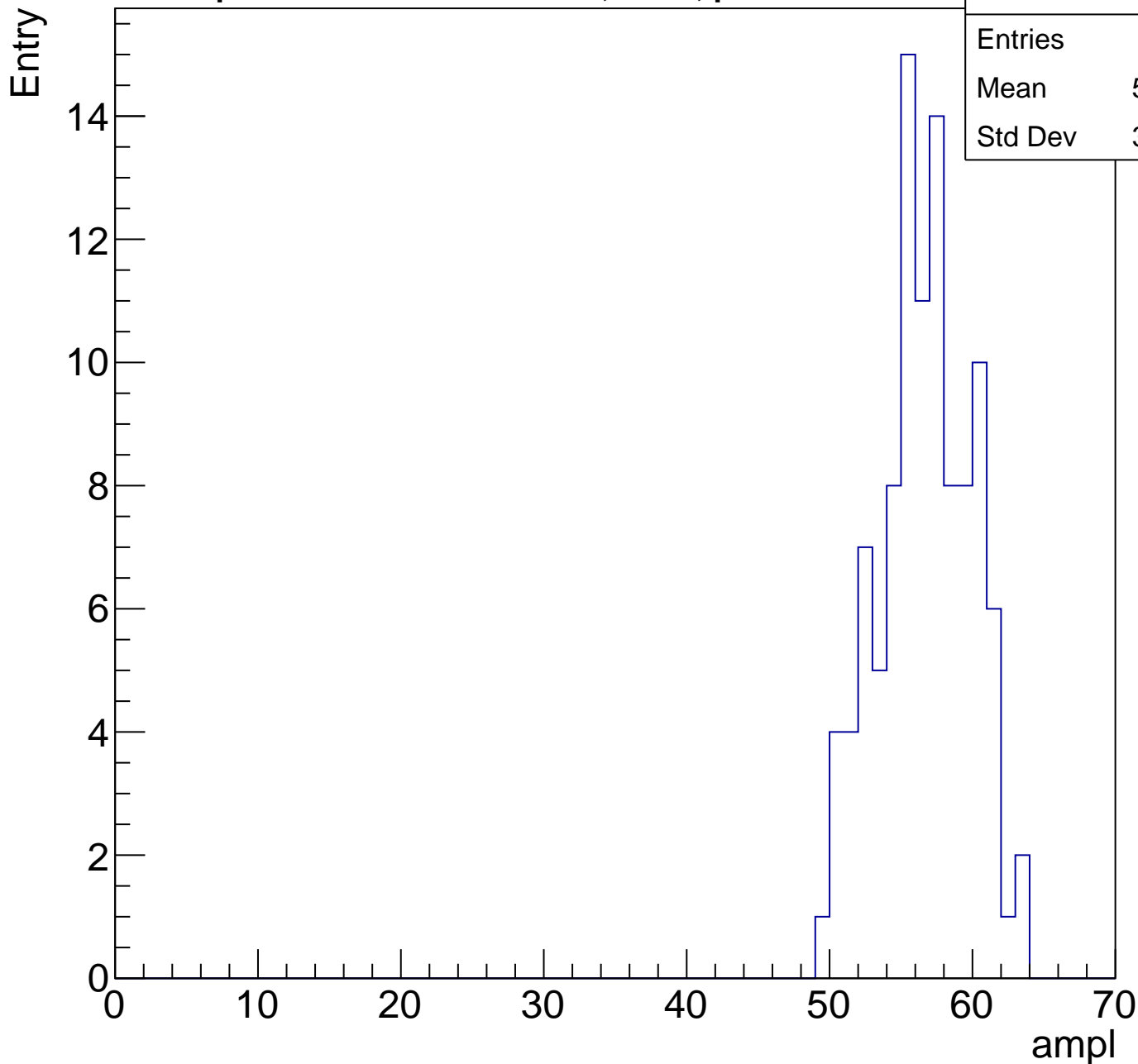
ampl



B1L001S, U24-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	56.18
Std Dev	3.186



B1L001S, U24-ch47, adc5

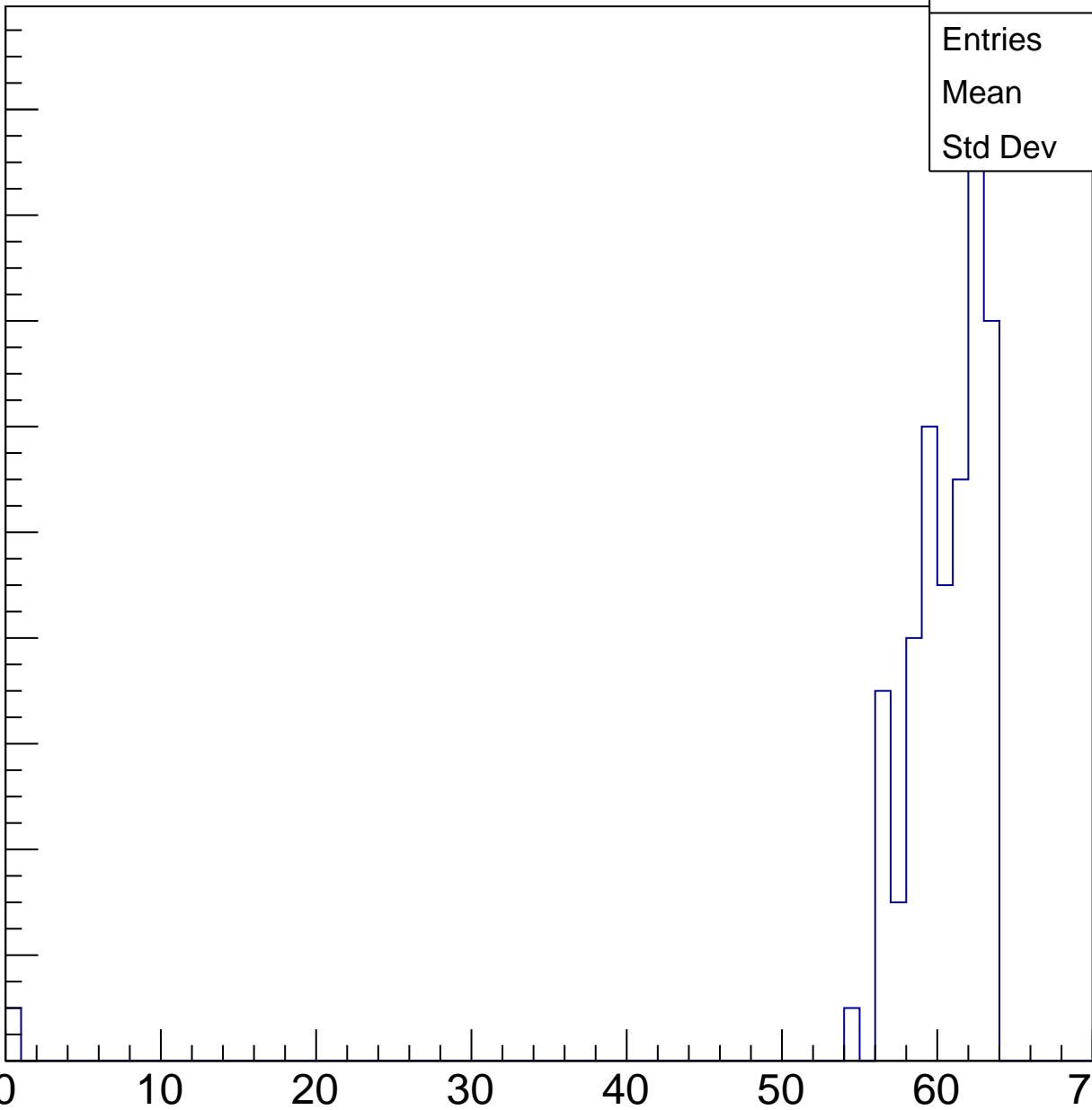
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.53
Std Dev	6.871

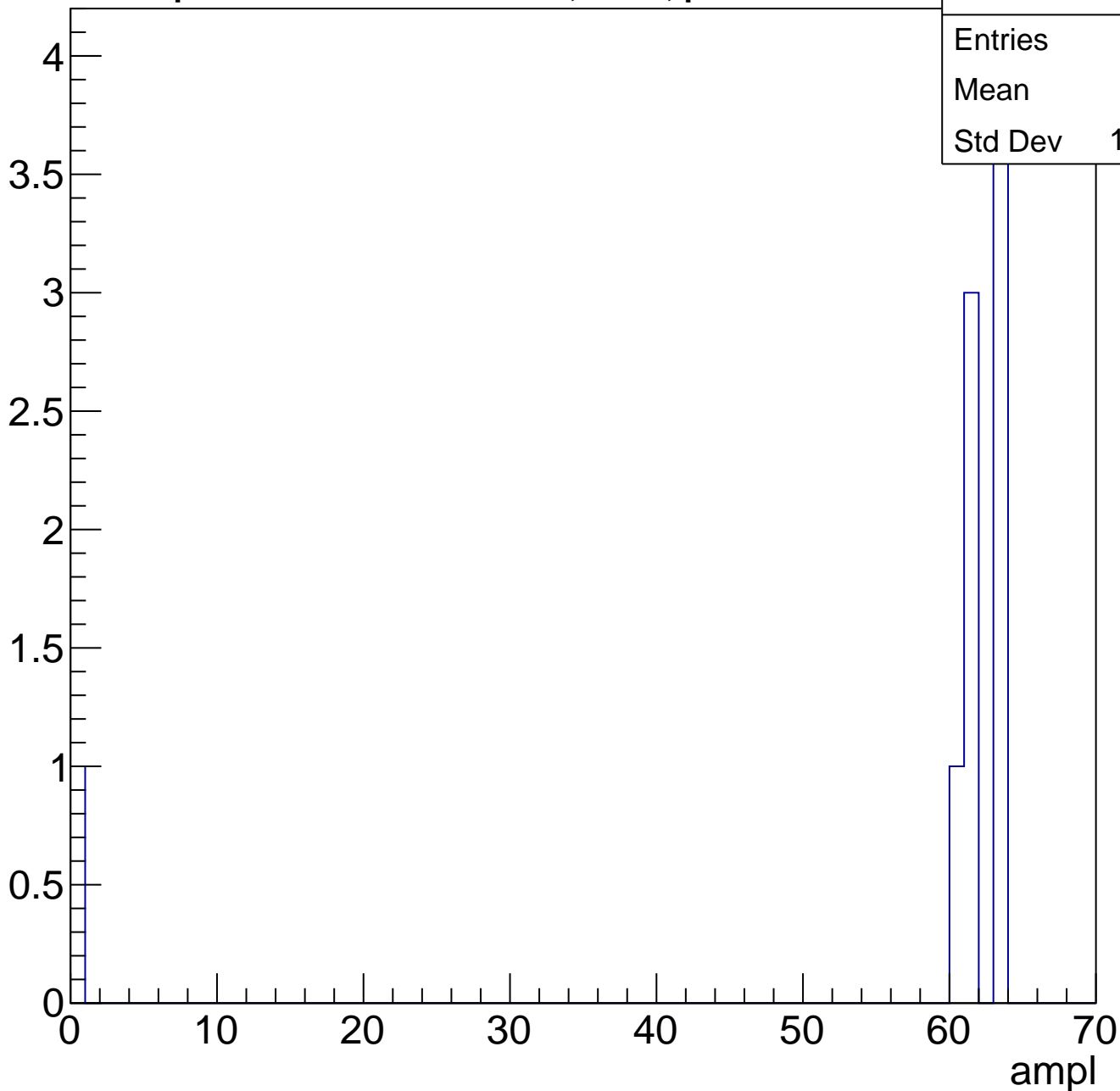
ampl



B1L001S, U24-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	9
Mean	55
Std Dev	19.48

B1L001S, U24-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L001S, U24-ch48, adc0

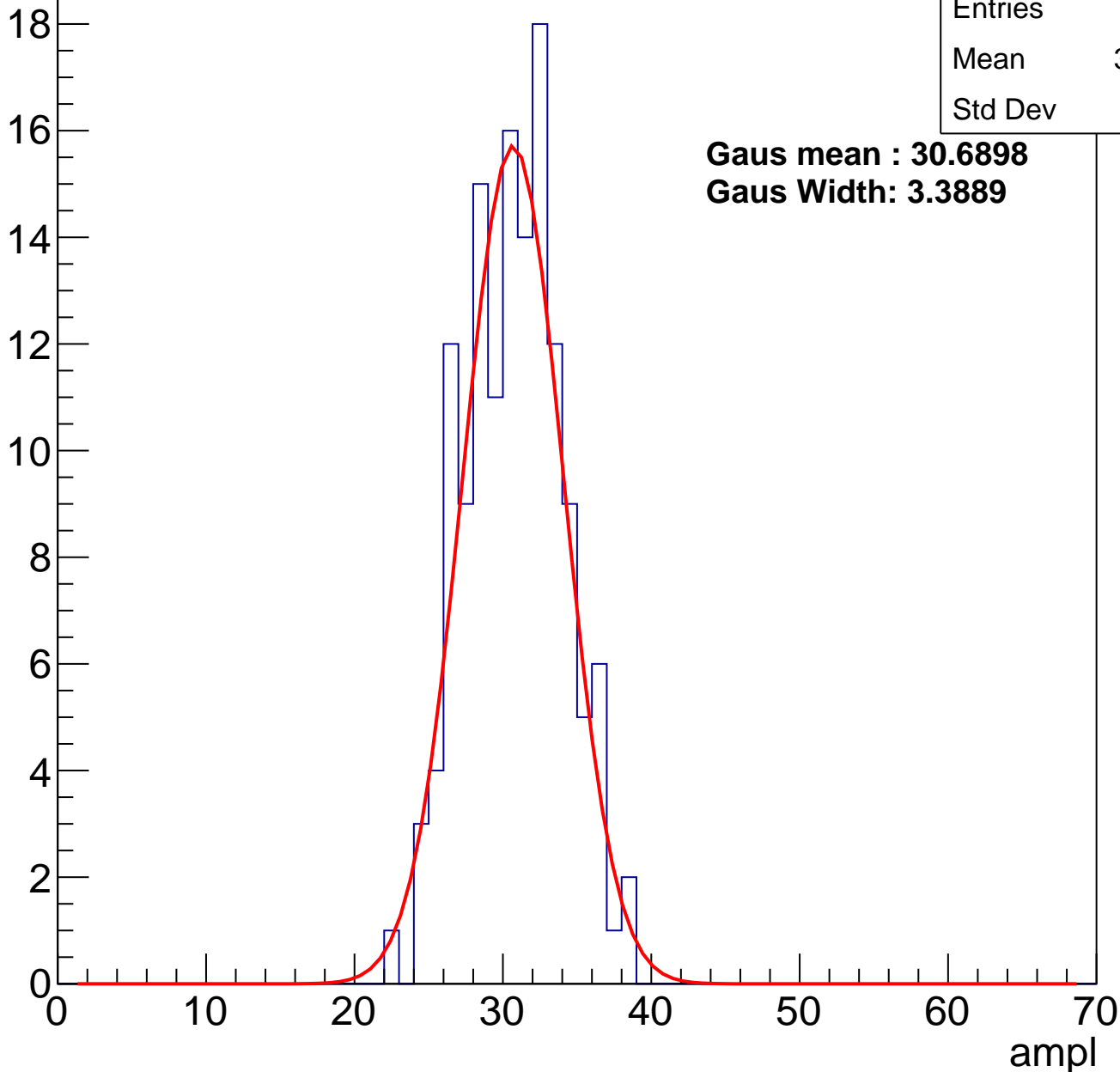
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	30.32
Std Dev	3.25

Gaus mean : 30.6898

Gaus Width: 3.3889

Entry



B1L001S, U24-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	37.35
Std Dev	3.542

Gaus mean : 38.0925

Gaus Width: 2.9655

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

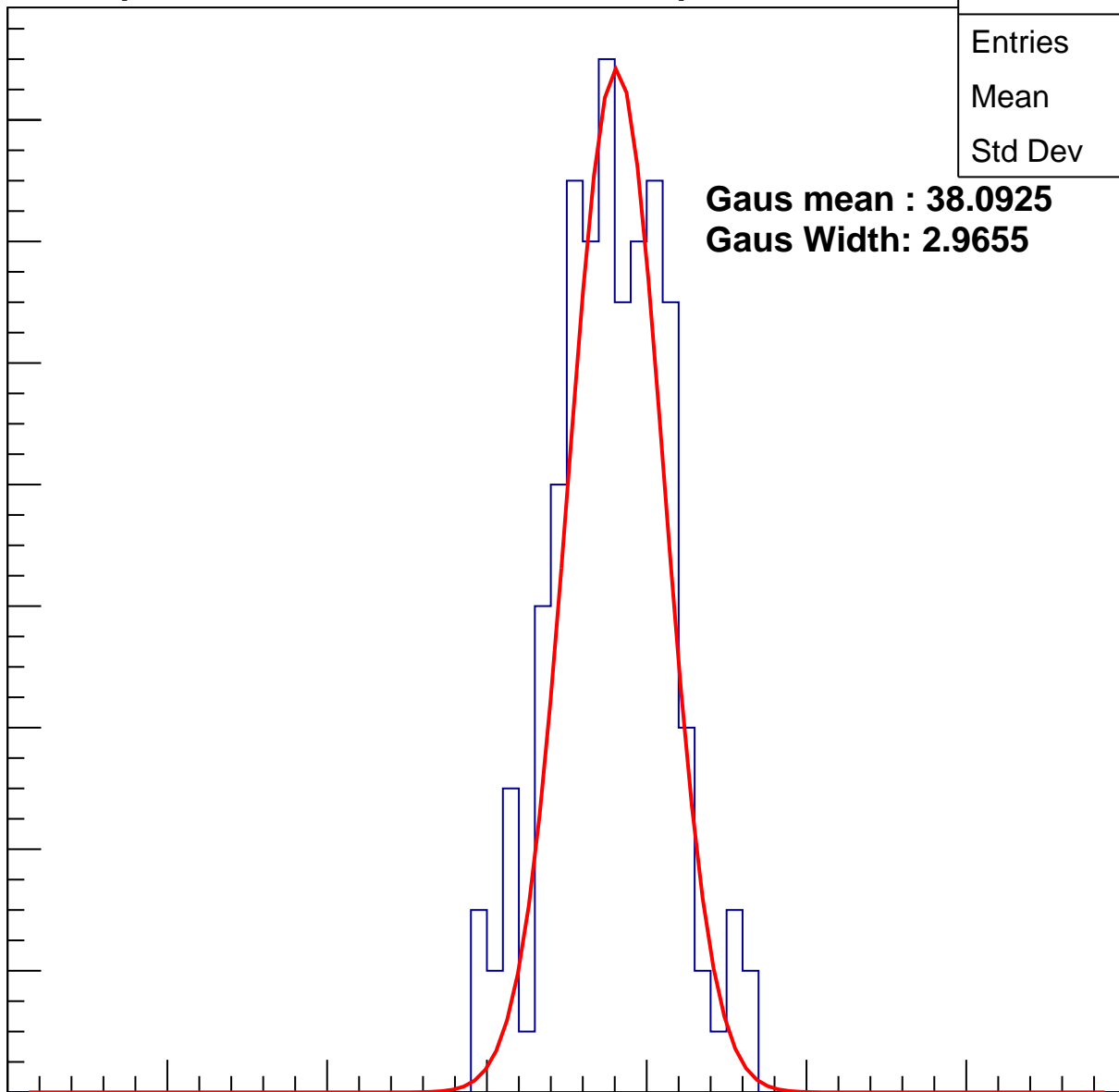
40

50

60

70

ampl



B1L001S, U24-ch48, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

102

Mean

43.75

Std Dev

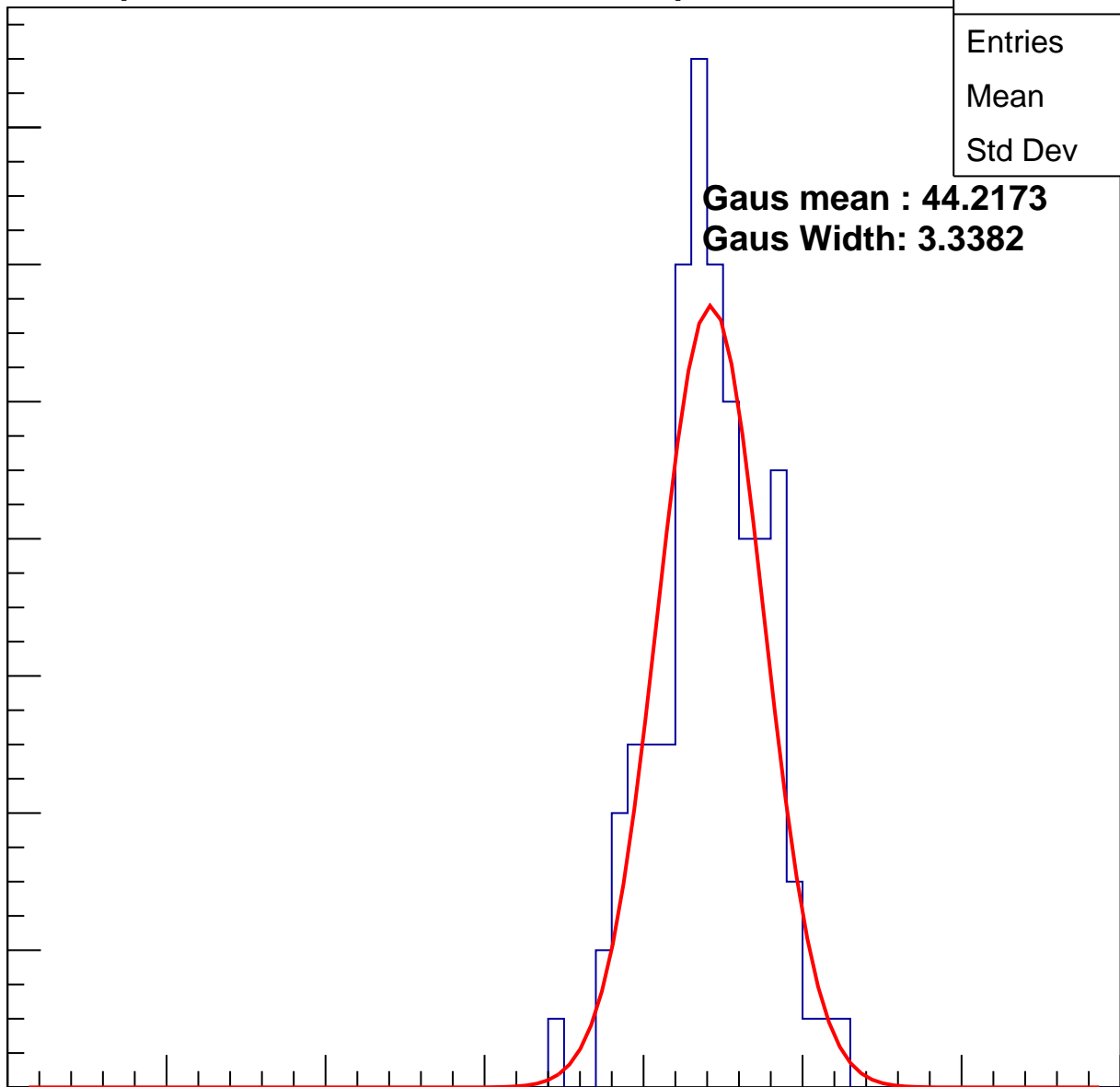
3.33

Gaus mean : 44.2173

Gaus Width: 3.3382

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

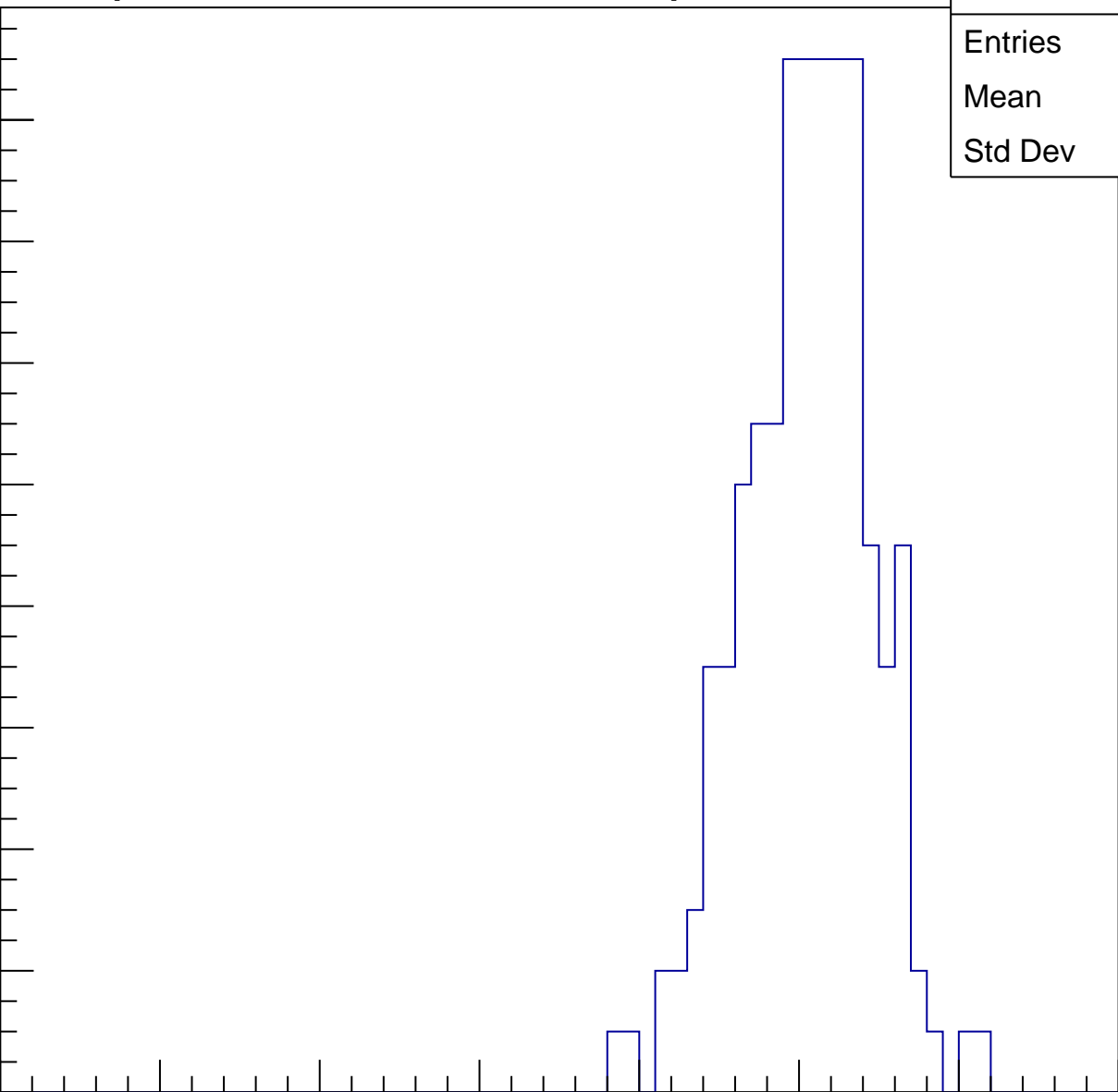
Entries	170
Mean	50.02
Std Dev	3.995

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

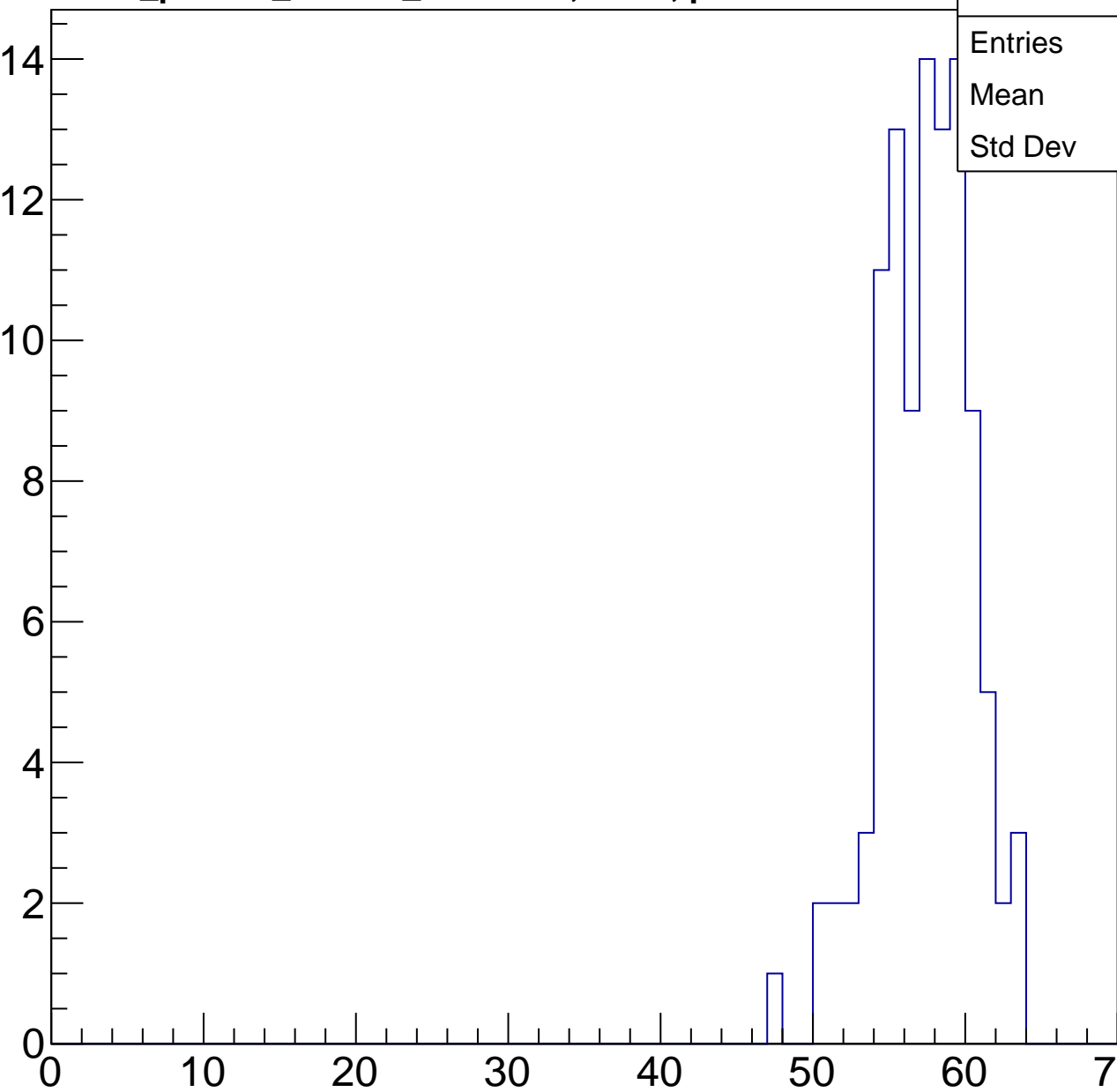
Entries	103
Mean	56.9
Std Dev	2.981

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch48, adc5

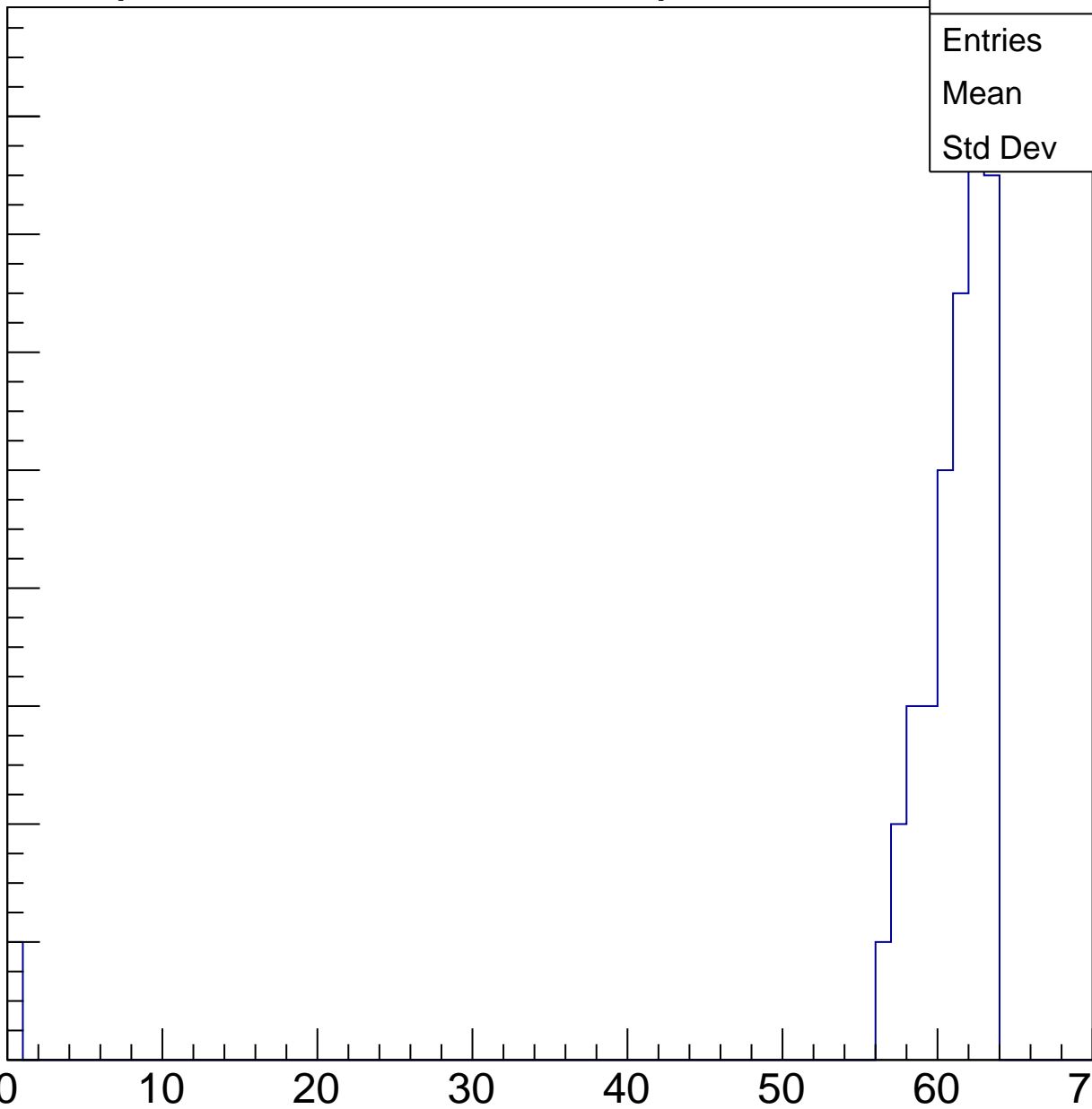
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.12
Std Dev	9.971

ampl



B1L001S, U24-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L001S, U24-ch49, adc0

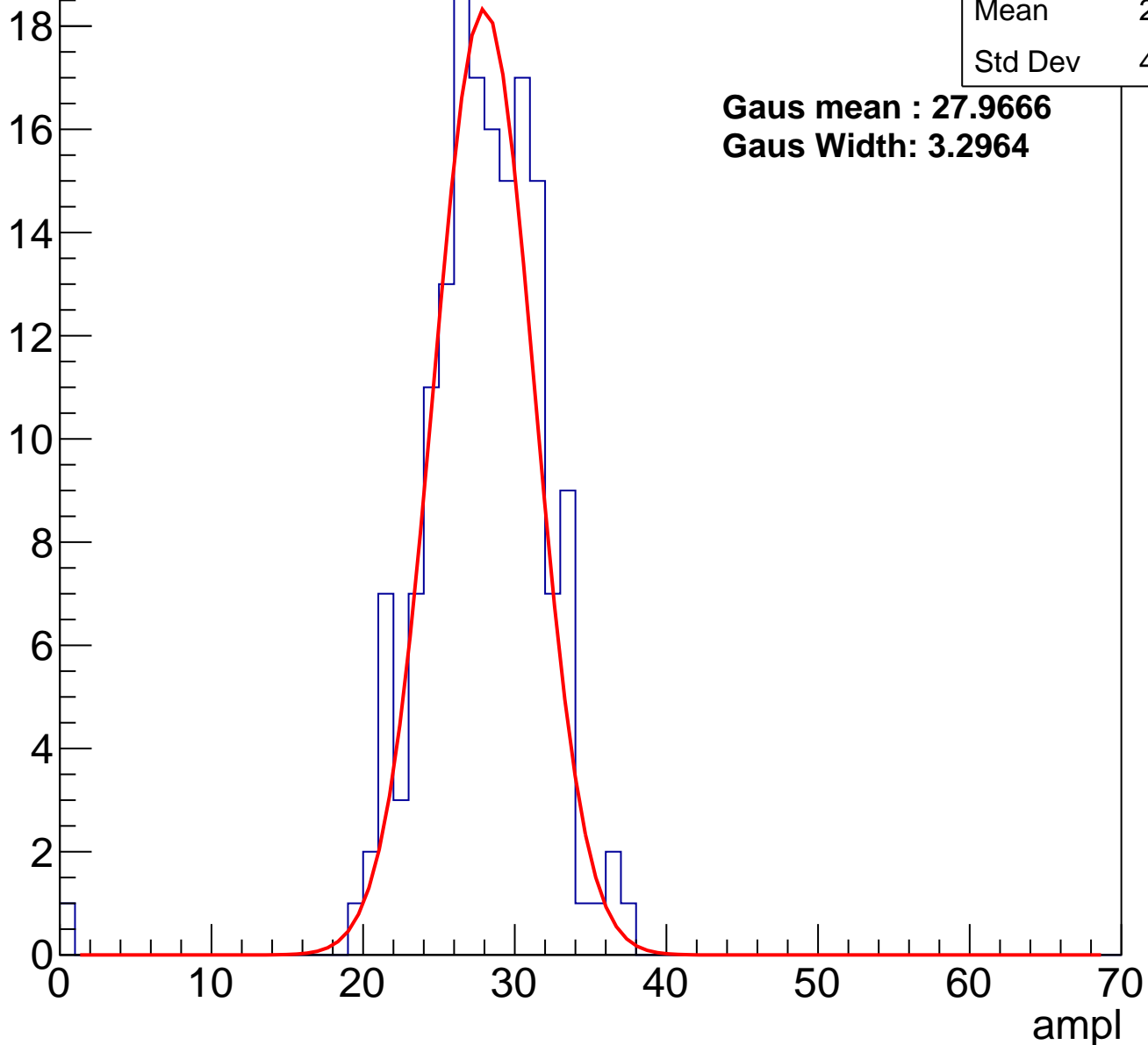
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	27.47
Std Dev	4.125

Gaus mean : 27.9666

Gaus Width: 3.2964

Entry



B1L001S, U24-ch49, adc1

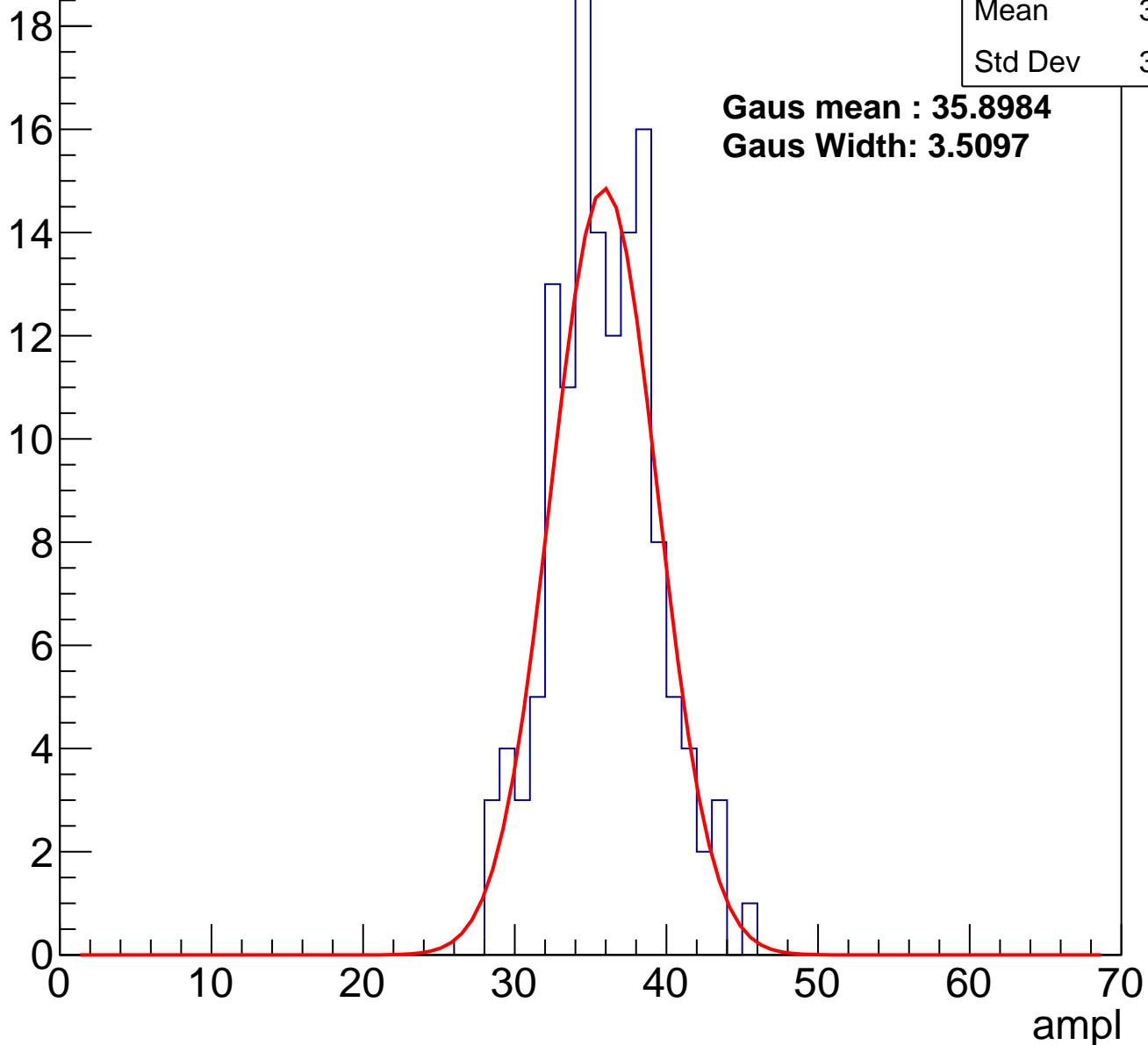
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.42
Std Dev	3.419

Gaus mean : 35.8984

Gaus Width: 3.5097

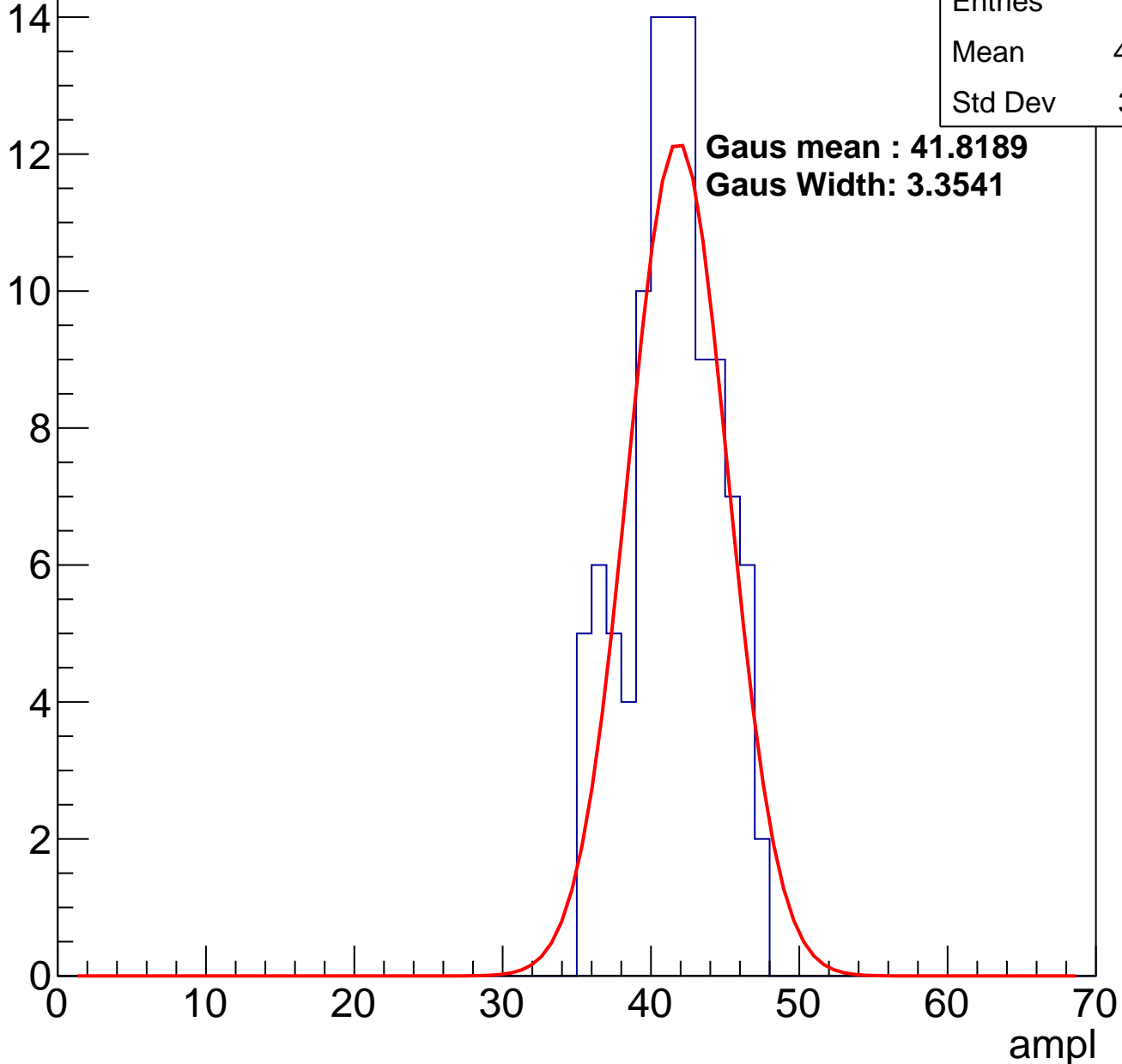
Entry



B1L001S, U24-ch49, adc2

calib_packv5_042523_0143.root, FC#2, port C2

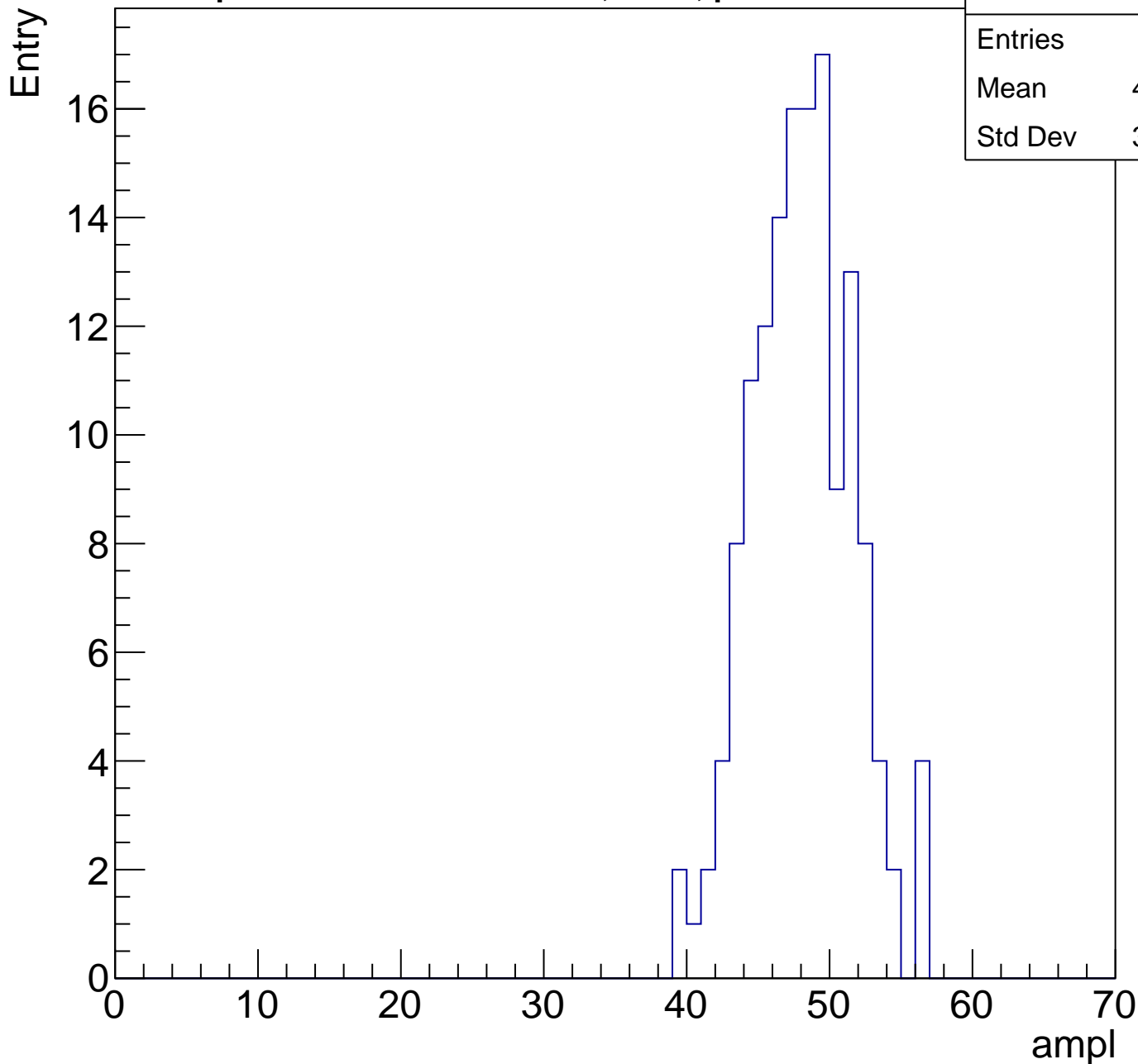
Entry



B1L001S, U24-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	47.59
Std Dev	3.475



B1L001S, U24-ch49, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	54.05
Std Dev	3.315

Entry

14
12
10
8
6
4
2
0

0

10

20

30

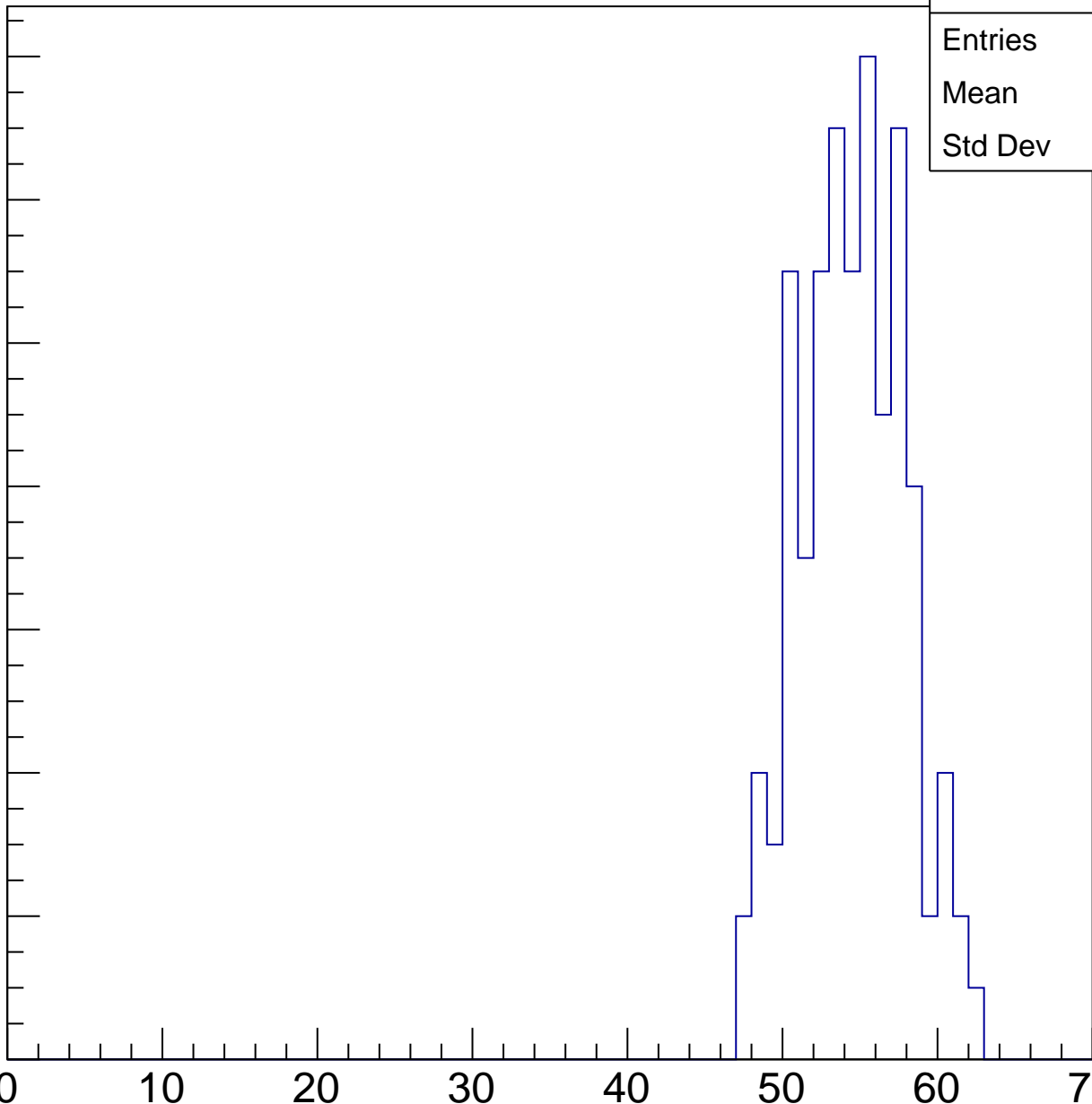
40

50

60

70

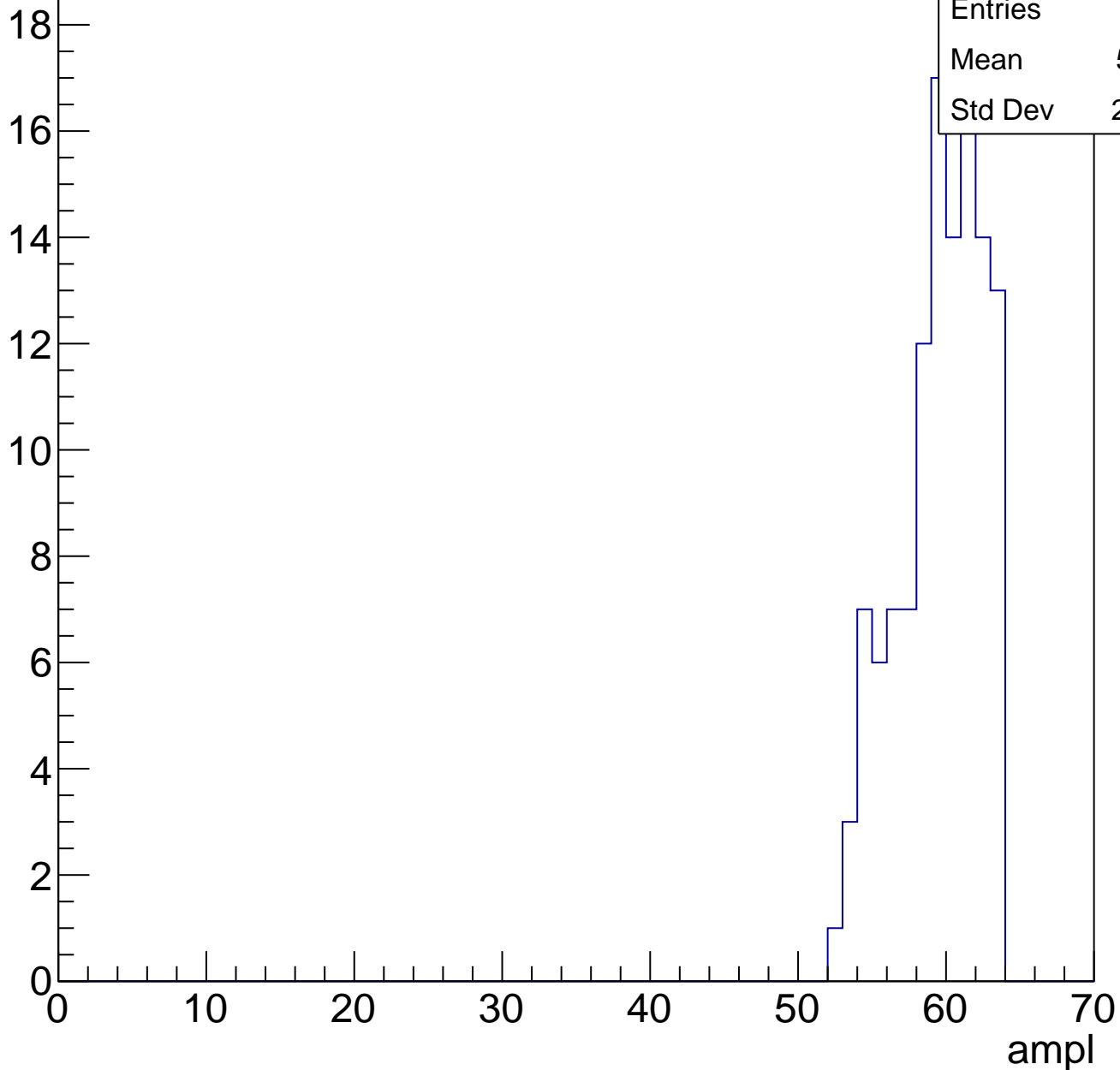
ampl



B1L001S, U24-ch49, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

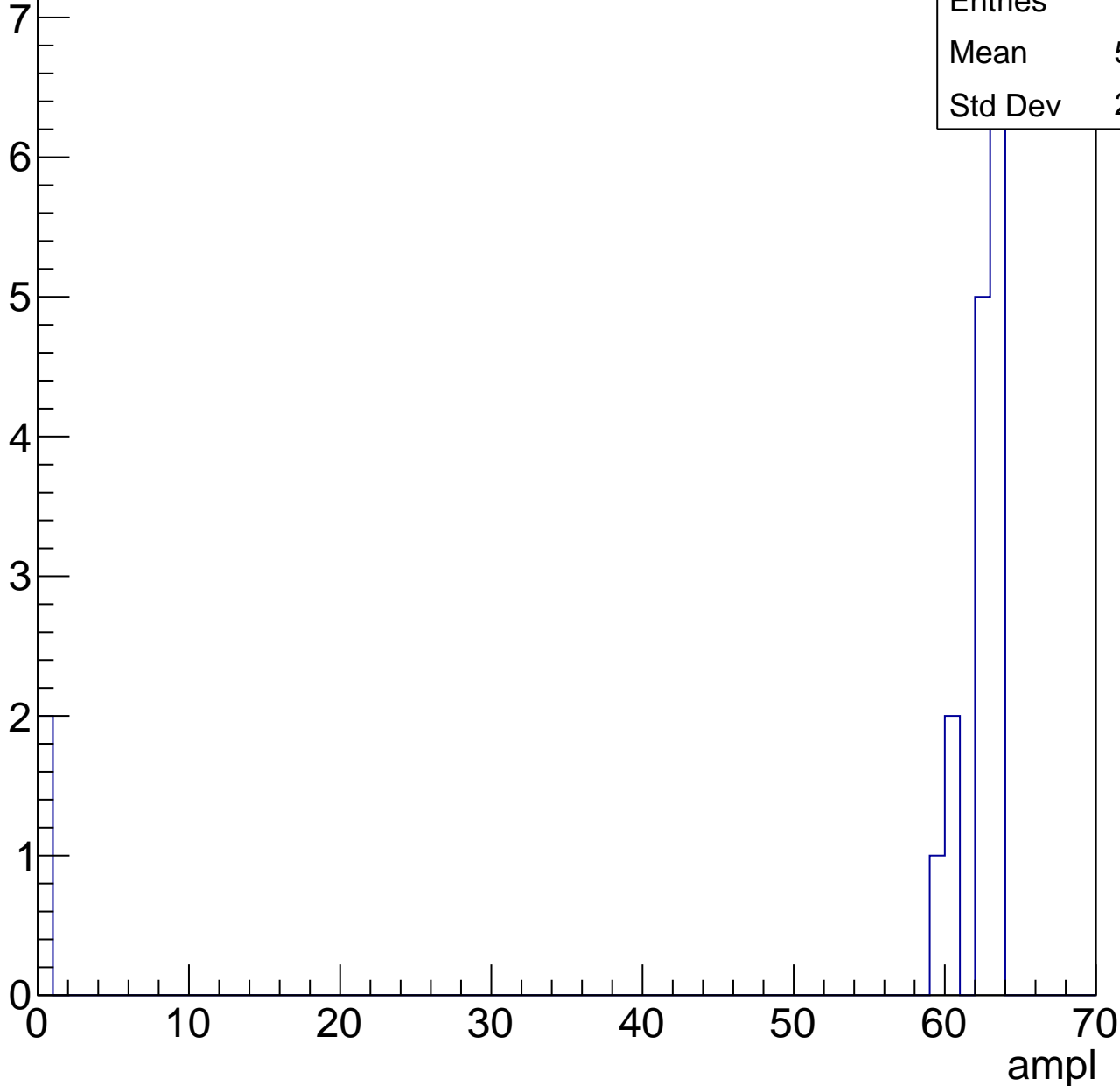


B1L001S, U24-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

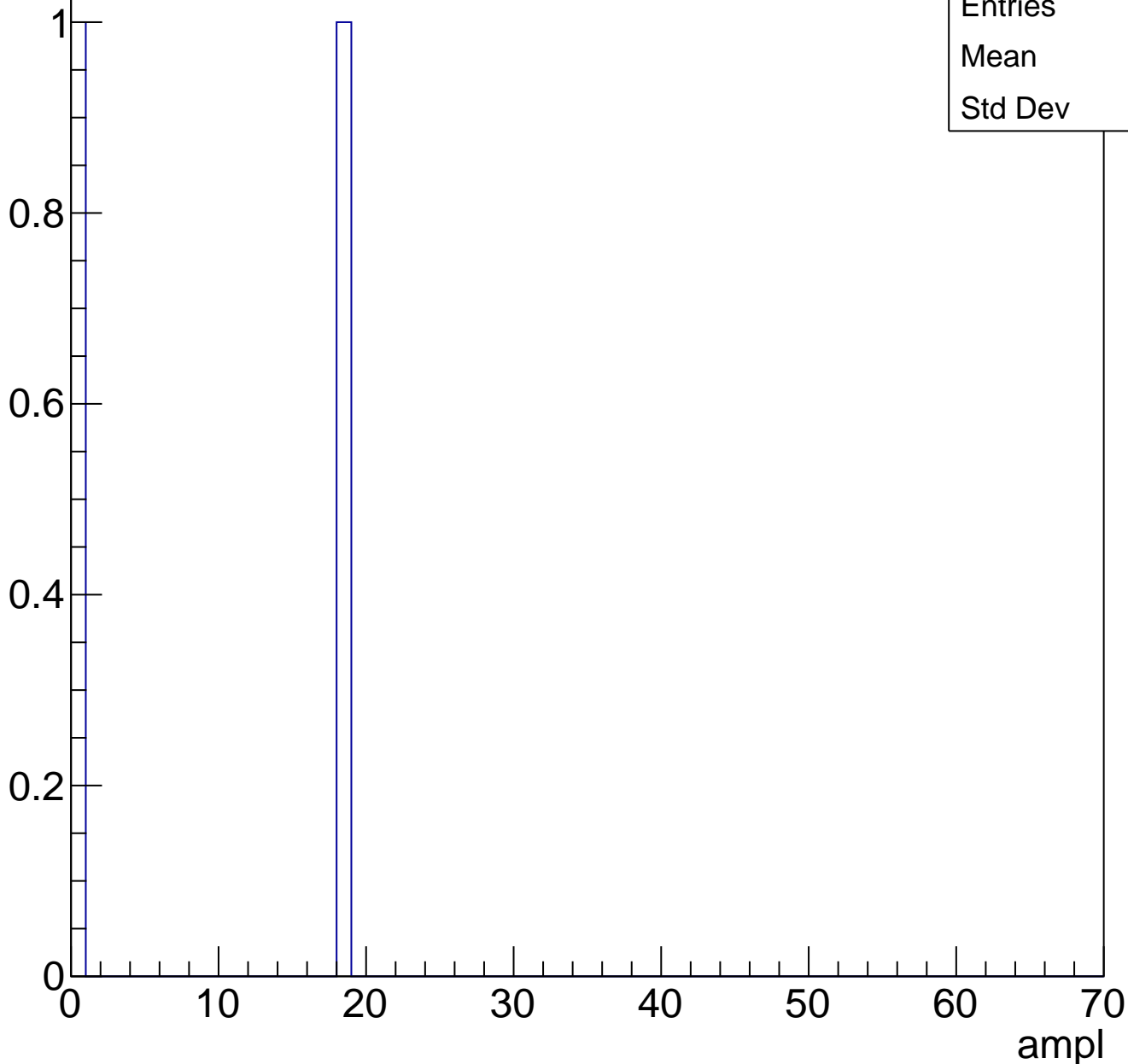
Entries	17
Mean	54.71
Std Dev	20.01



B1L001S, U24-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch50, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	29.81
Std Dev	3.223

Gaus mean : 30.2364

Gaus Width: 2.9936

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

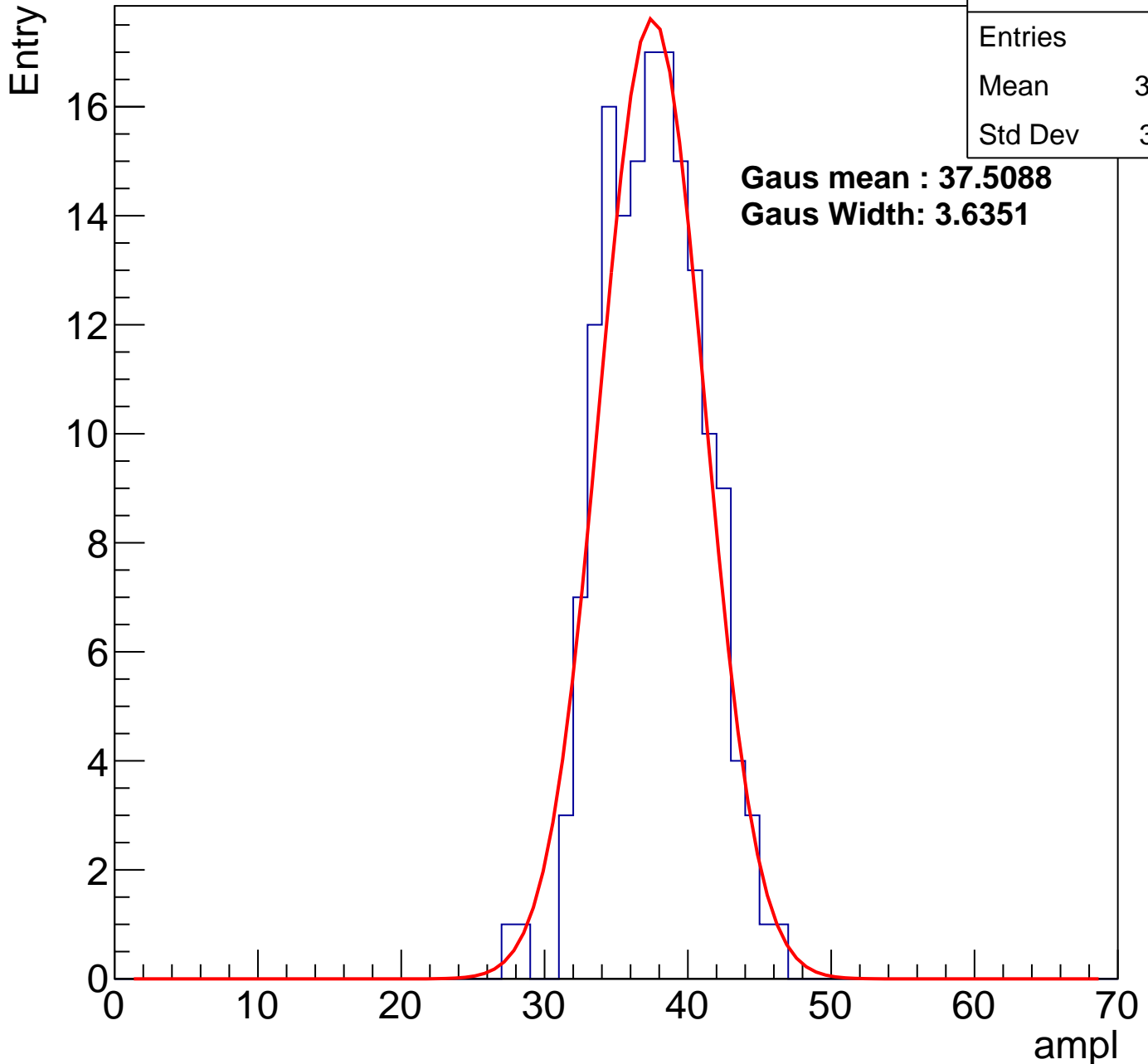
B1L001S, U24-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.14
Std Dev	3.441

Gaus mean : 37.5088

Gaus Width: 3.6351



B1L001S, U24-ch50, adc2

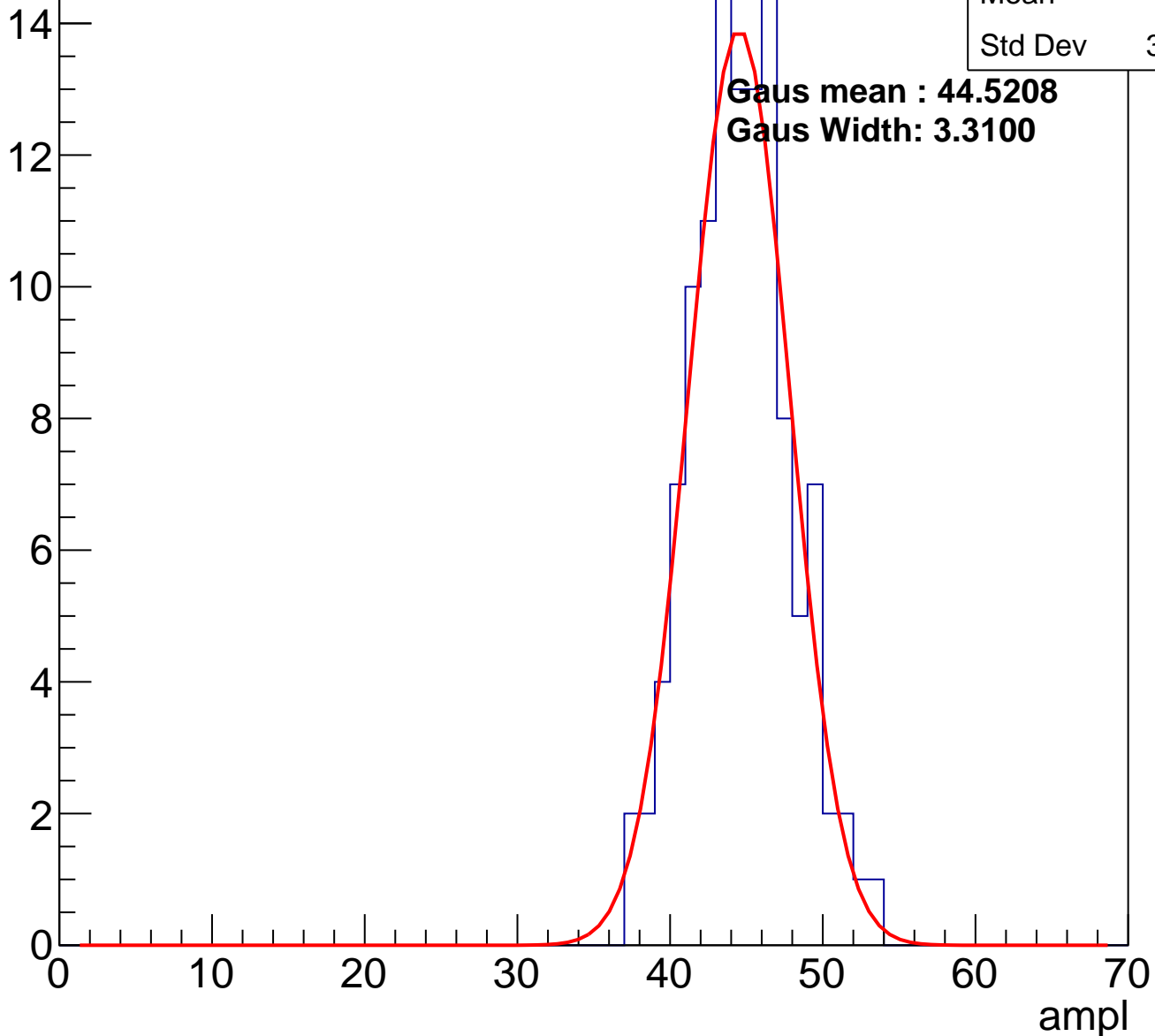
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	44.2
Std Dev	3.243

Gaus mean : 44.5208

Gaus Width: 3.3100

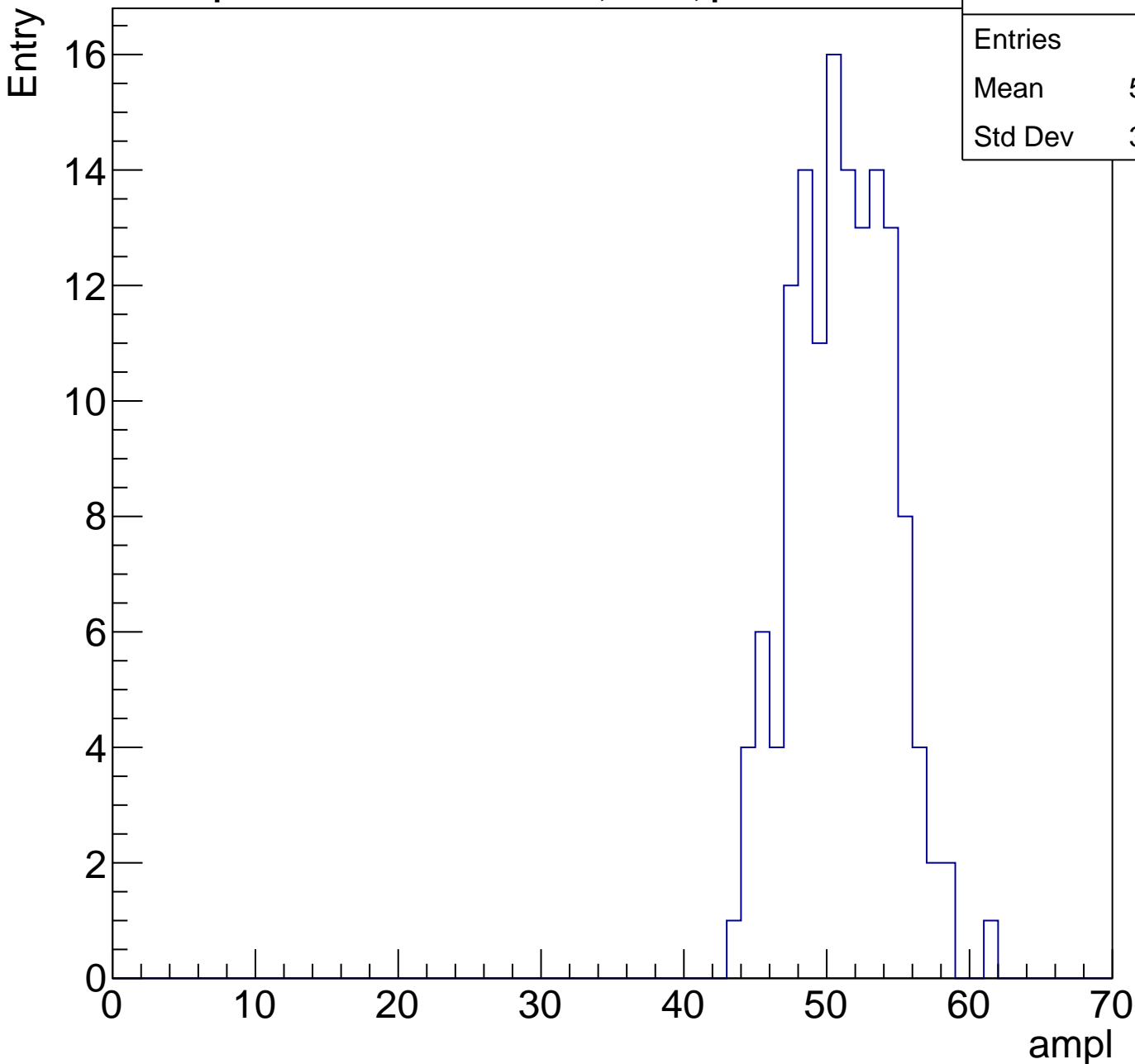
Entry



B1L001S, U24-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.63
Std Dev	3.412



B1L001S, U24-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

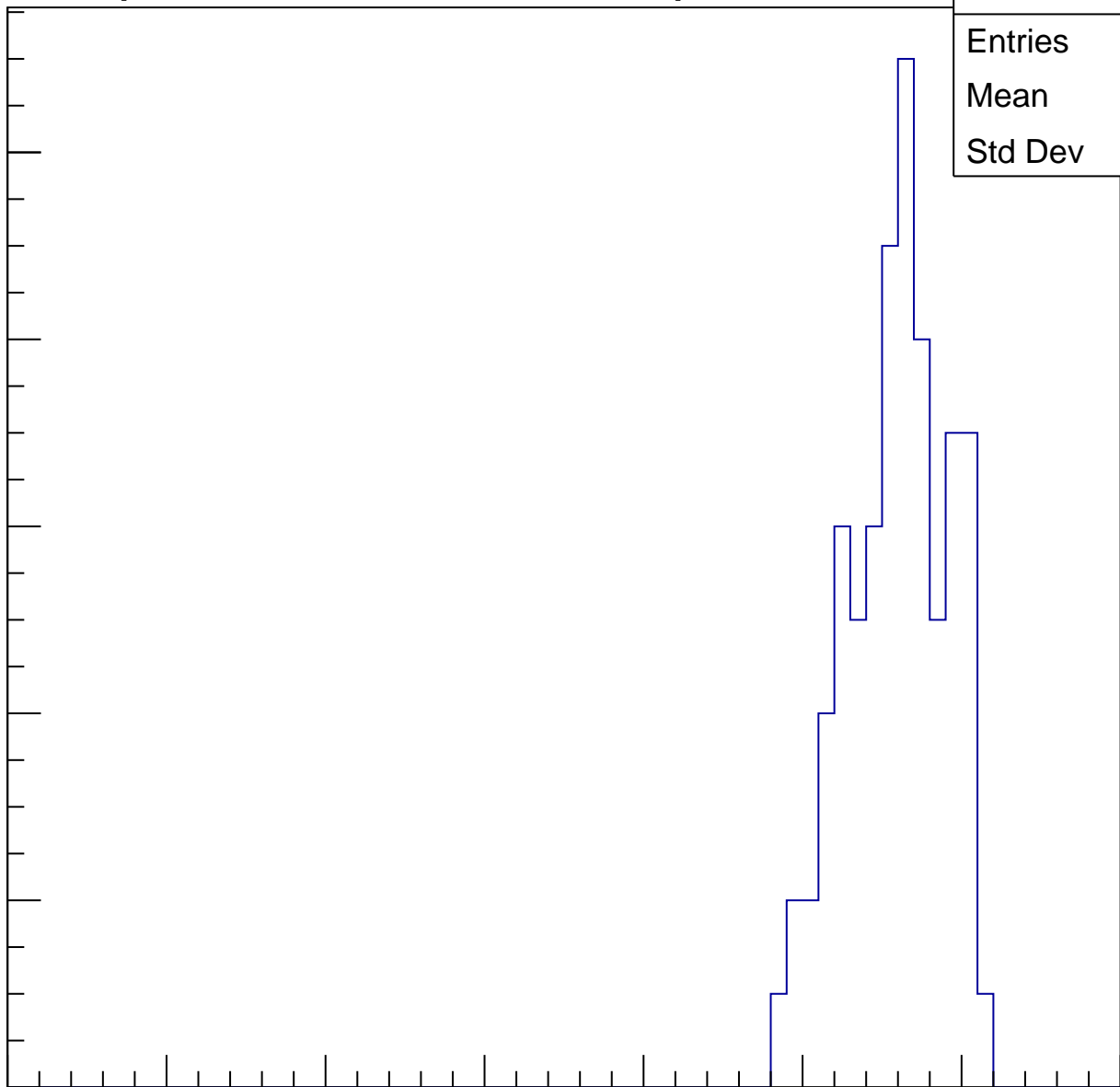
40

50

60

ampl

Entries	74
Mean	55.43
Std Dev	3.102



B1L001S, U24-ch50, adc5

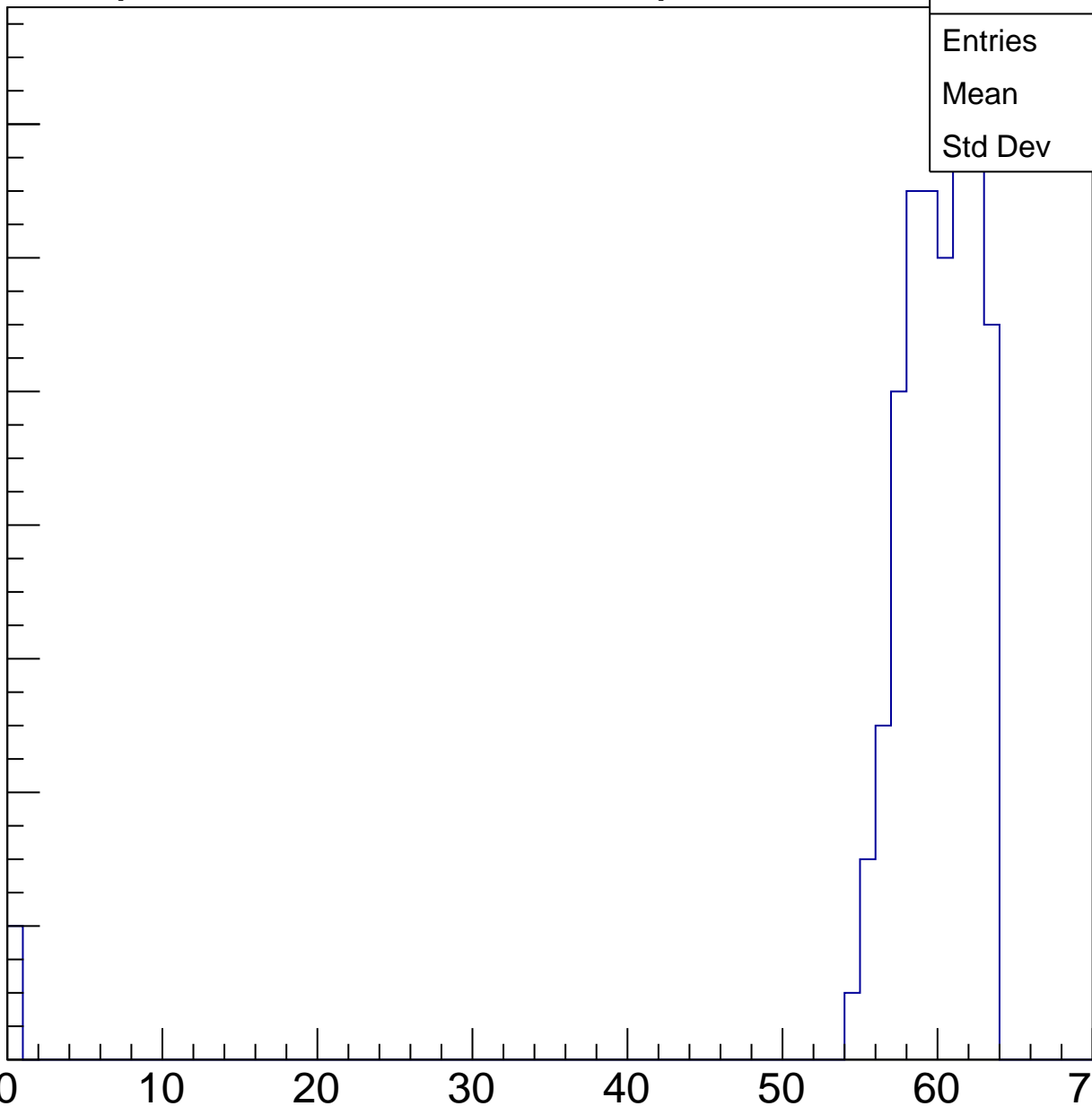
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	58.48
Std Dev	8.654

ampl

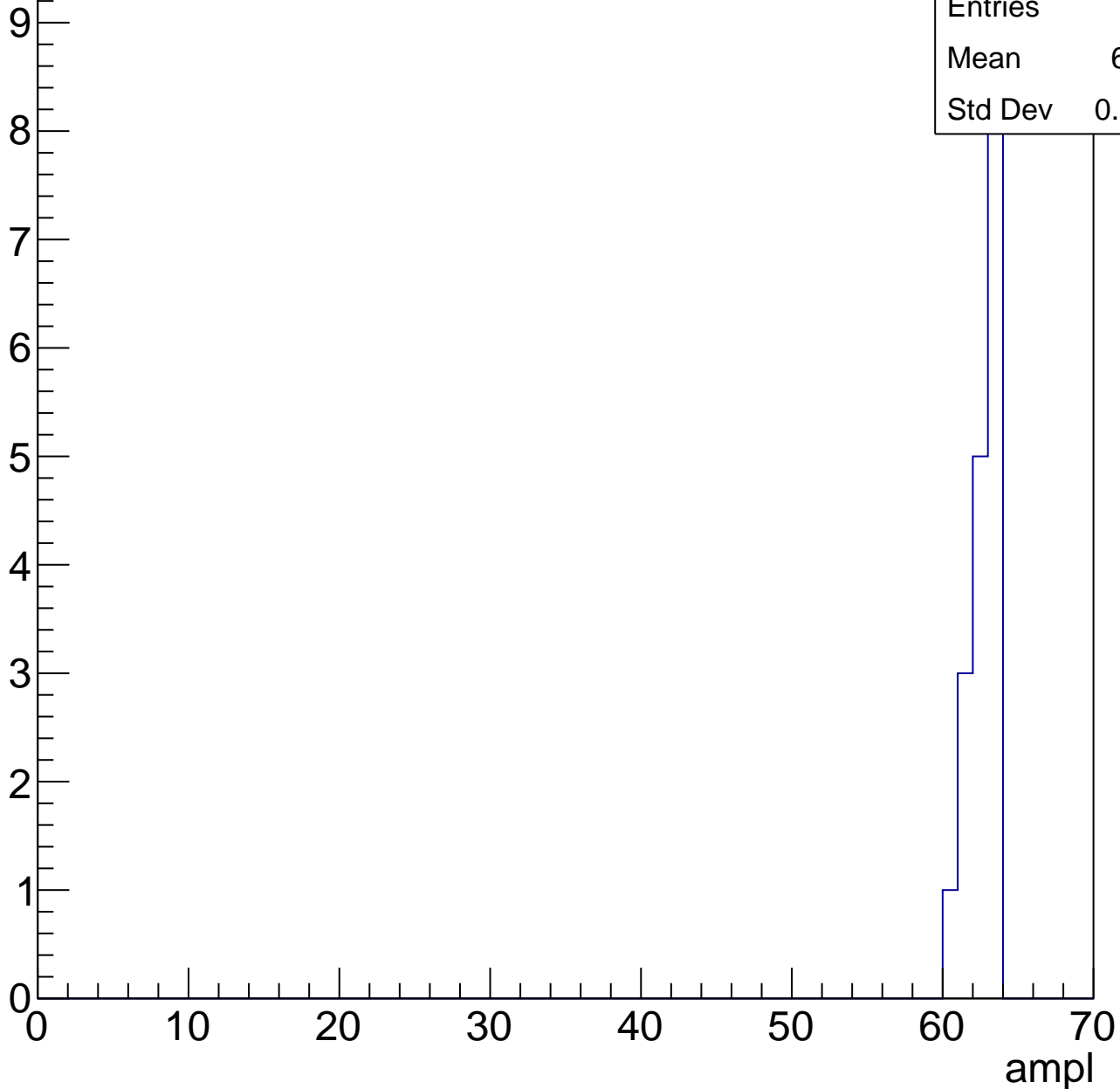


B1L001S, U24-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

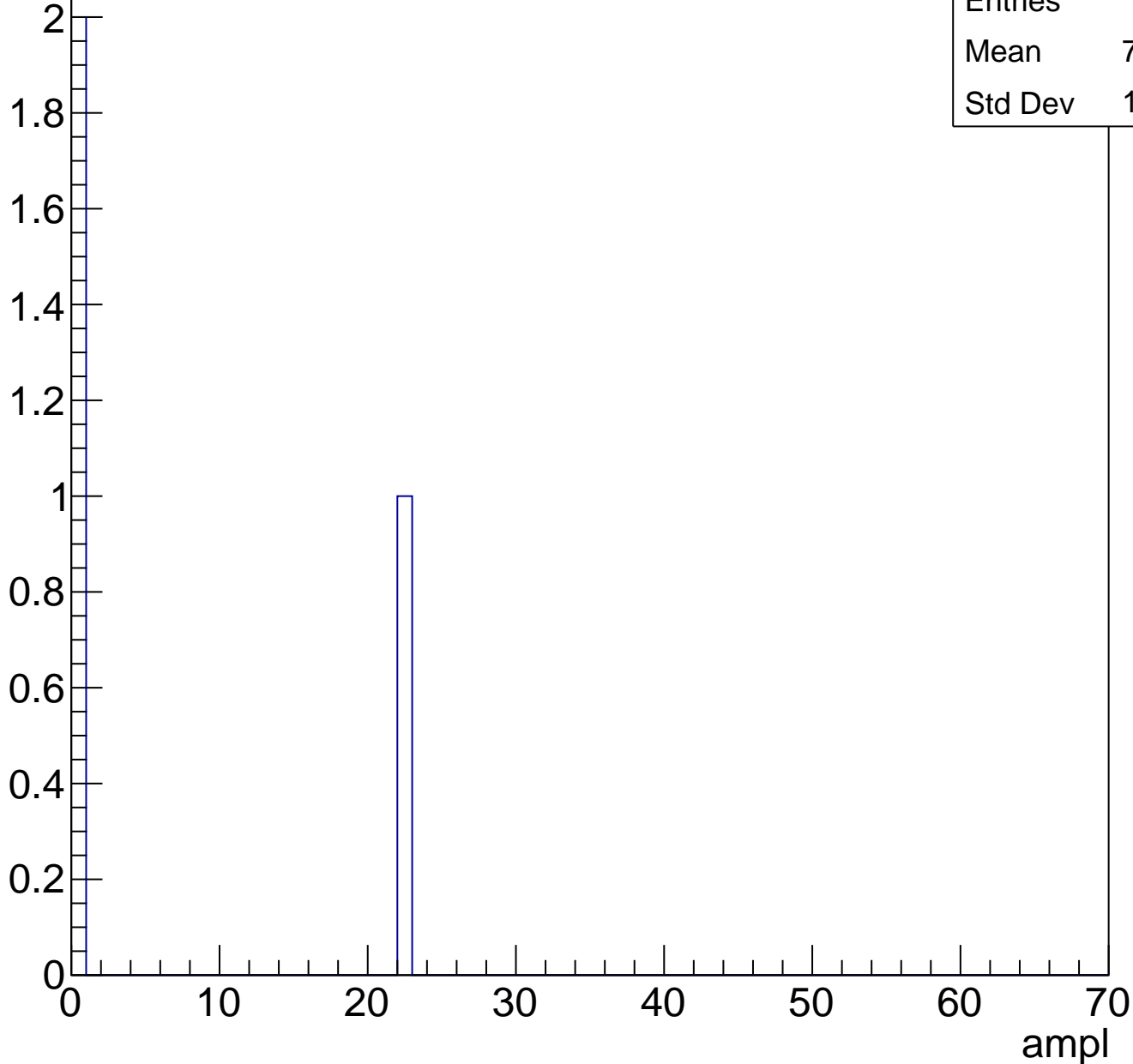
Entries	18
Mean	62.22
Std Dev	0.9162



B1L001S, U24-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch51, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

147

Mean

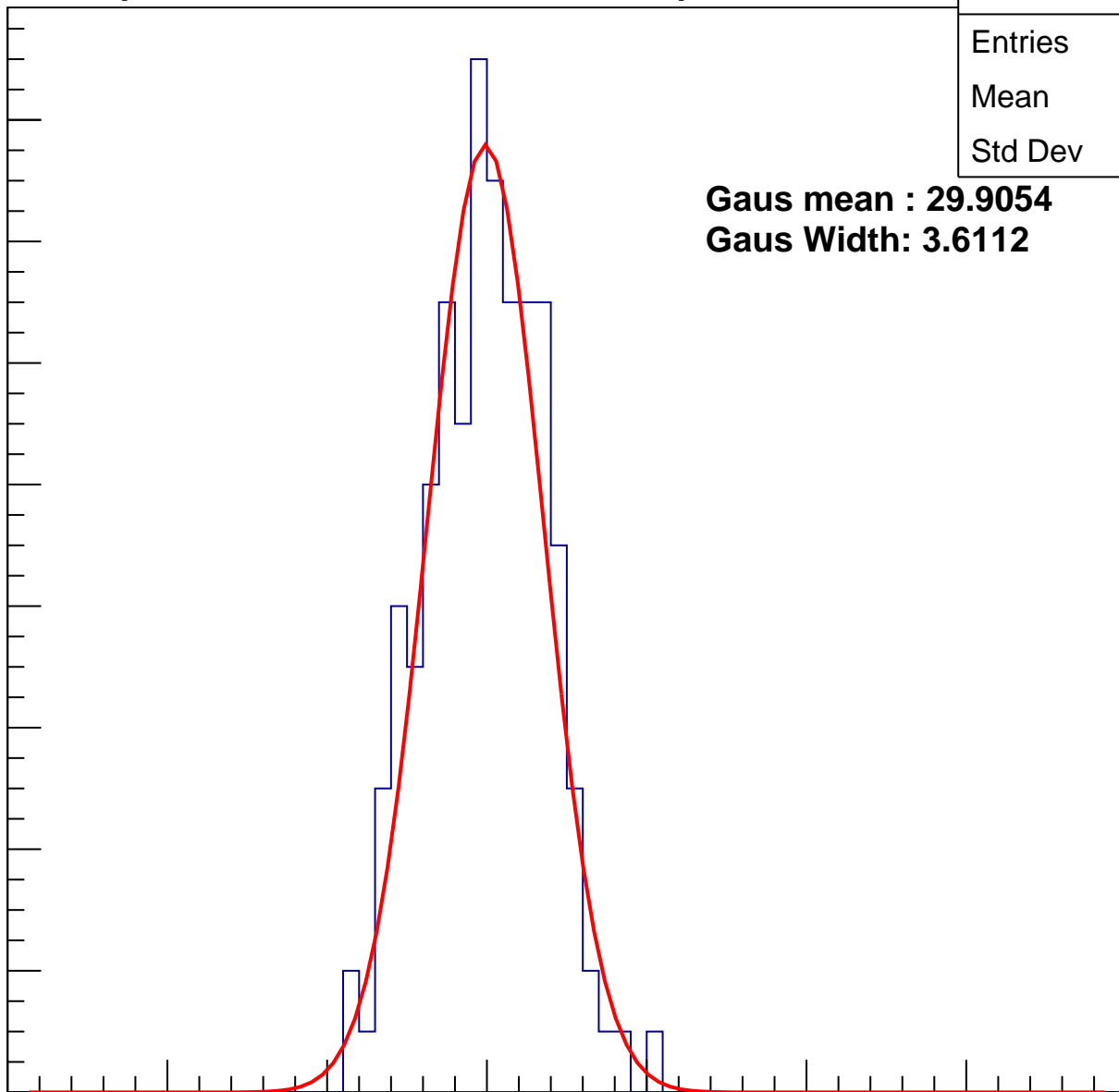
29.41

Std Dev

3.649

Gaus mean : 29.9054

Gaus Width: 3.6112

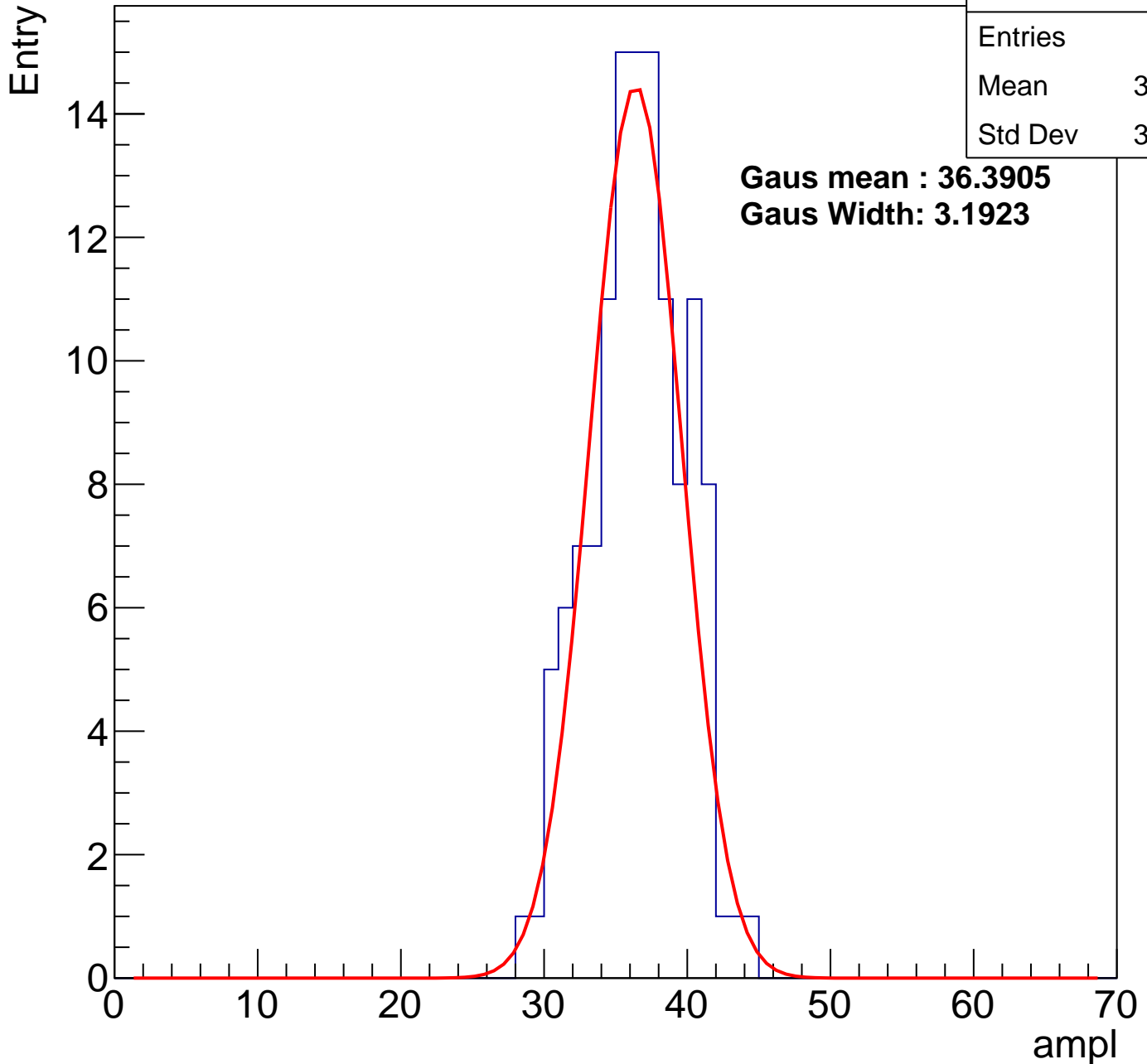


B1L001S, U24-ch51, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.04
Std Dev	3.278

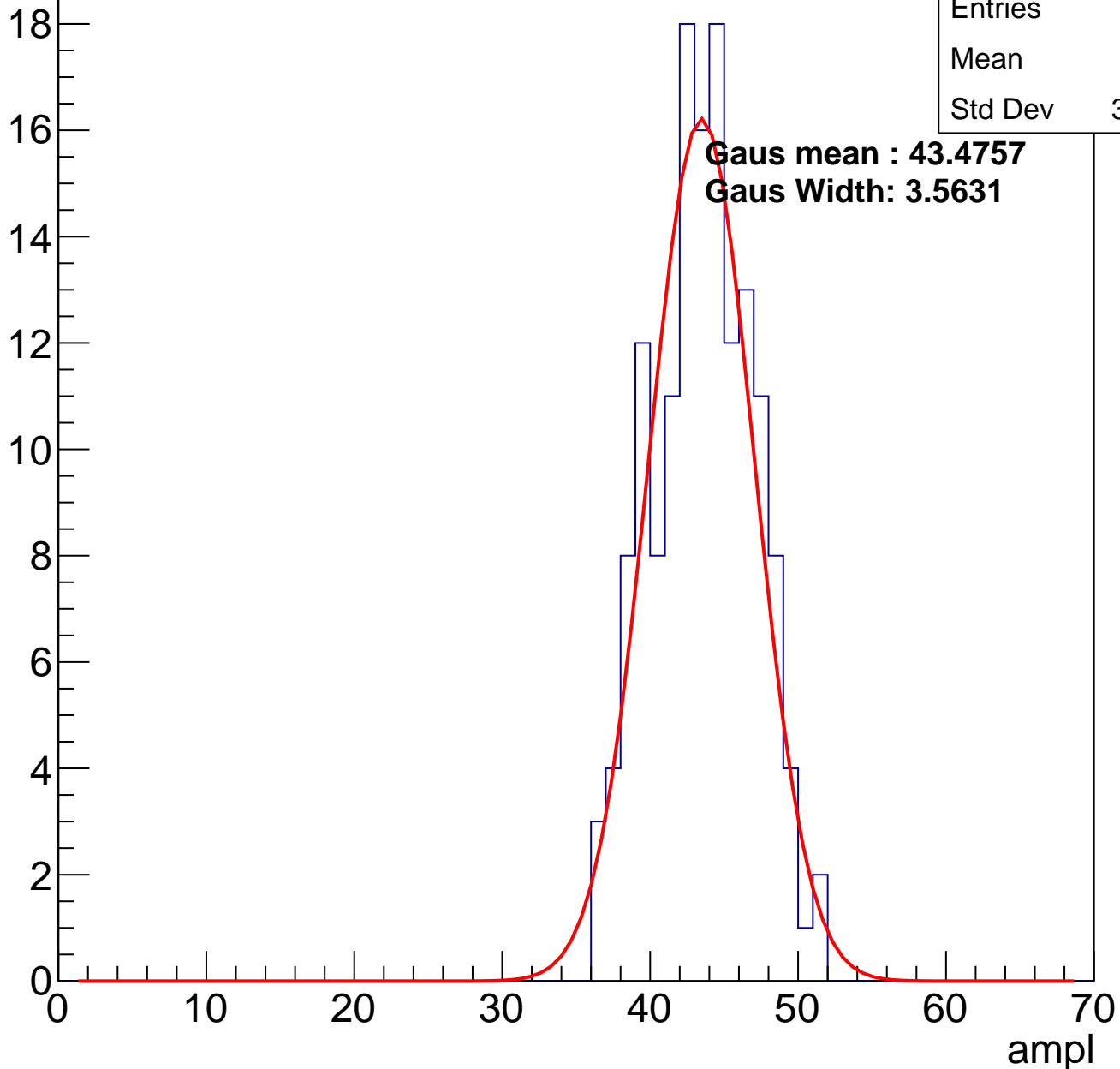
Gaus mean : 36.3905
Gaus Width: 3.1923



B1L001S, U24-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

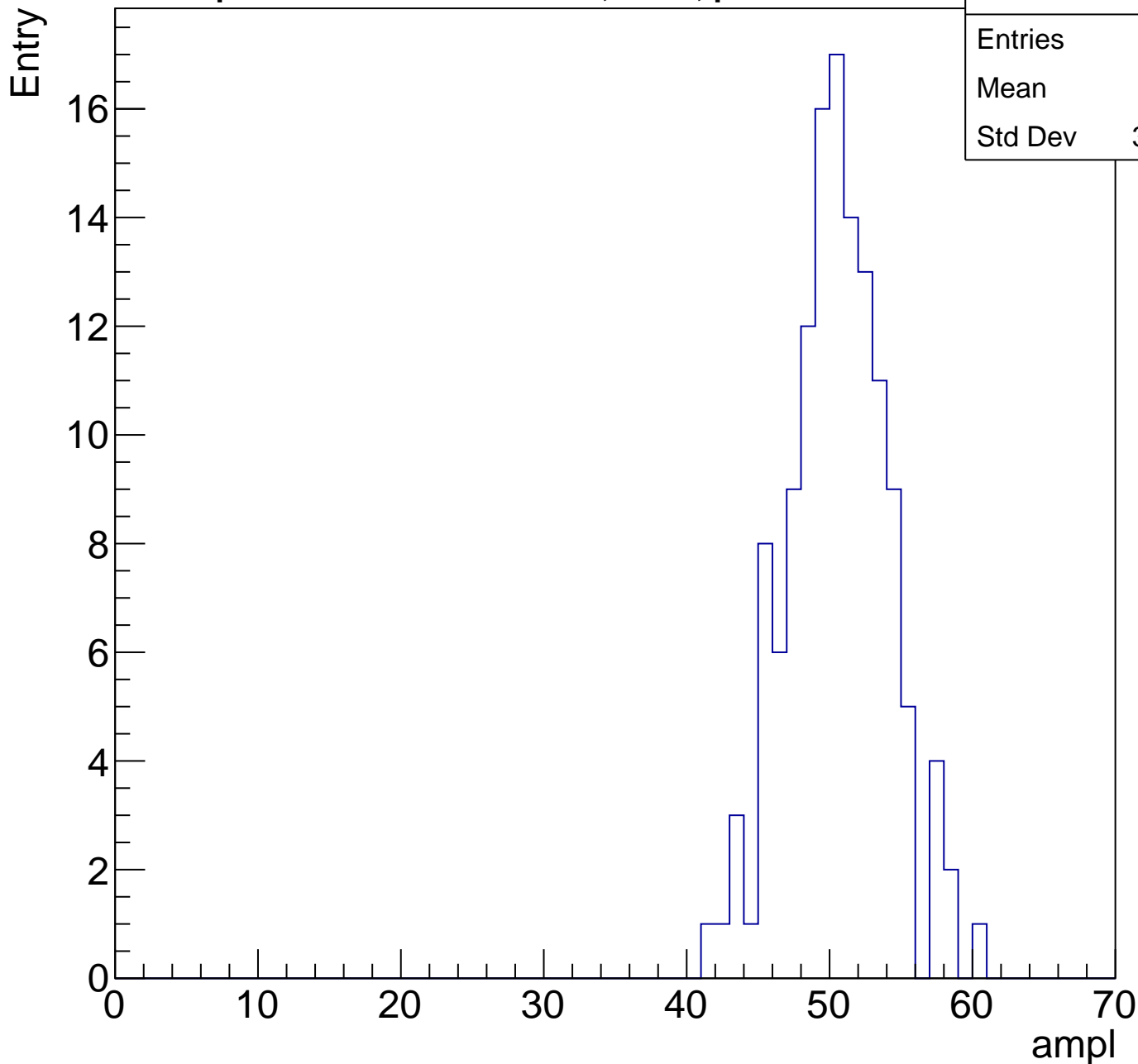
Entry



B1L001S, U24-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

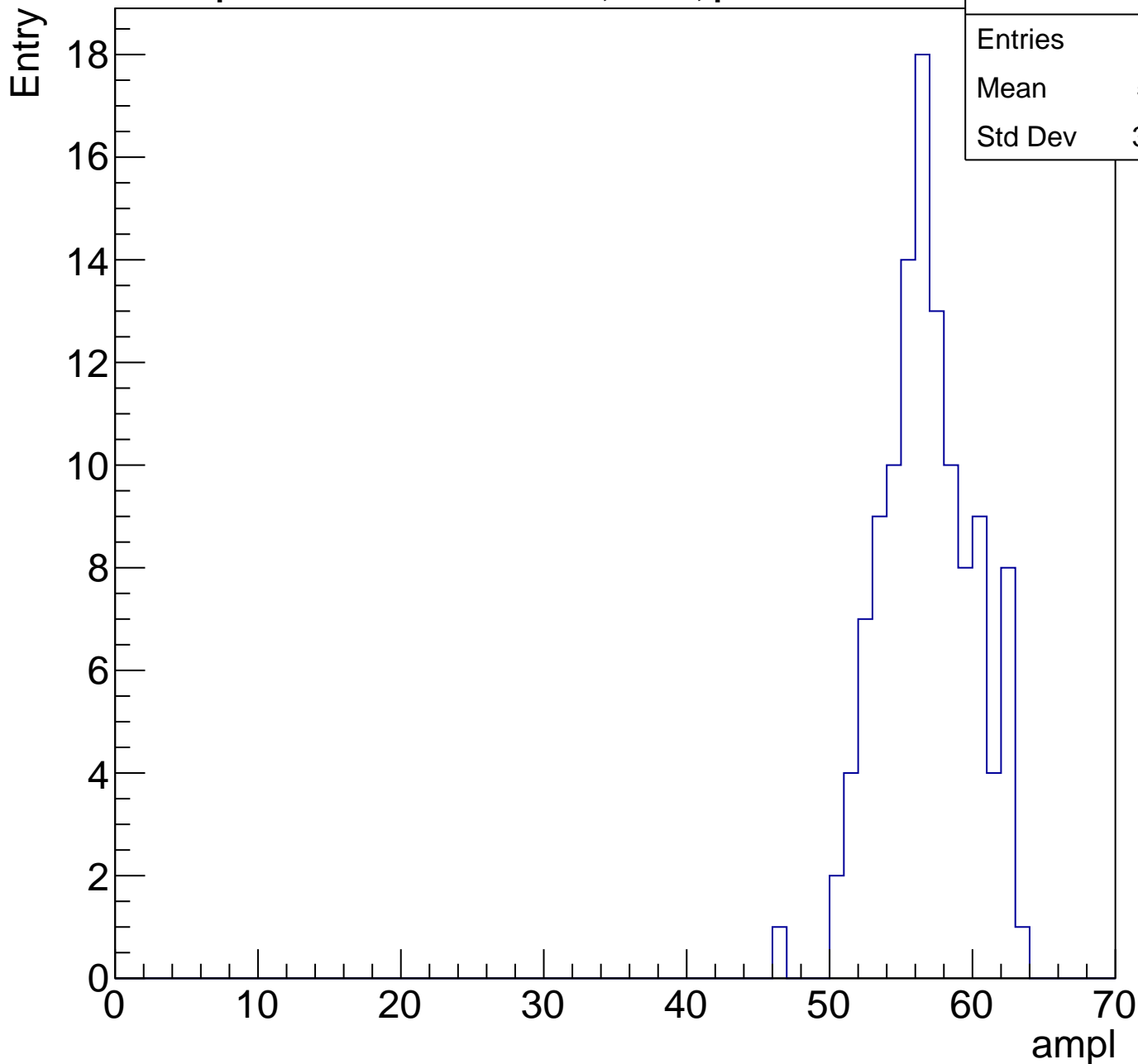
Entries	133
Mean	50.1
Std Dev	3.496



B1L001S, U24-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	56.31
Std Dev	3.207



B1L001S, U24-ch51, adc5

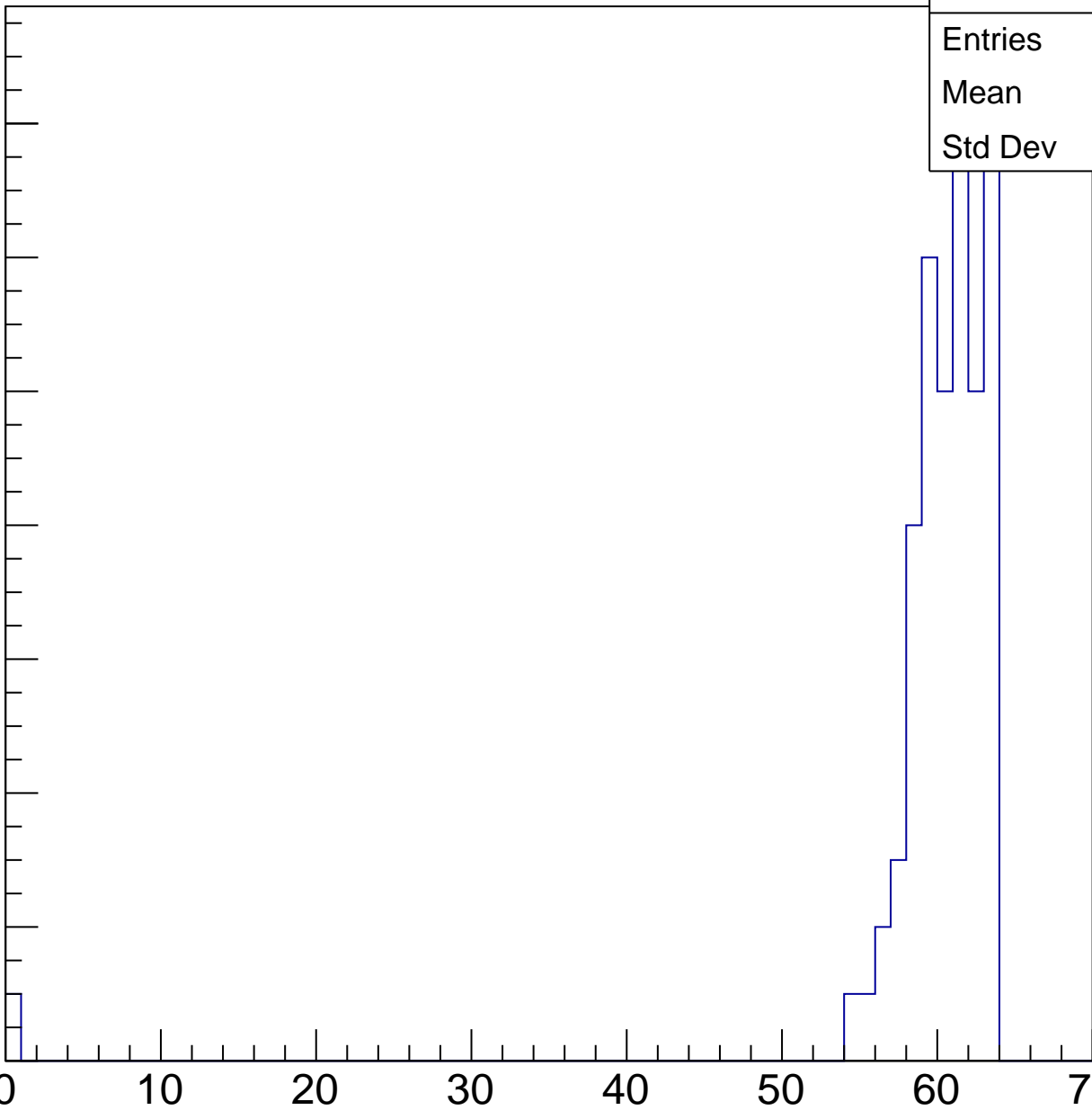
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.52
Std Dev	7.149

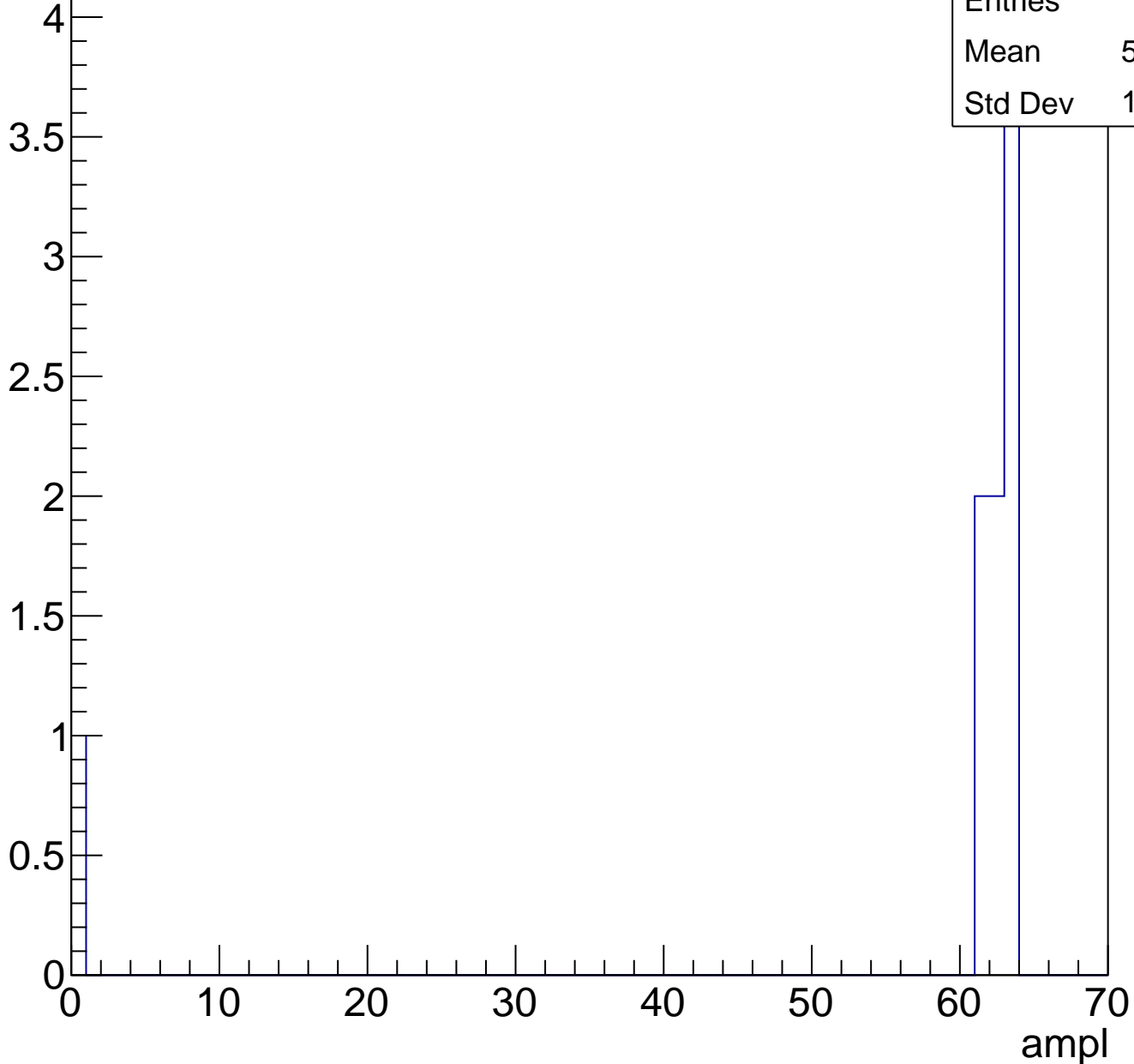
ampl



B1L001S, U24-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	27.86
Std Dev	4.219

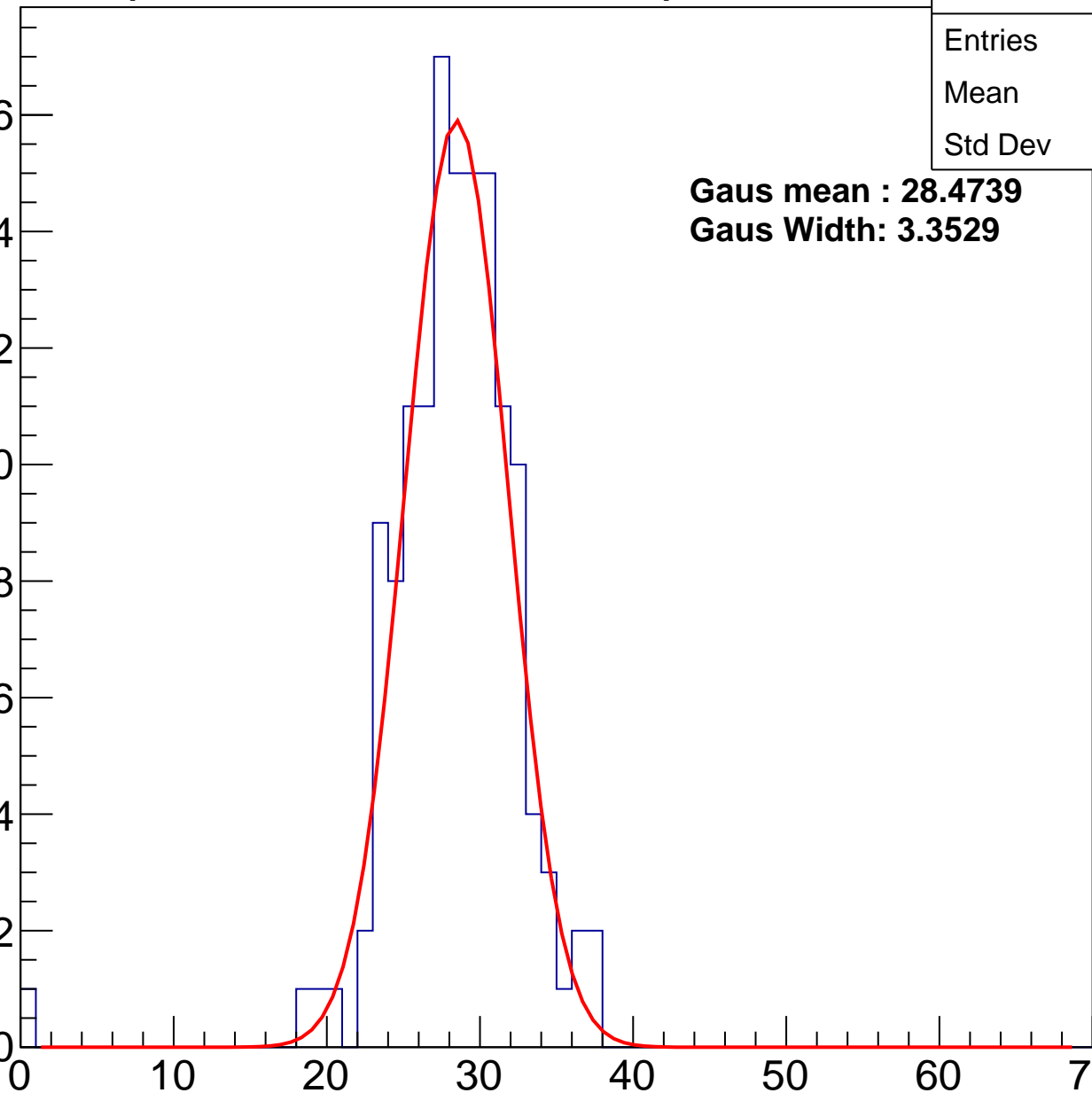
Gaus mean : 28.4739

Gaus Width: 3.3529

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch52, adc1

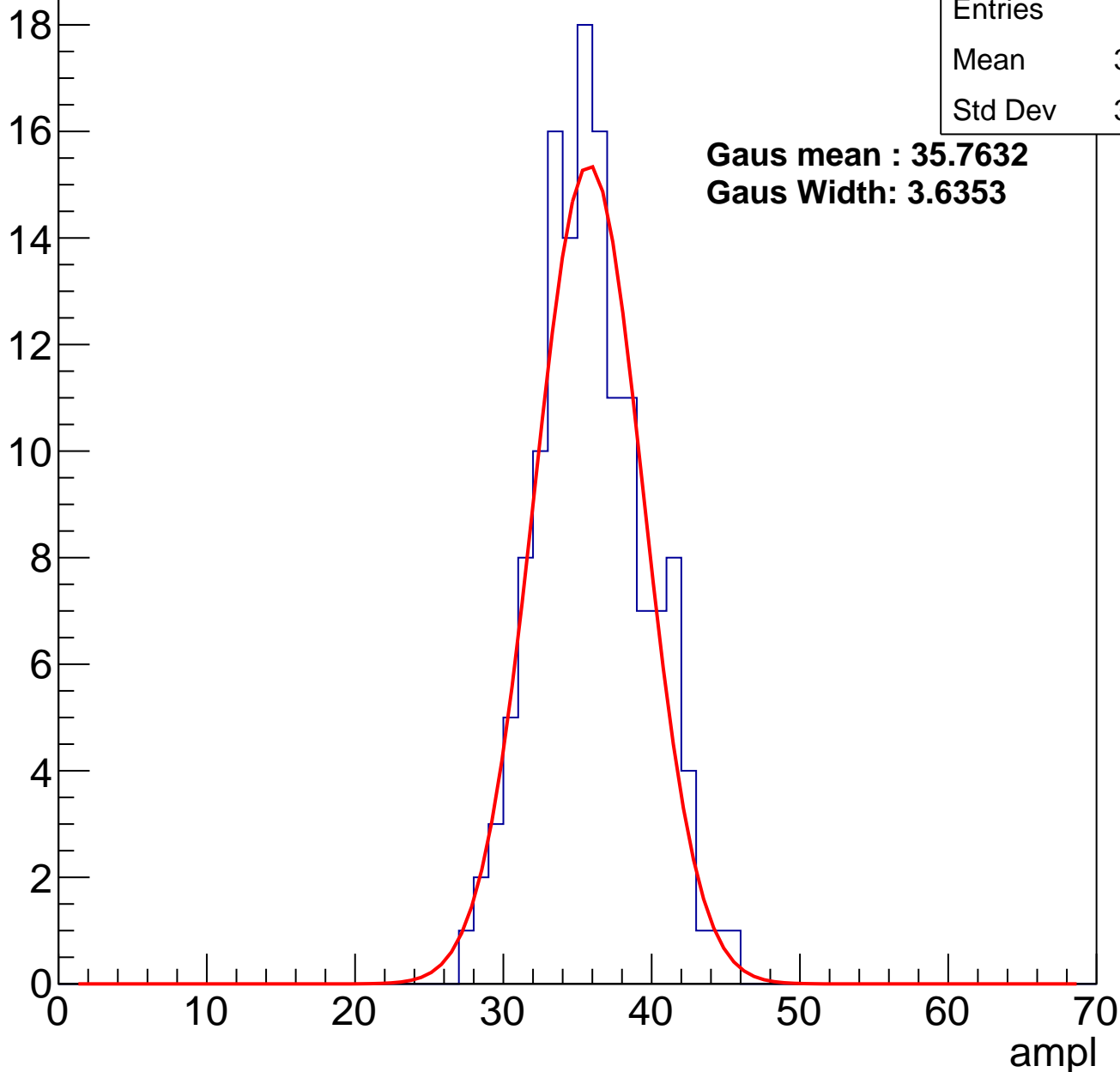
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	35.44
Std Dev	3.572

Gaus mean : 35.7632

Gaus Width: 3.6353

Entry



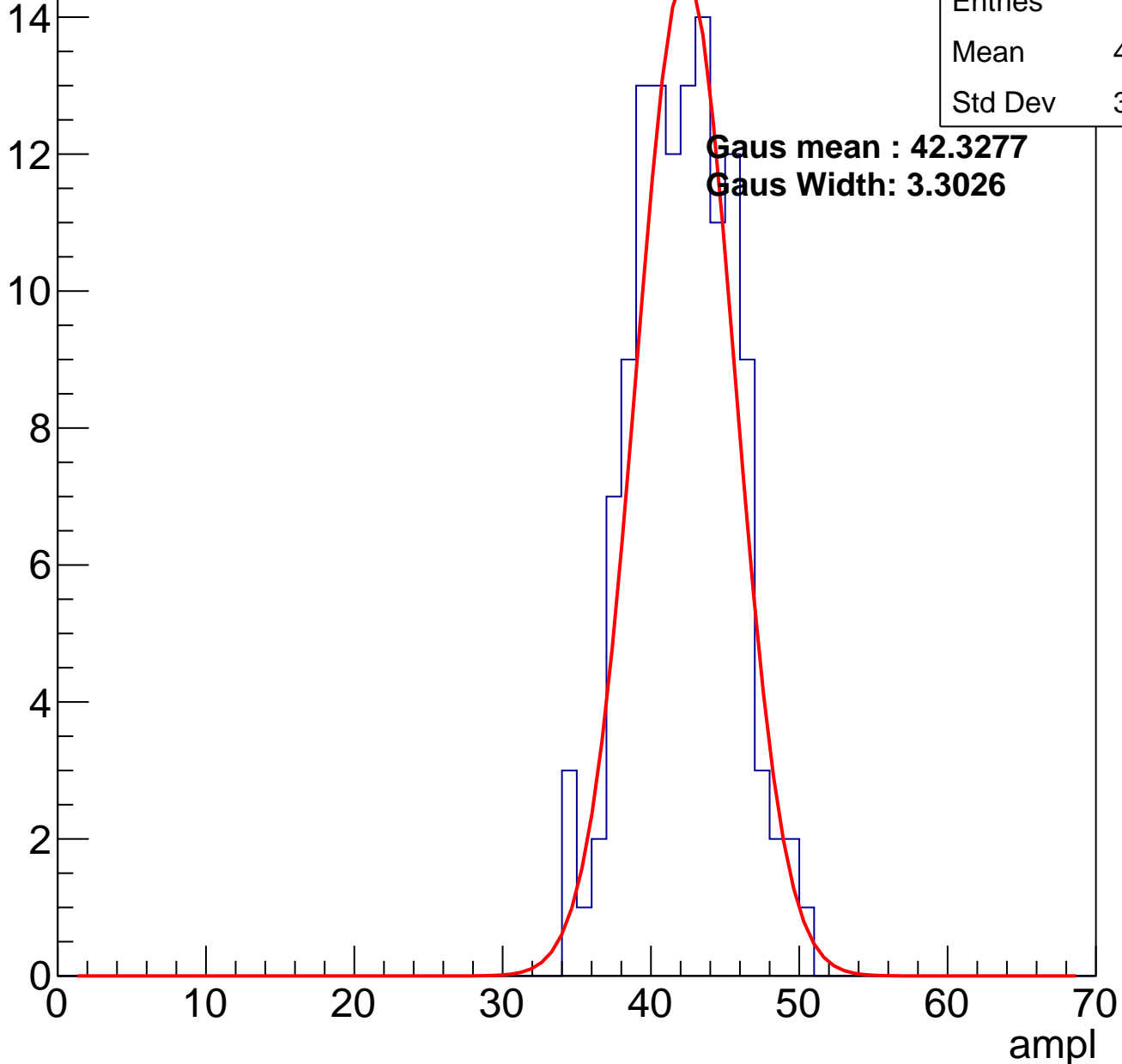
B1L001S, U24-ch52, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	41.73
Std Dev	3.347

Gaus mean : 42.3277
Gaus Width: 3.3026

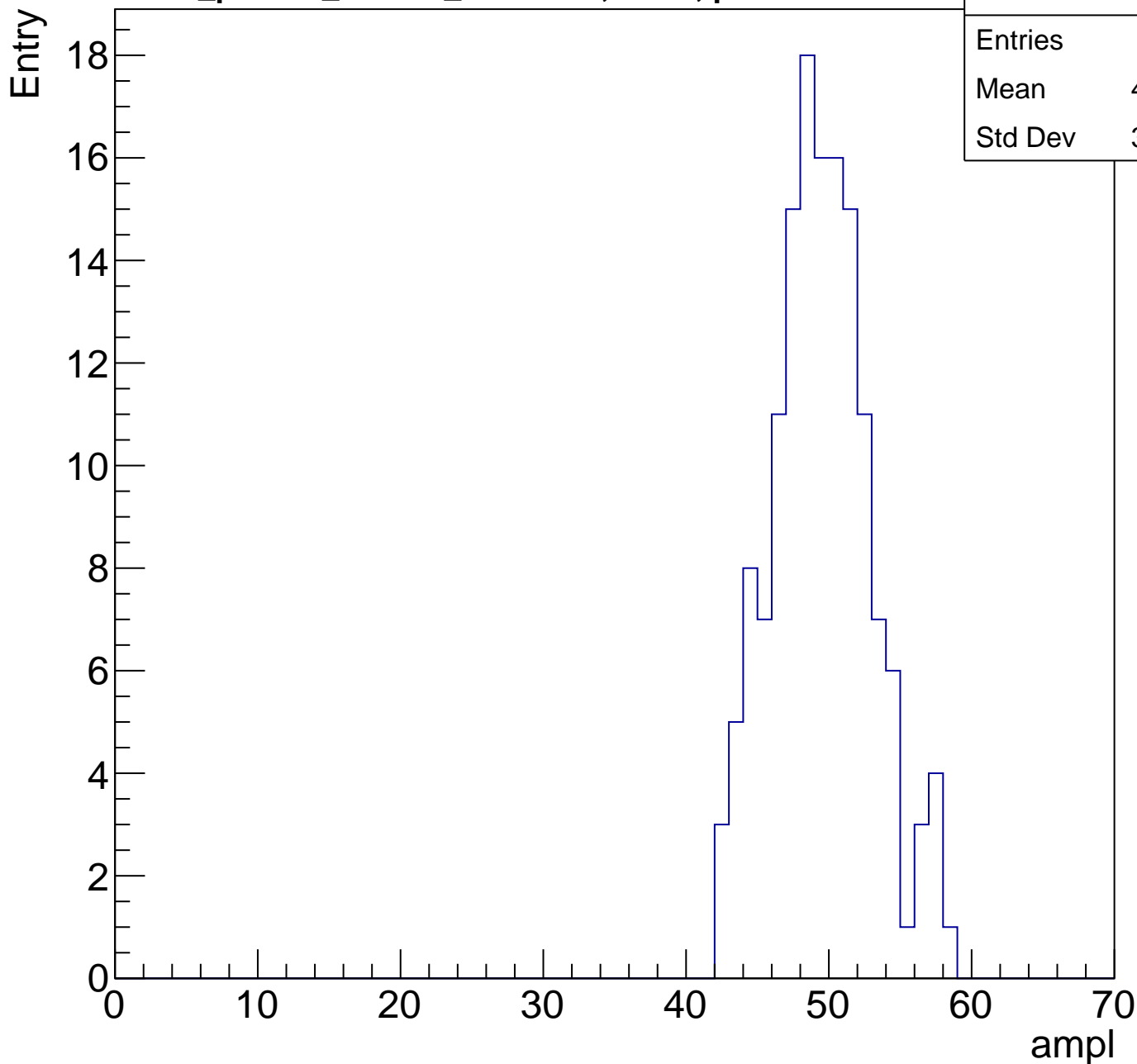
Entry



B1L001S, U24-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	49.03
Std Dev	3.473



B1L001S, U24-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

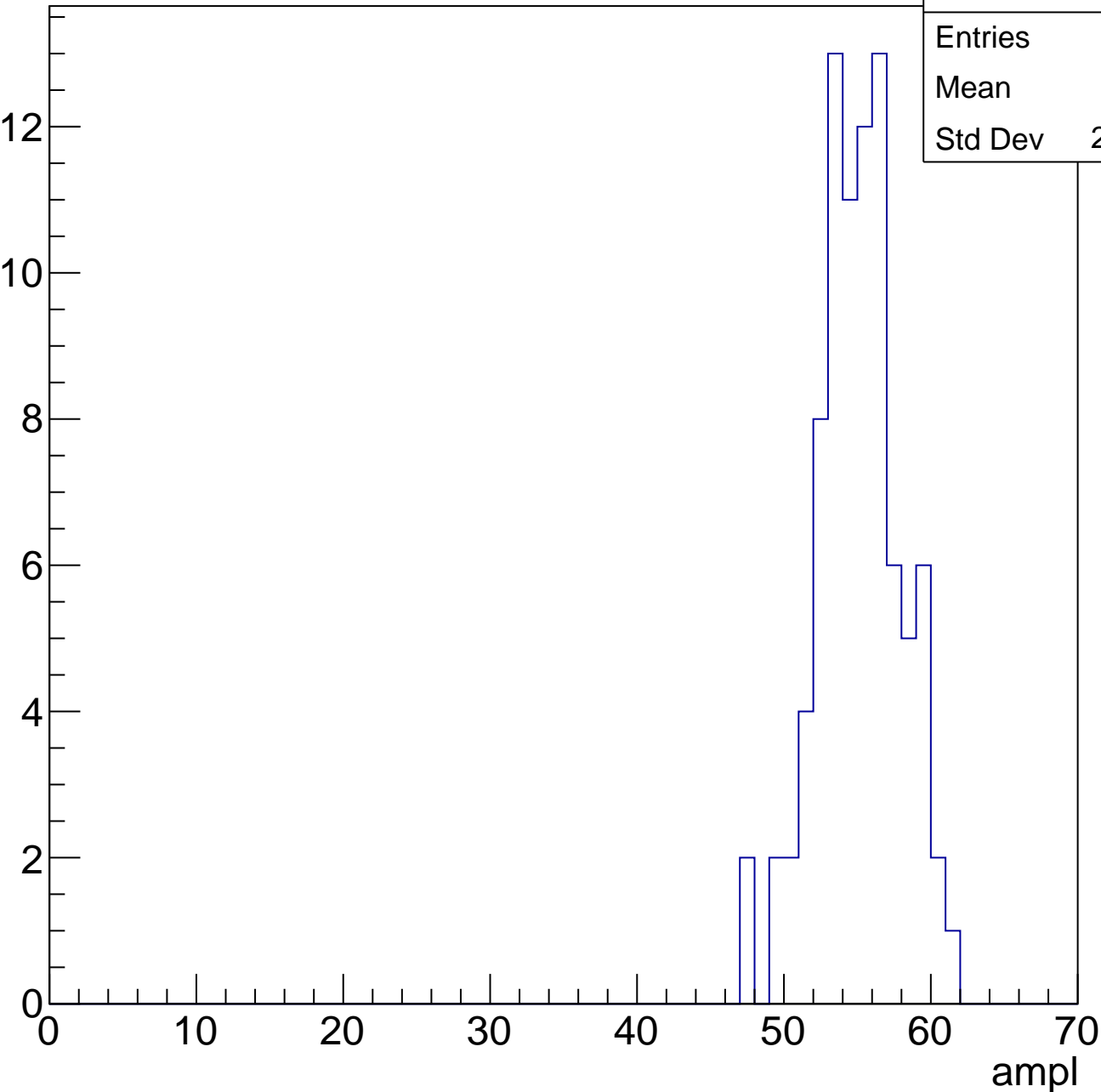
40

50

60

ampl

Entries	87
Mean	54.6
Std Dev	2.834



B1L001S, U24-ch52, adc5

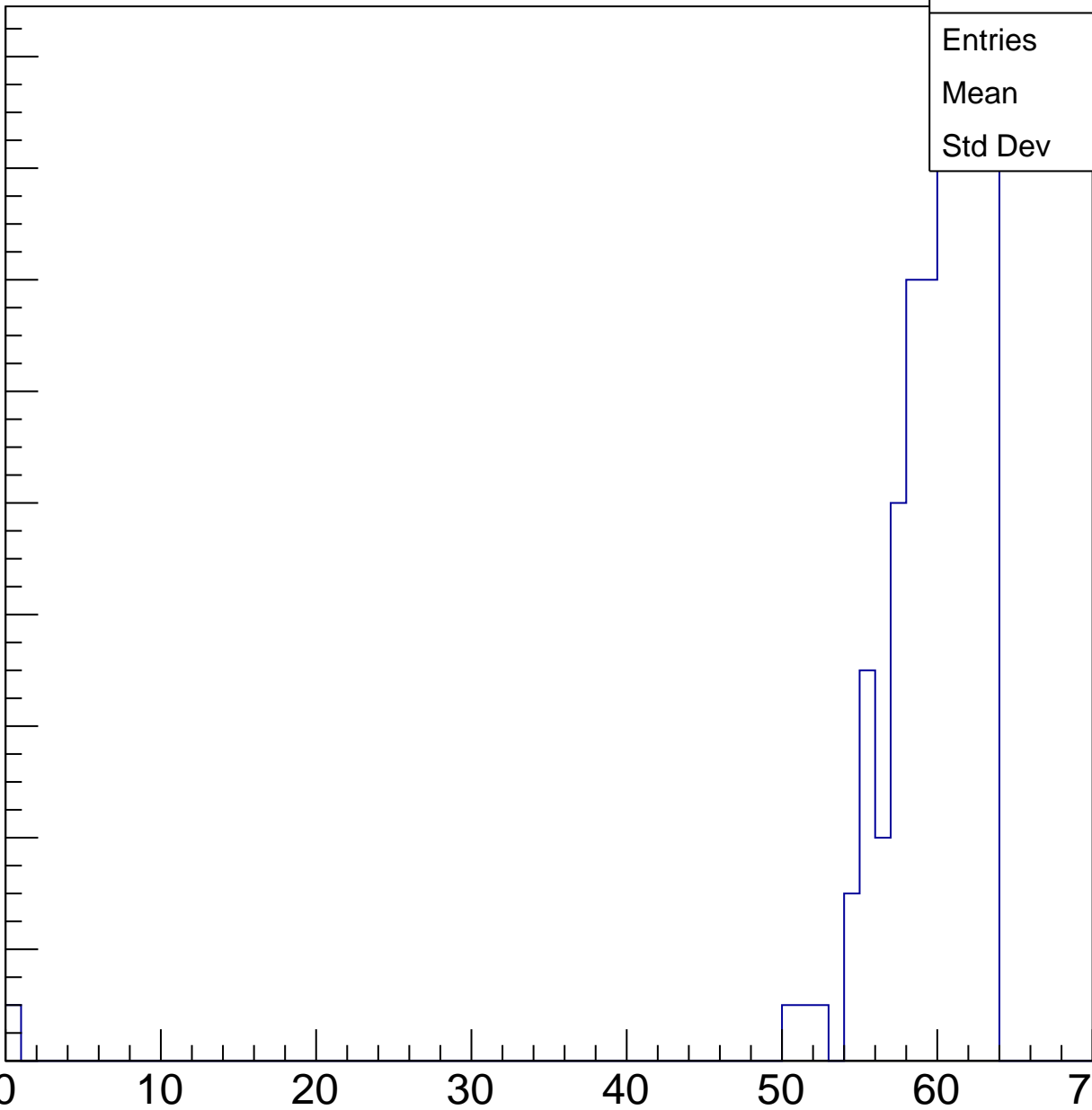
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.98
Std Dev	5.994

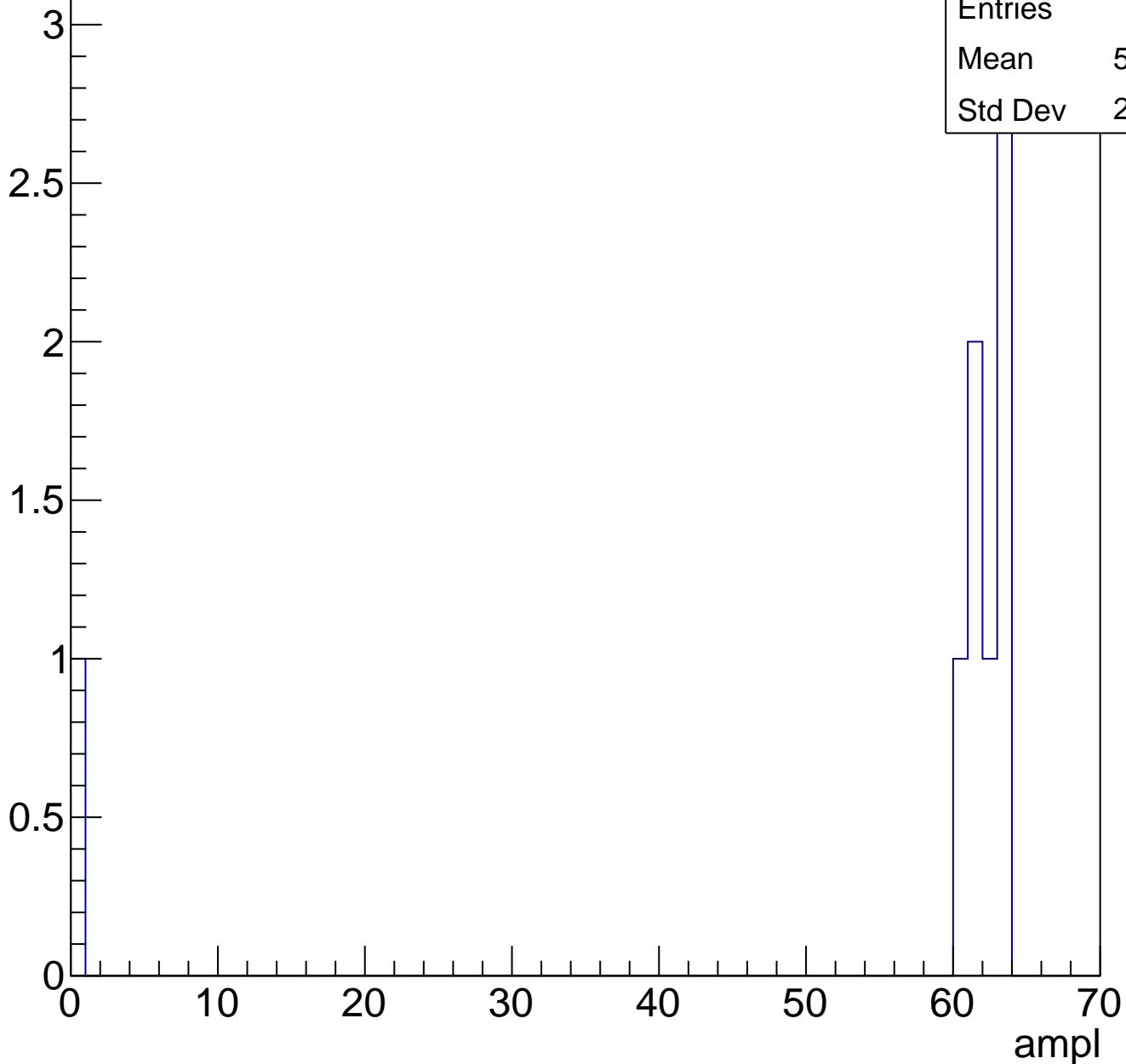
ampl



B1L001S, U24-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	3
Mean	48.67
Std Dev	19.57

B1L001S, U24-ch53, adc0

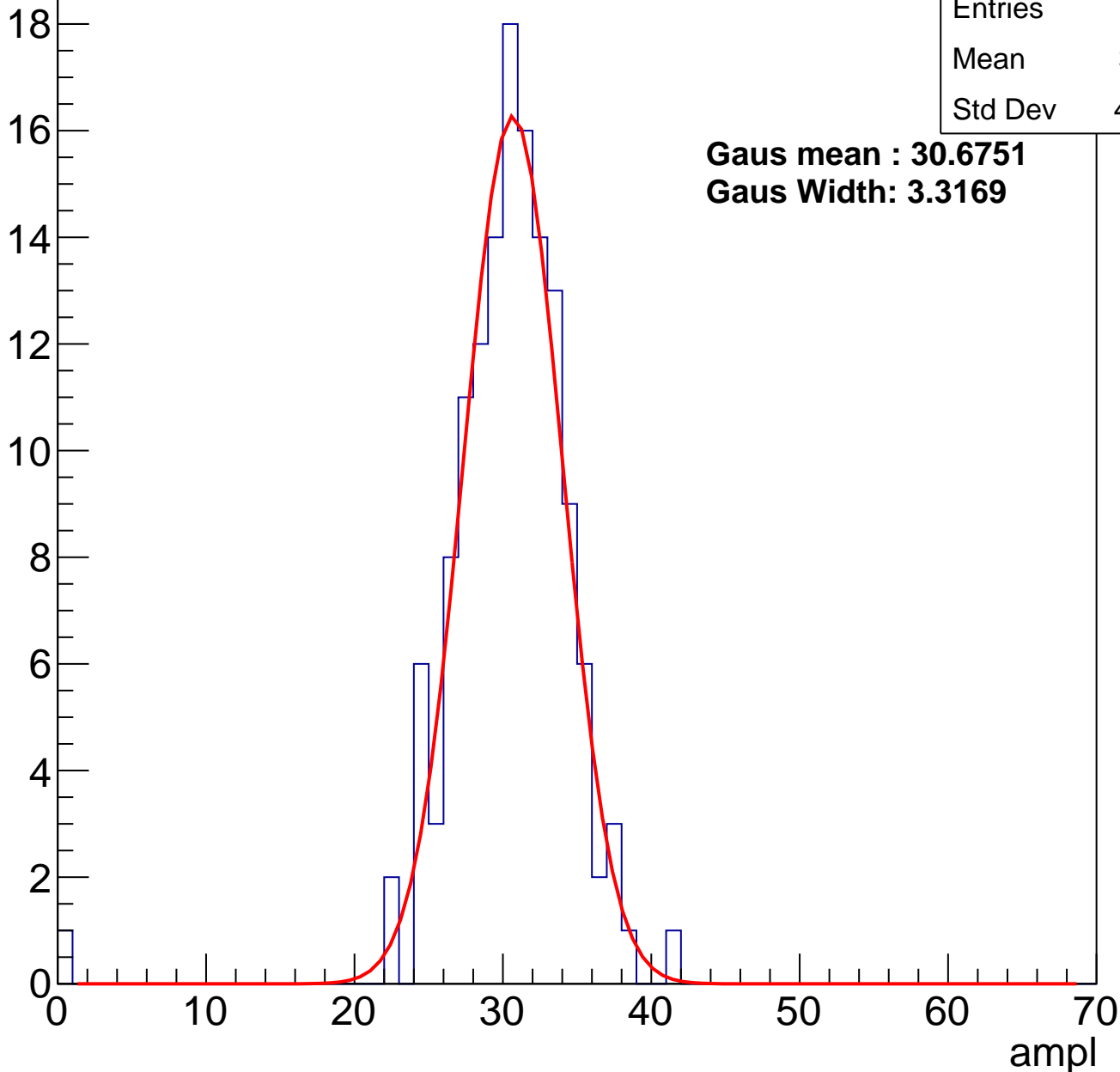
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.01
Std Dev	4.222

Gaus mean : 30.6751

Gaus Width: 3.3169

Entry



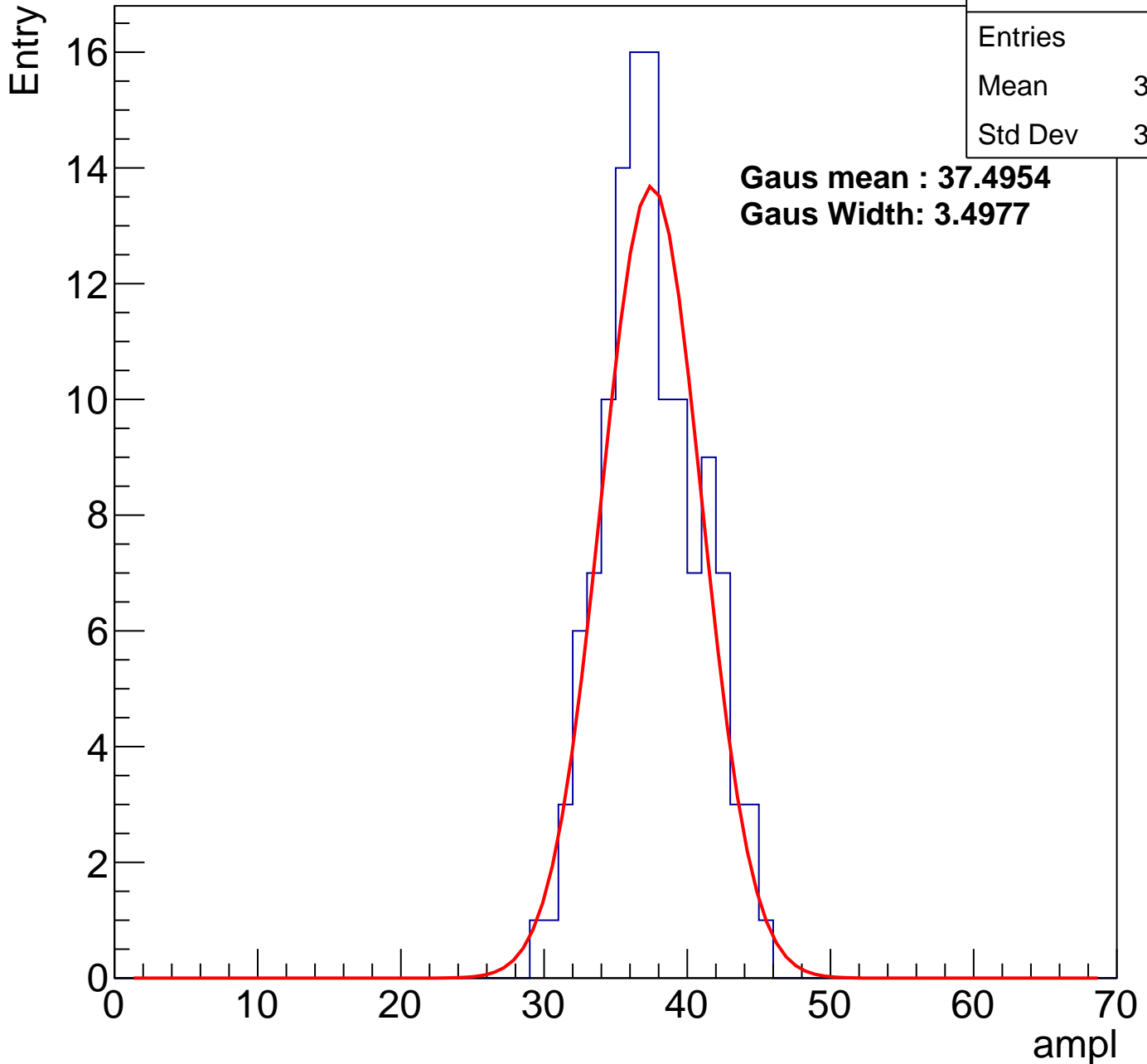
B1L001S, U24-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	37.03
Std Dev	3.355

Gaus mean : 37.4954

Gaus Width: 3.4977



B1L001S, U24-ch53, adc2

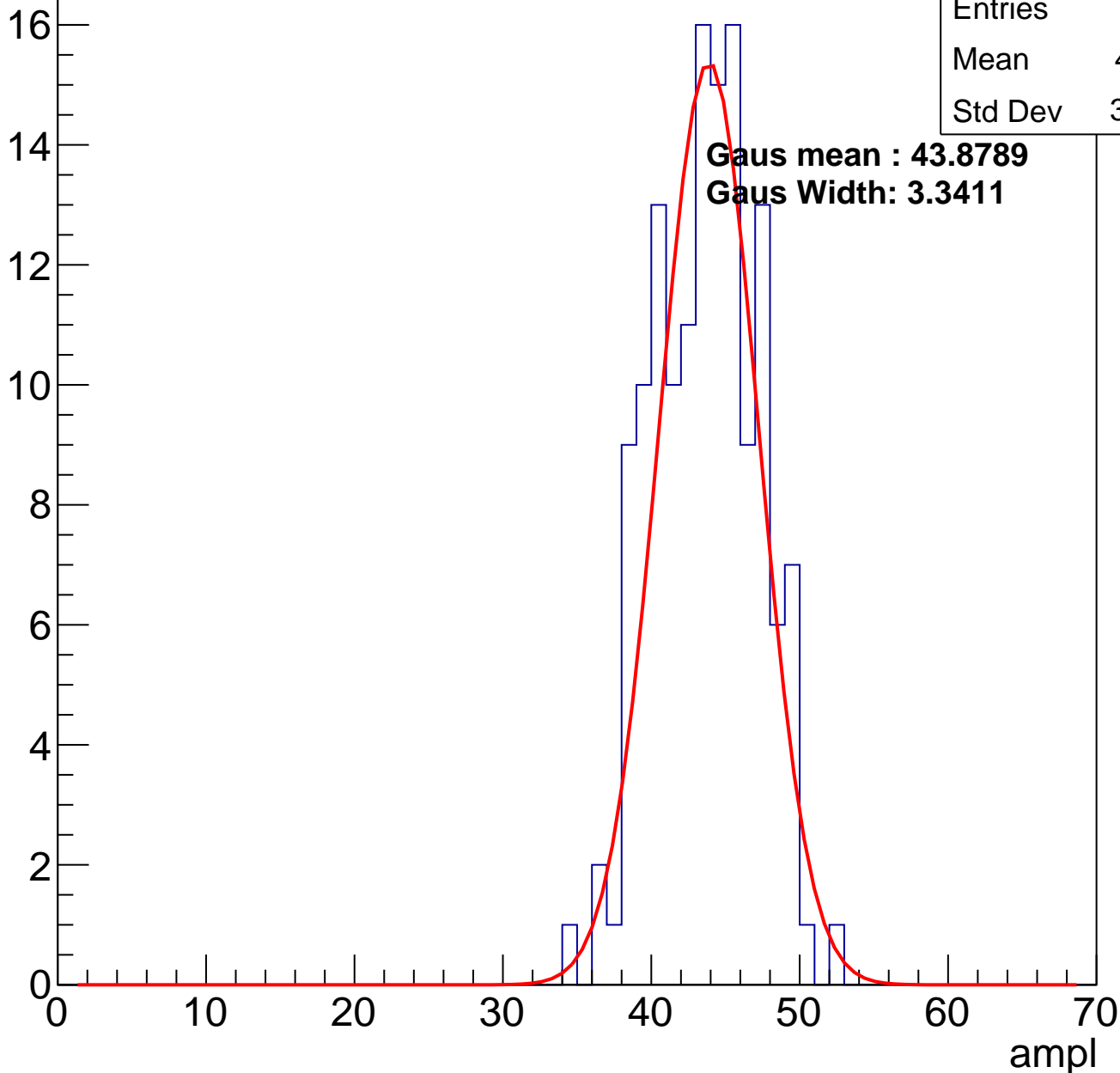
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	43.21
Std Dev	3.433

Gaus mean : 43.8789

Gaus Width: 3.3411

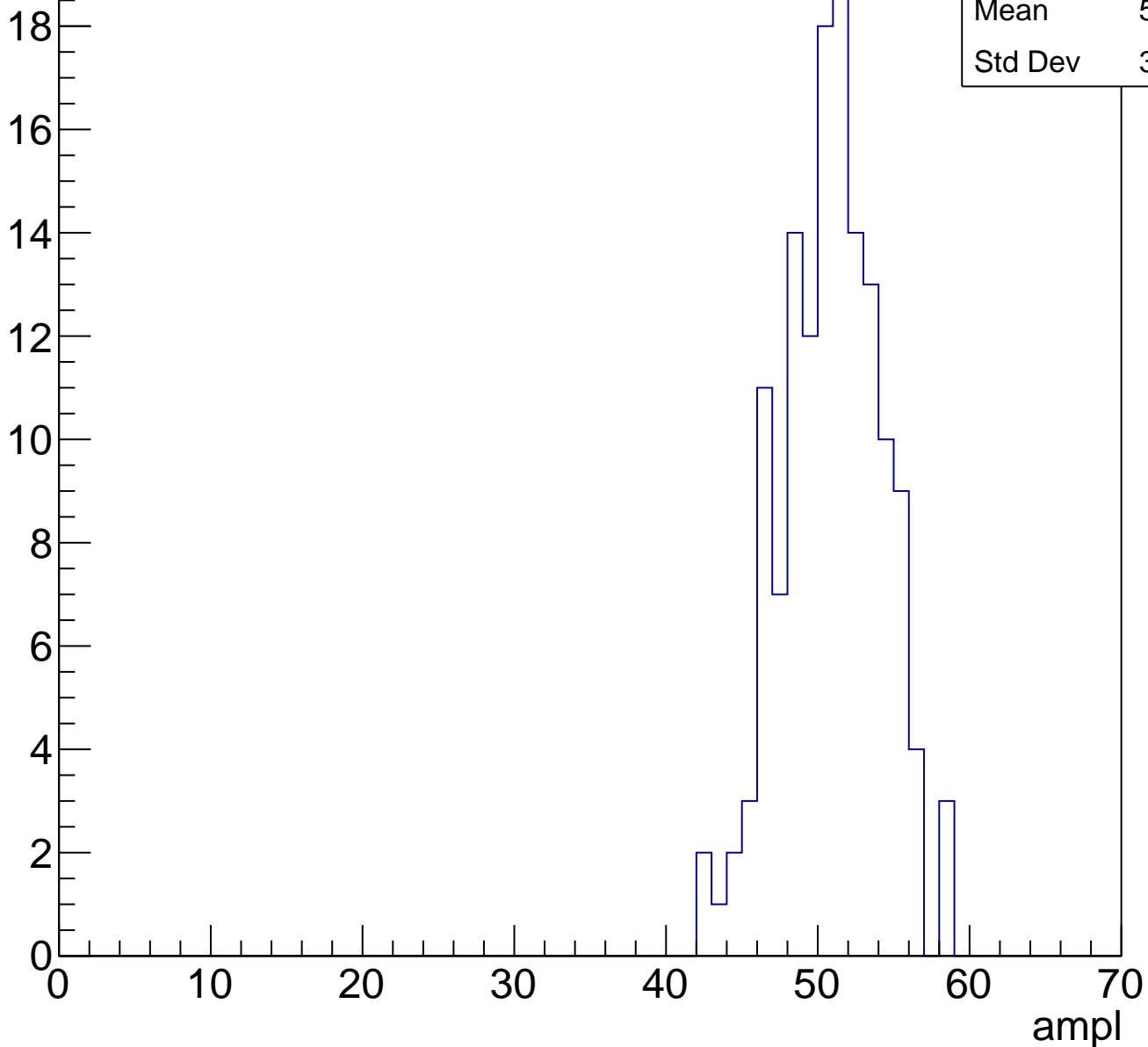
Entry



B1L001S, U24-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

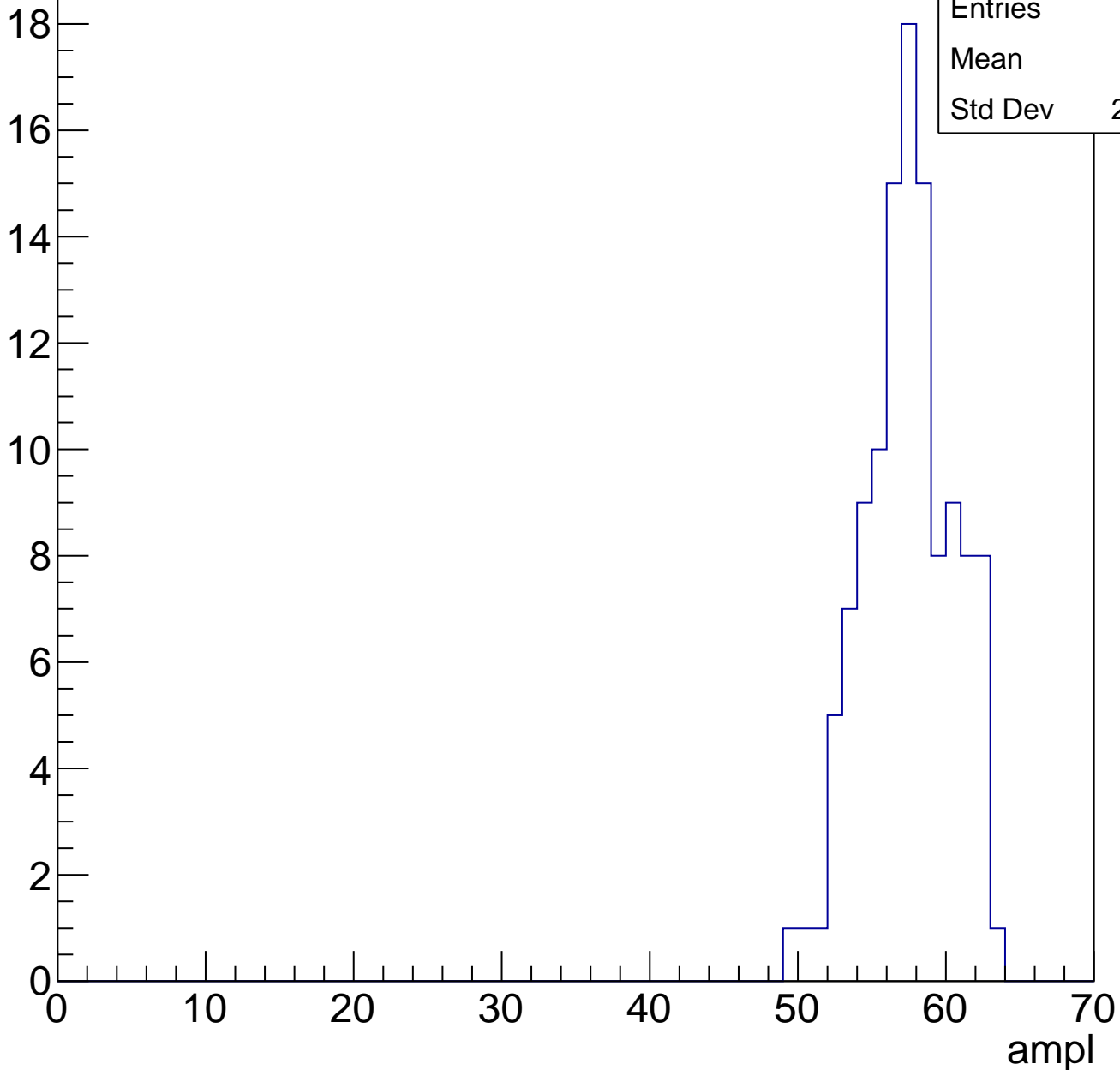


B1L001S, U24-ch53, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	57
Std Dev	2.957

Entry



B1L001S, U24-ch53, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

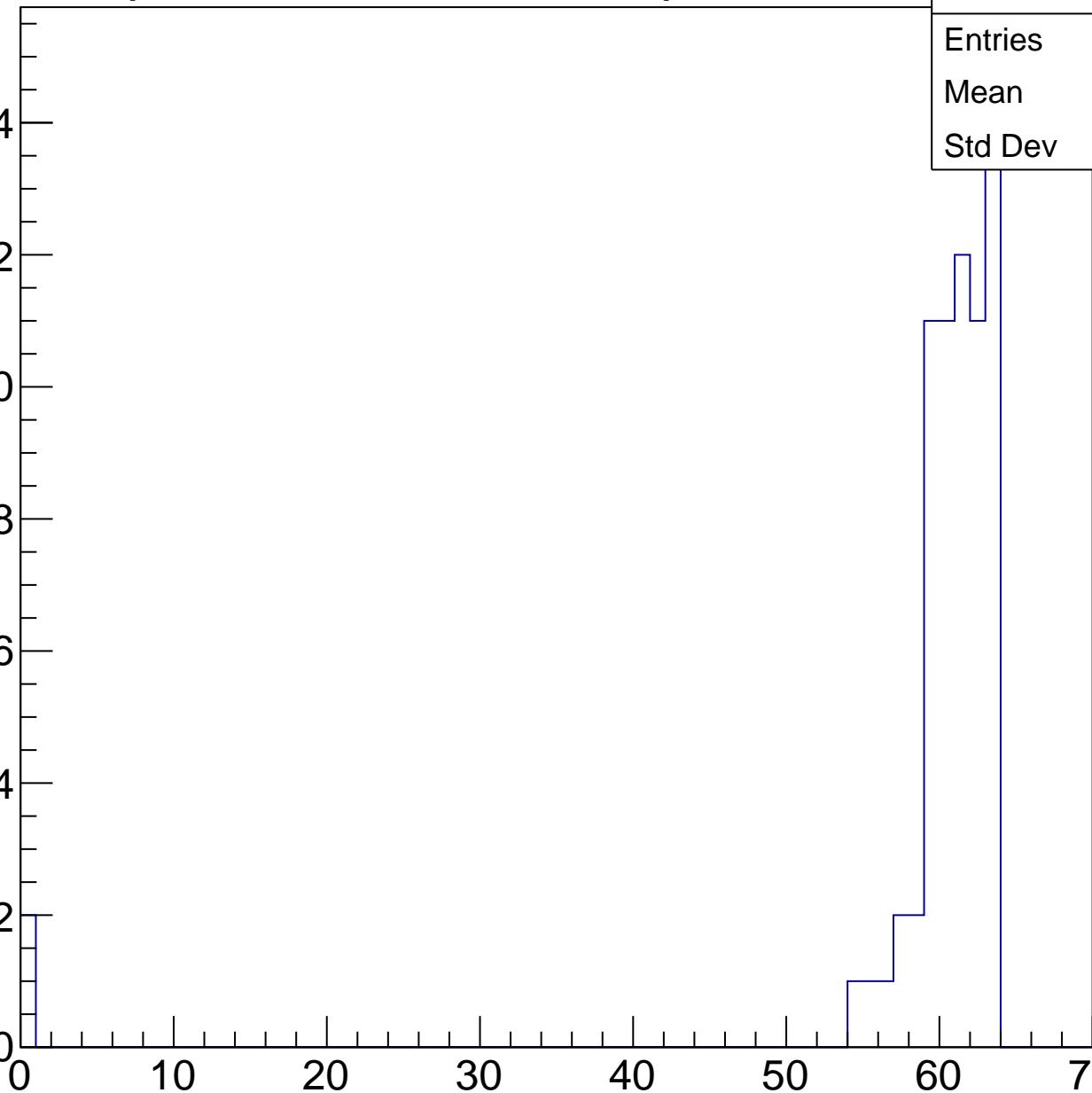
4

2

0

Entries	69
Mean	58.88
Std Dev	10.37

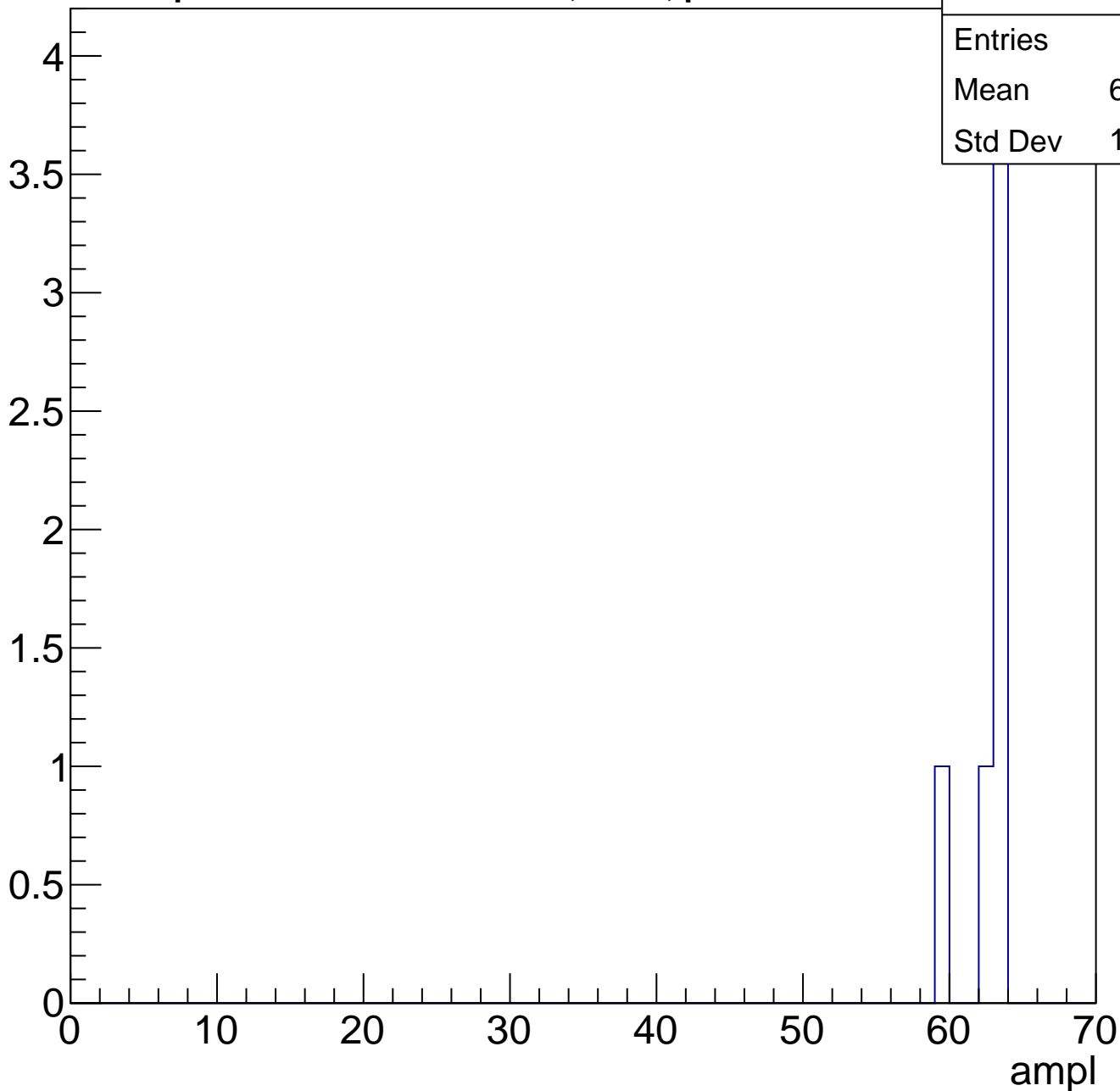
ampl



B1L001S, U24-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch54, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	31.04
Std Dev	3.472

Gaus mean : 31.5260

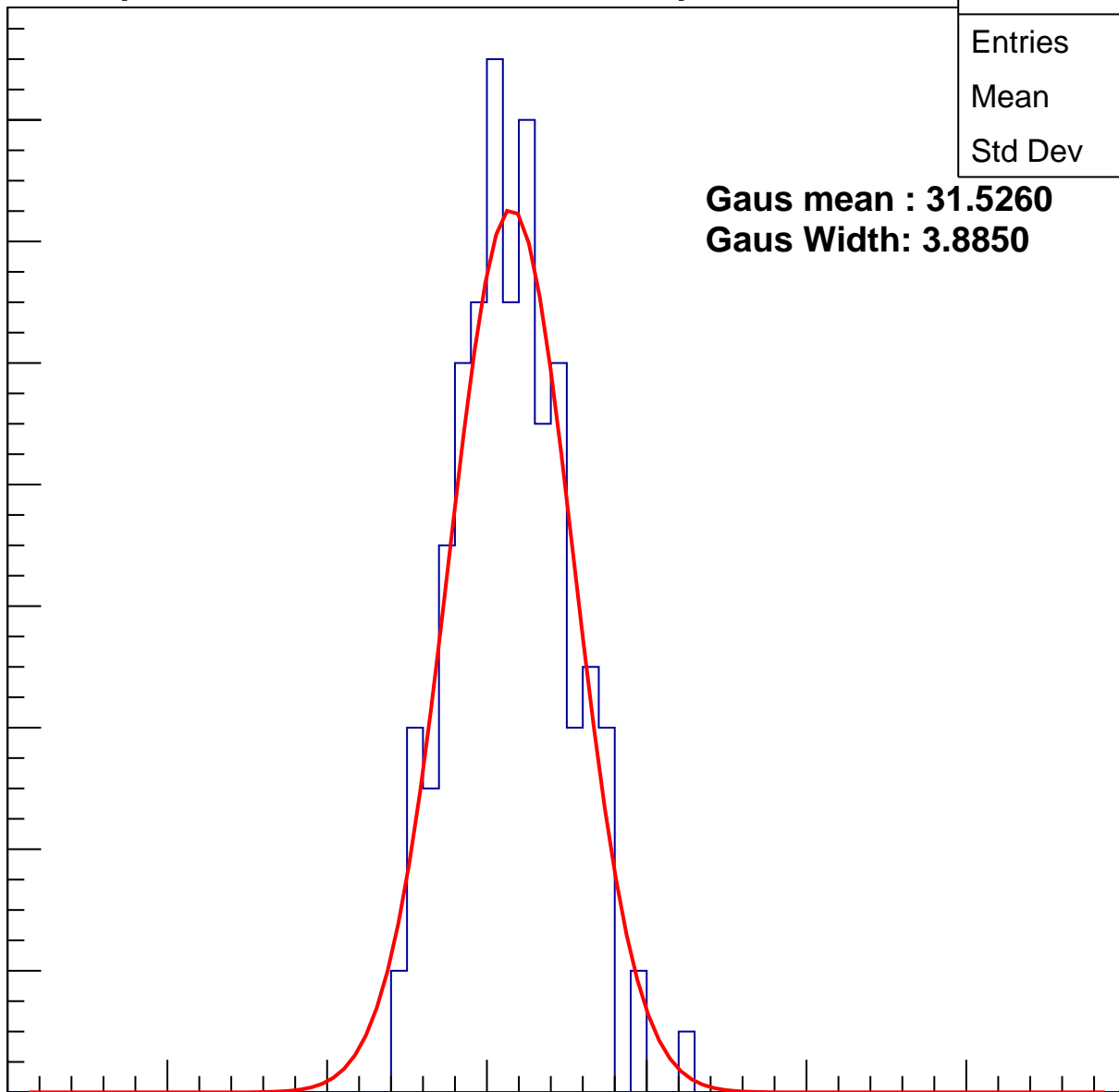
Gaus Width: 3.8850

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

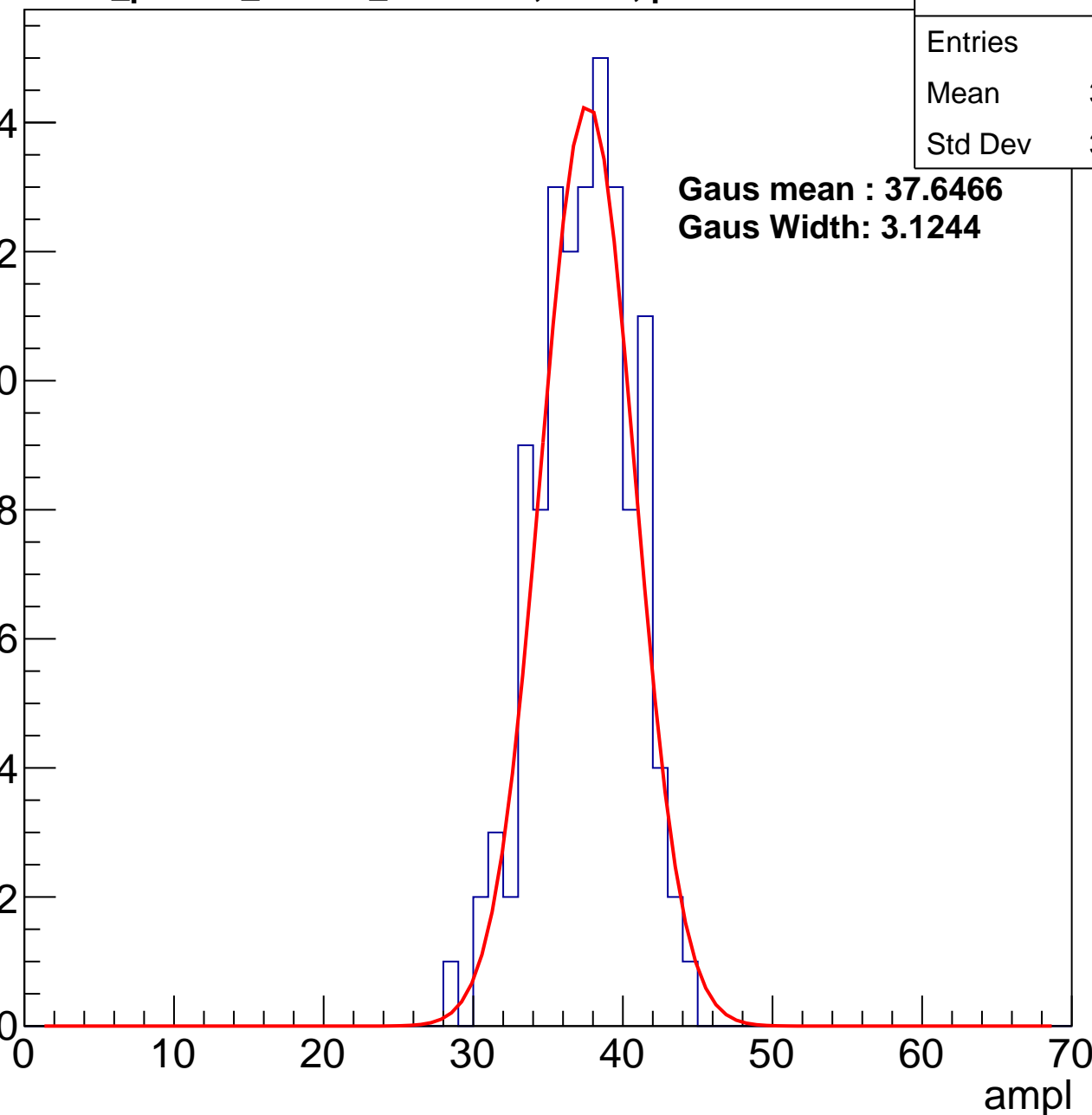
Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	36.99
Std Dev	3.139

Gaus mean : 37.6466

Gaus Width: 3.1244



B1L001S, U24-ch54, adc2

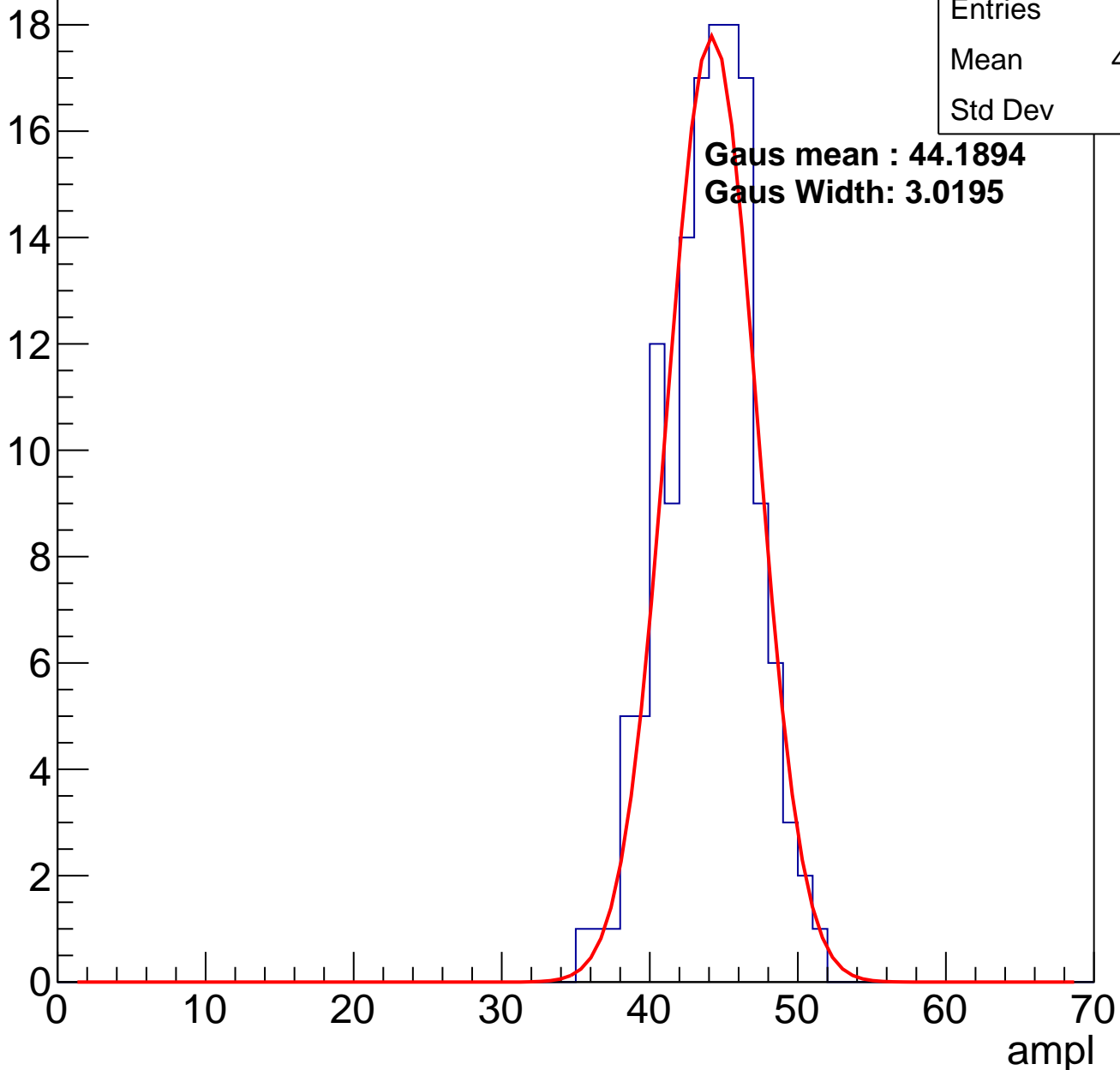
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.55
Std Dev	3.03

Gaus mean : 44.1894

Gaus Width: 3.0195

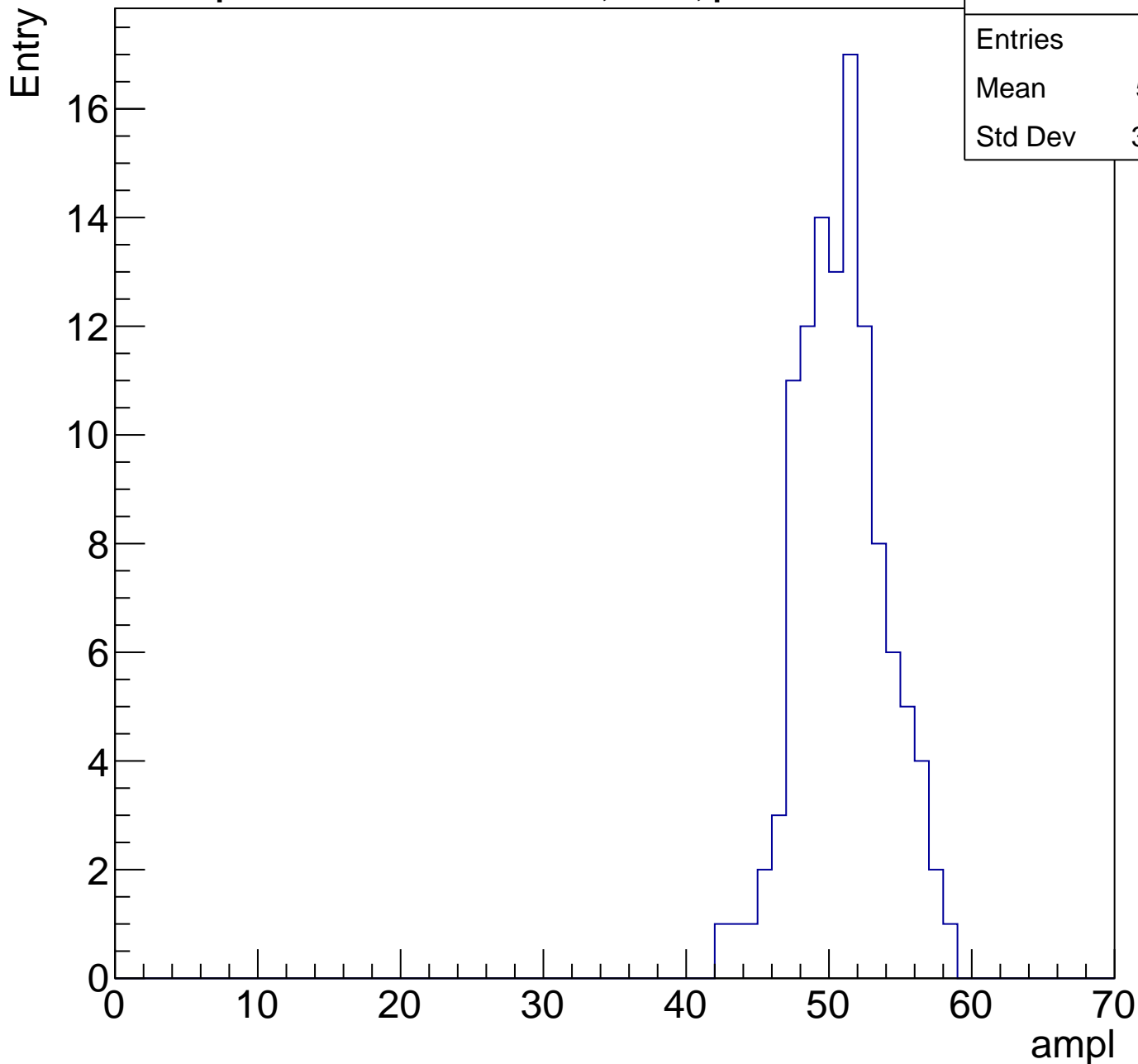
Entry



B1L001S, U24-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	50.41
Std Dev	3.053



B1L001S, U24-ch54, adc4

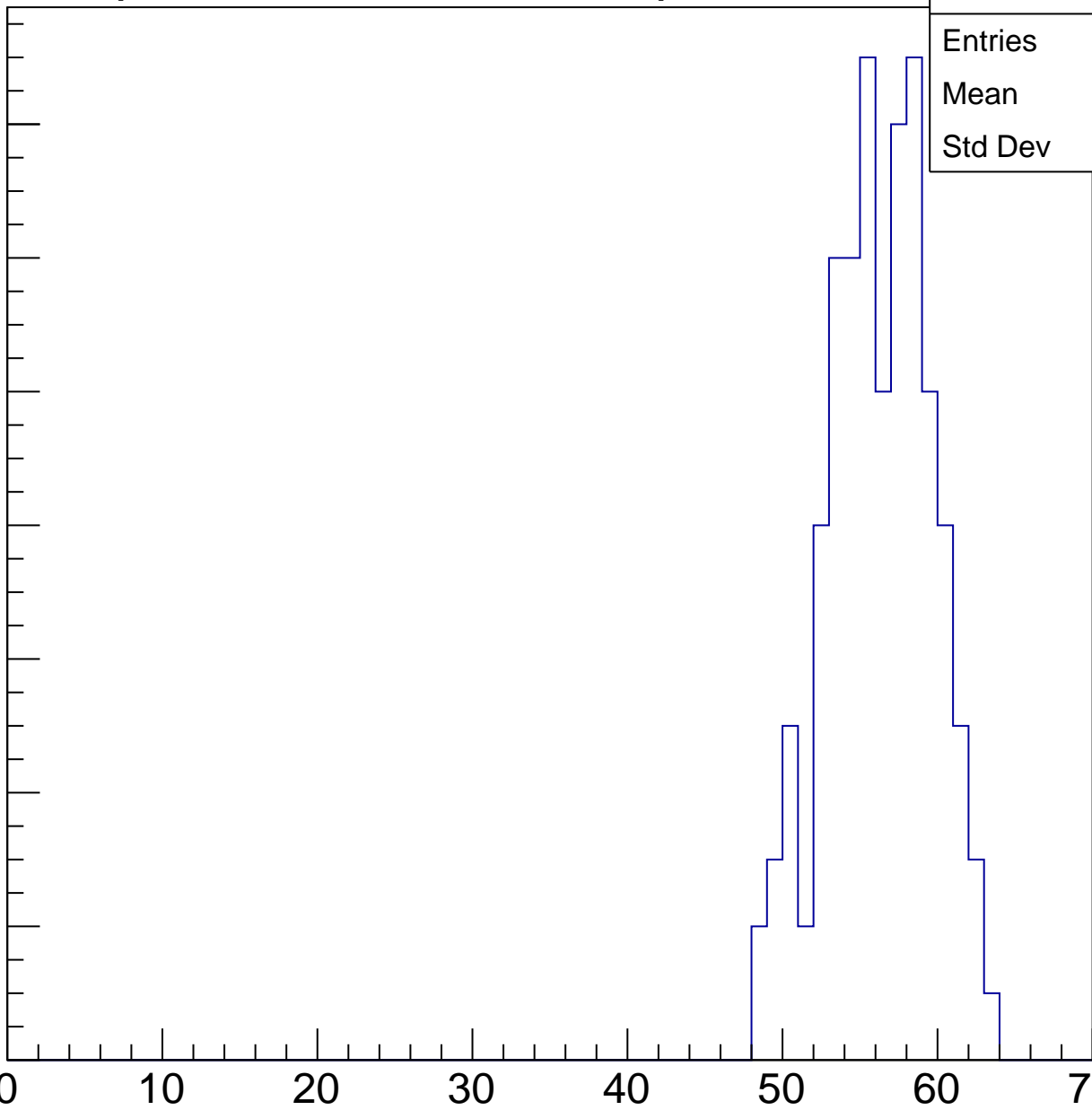
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.78
Std Dev	3.313

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch54, adc5

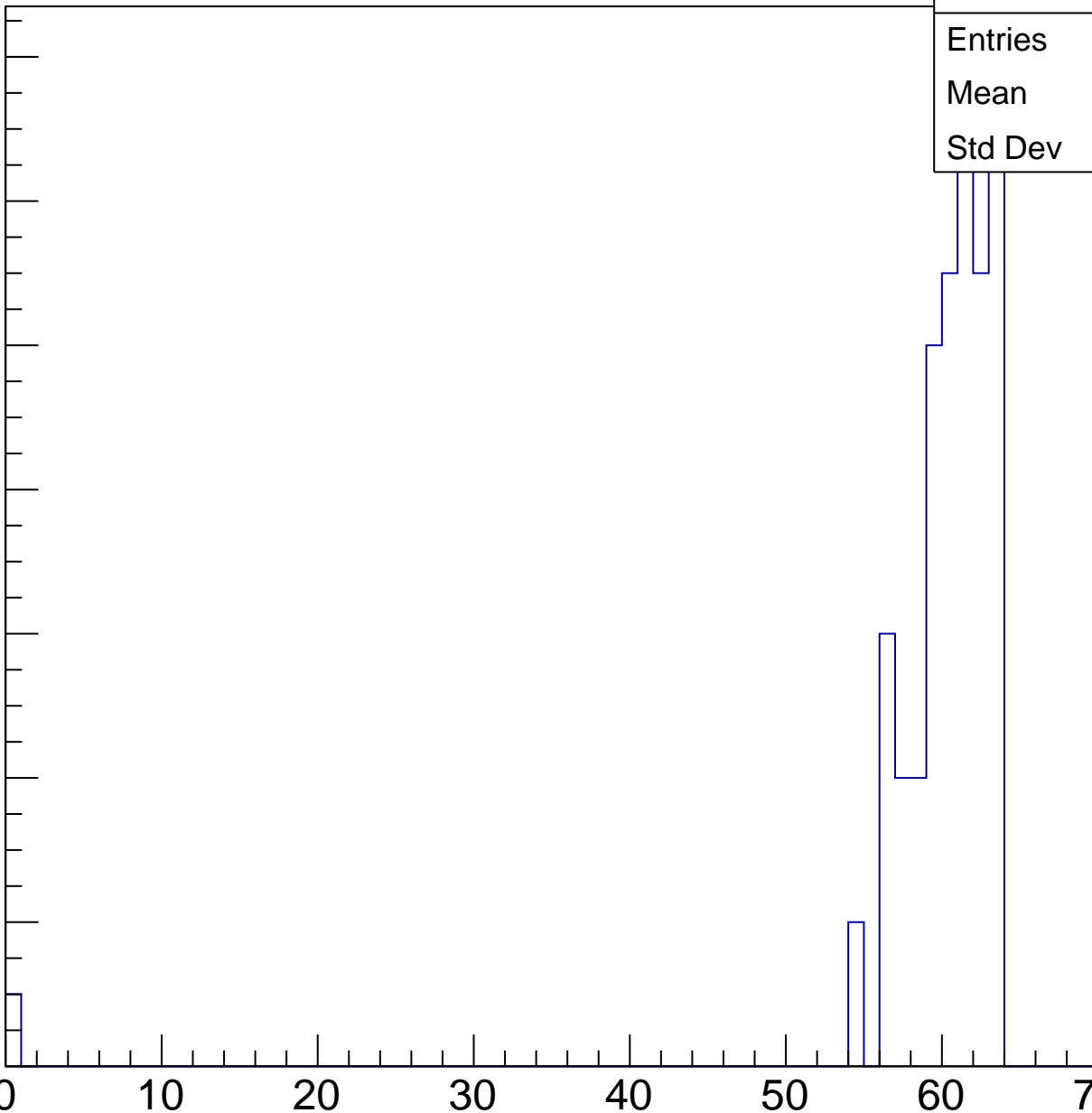
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.38
Std Dev	7.193

ampl

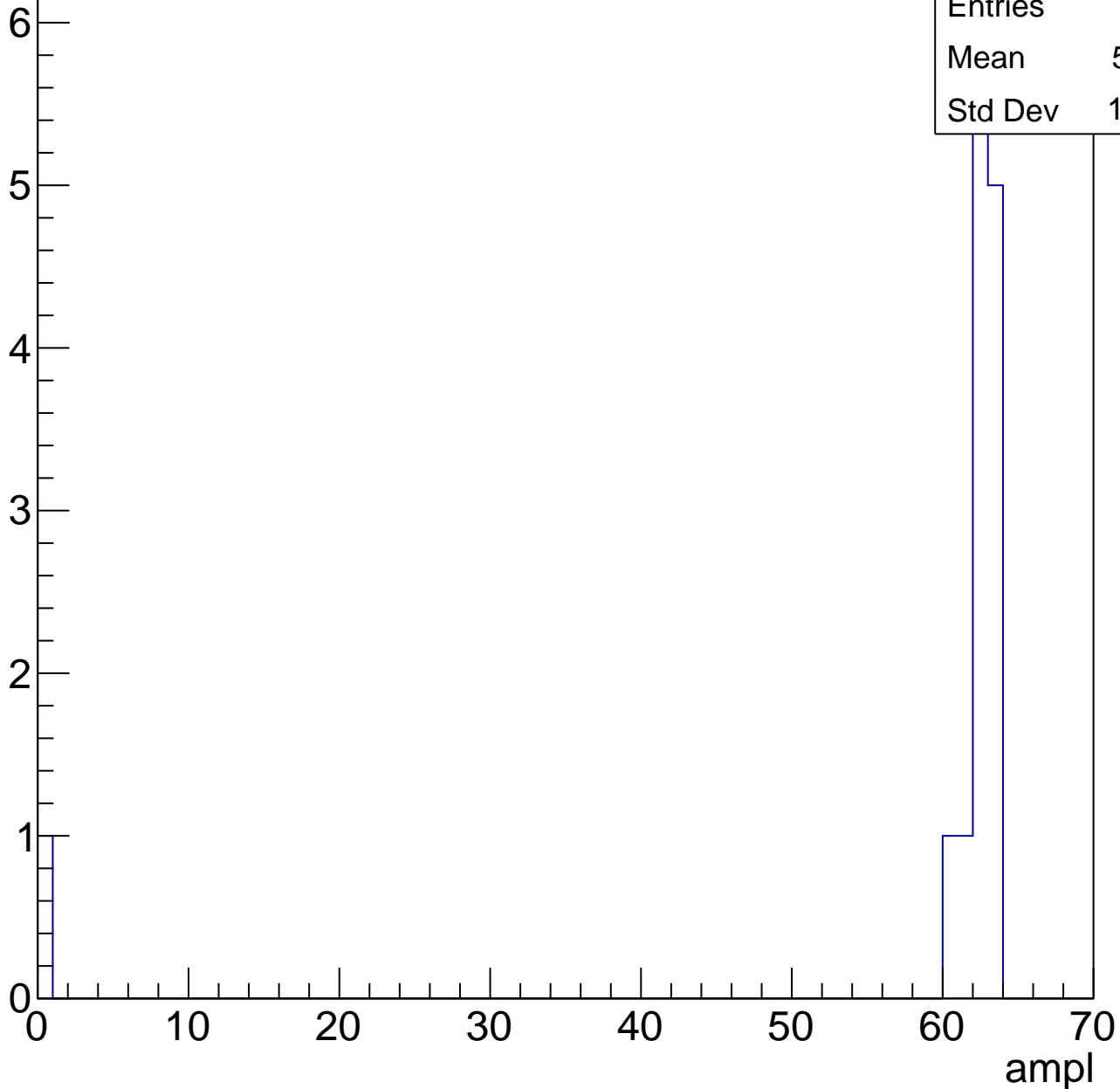


B1L001S, U24-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.71
Std Dev	16.03



B1L001S, U24-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L001S, U24-ch55, adc0

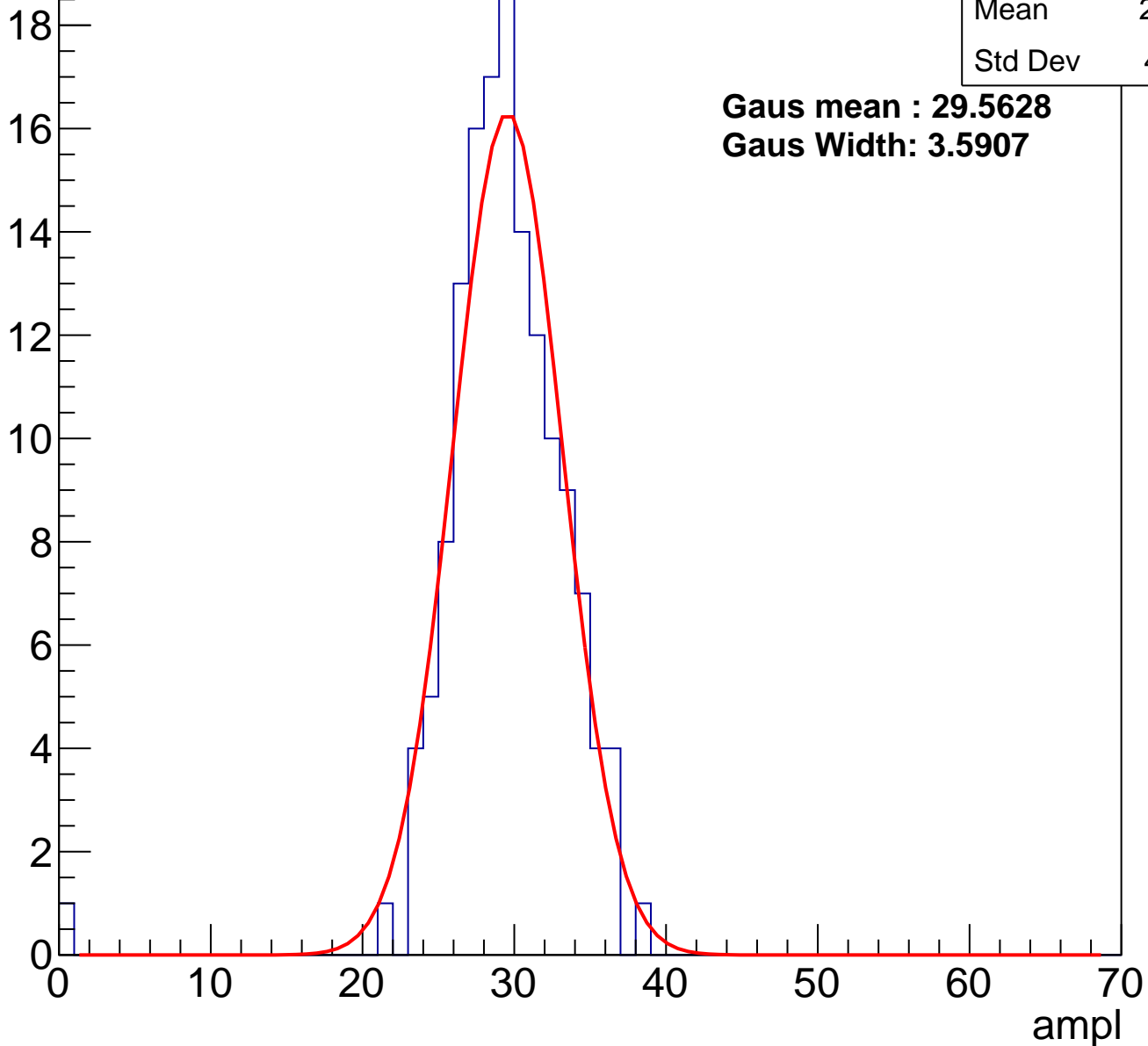
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.96
Std Dev	4.061

Gaus mean : 29.5628

Gaus Width: 3.5907

Entry



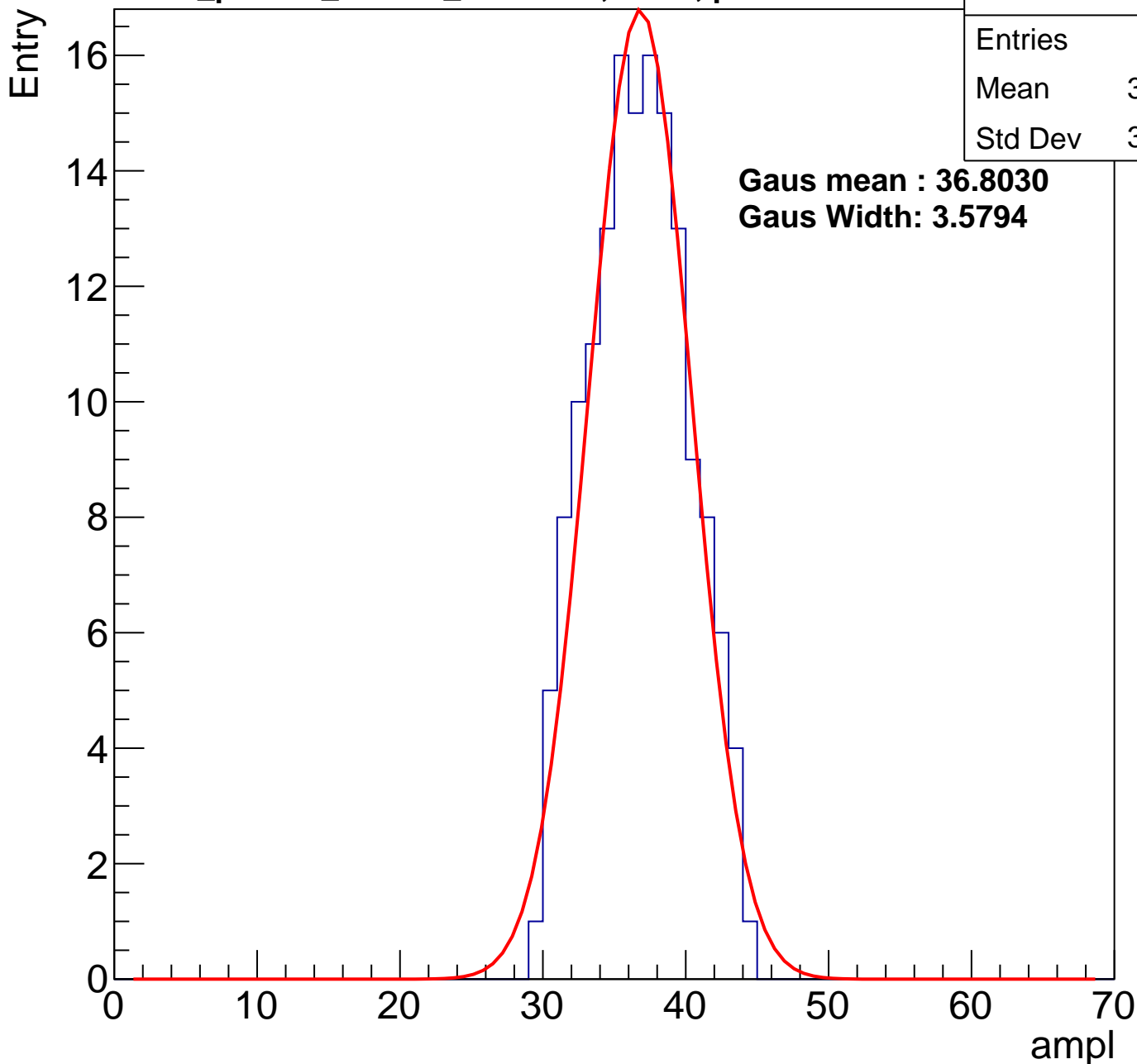
B1L001S, U24-ch55, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	36.27
Std Dev	3.412

Gaus mean : 36.8030

Gaus Width: 3.5794



B1L001S, U24-ch55, adc2

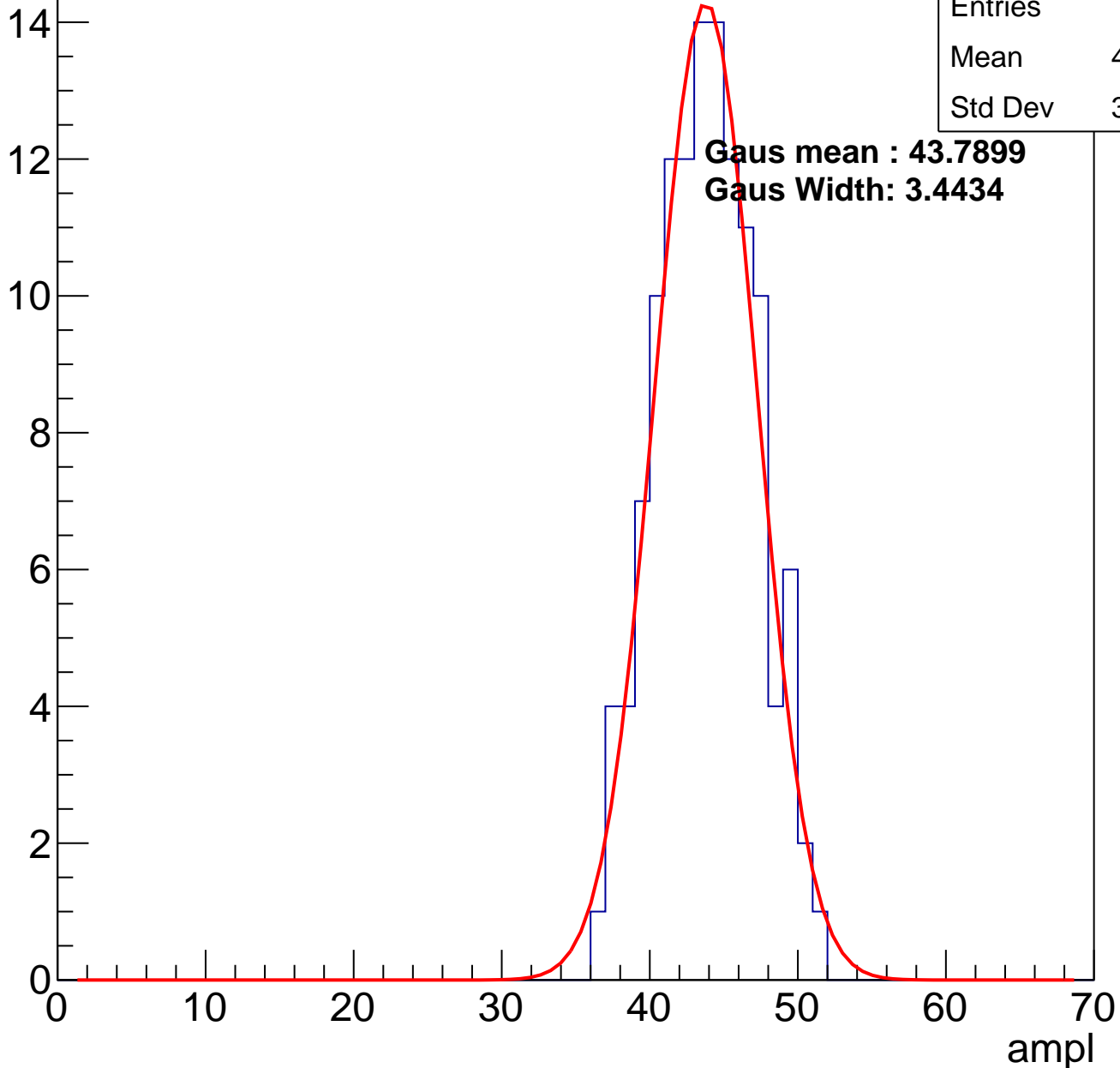
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	43.35
Std Dev	3.285

Gaus mean : 43.7899

Gaus Width: 3.4434

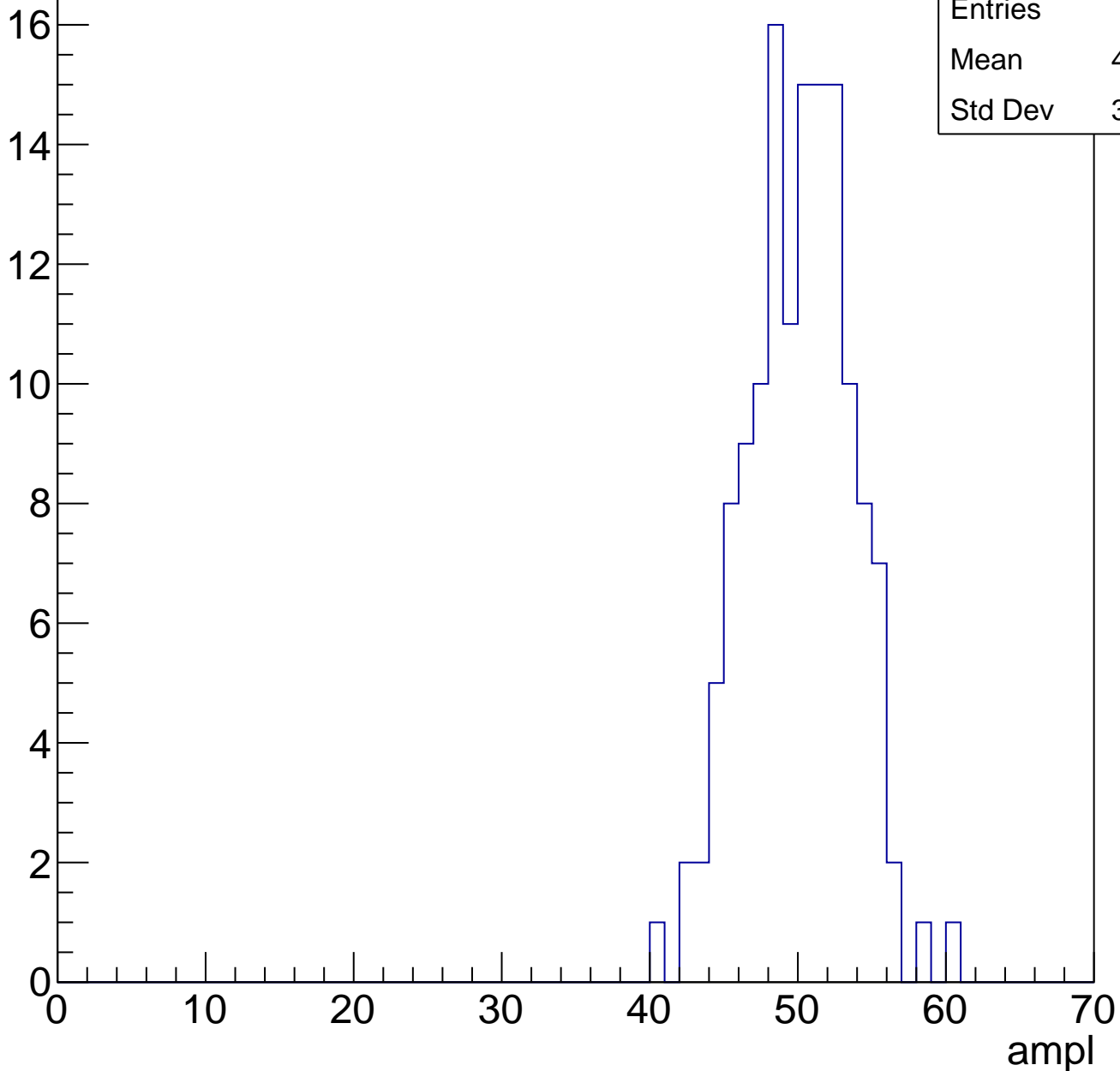
Entry



B1L001S, U24-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	138
Mean	49.66
Std Dev	3.513

B1L001S, U24-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	91
Mean	55.6
Std Dev	3.012

Entry

12

10

8

6

4

2

0

0

10

20

30

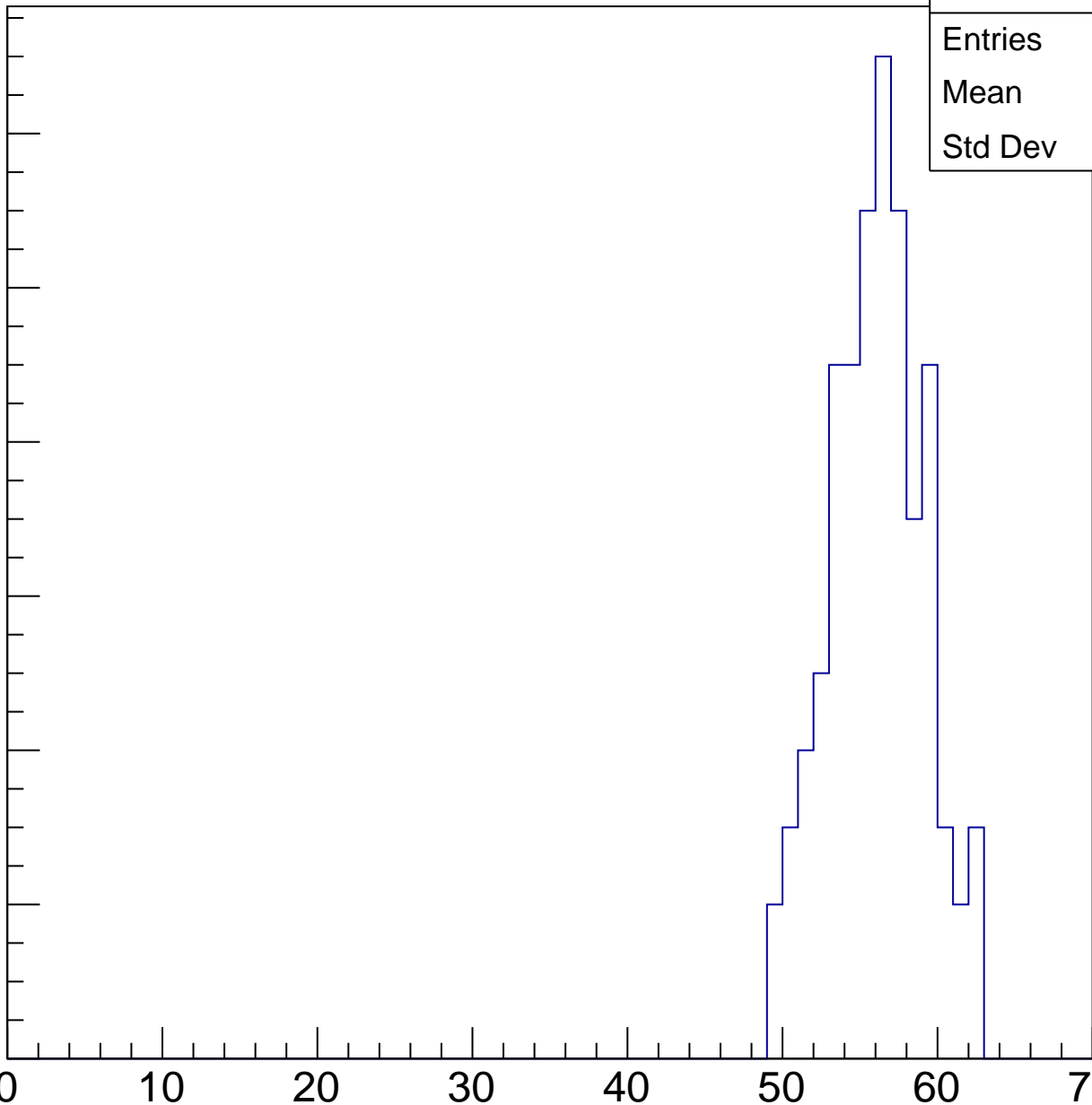
40

50

60

70

ampl



B1L001S, U24-ch55, adc5

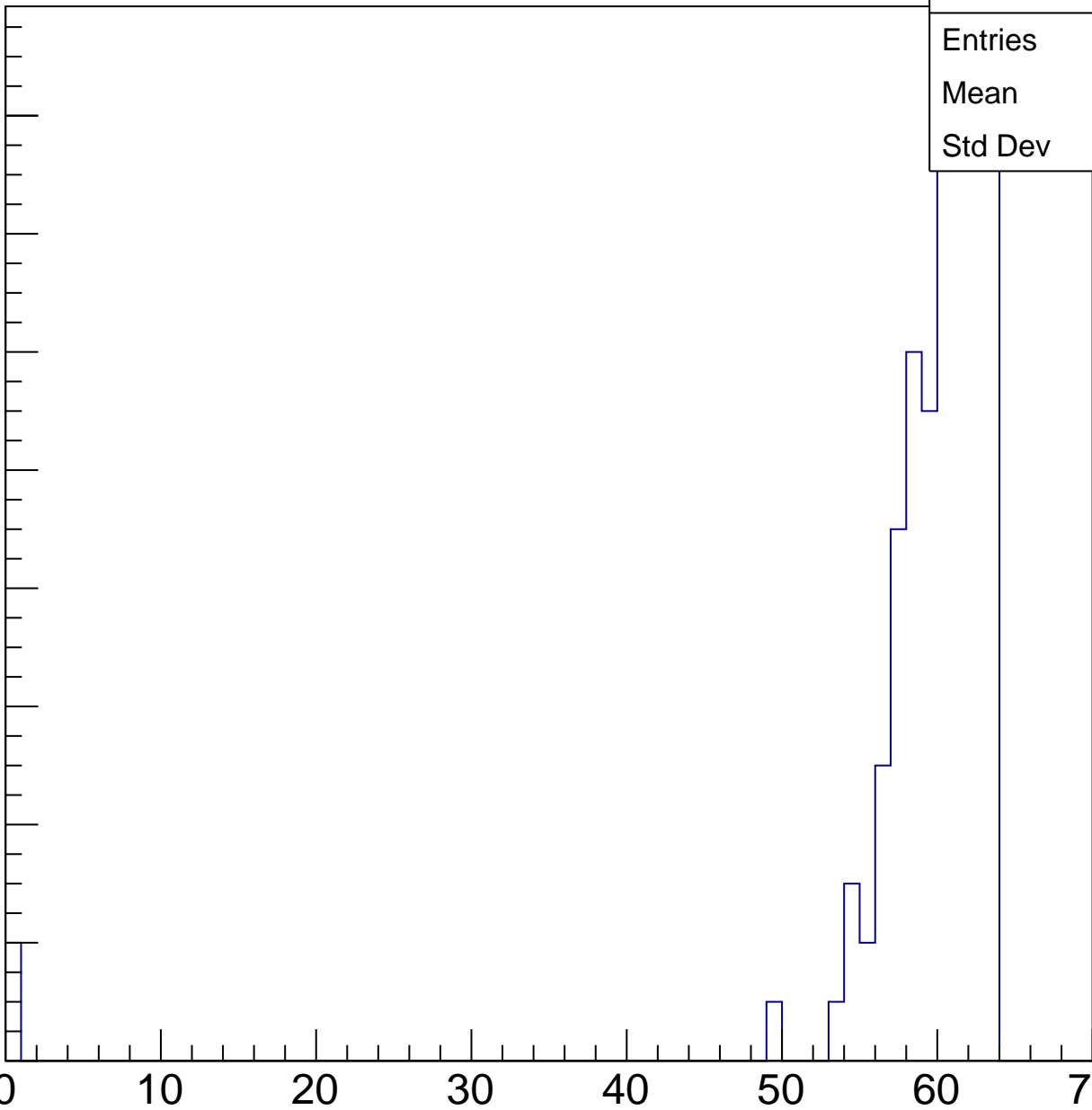
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.67
Std Dev	8.334

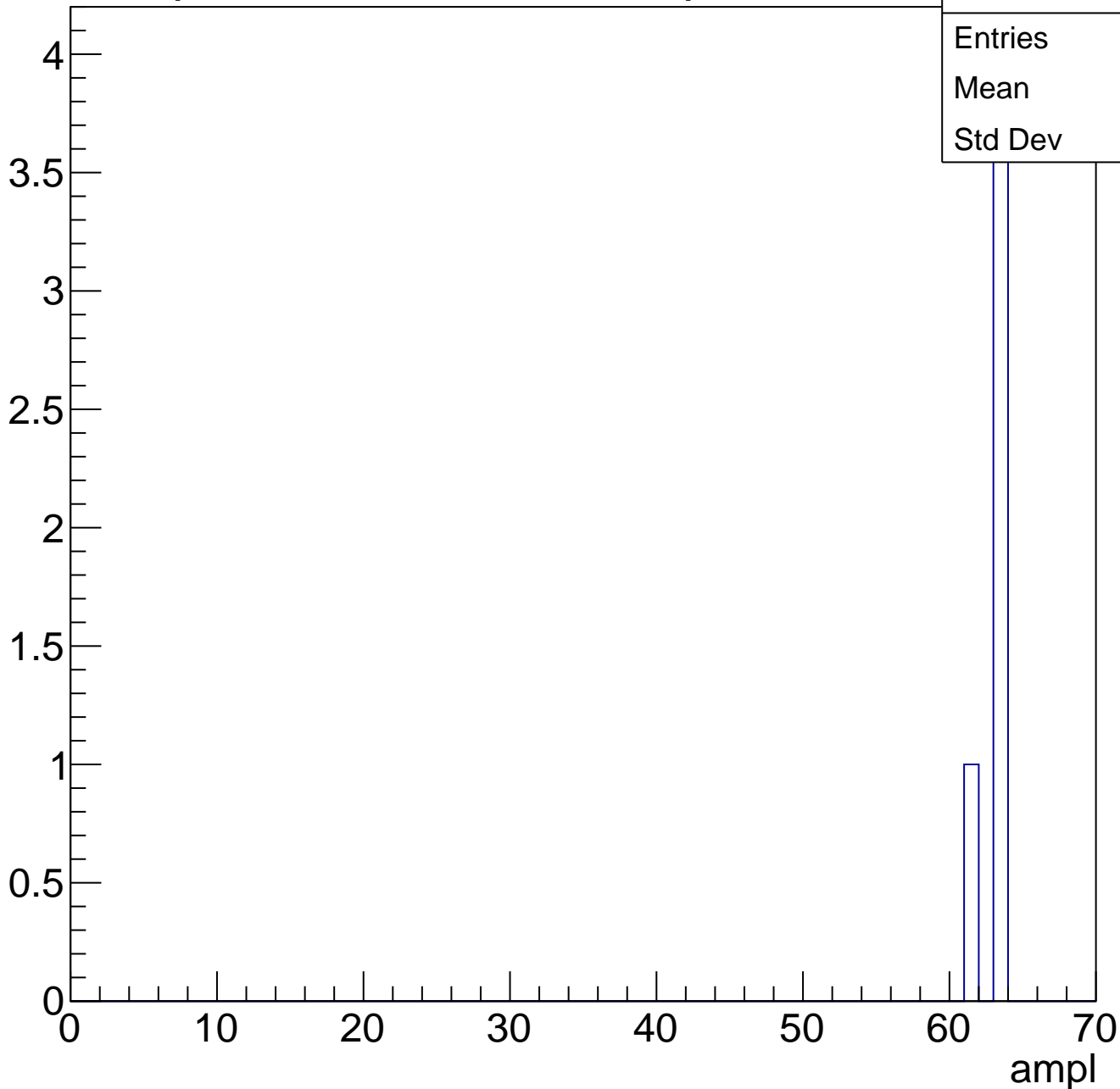
ampl



B1L001S, U24-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch56, adc0

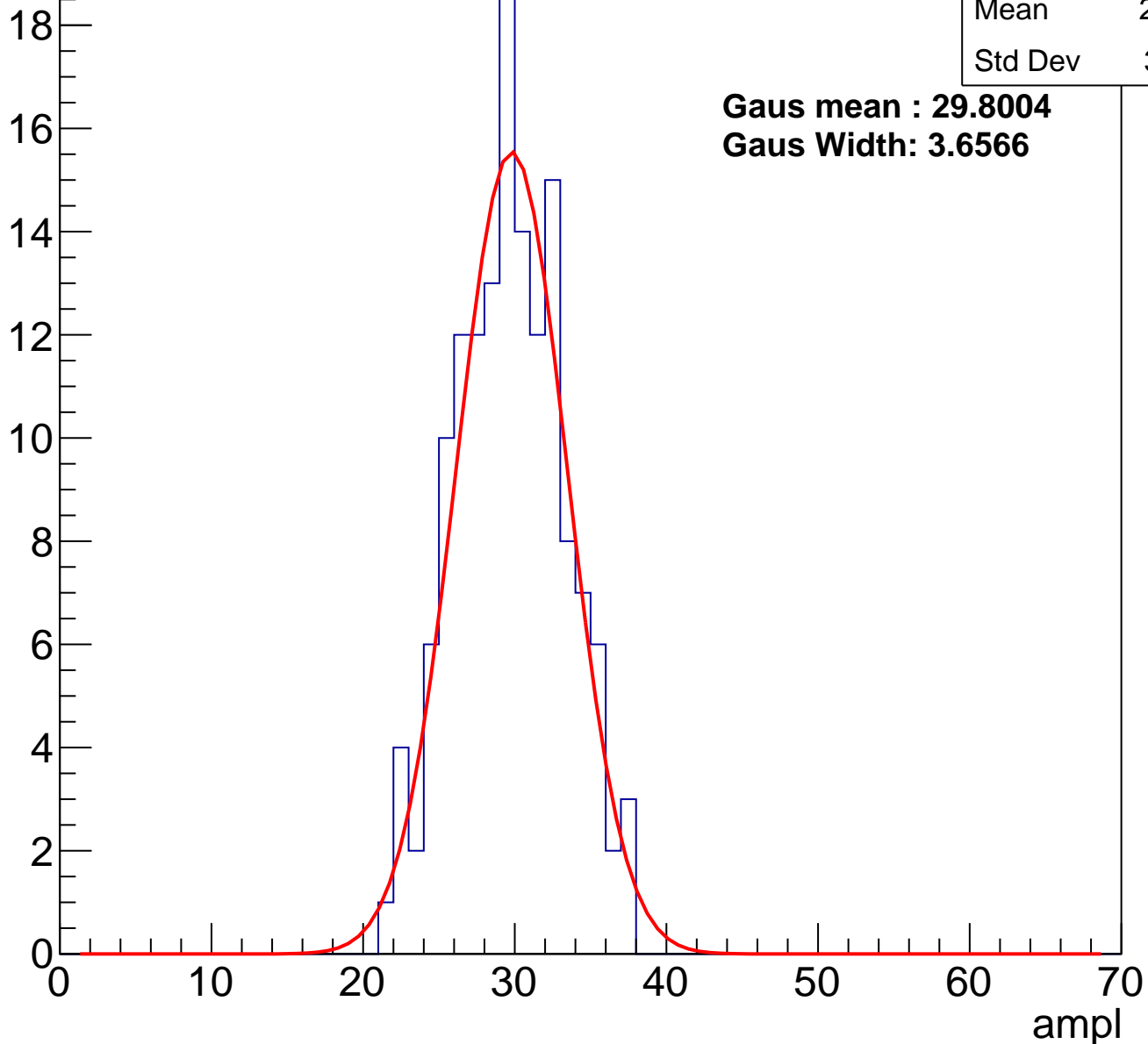
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.23
Std Dev	3.521

Gaus mean : 29.8004

Gaus Width: 3.6566

Entry

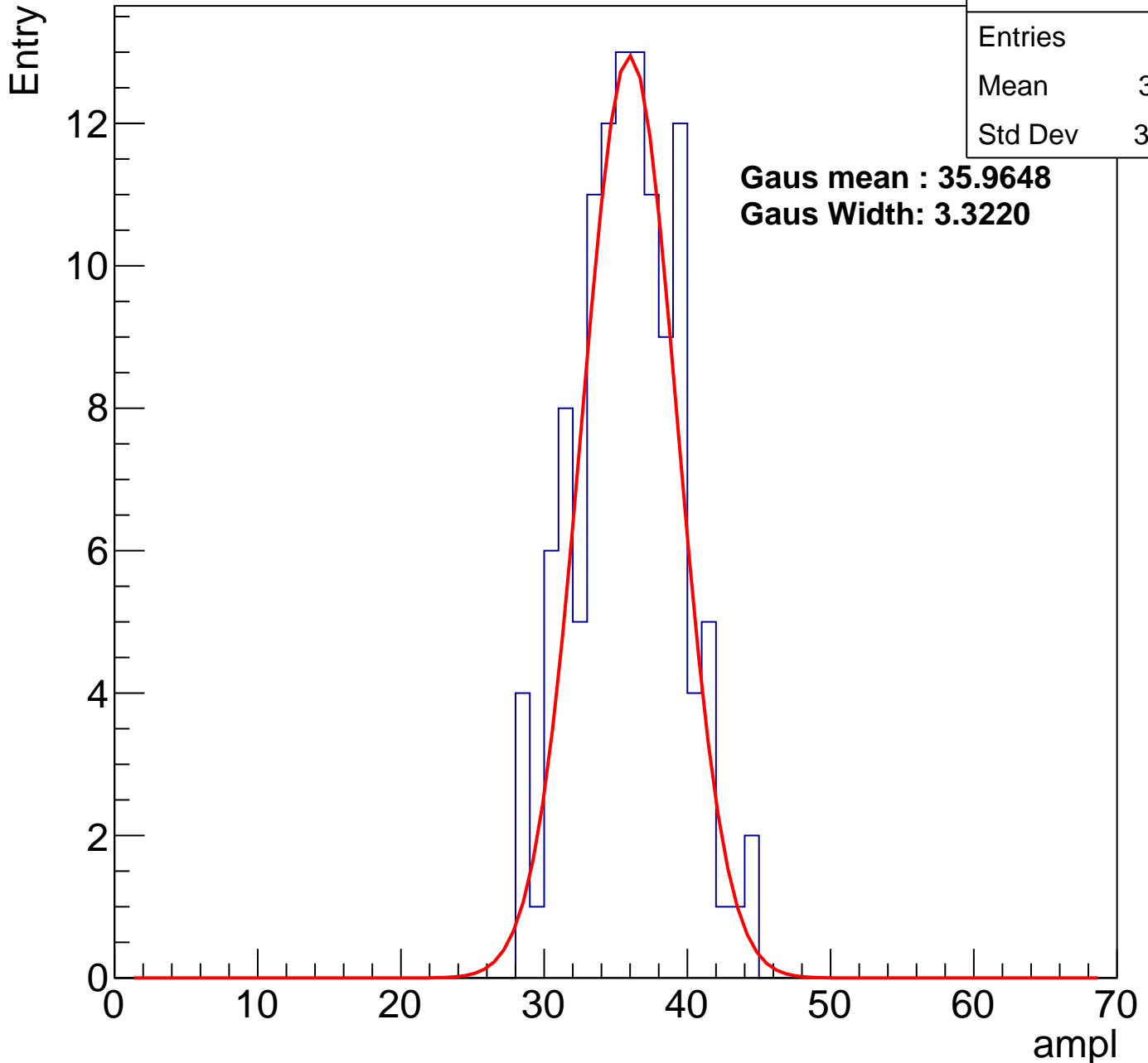


B1L001S, U24-ch56, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	35.41
Std Dev	3.537

Gaus mean : 35.9648
Gaus Width: 3.3220



B1L001S, U24-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

128

Mean

41.59

Std Dev

3.287

Gaus mean : 42.1096

Gaus Width: 3.3074

0

10

20

30

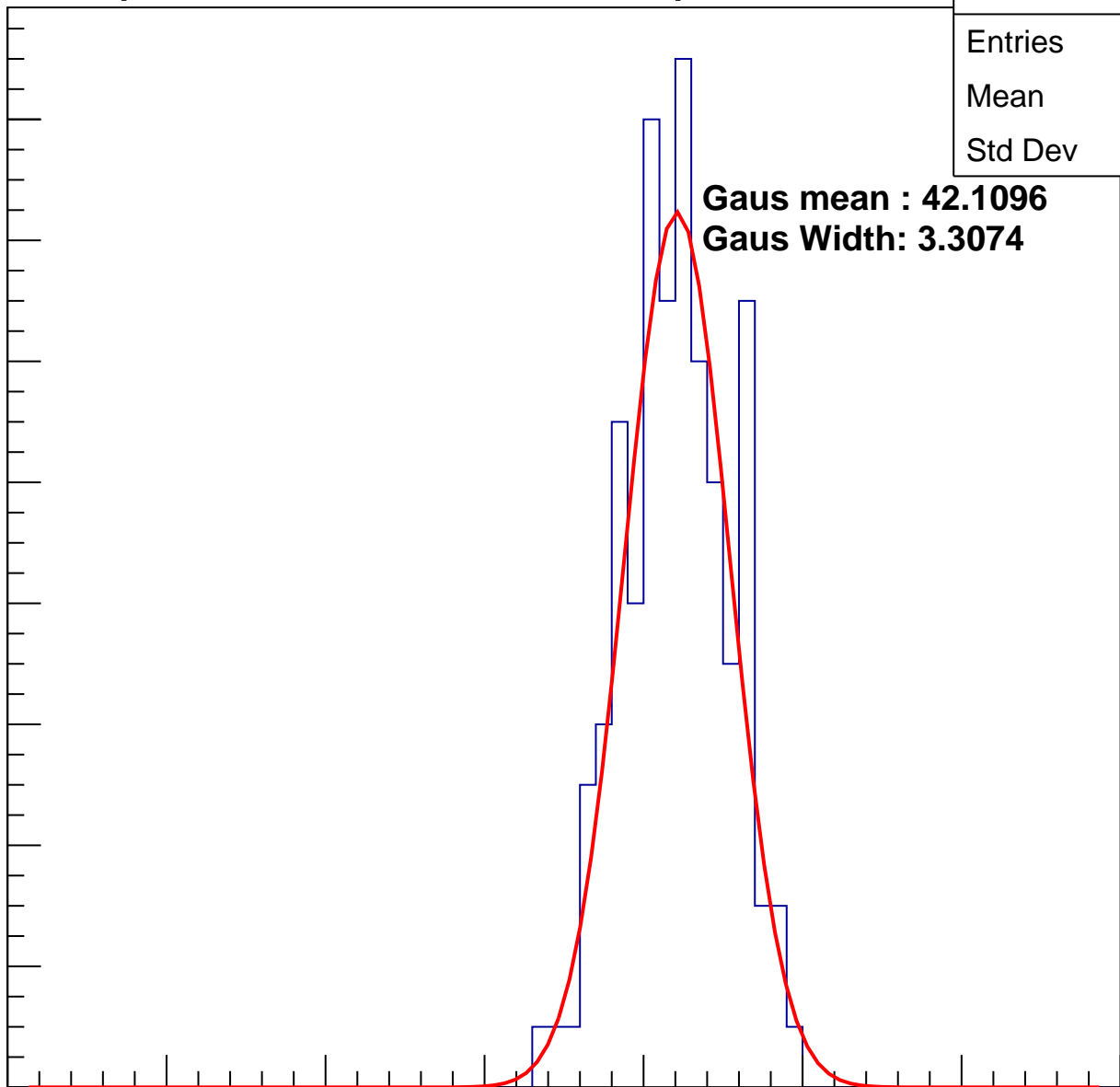
40

50

60

70

ampl



B1L001S, U24-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	48.48
Std Dev	3.382

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

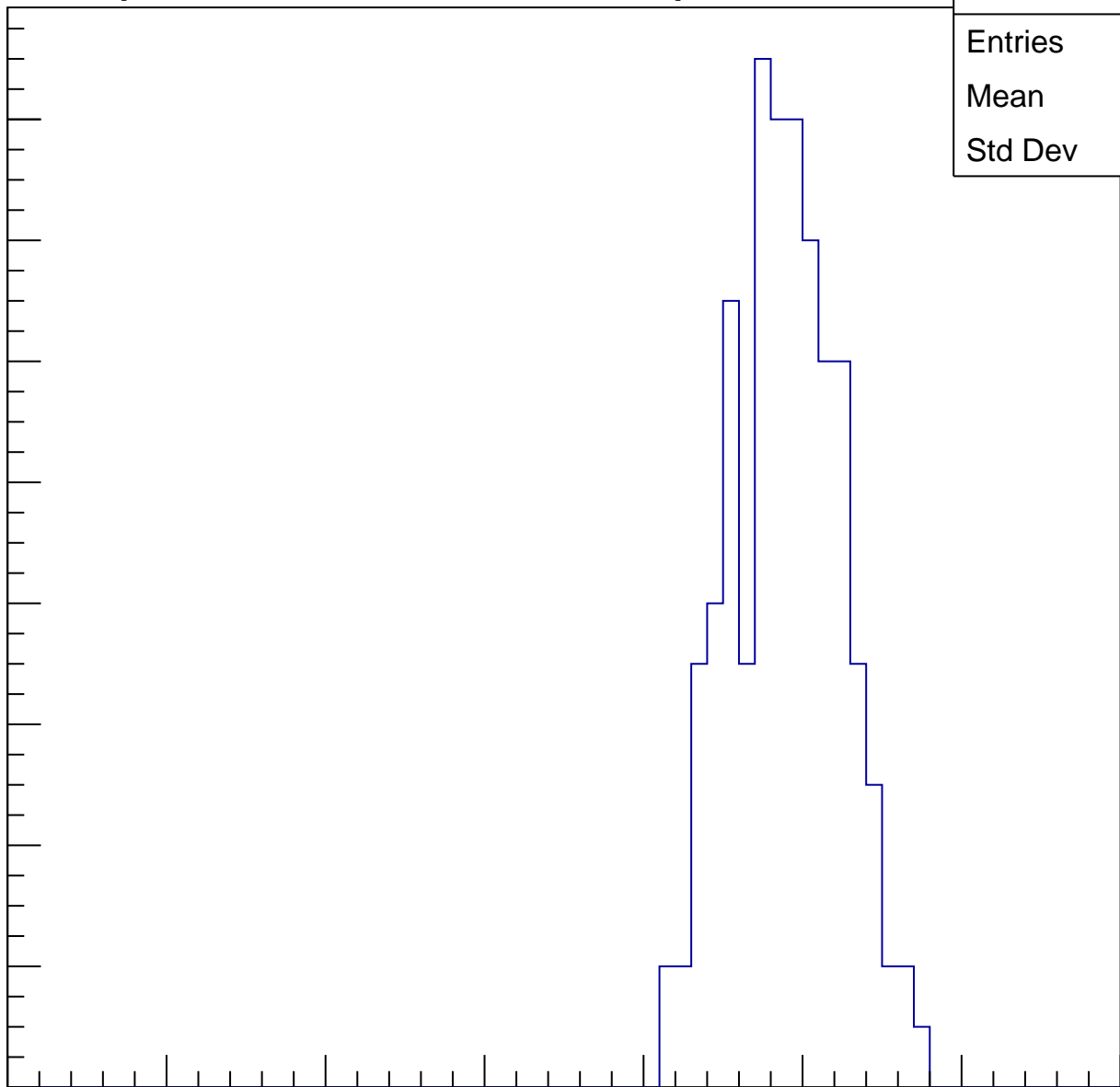
40

50

60

70

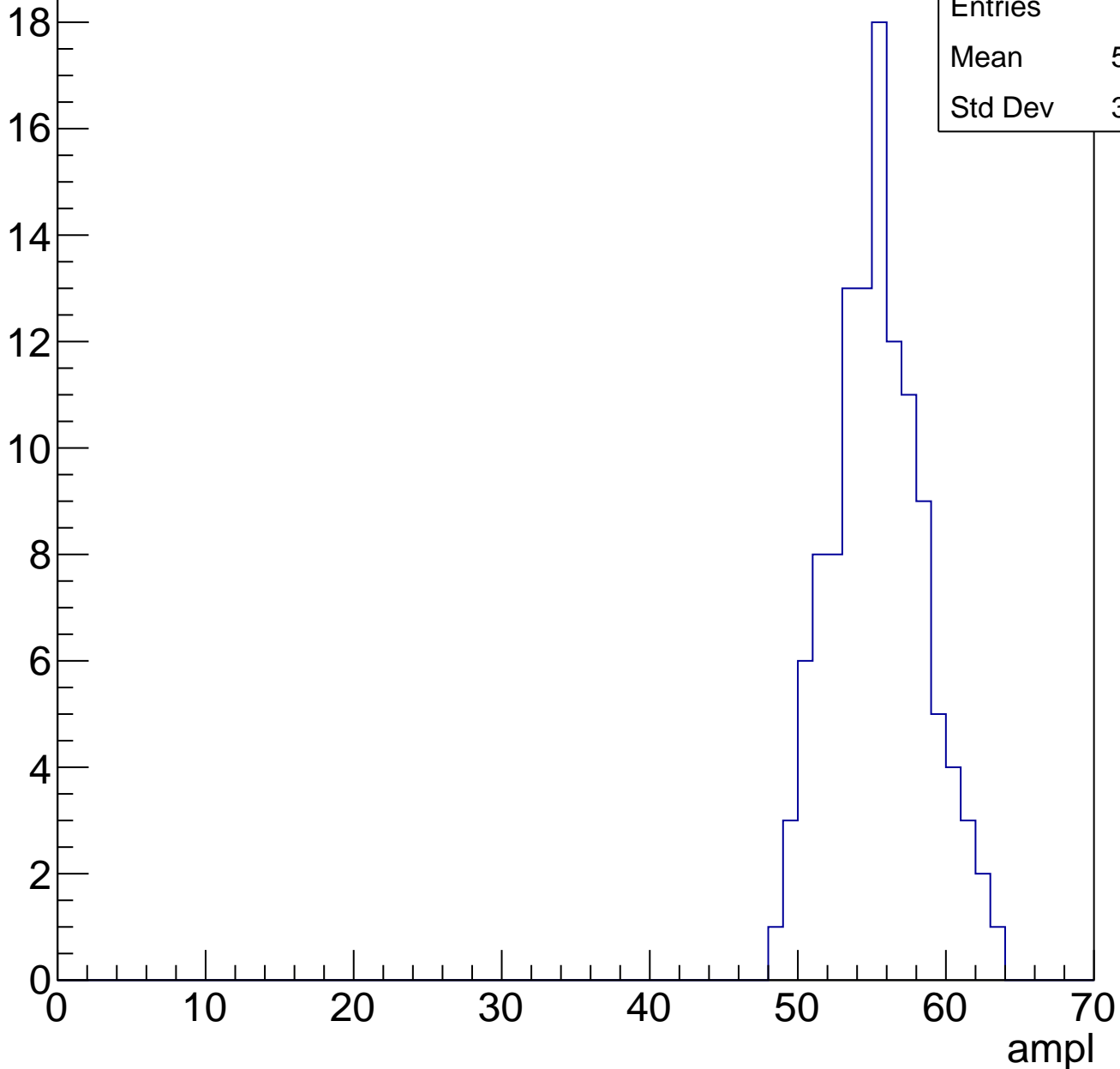
ampl



B1L001S, U24-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch56, adc5

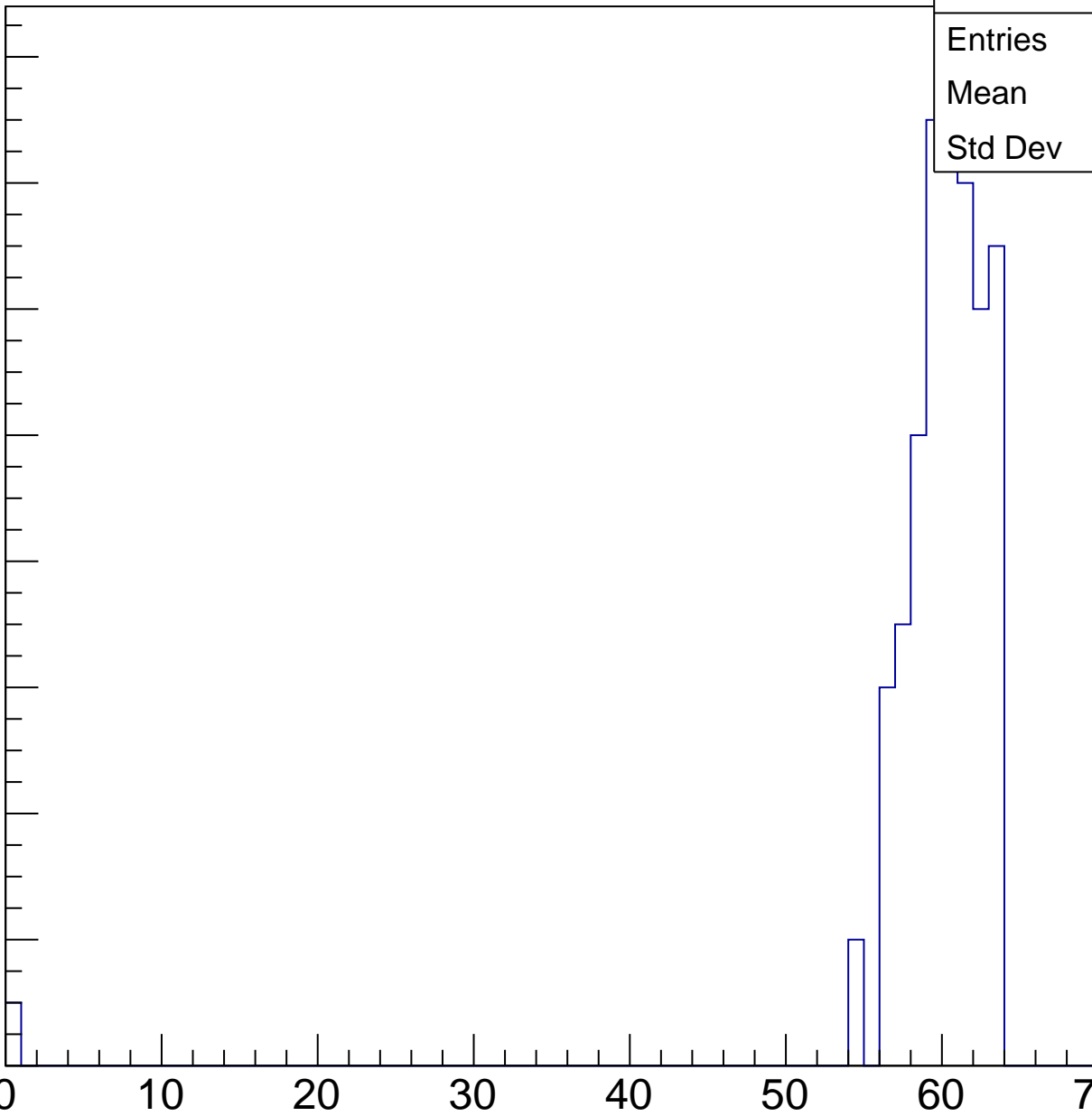
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.22
Std Dev	6.46

ampl

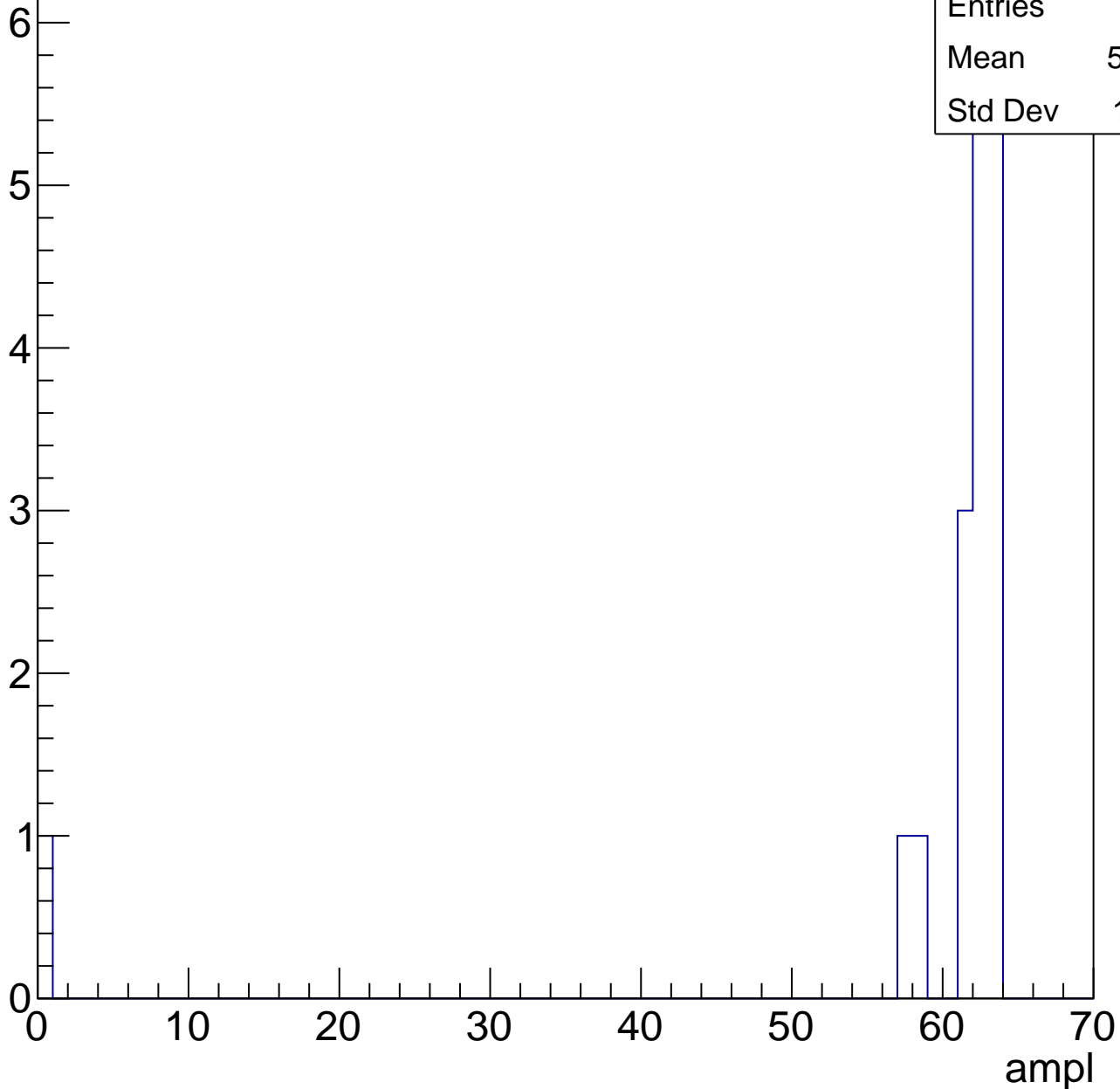


B1L001S, U24-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.22
Std Dev	14.21



B1L001S, U24-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch57, adc0

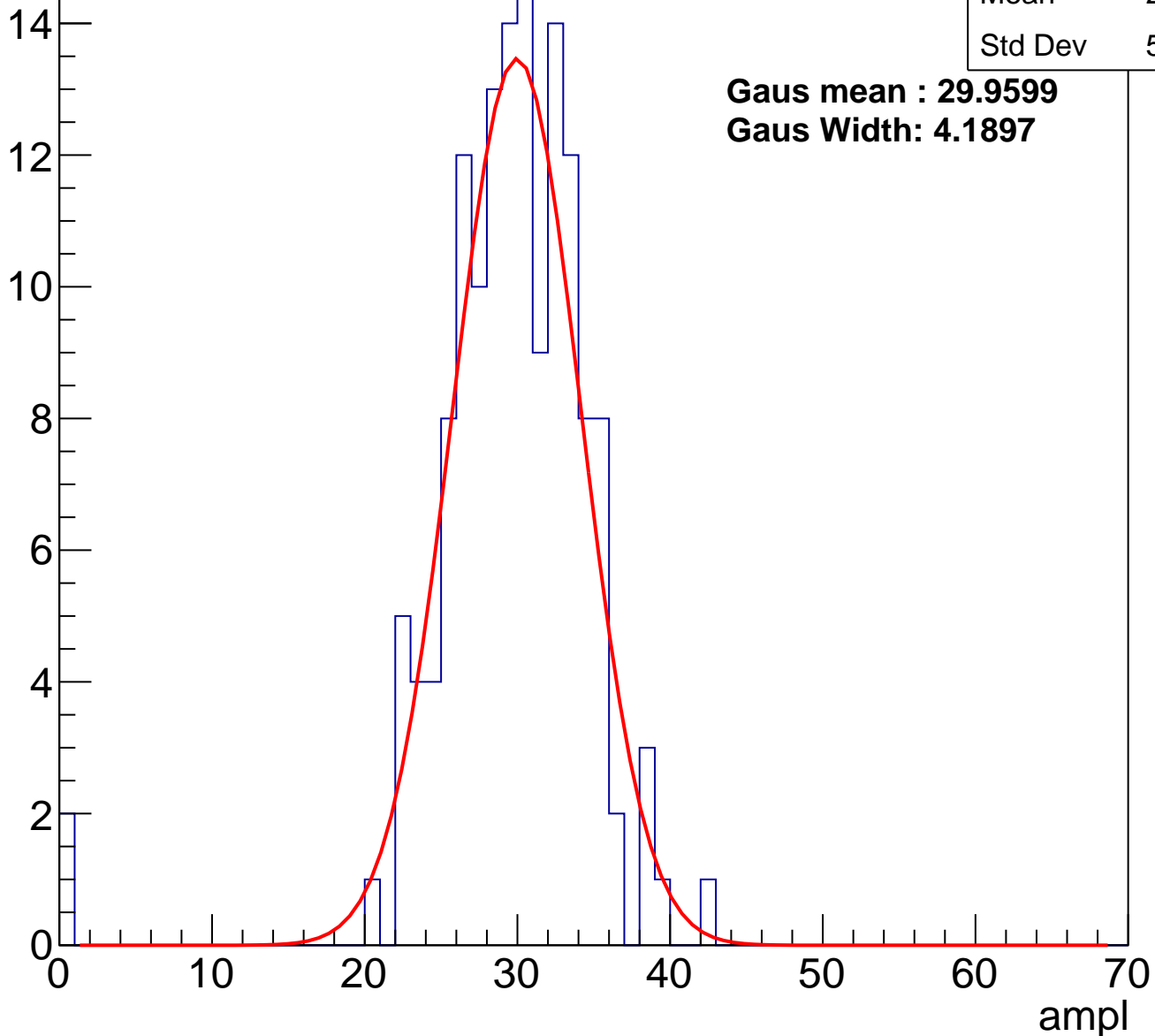
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.19
Std Dev	5.236

Gaus mean : 29.9599

Gaus Width: 4.1897

Entry

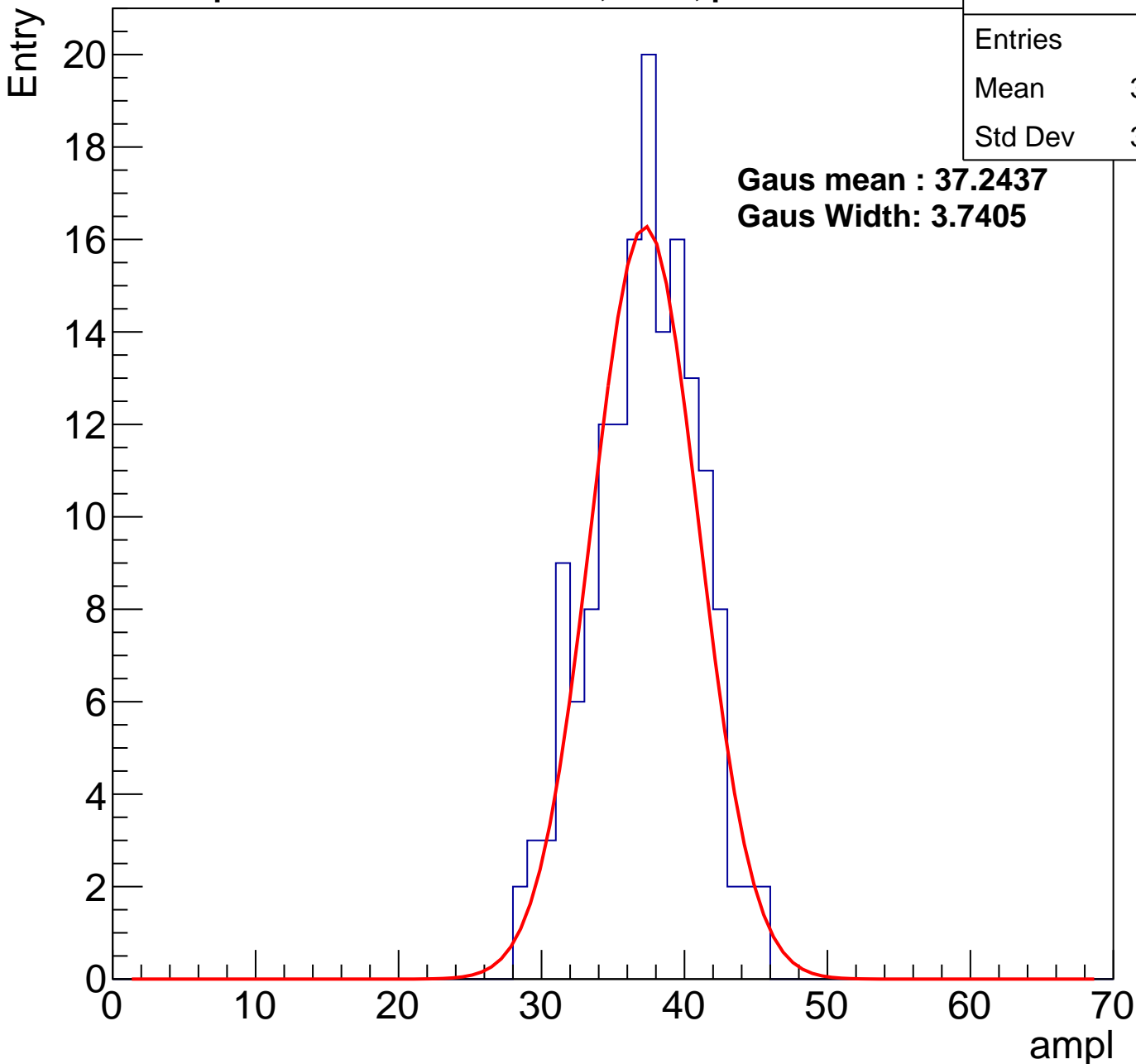


B1L001S, U24-ch57, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	36.72
Std Dev	3.662

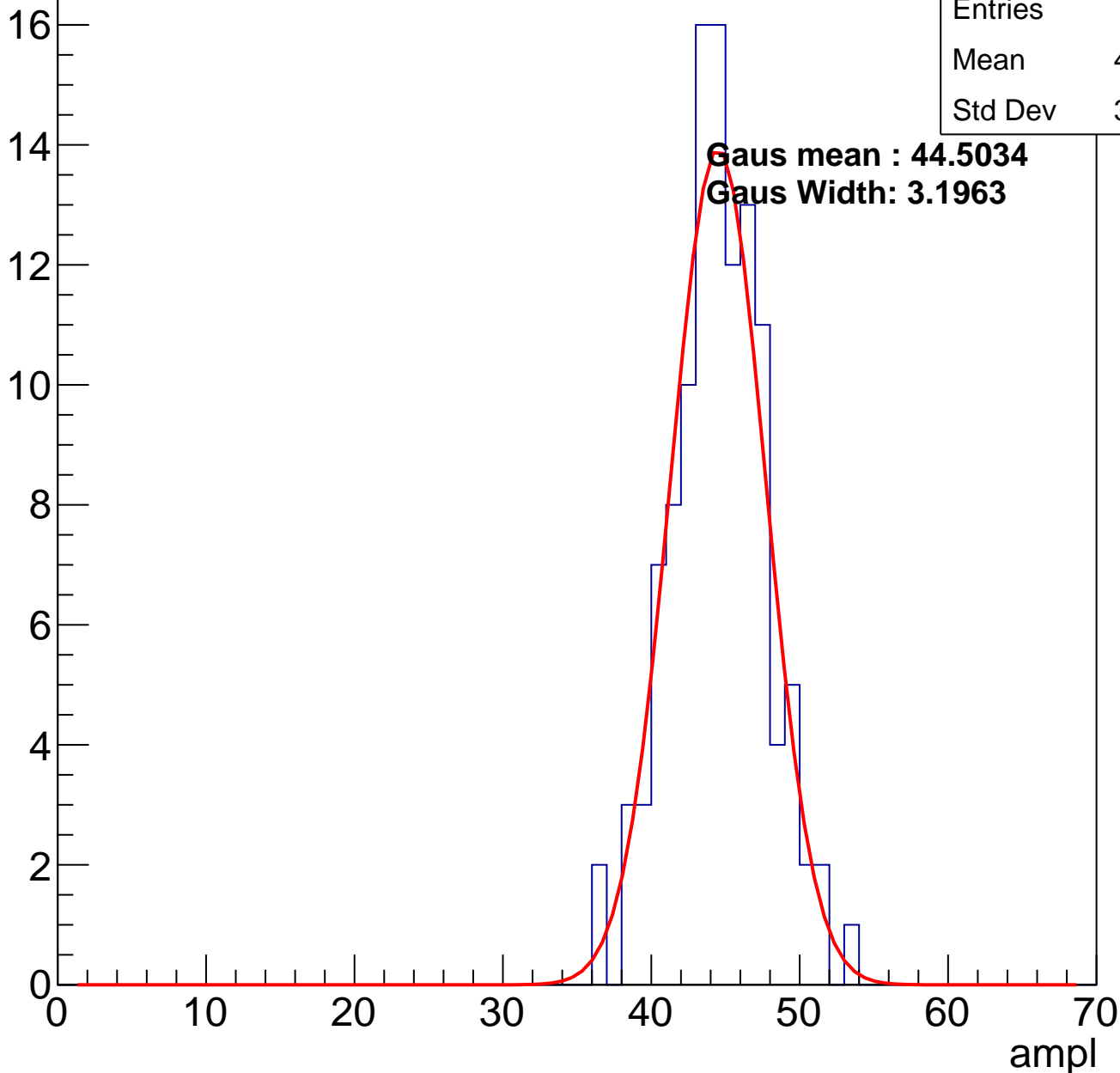
Gaus mean : 37.2437
Gaus Width: 3.7405



B1L001S, U24-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

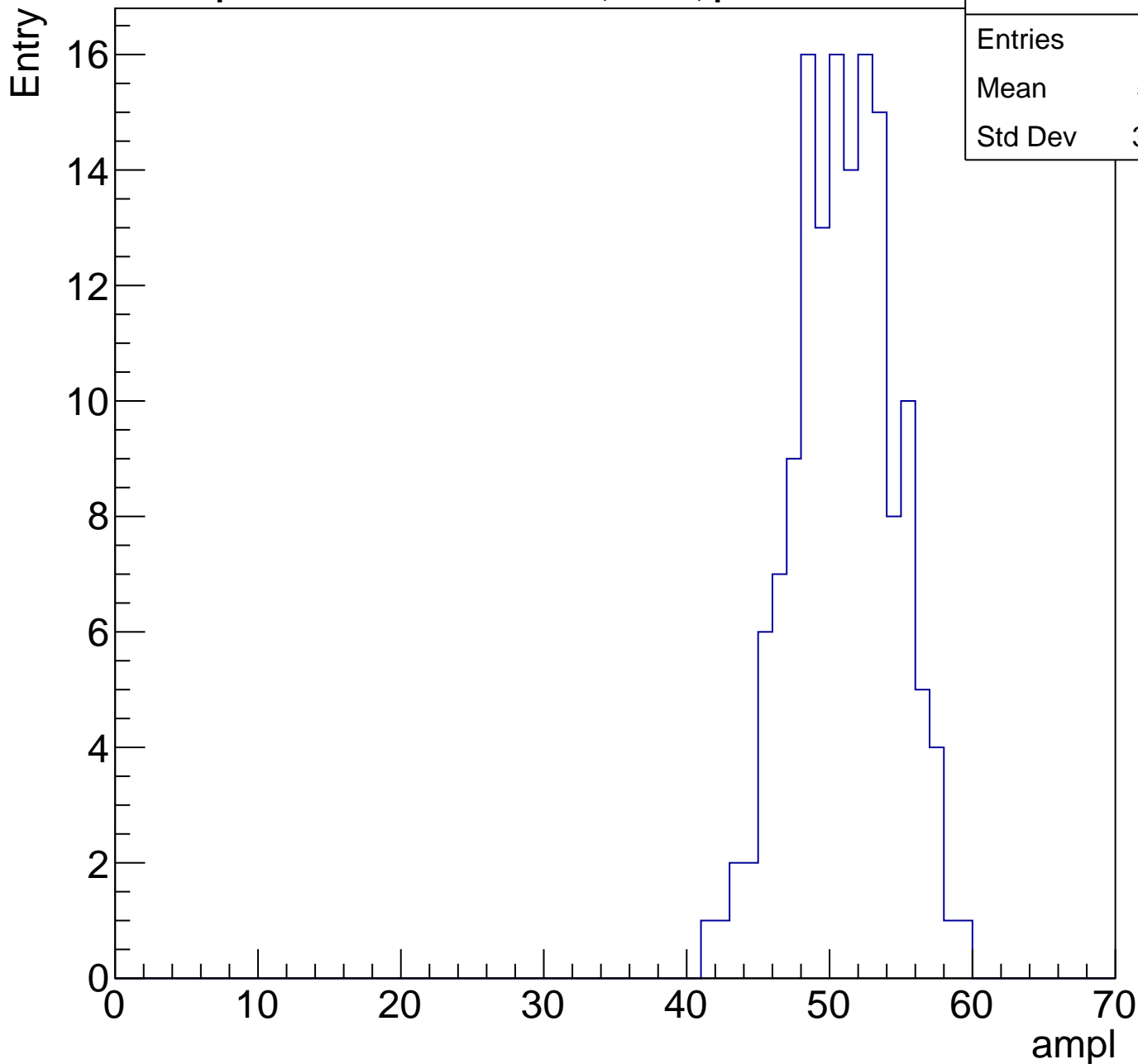
Entry



B1L001S, U24-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	50.51
Std Dev	3.508

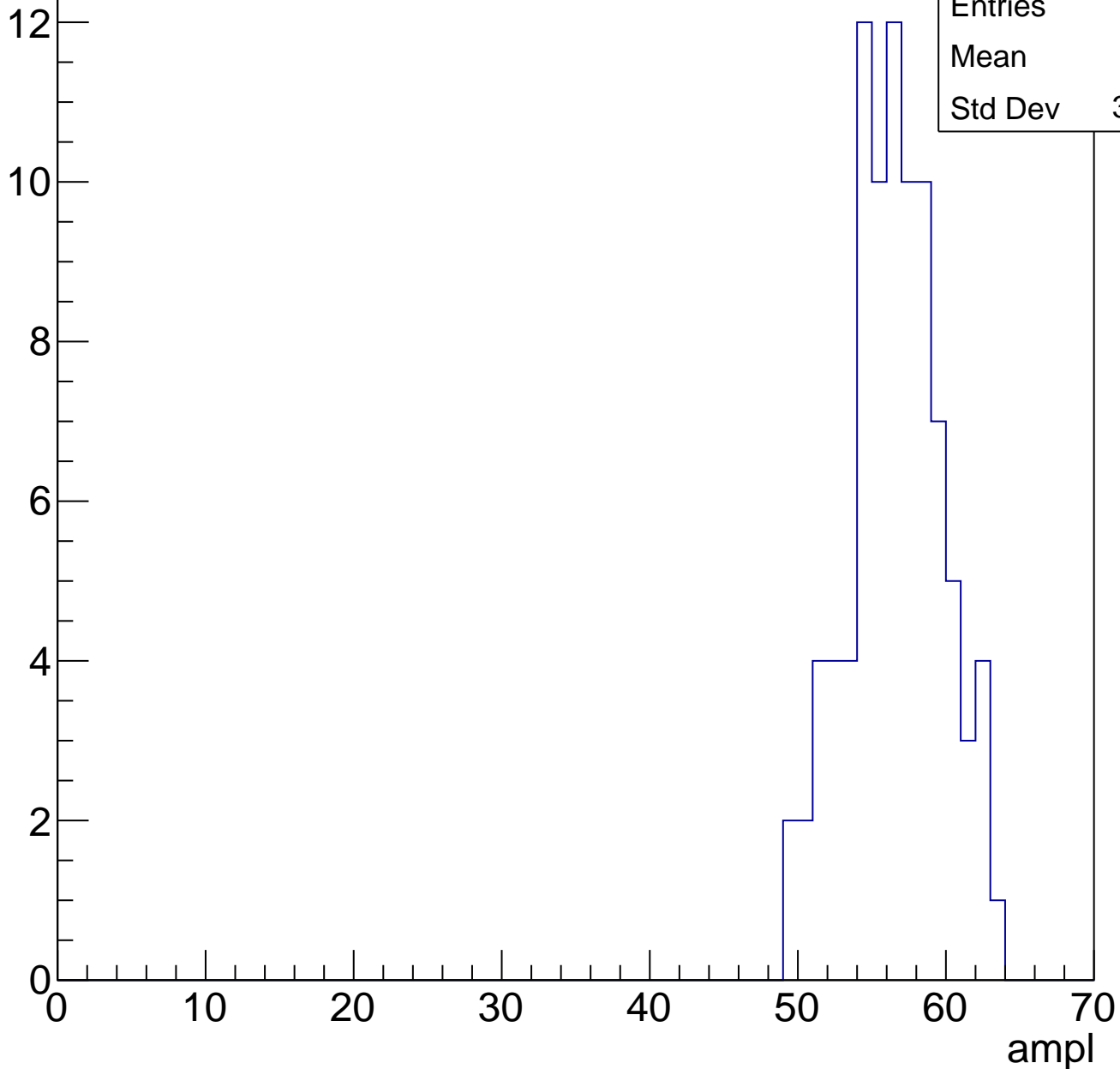


B1L001S, U24-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	56.1
Std Dev	3.141

Entry



B1L001S, U24-ch57, adc5

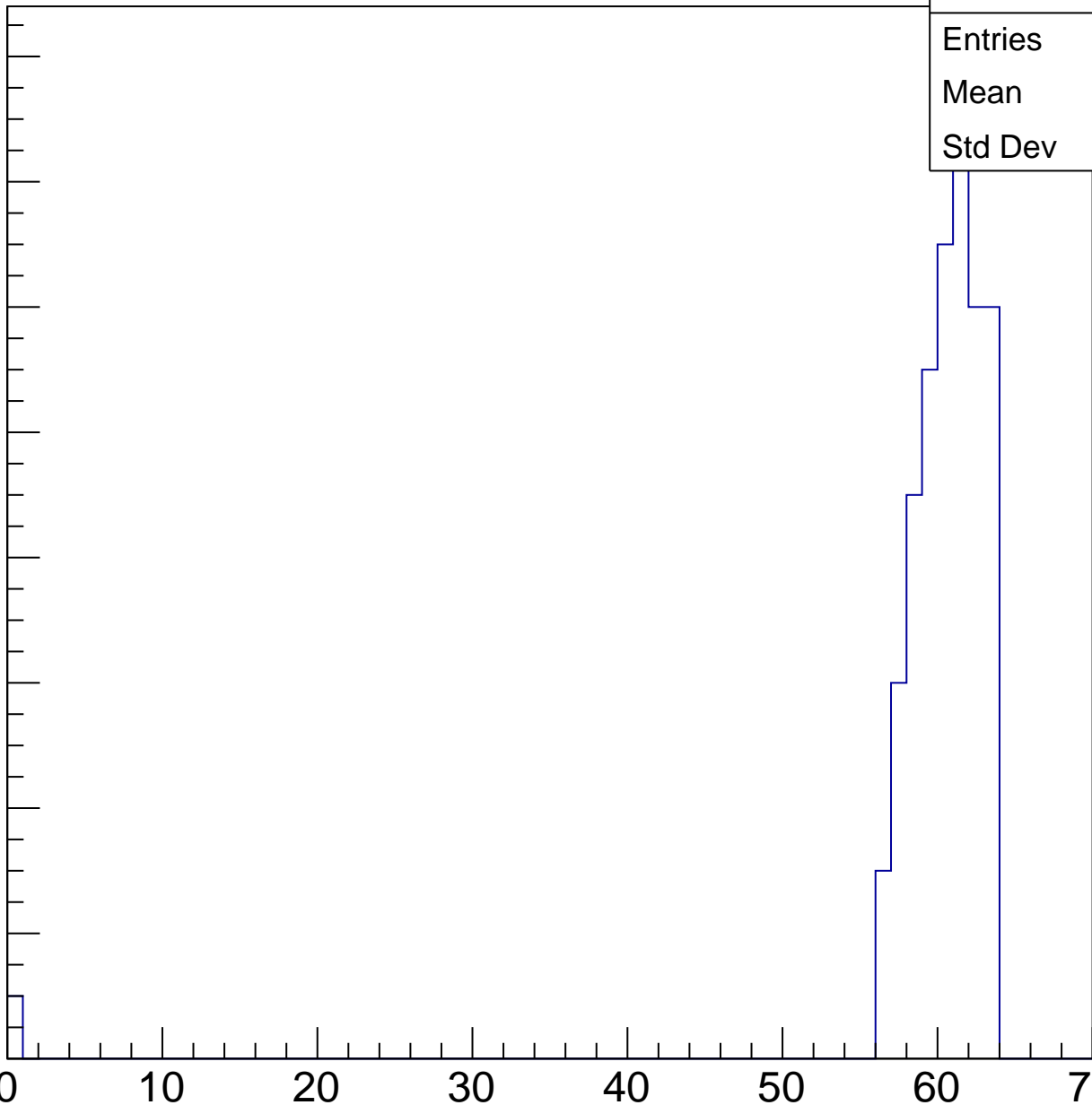
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.48
Std Dev	6.854

ampl

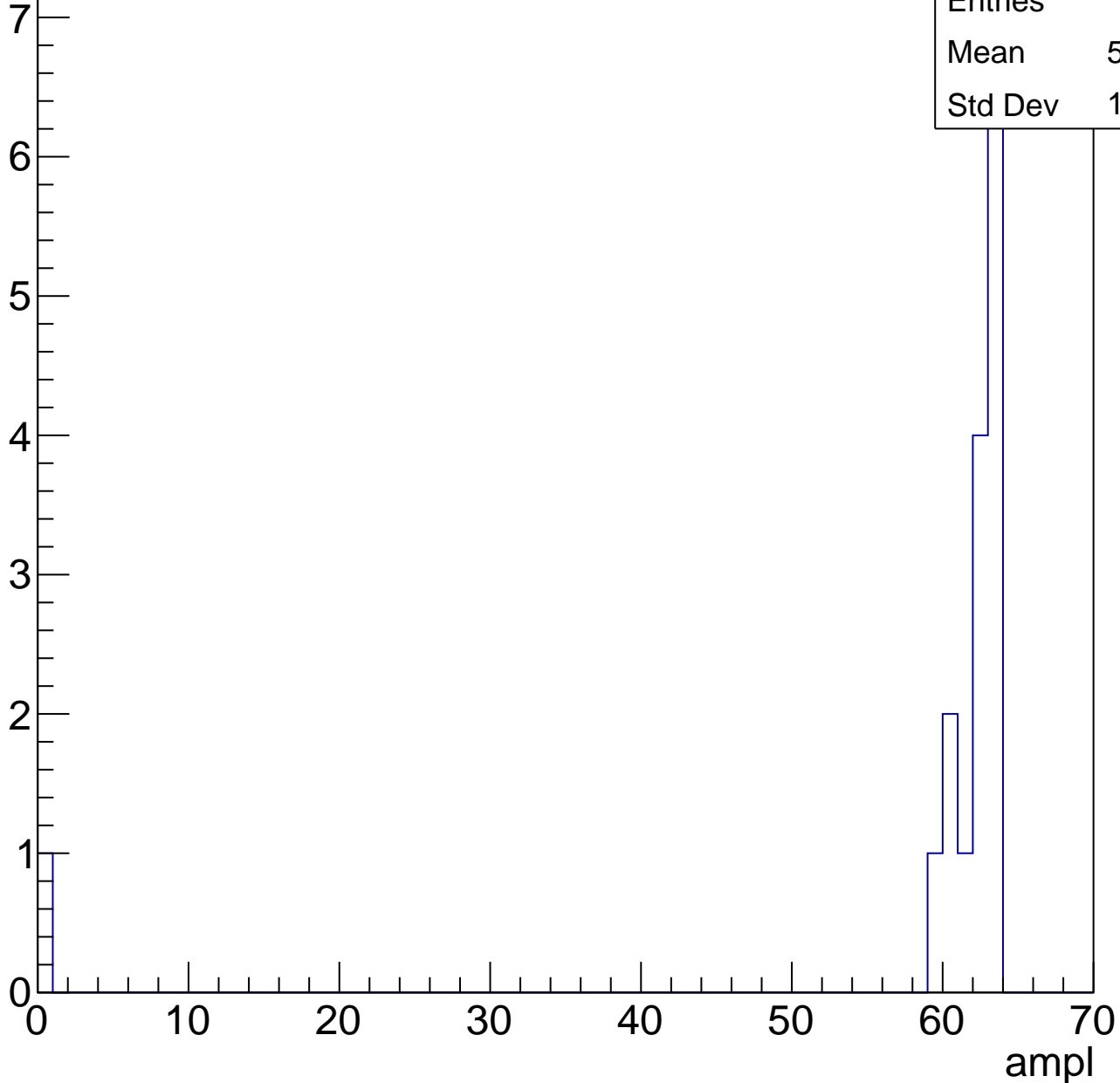


B1L001S, U24-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	58.06
Std Dev	15.04



B1L001S, U24-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	30.19
Std Dev	3.285

Gaus mean : 30.4502

Gaus Width: 3.2581

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

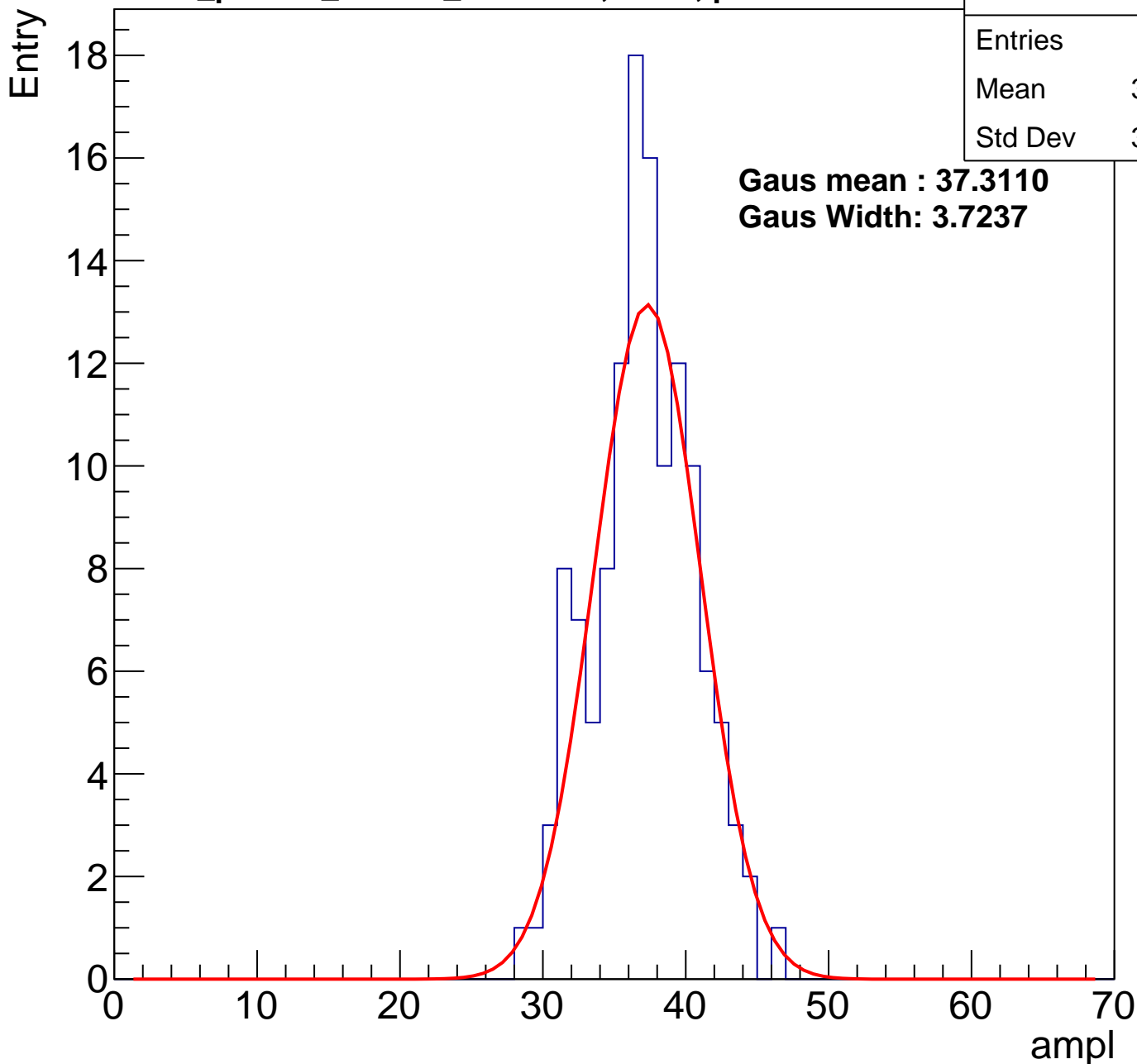
B1L001S, U24-ch58, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.59
Std Dev	3.543

Gaus mean : 37.3110

Gaus Width: 3.7237



B1L001S, U24-ch58, adc2

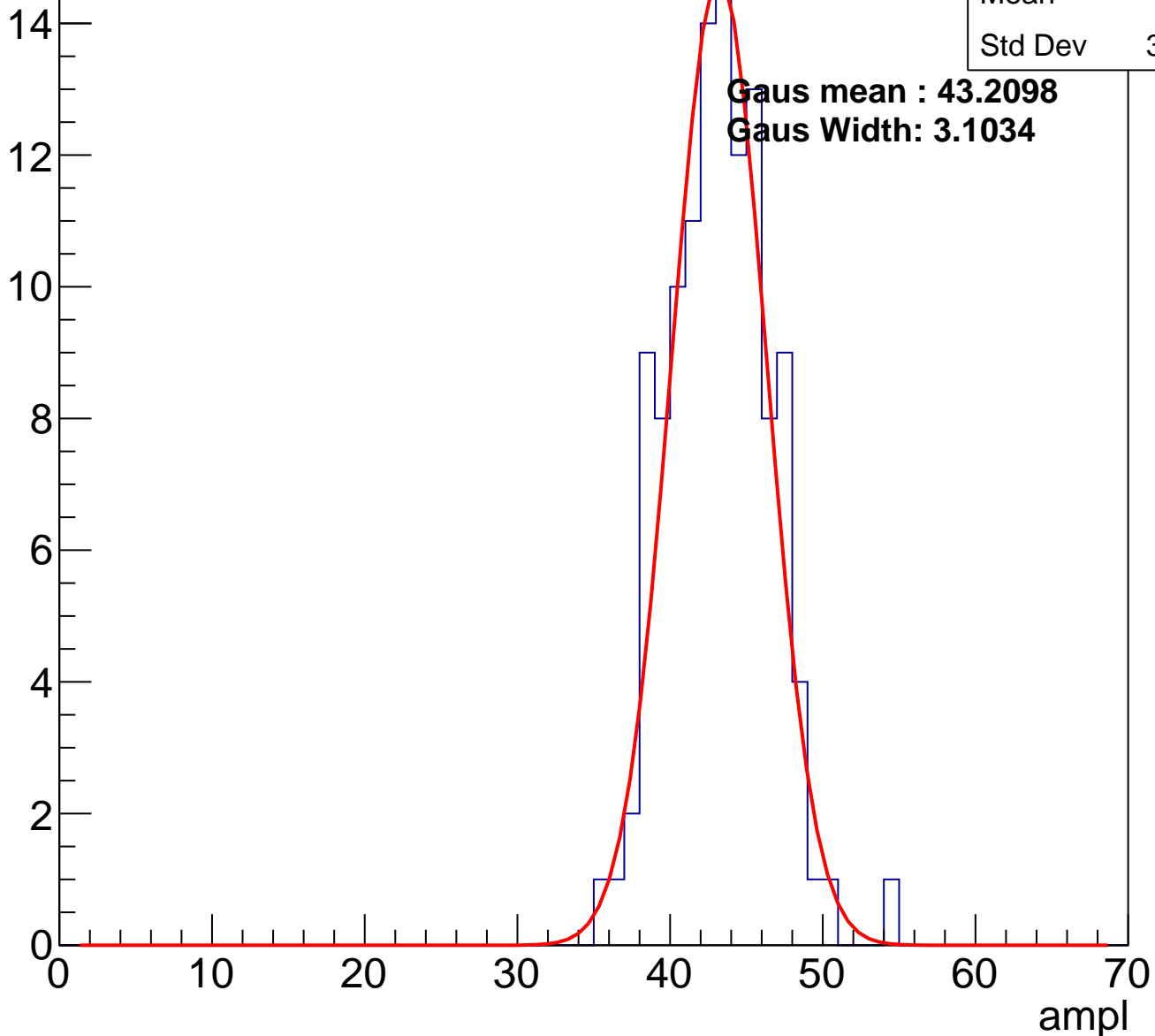
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	42.77
Std Dev	3.247

Gaus mean : 43.2098

Gaus Width: 3.1034

Entry



B1L001S, U24-ch58, adc3

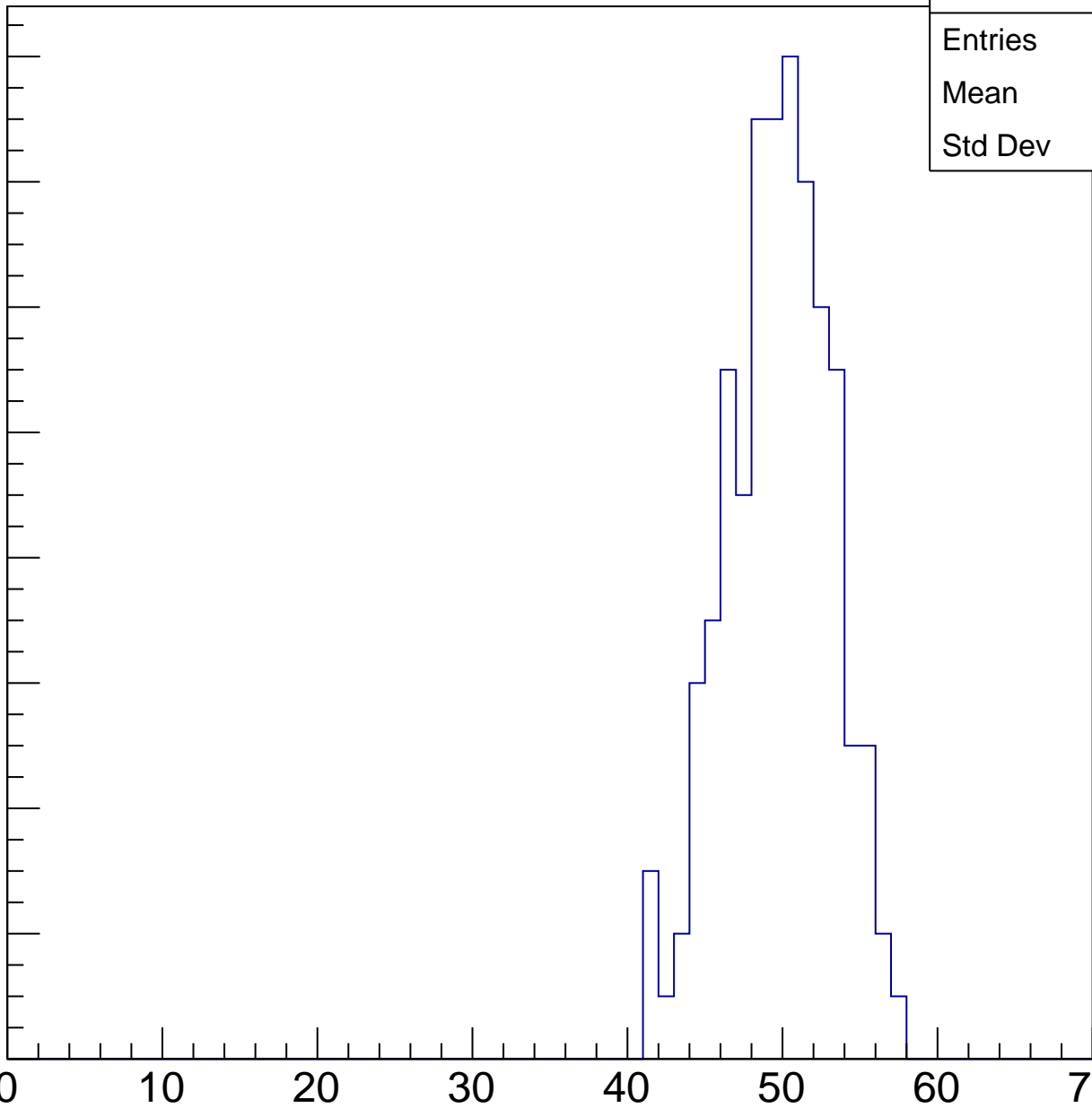
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	49.25
Std Dev	3.377

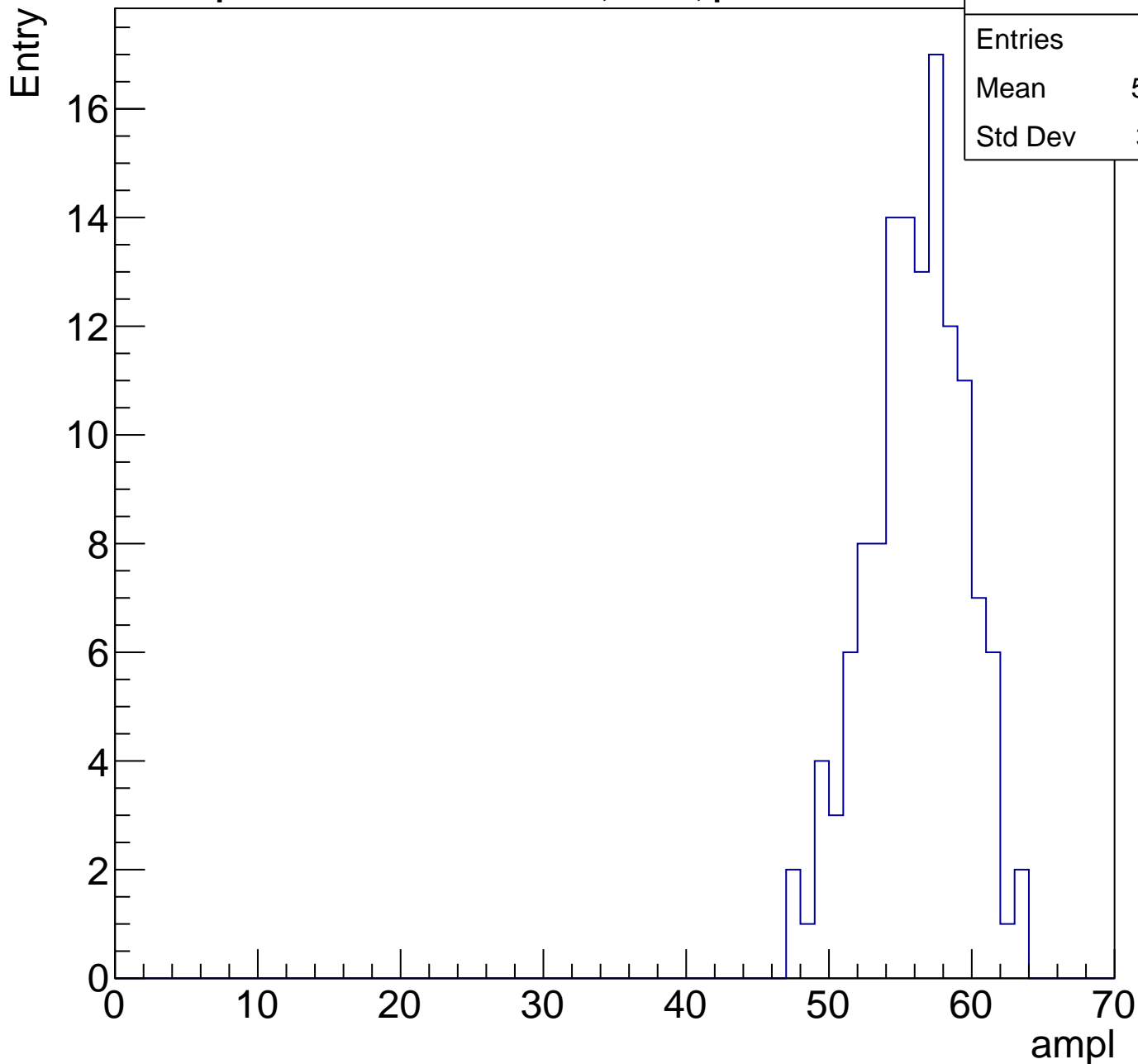
ampl



B1L001S, U24-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	55.63
Std Dev	3.401



B1L001S, U24-ch58, adc5

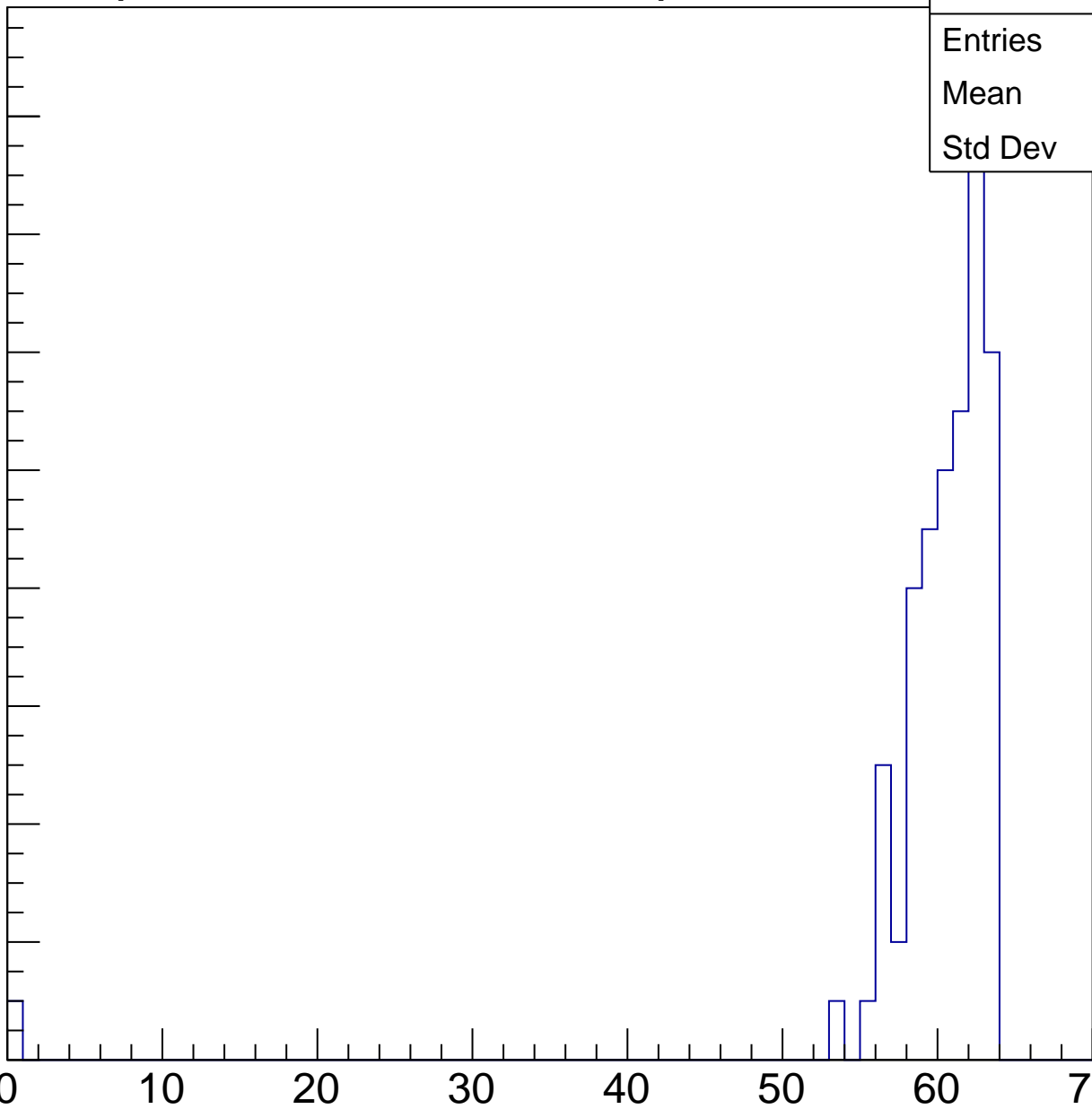
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.45
Std Dev	7.187

ampl

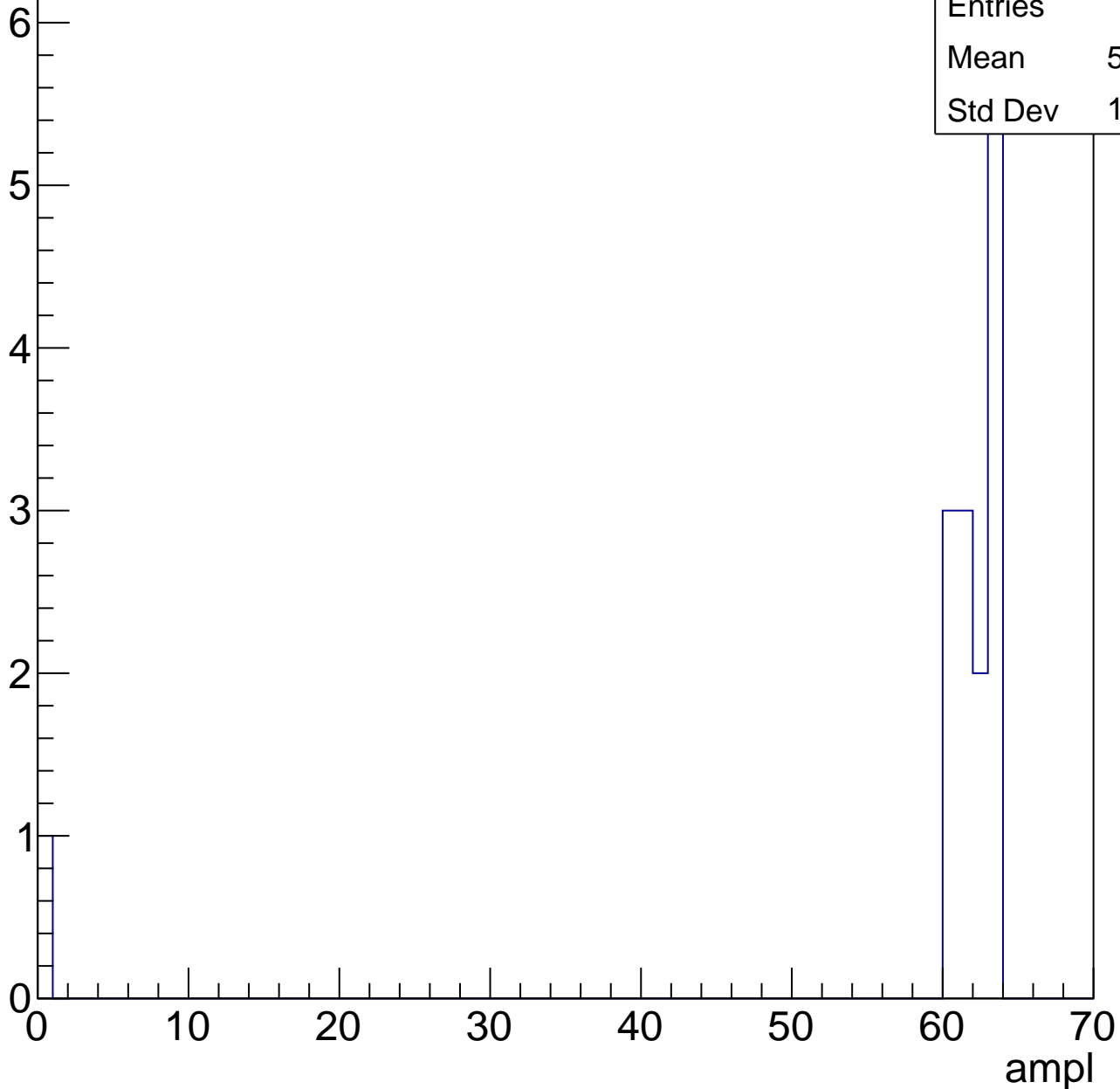


B1L001S, U24-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	57.67
Std Dev	15.46



B1L001S, U24-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	31.38
Std Dev	4.463

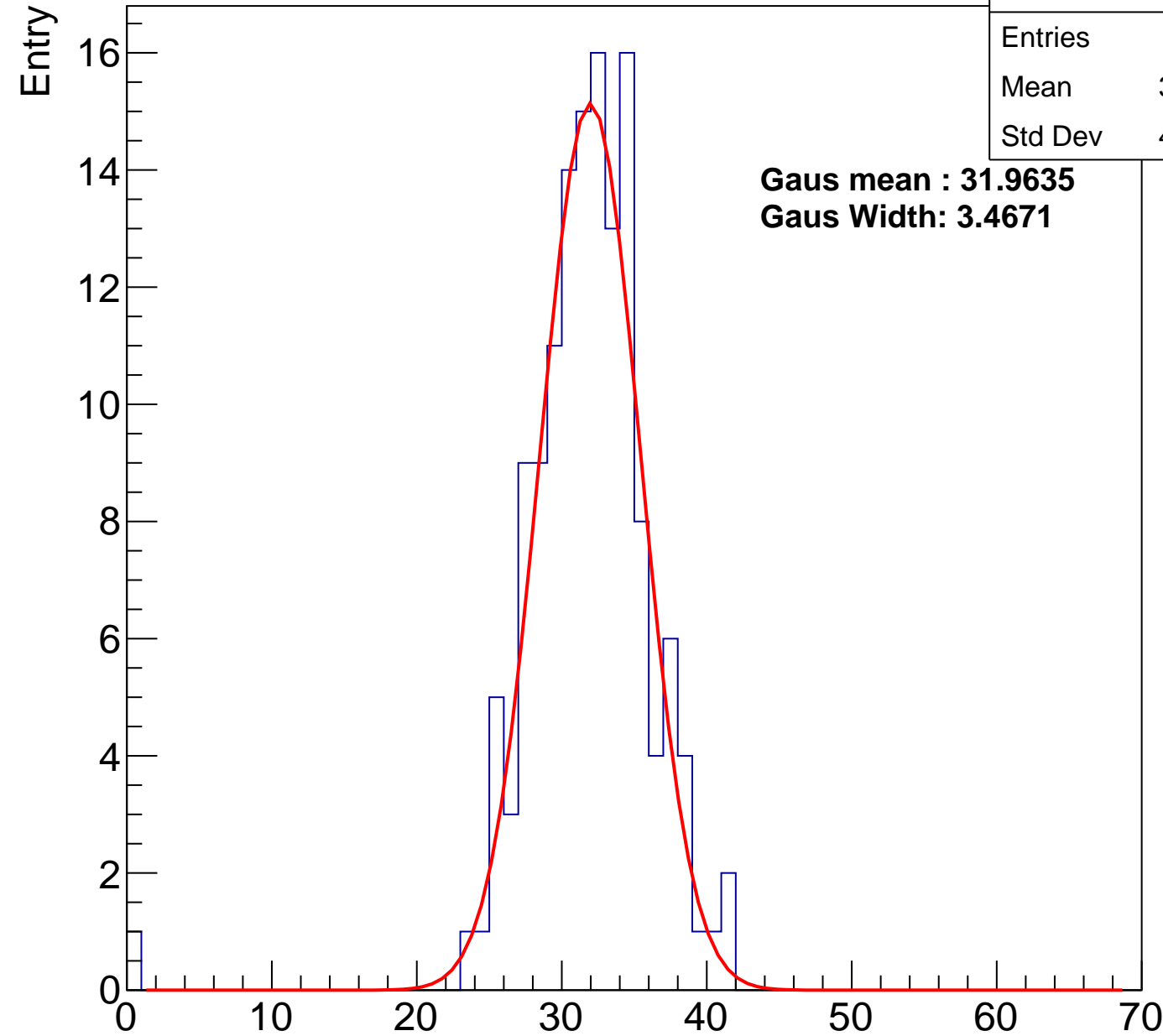
Gaus mean : 31.9635

Gaus Width: 3.4671

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch59, adc1

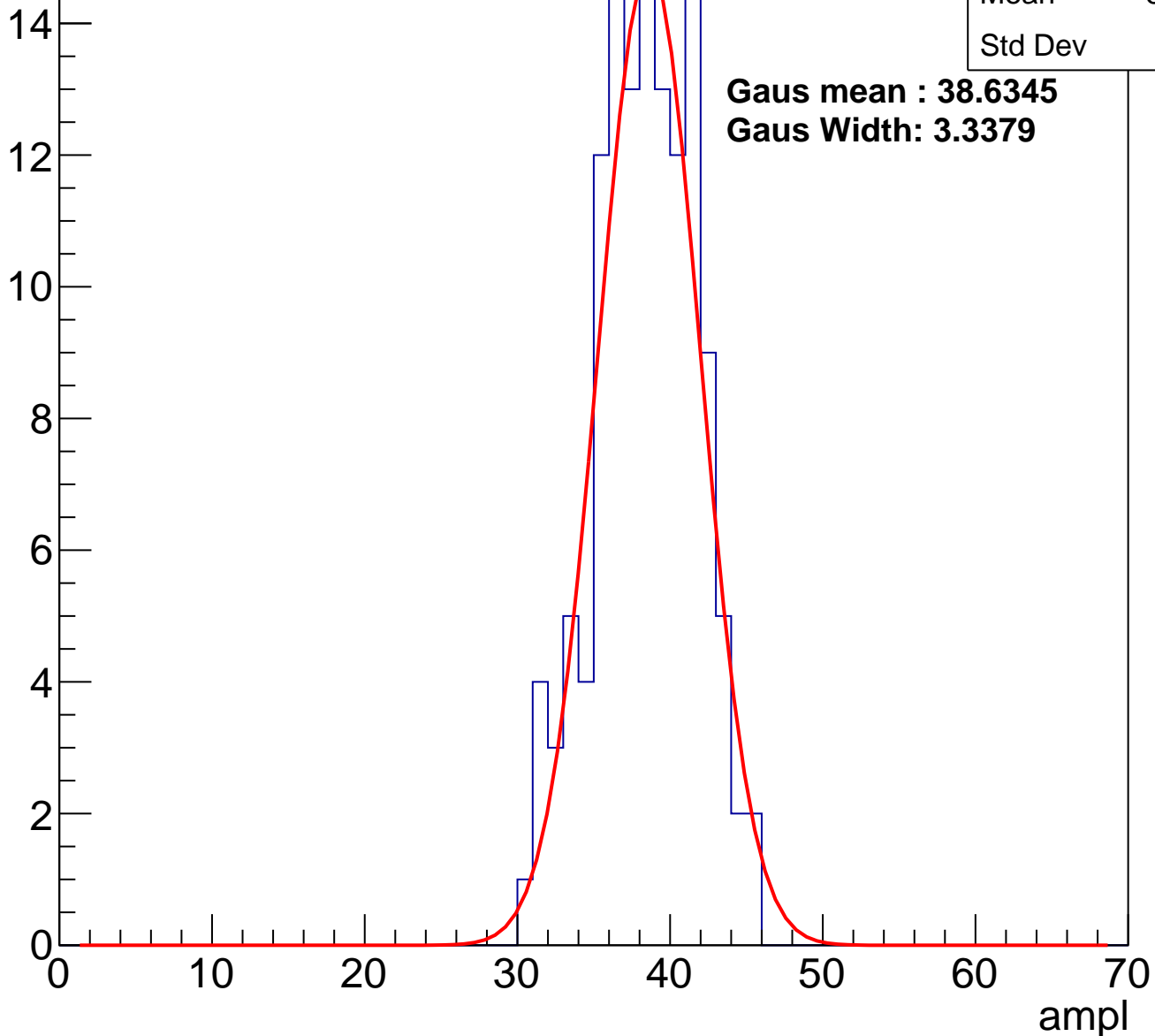
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	37.96
Std Dev	3.25

Gaus mean : 38.6345

Gaus Width: 3.3379

Entry



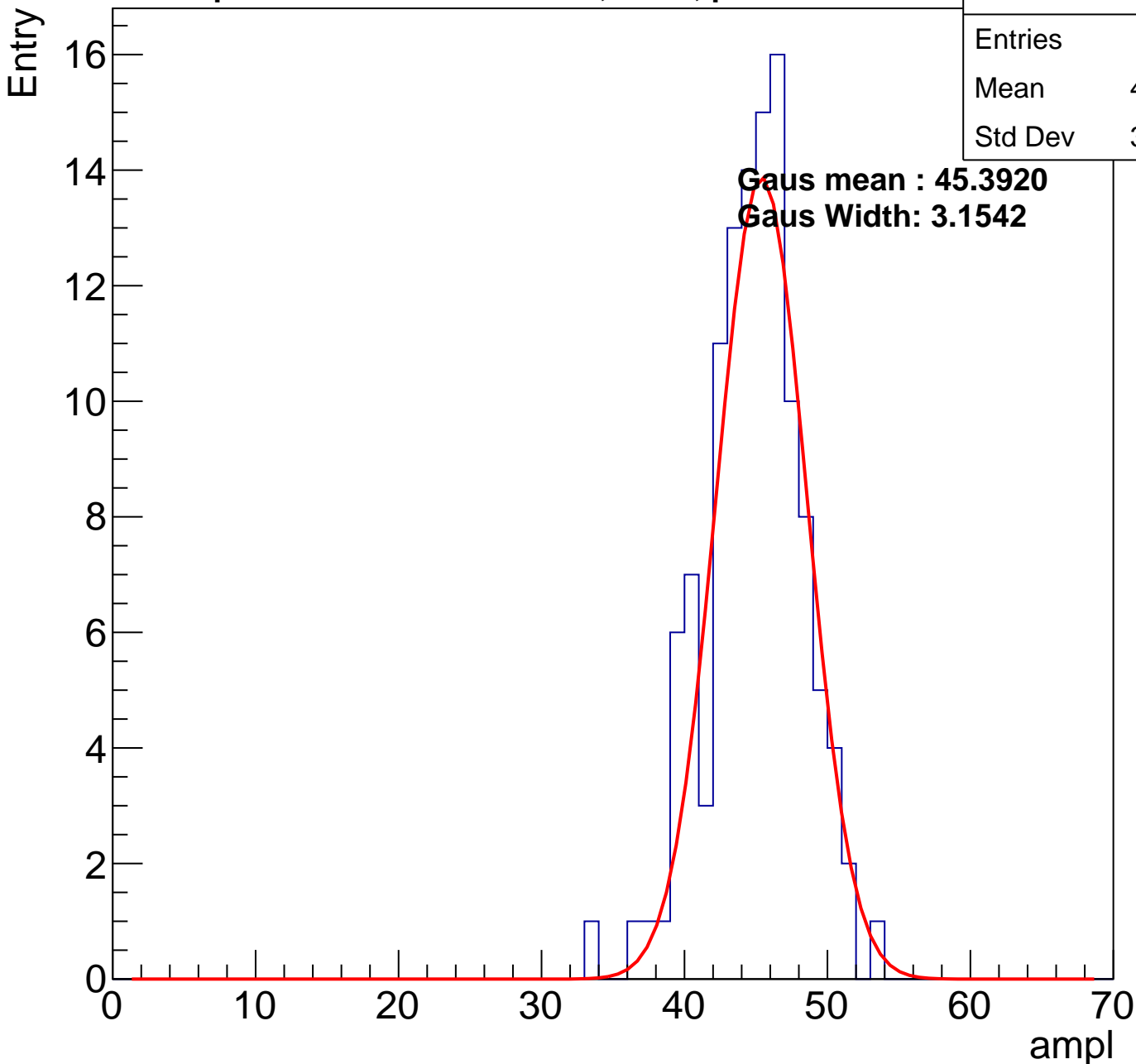
B1L001S, U24-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	44.39
Std Dev	3.359

Gaus mean : 45.3920

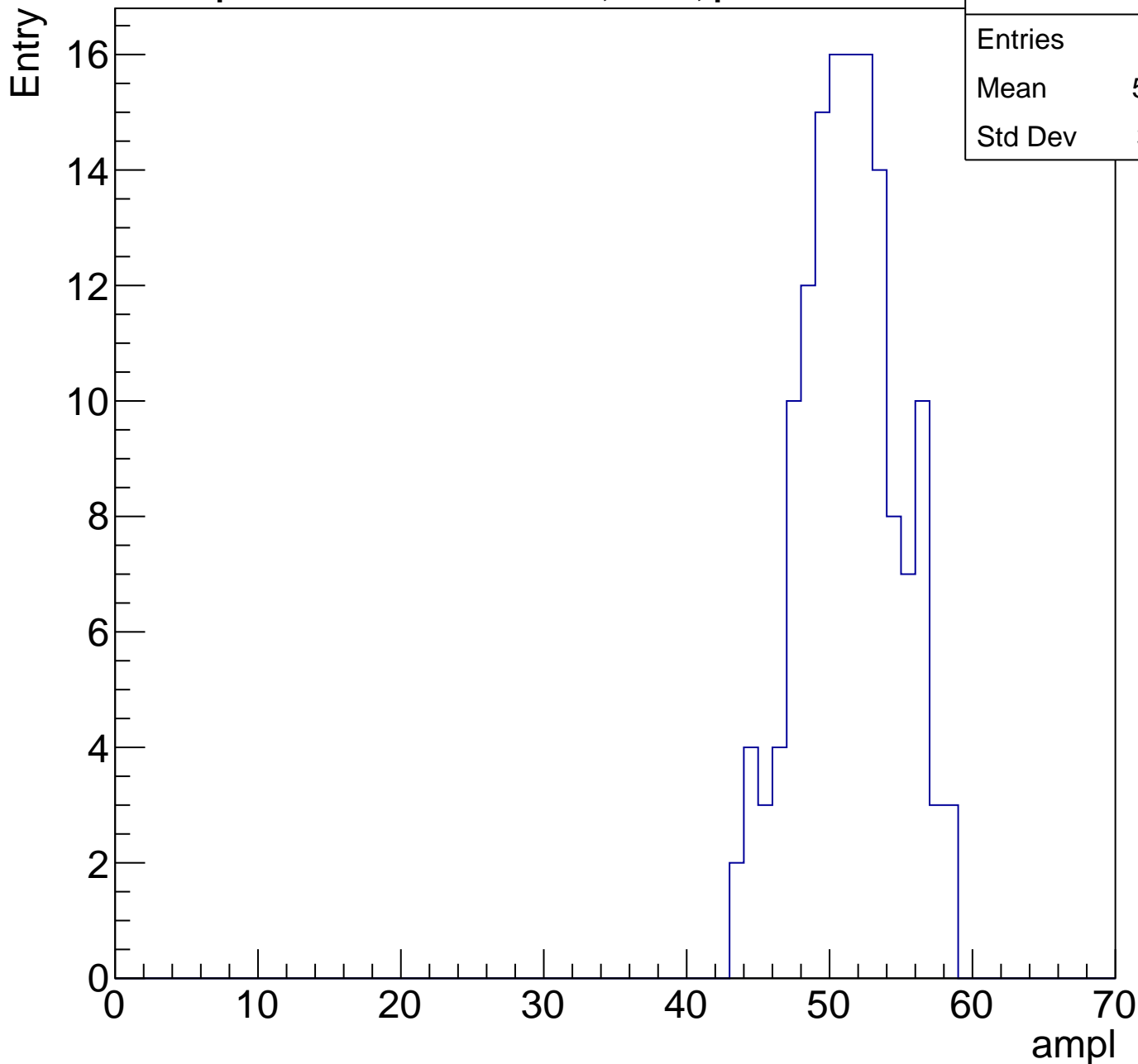
Gaus Width: 3.1542



B1L001S, U24-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	50.87
Std Dev	3.381



B1L001S, U24-ch59, adc4

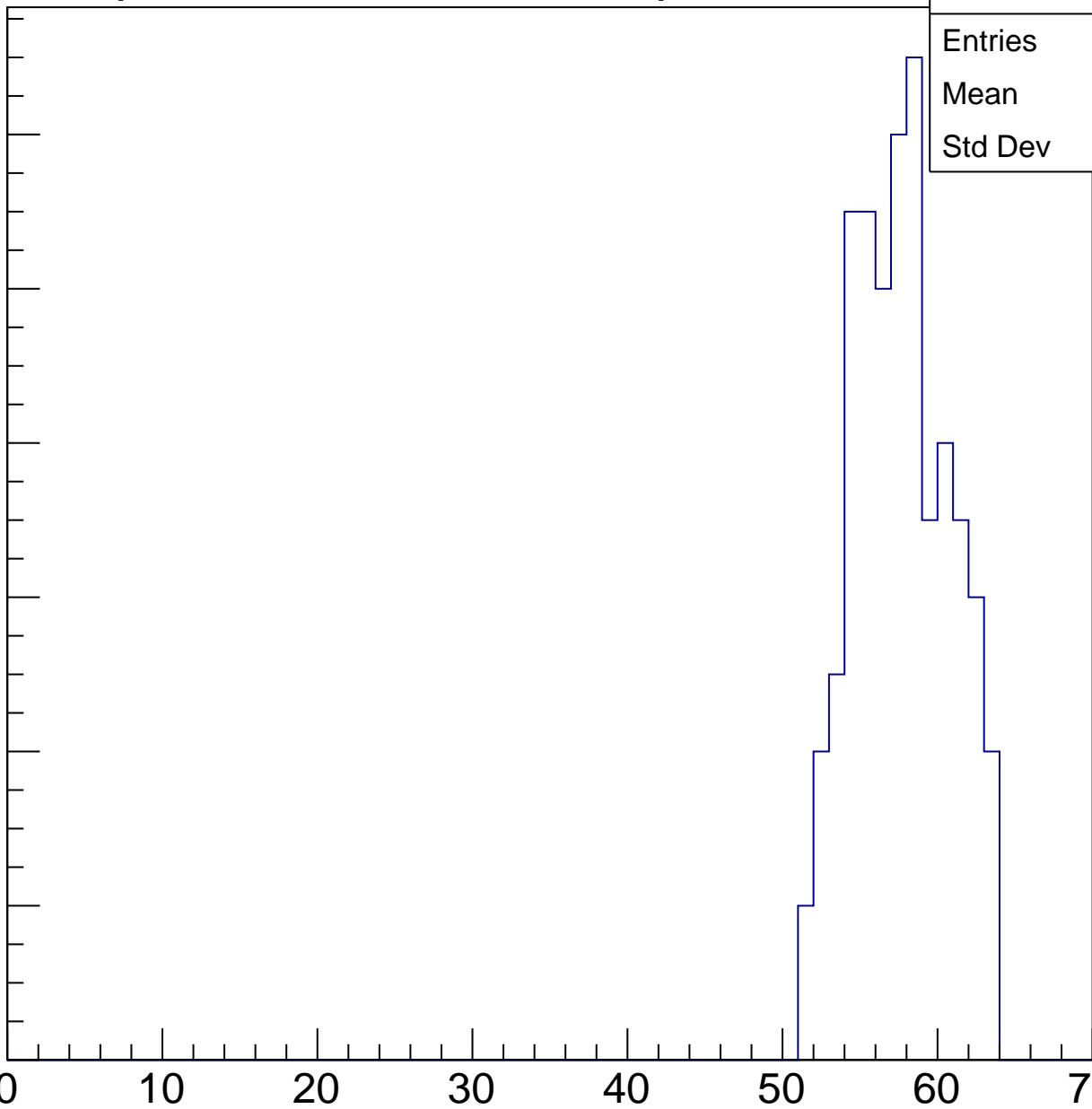
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	100
Mean	57.16
Std Dev	3.036

ampl



B1L001S, U24-ch59, adc5

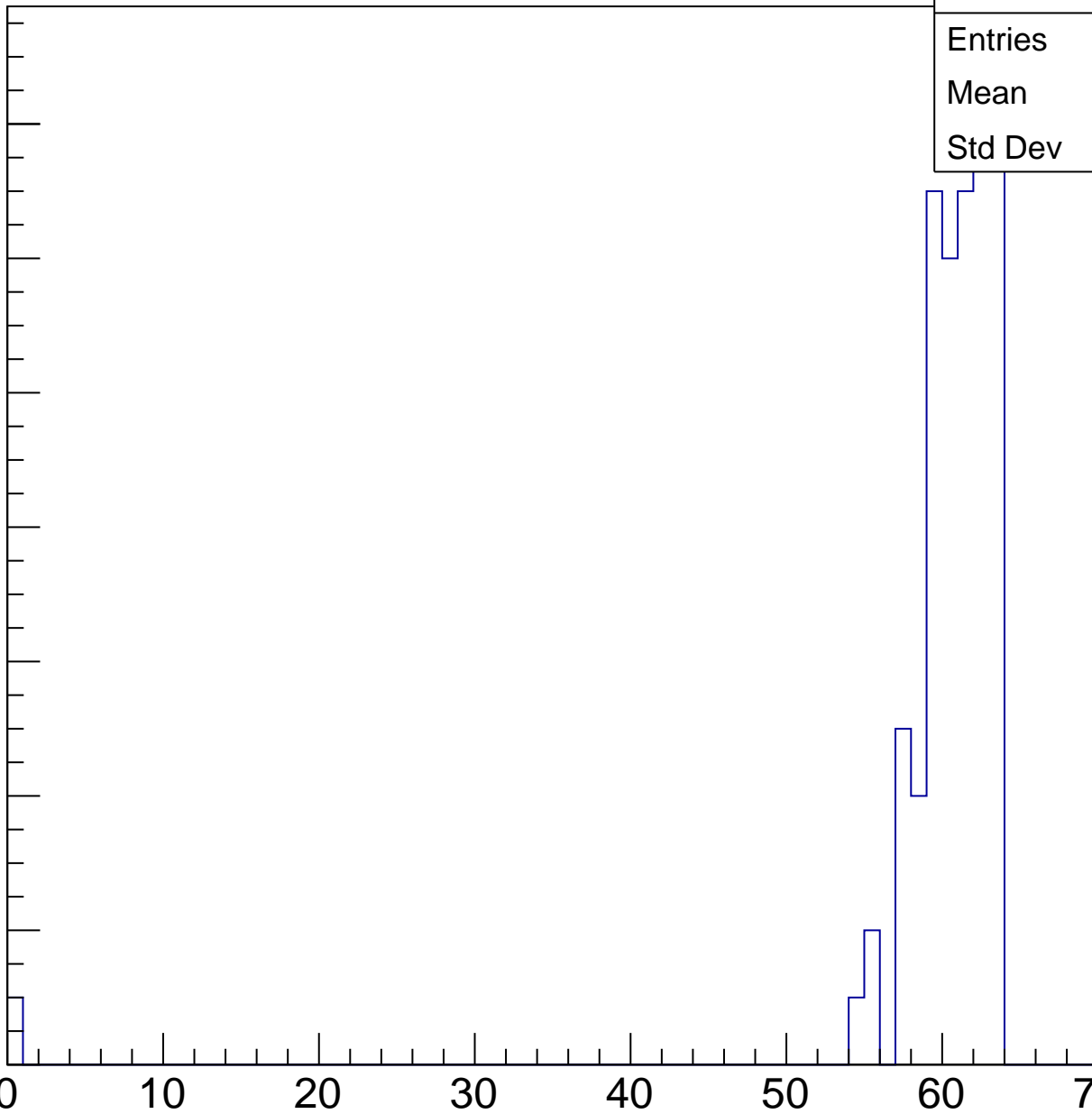
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.67
Std Dev	7.032

ampl



B1L001S, U24-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L001S, U24-ch60, adc0

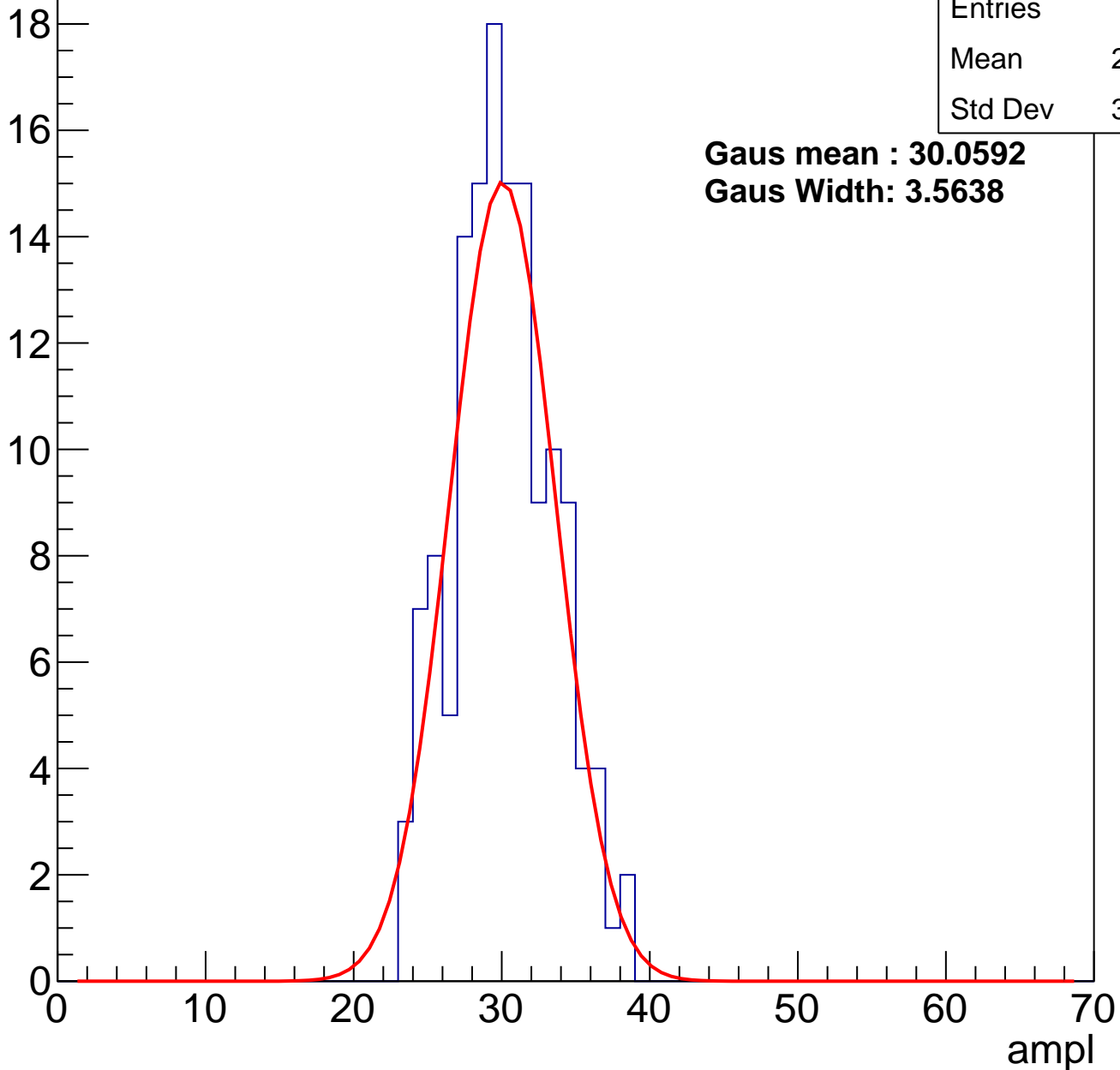
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	29.66
Std Dev	3.364

Gaus mean : 30.0592

Gaus Width: 3.5638

Entry

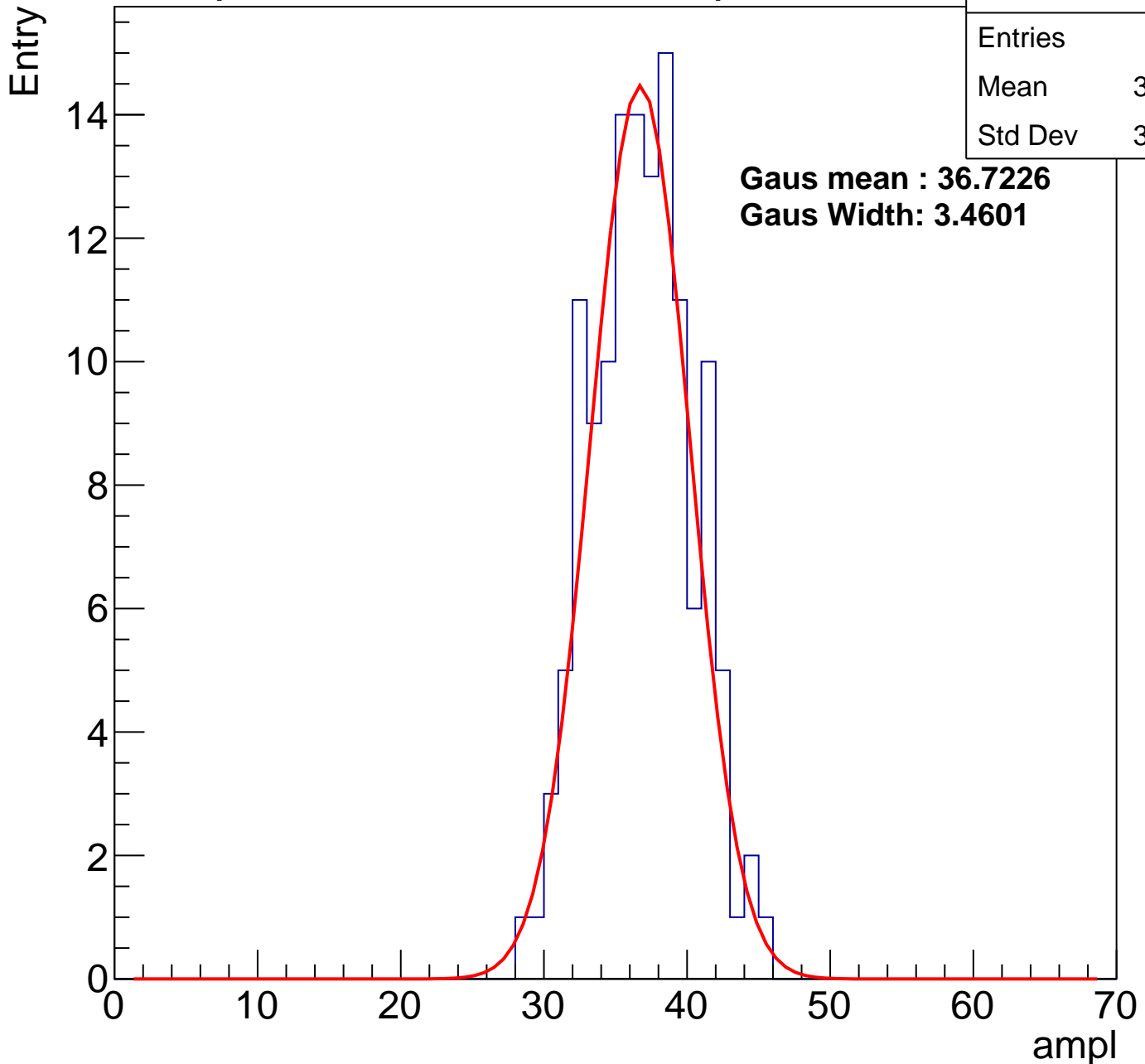


B1L001S, U24-ch60, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.37
Std Dev	3.476

Gaus mean : 36.7226
Gaus Width: 3.4601



B1L001S, U24-ch60, adc2

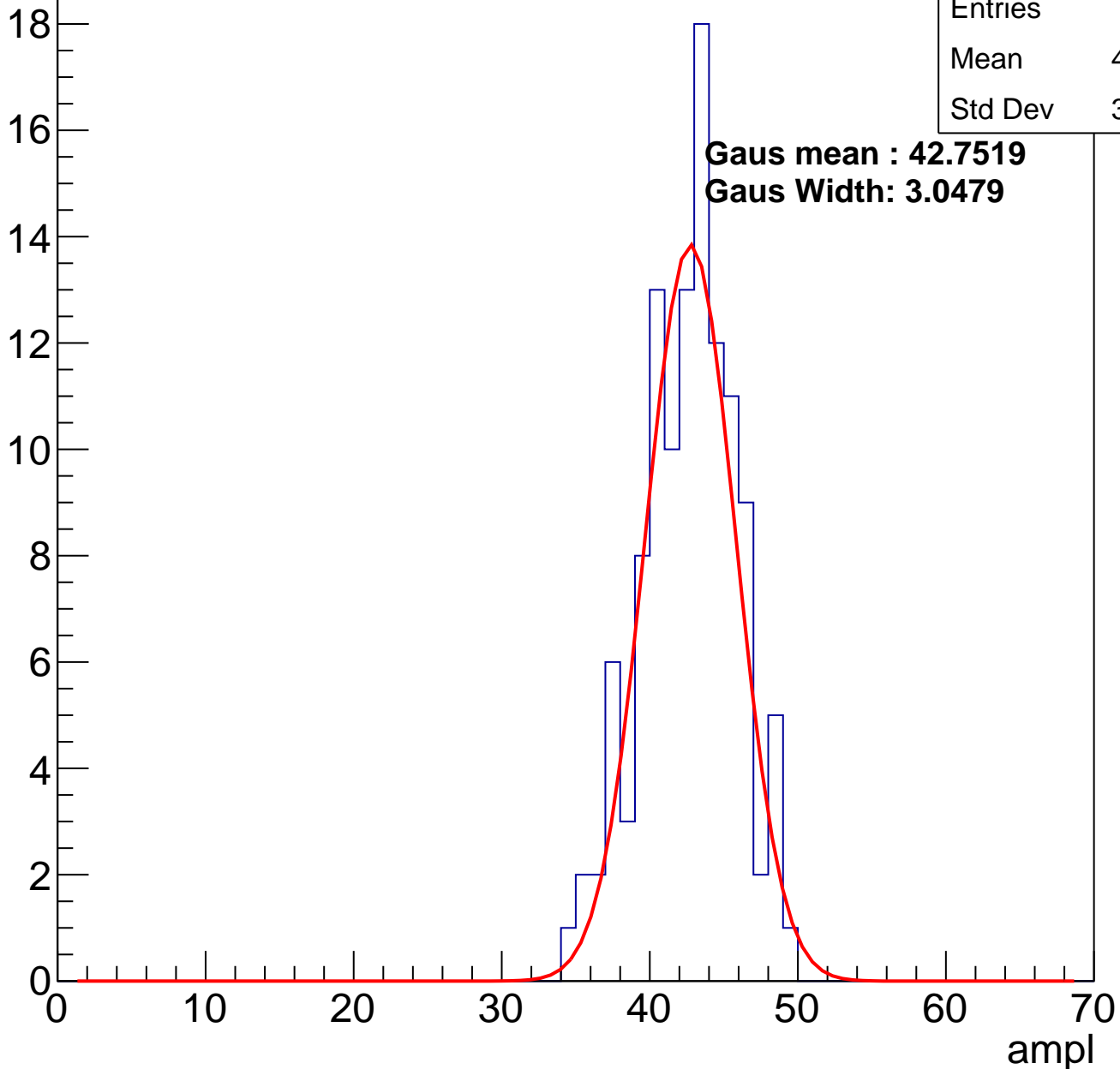
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	42.19
Std Dev	3.148

Gaus mean : 42.7519

Gaus Width: 3.0479

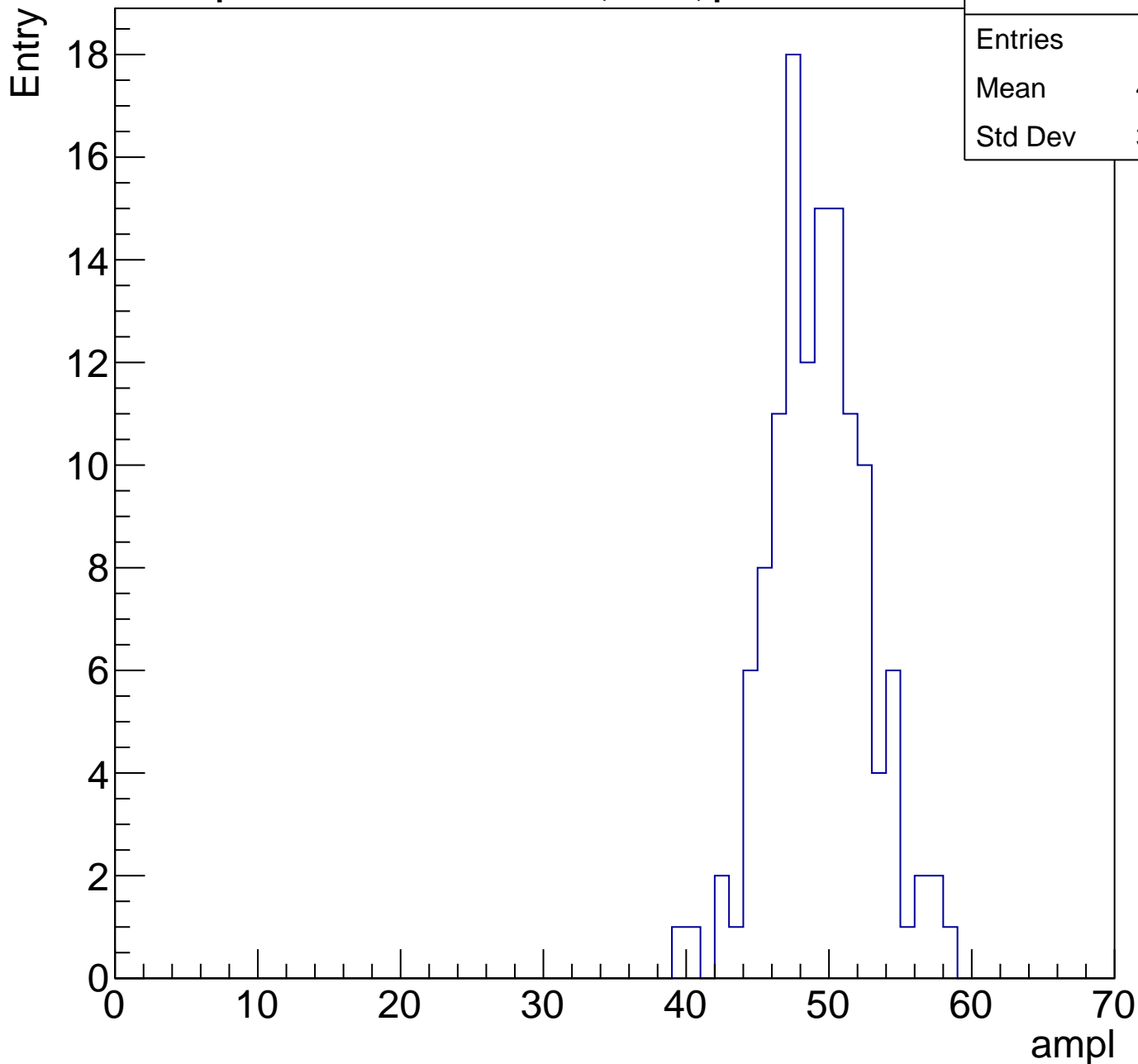
Entry



B1L001S, U24-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	48.81
Std Dev	3.411



B1L001S, U24-ch60, adc4

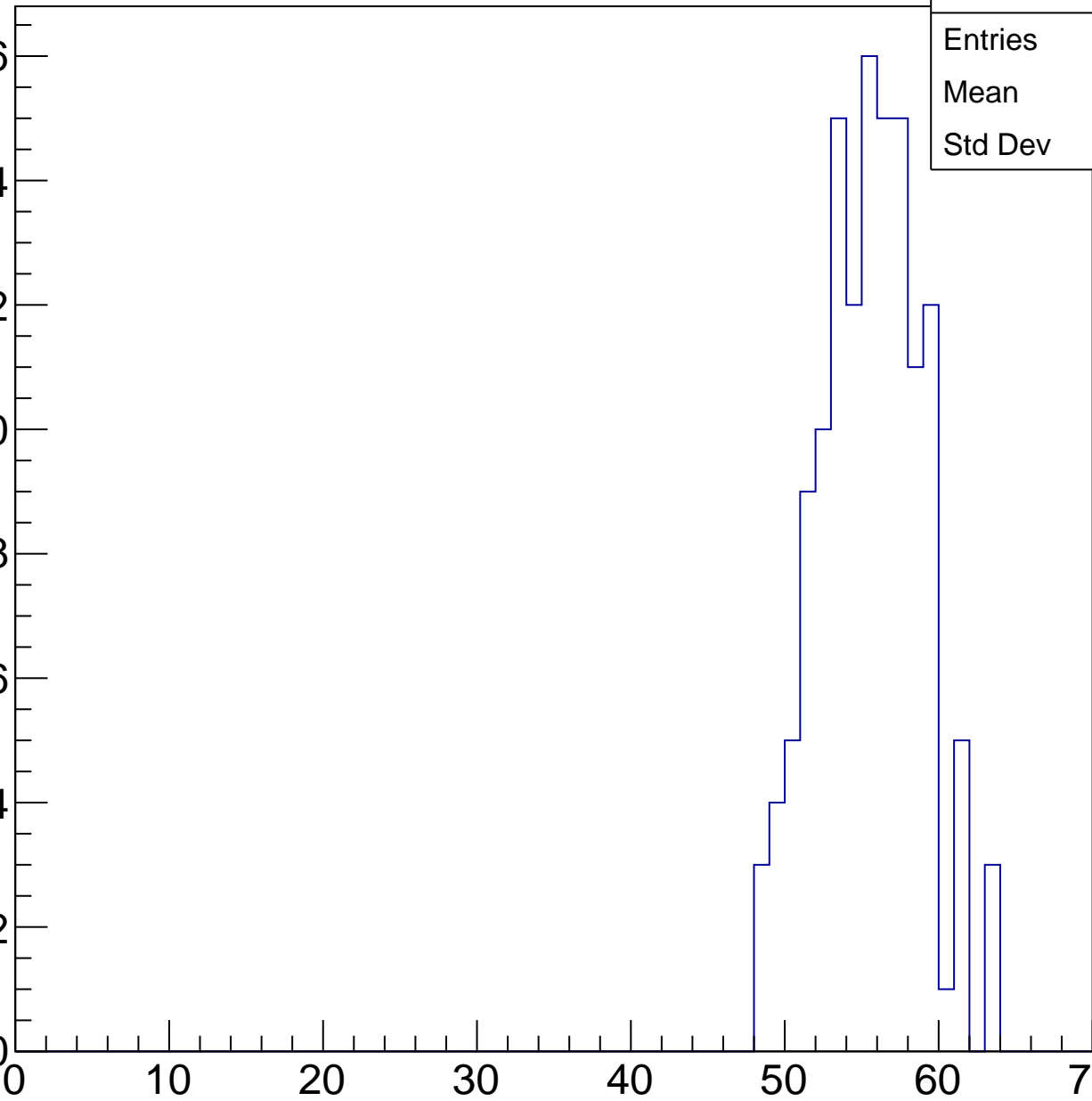
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	55.05
Std Dev	3.304

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch60, adc5

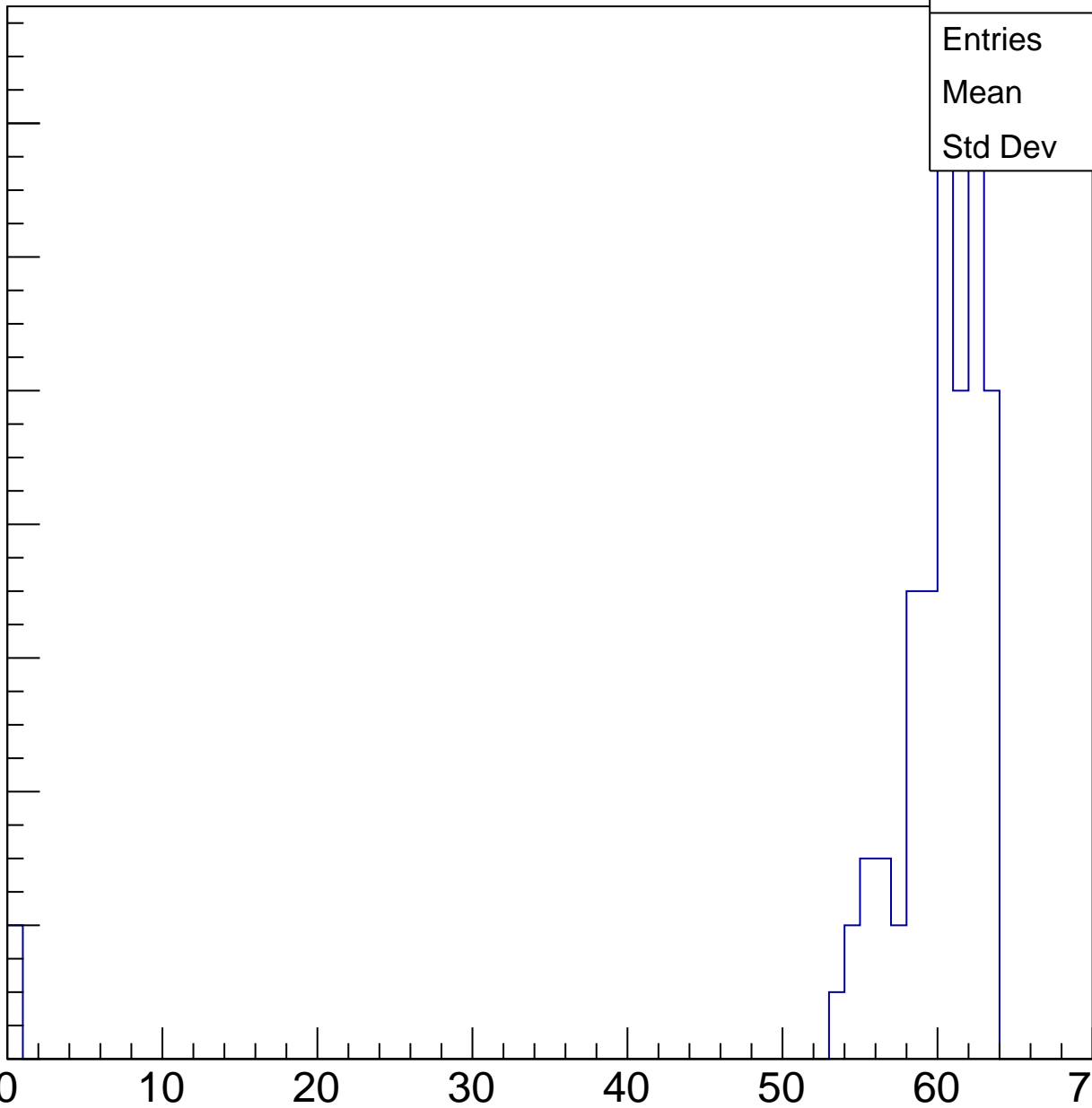
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.36
Std Dev	9.899

ampl

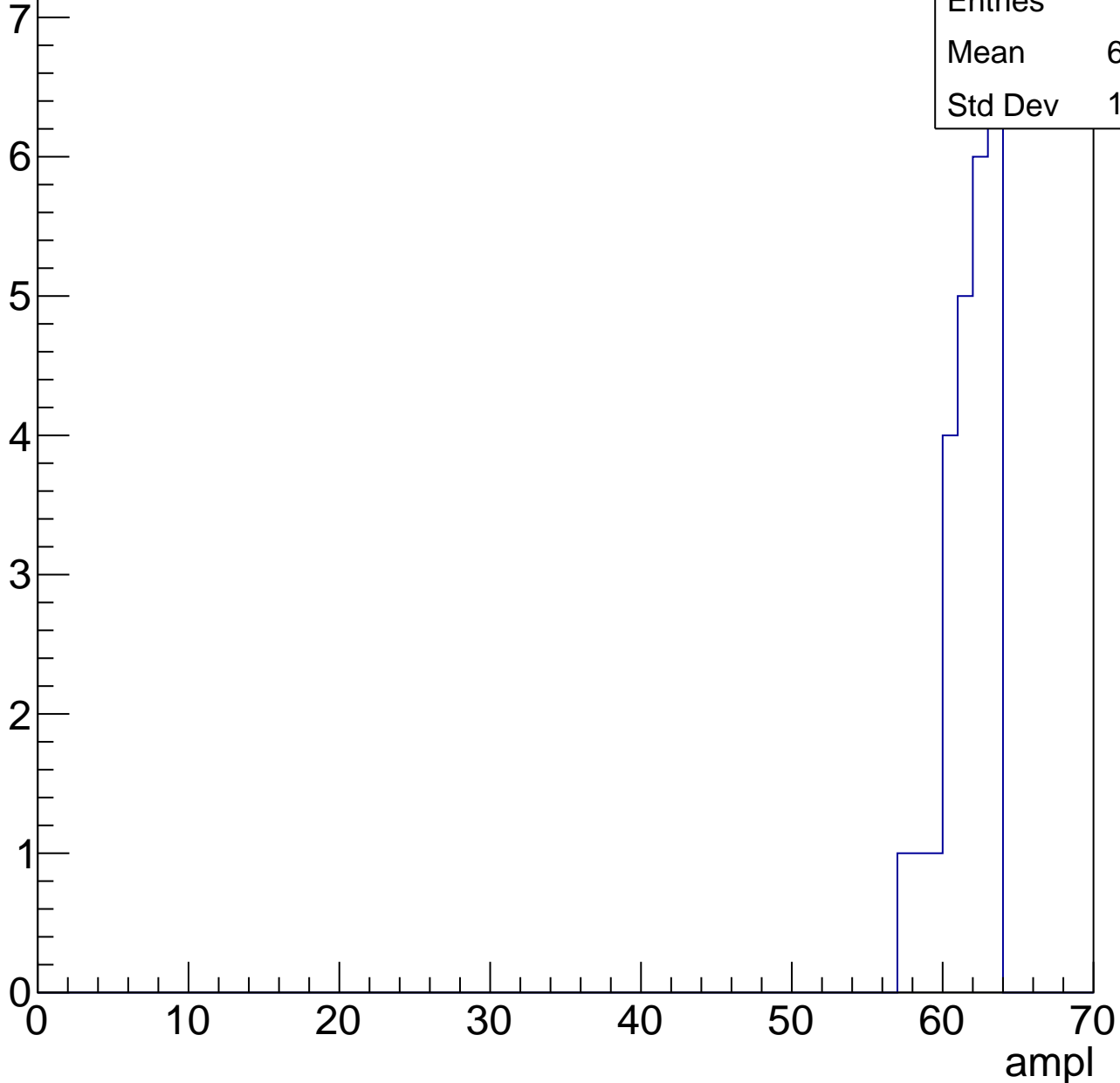


B1L001S, U24-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	25
Mean	61.28
Std Dev	1.613



B1L001S, U24-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U24-ch61, adc0

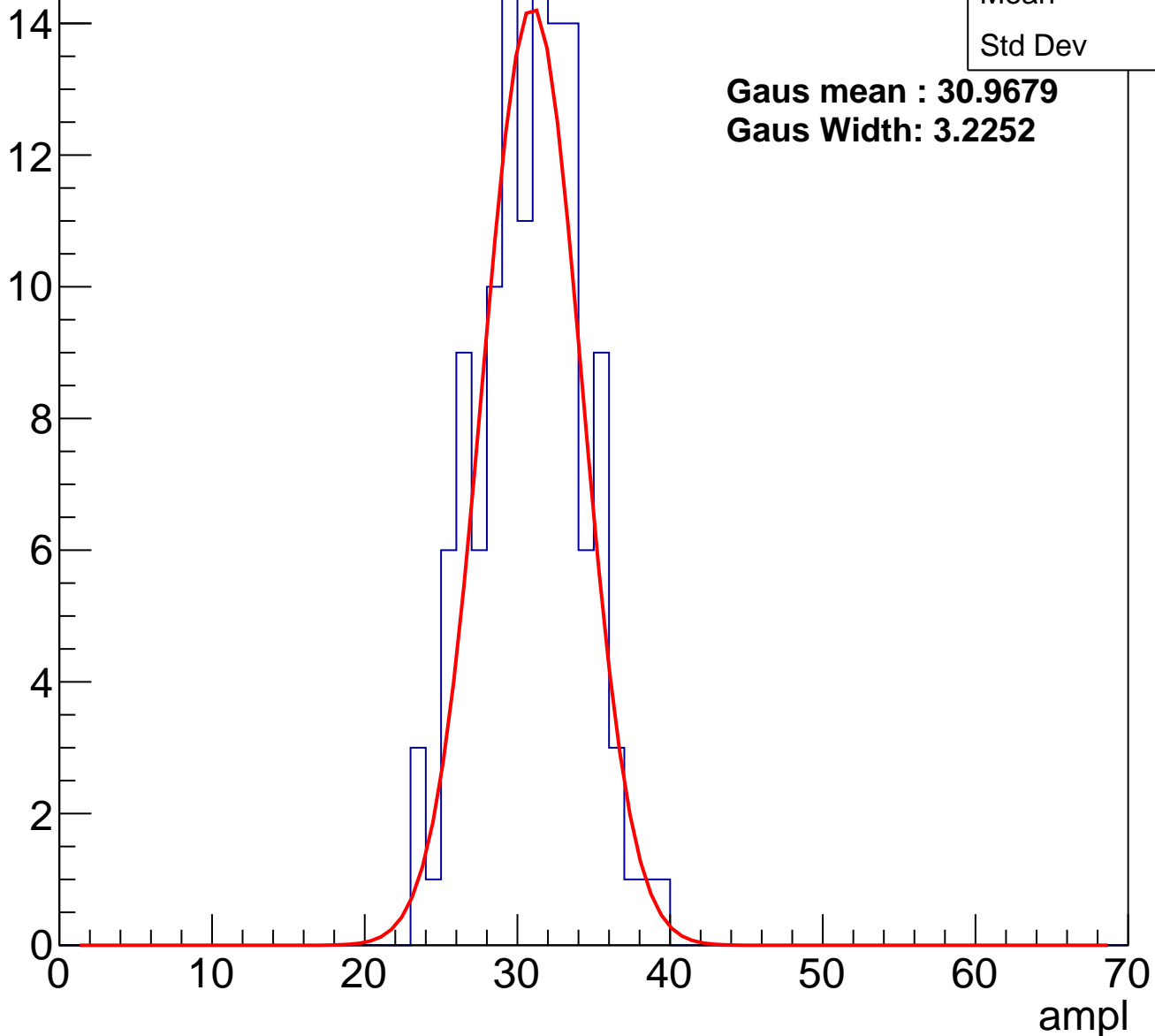
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.4
Std Dev	3.33

Gaus mean : 30.9679

Gaus Width: 3.2252

Entry



B1L001S, U24-ch61, adc1

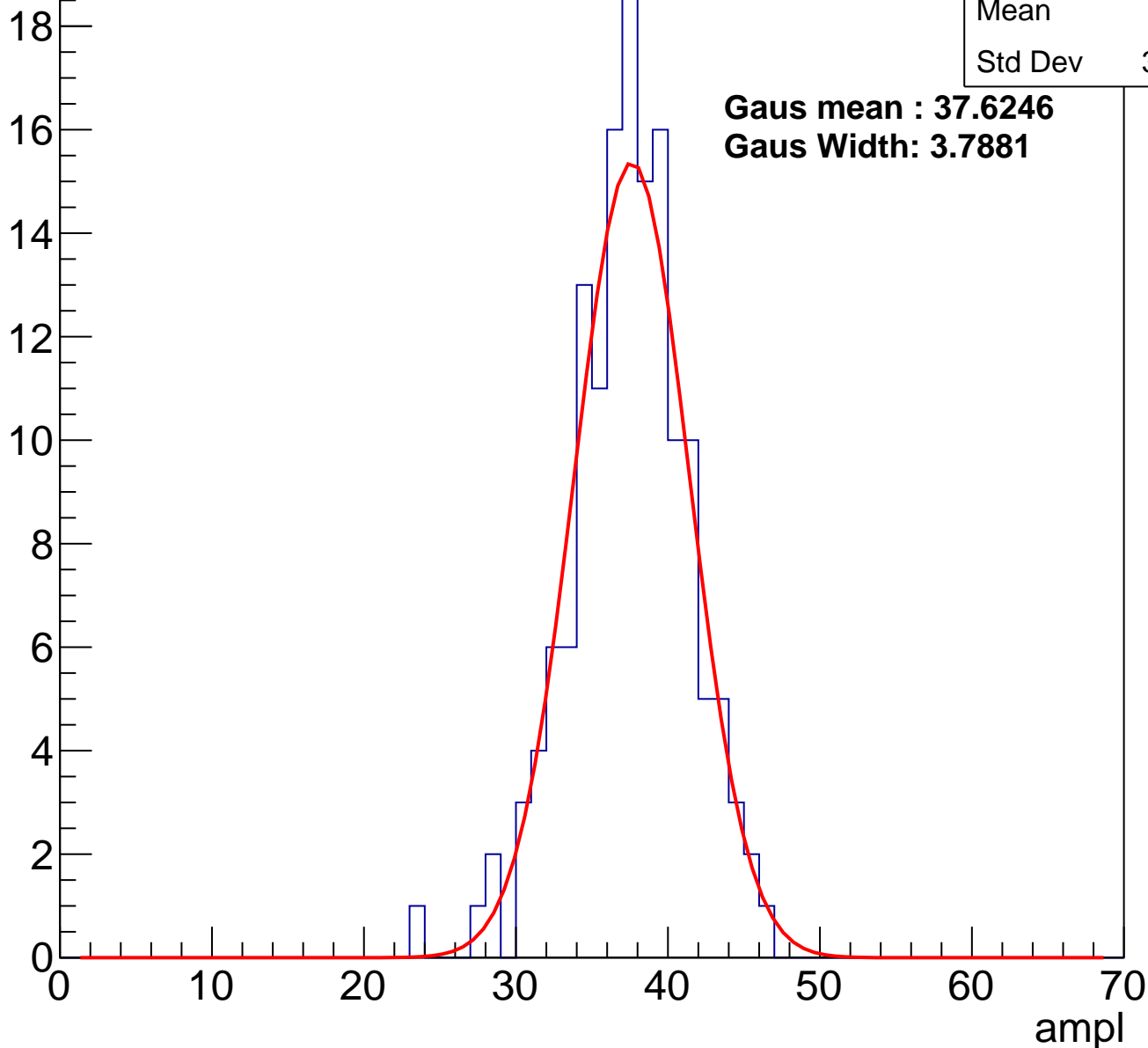
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37
Std Dev	3.809

Gaus mean : 37.6246

Gaus Width: 3.7881

Entry



B1L001S, U24-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

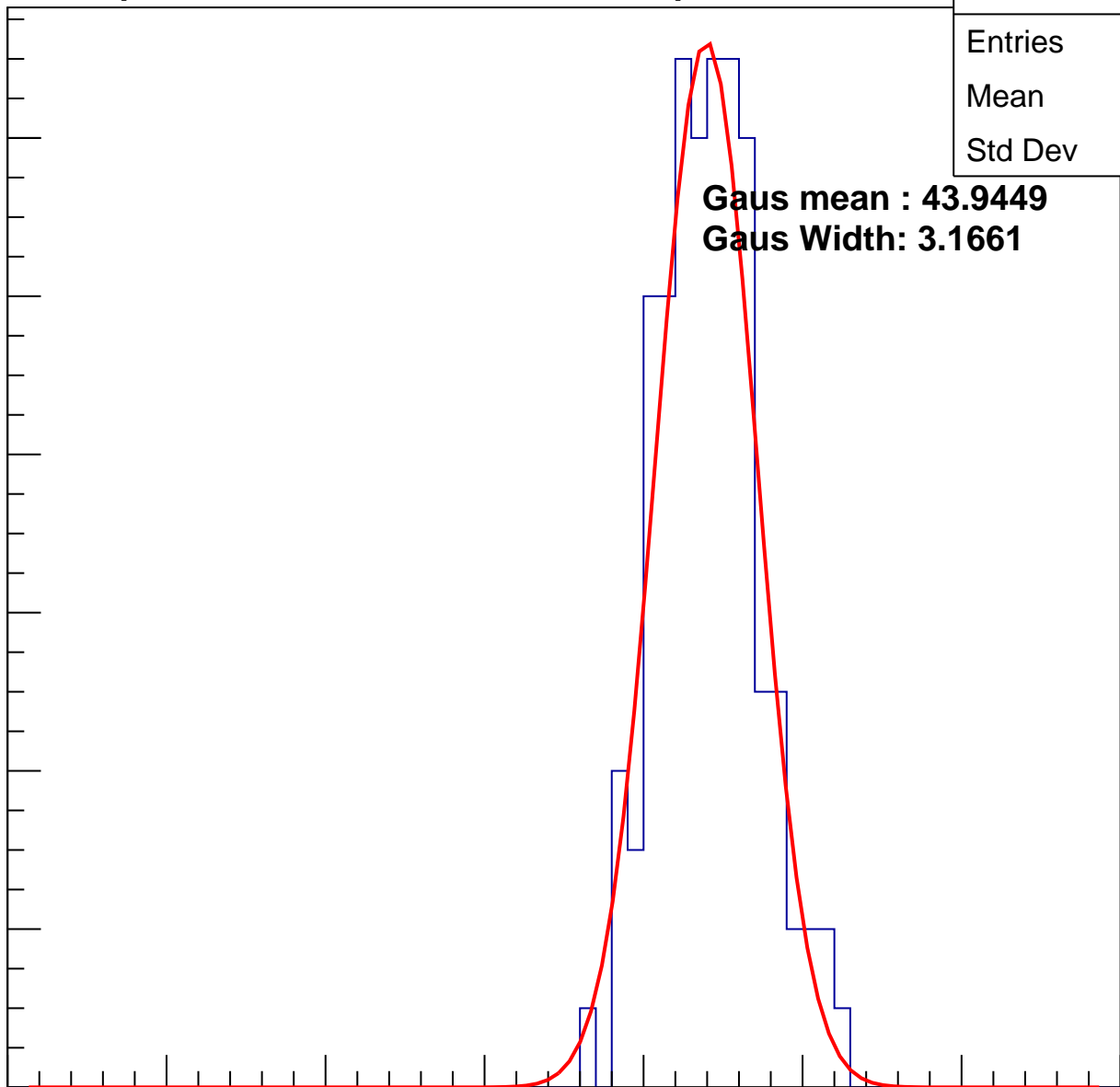
70

ampl

Entries	108
Mean	43.64
Std Dev	3.122

Gaus mean : 43.9449

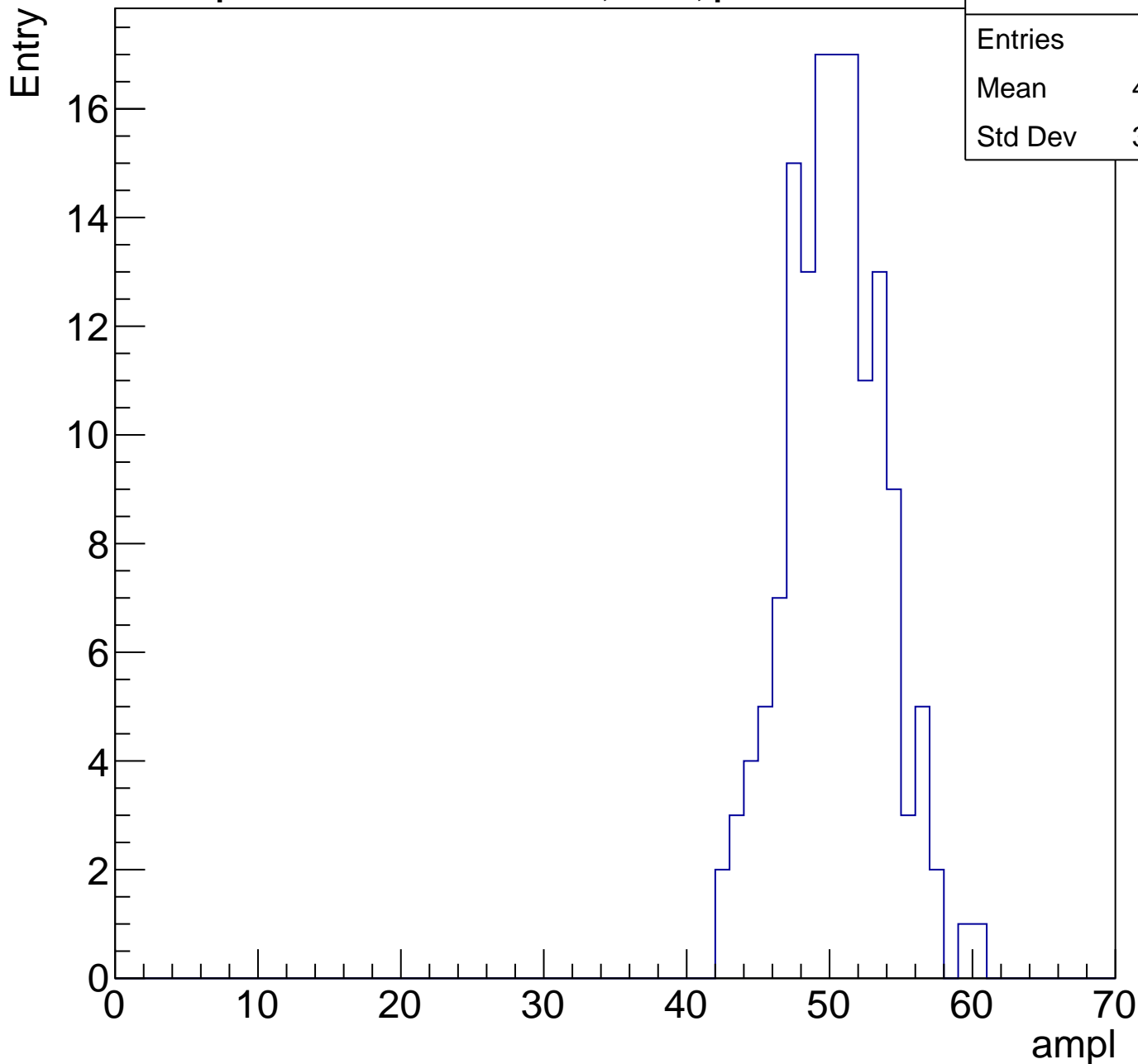
Gaus Width: 3.1661



B1L001S, U24-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

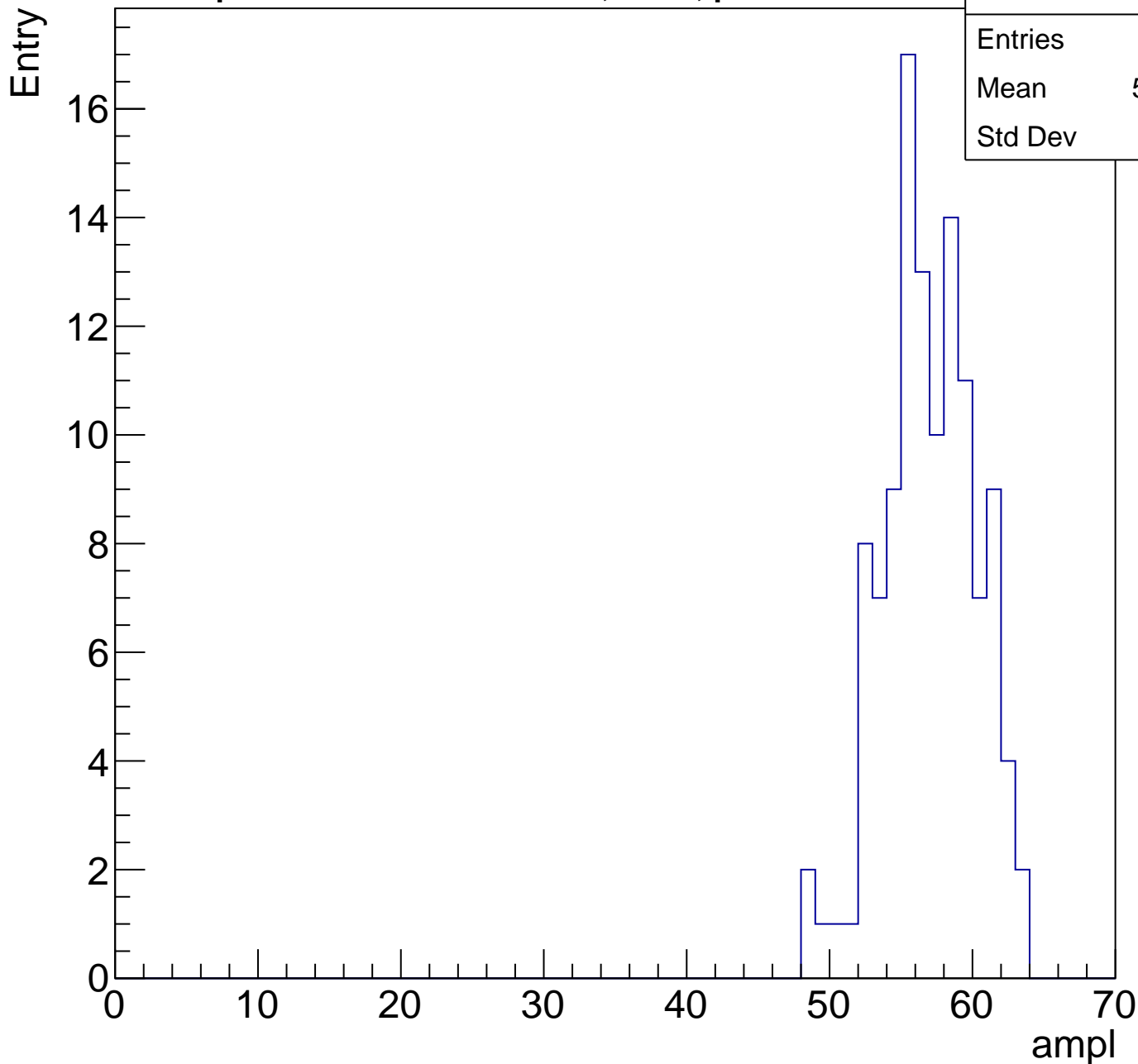
Entries	145
Mean	49.93
Std Dev	3.439



B1L001S, U24-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	56.52
Std Dev	3.21



B1L001S, U24-ch61, adc5

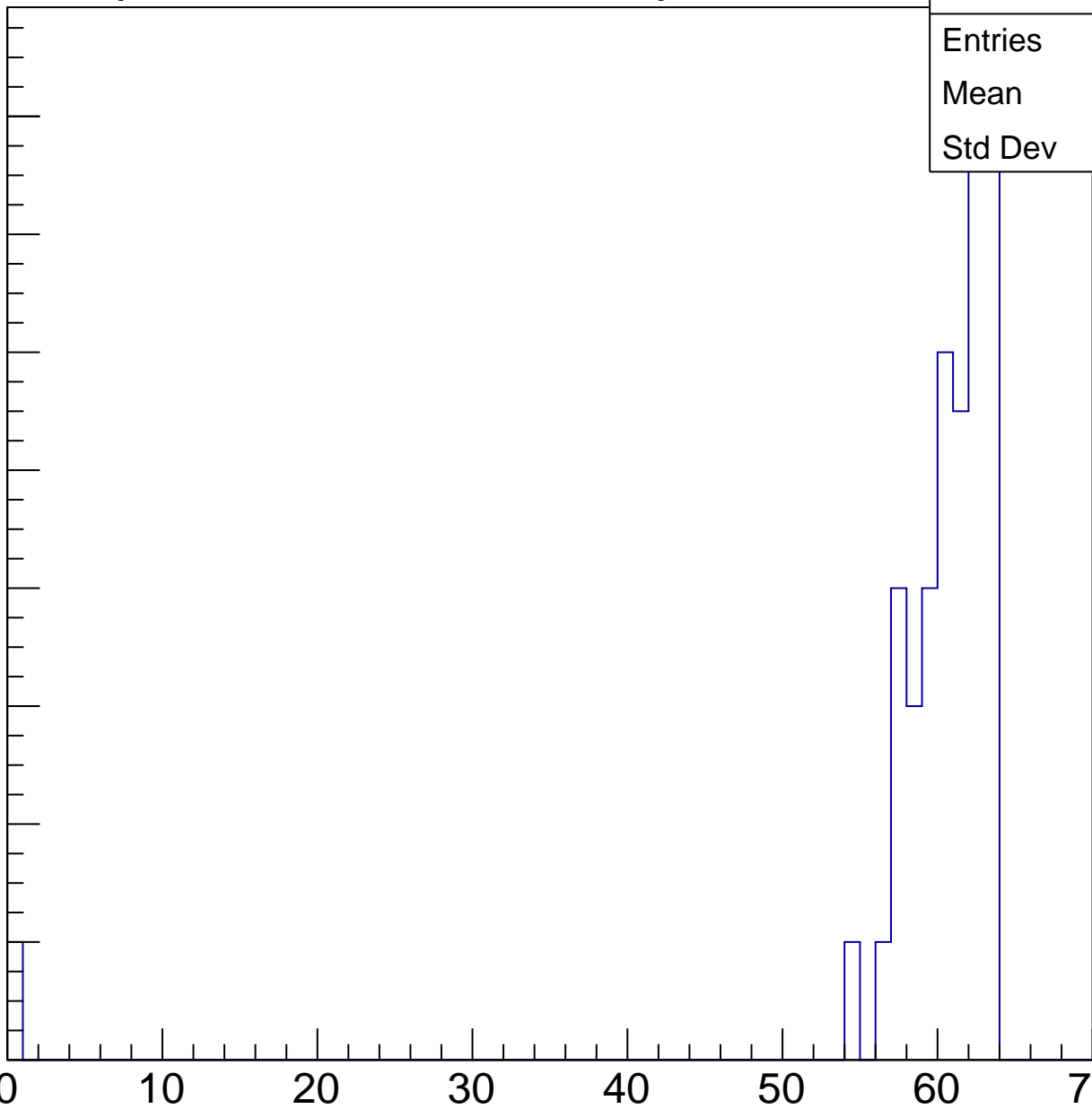
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.93
Std Dev	9.474

ampl



B1L001S, U24-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	29.76
Std Dev	3.761

Gaus mean : 30.2807

Gaus Width: 3.6965

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

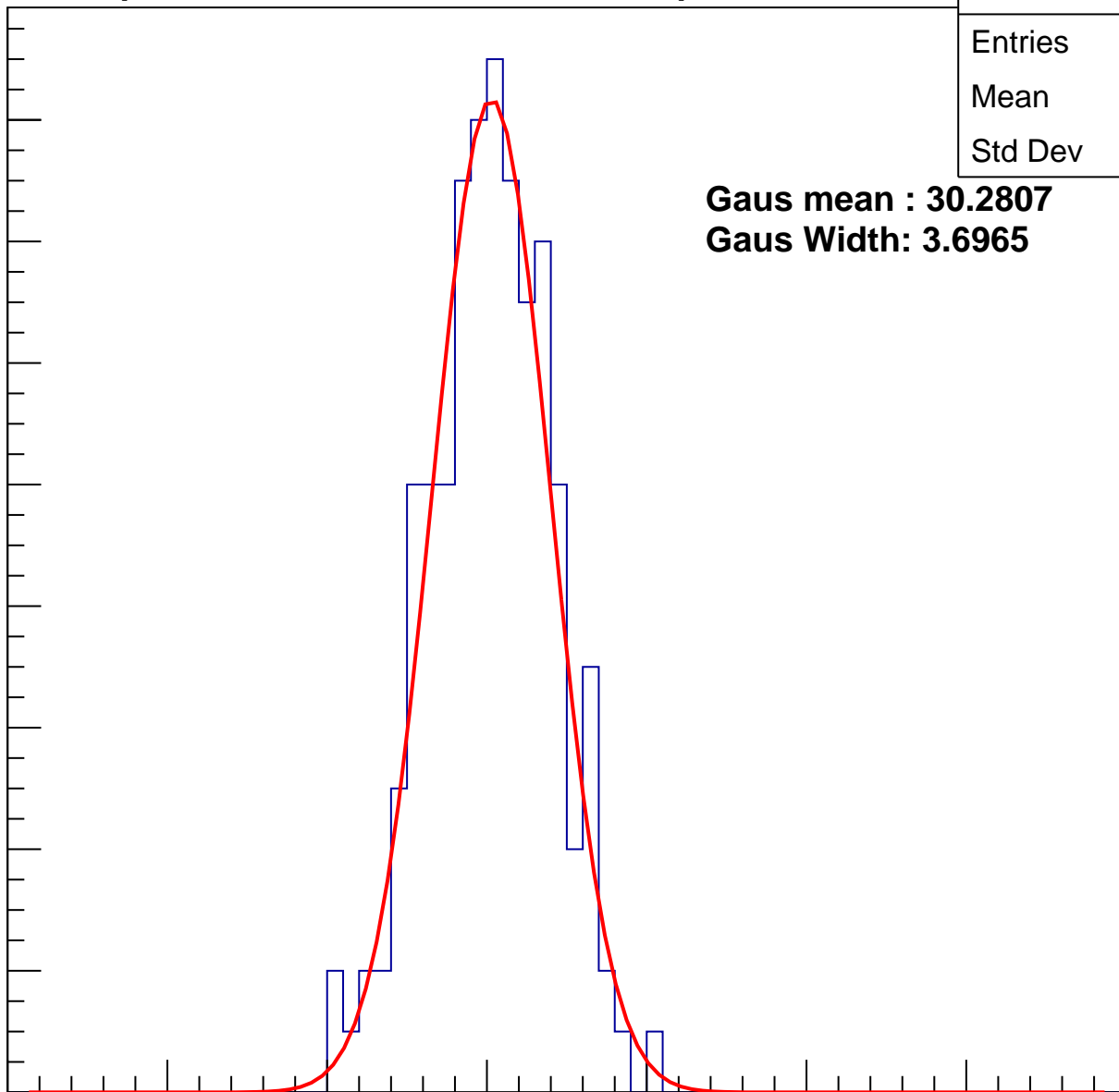
40

50

60

70

ampl



B1L001S, U24-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.73
Std Dev	3.157

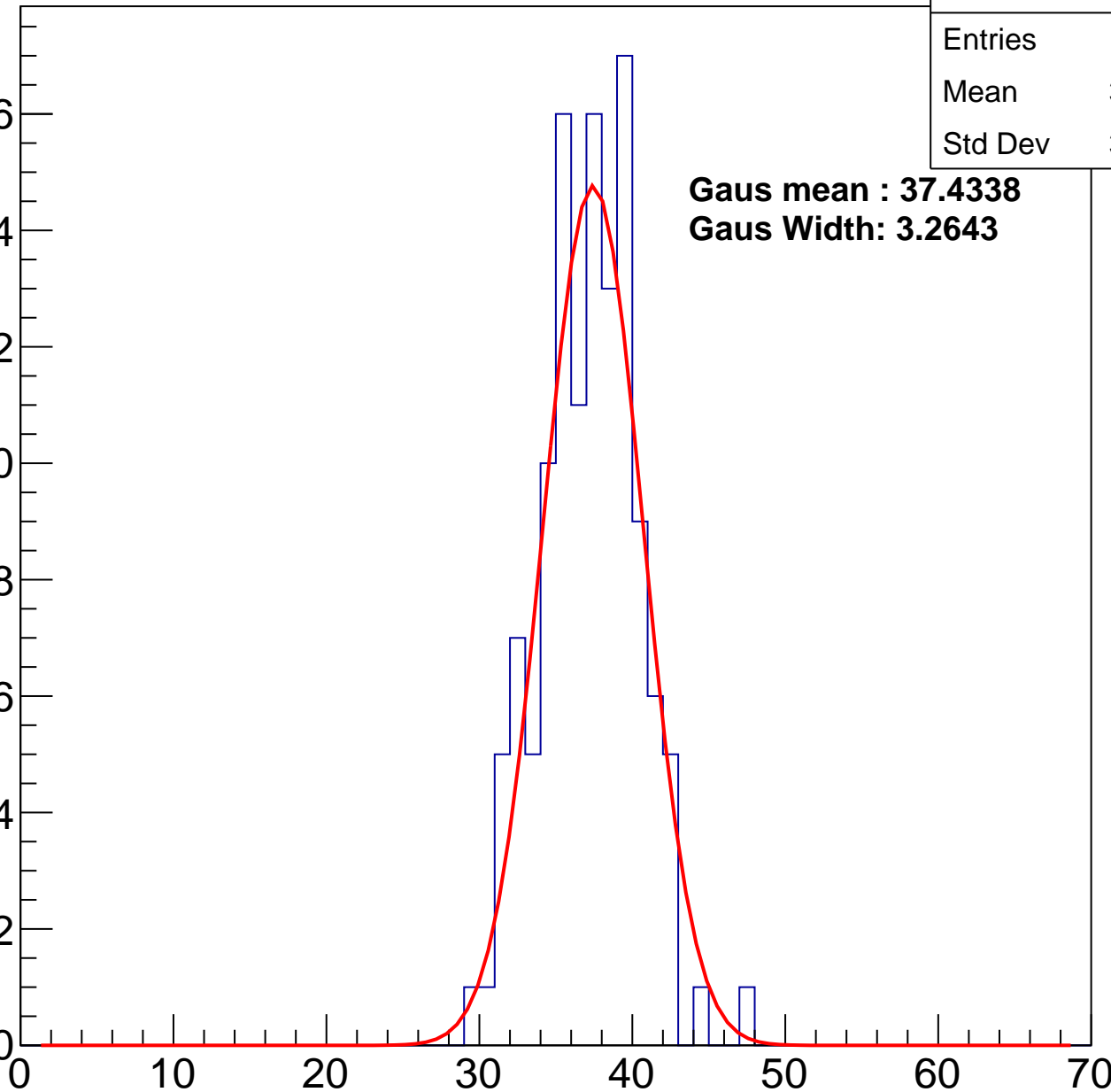
Gaus mean : 37.4338

Gaus Width: 3.2643

Entry

16
14
12
10
8
6
4
2
0

ampl

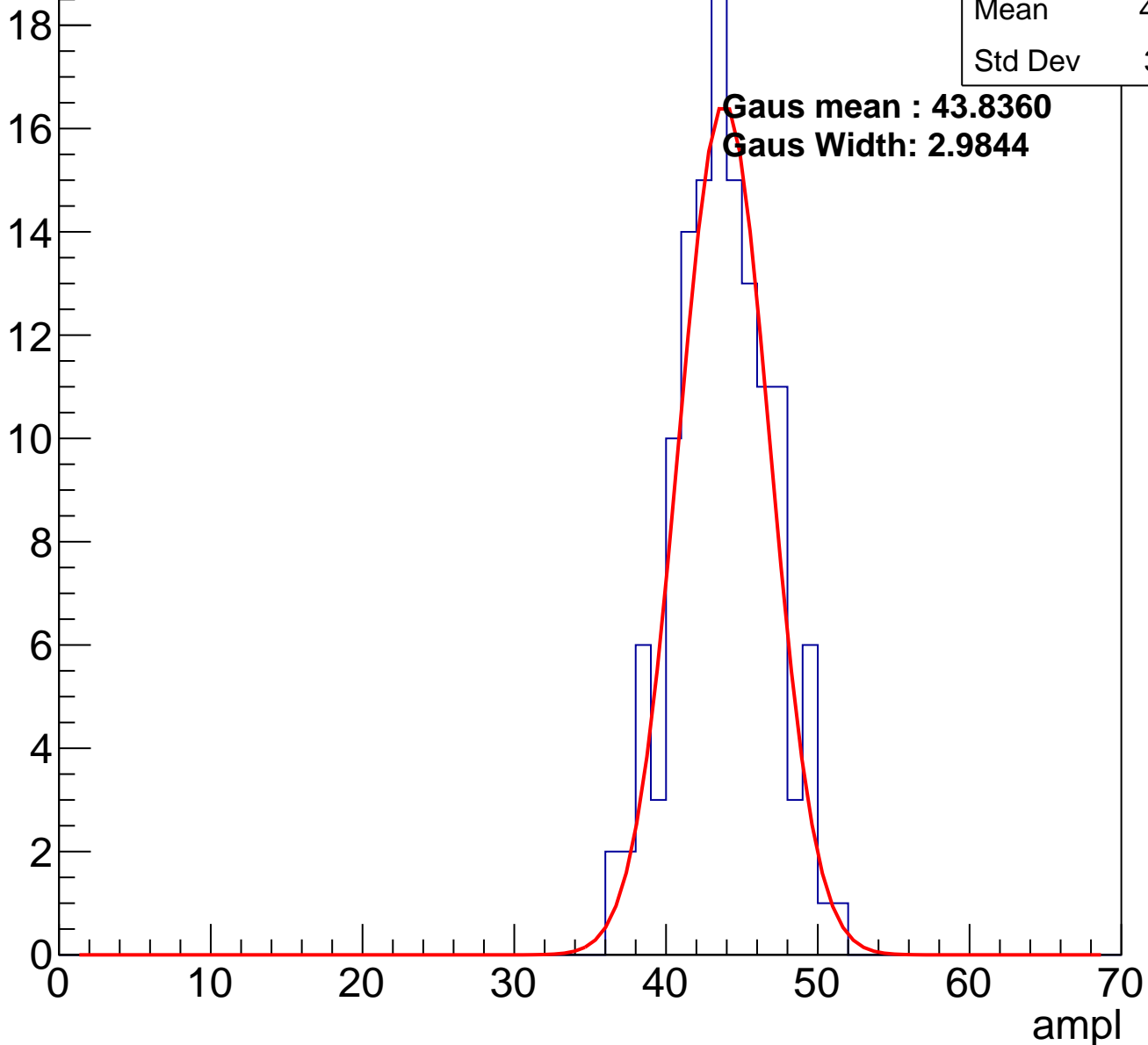


B1L001S, U24-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	43.33
Std Dev	3.091

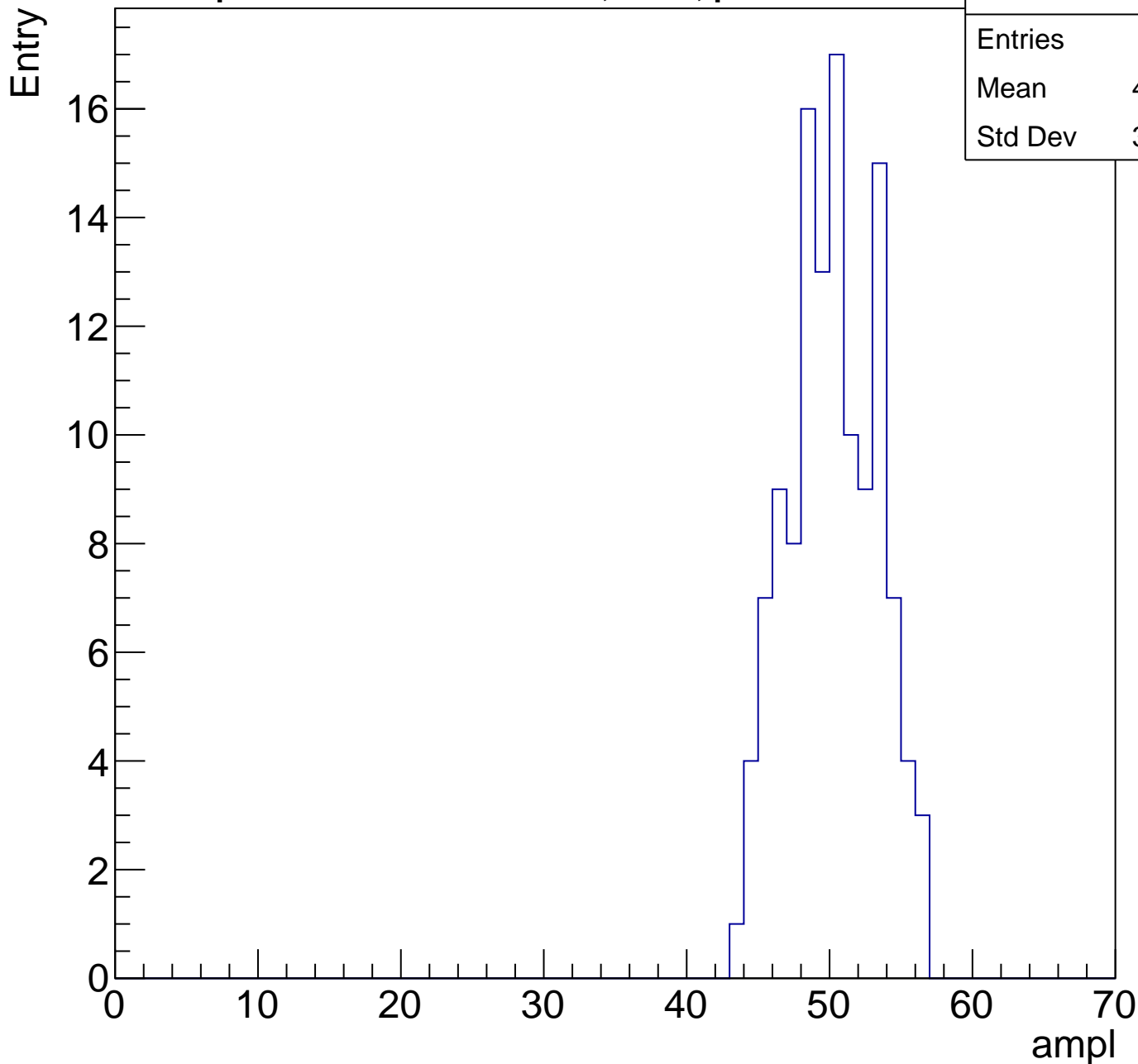
Entry



B1L001S, U24-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	49.74
Std Dev	3.063



B1L001S, U24-ch62, adc4

calib_packv5_042523_0143.root, FC#2, port C2

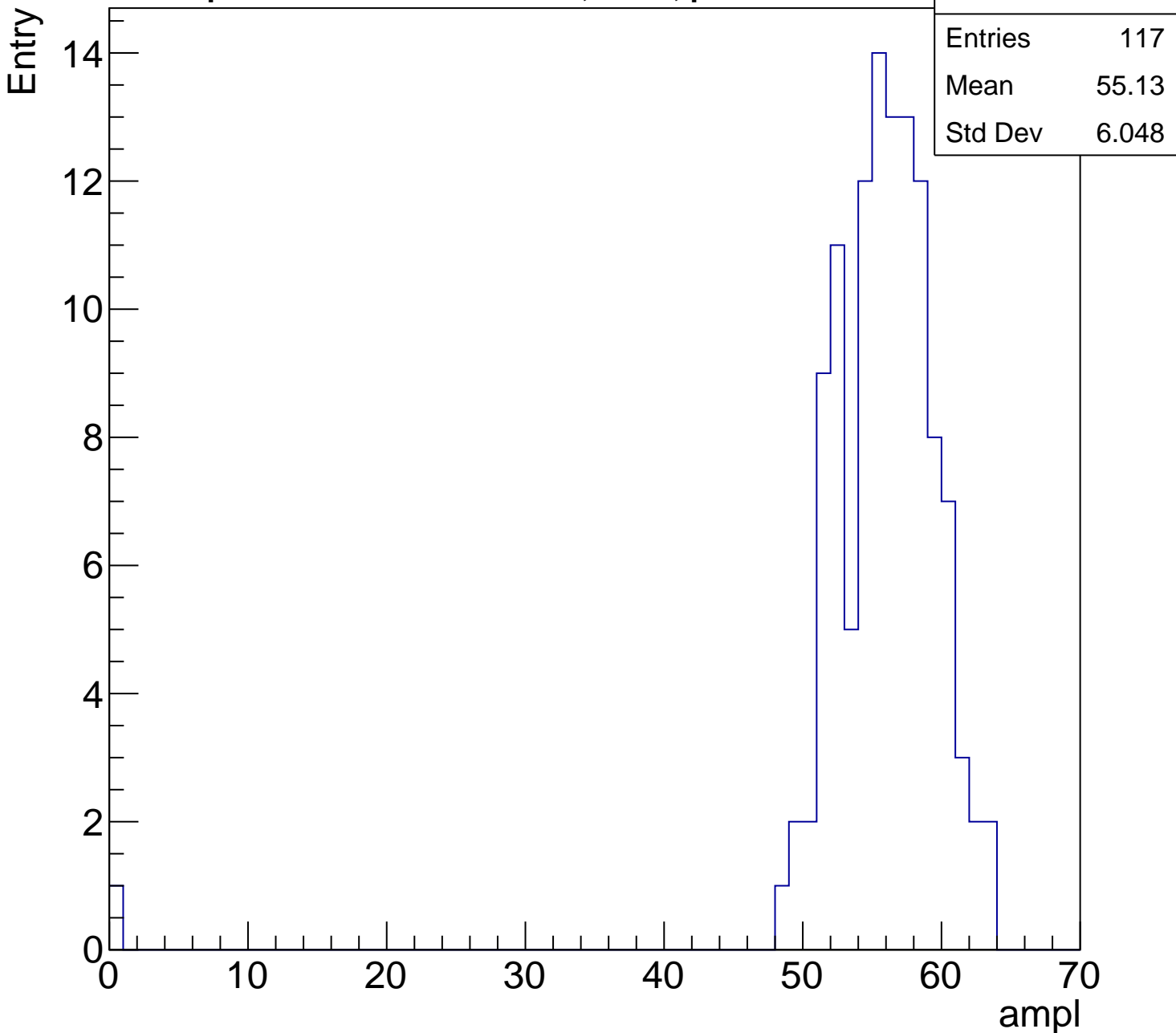
Entries	117
Mean	55.13
Std Dev	6.048

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch62, adc5

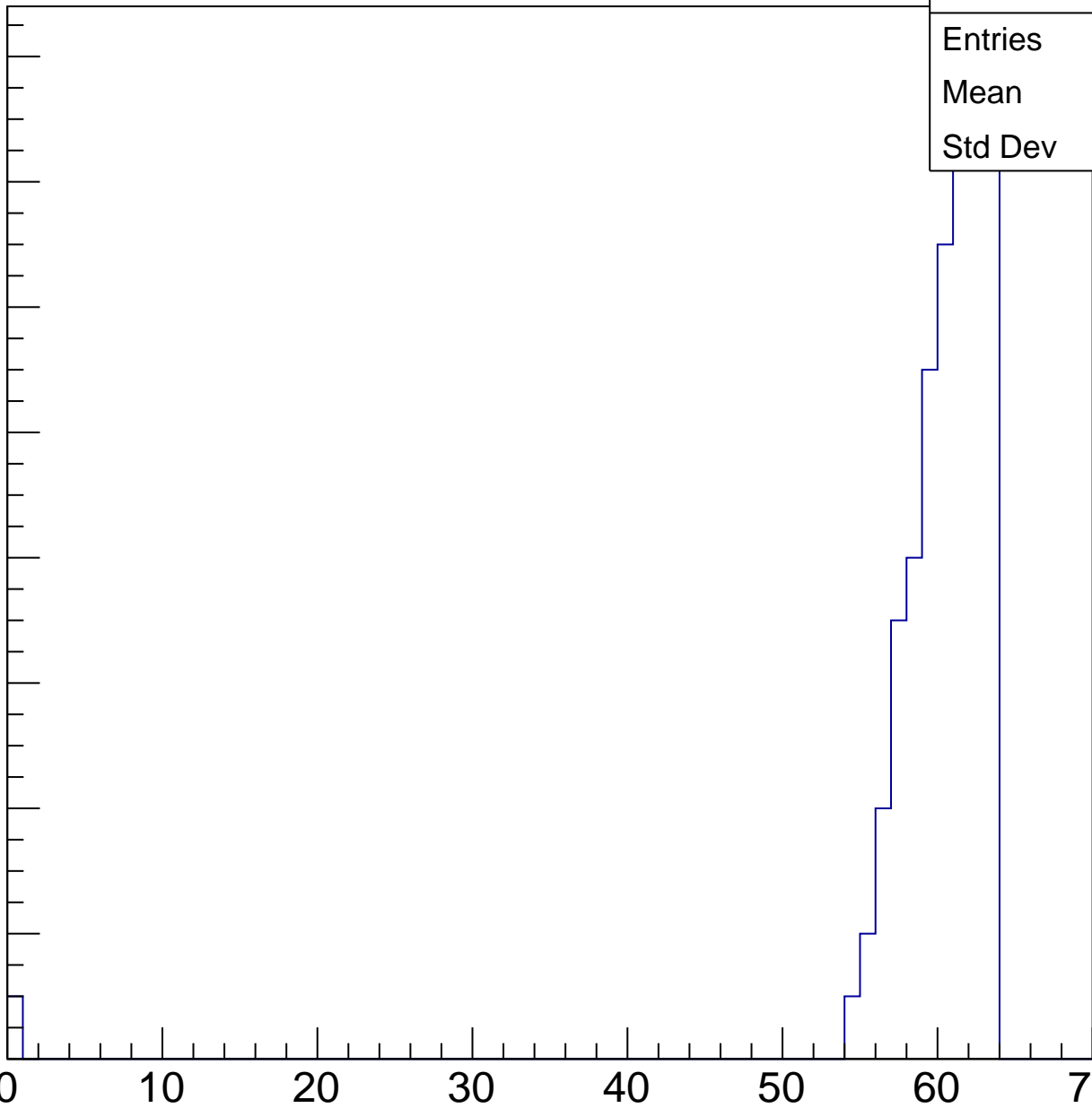
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	59.54
Std Dev	6.537

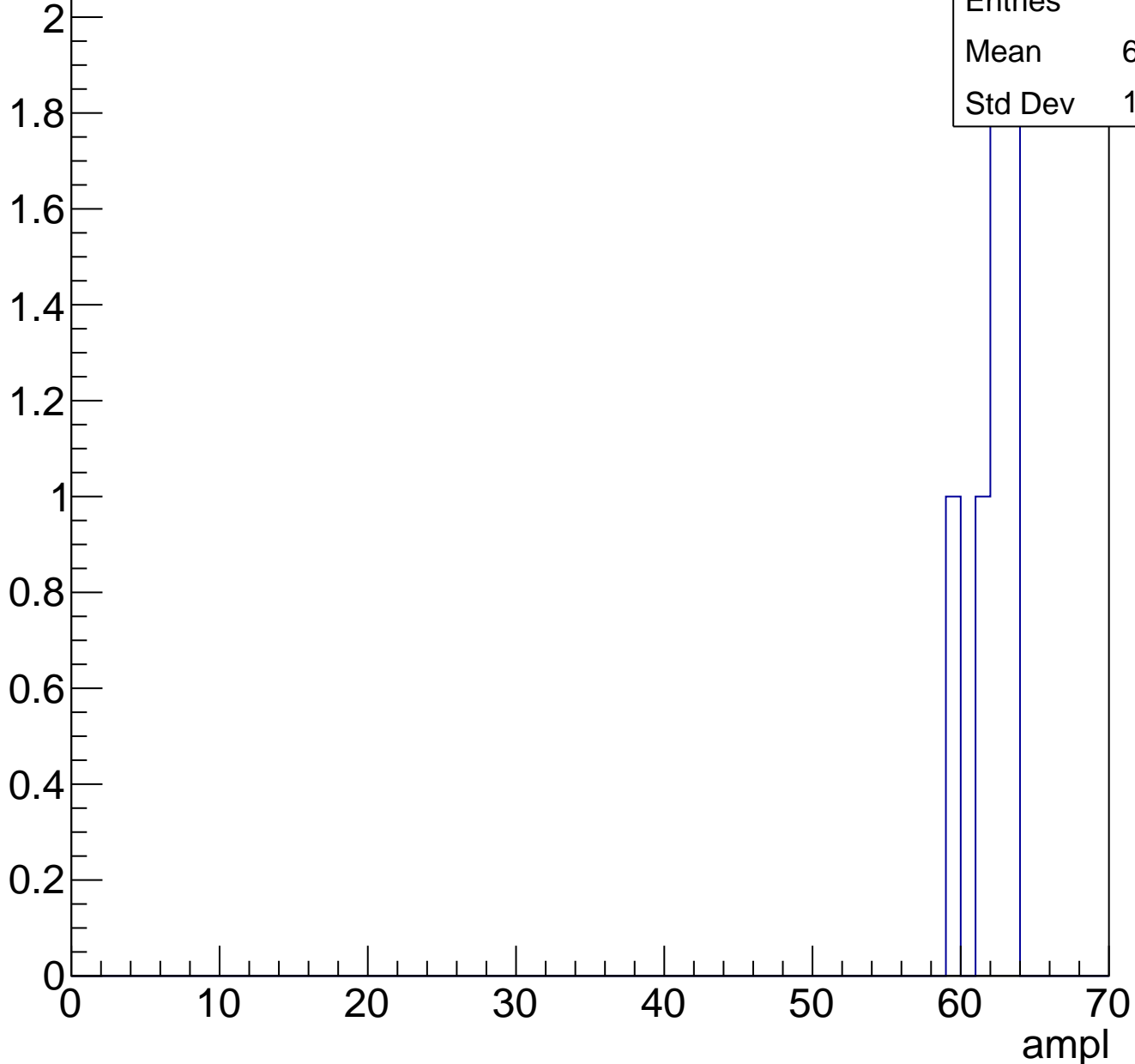
ampl



B1L001S, U24-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch63, adc0

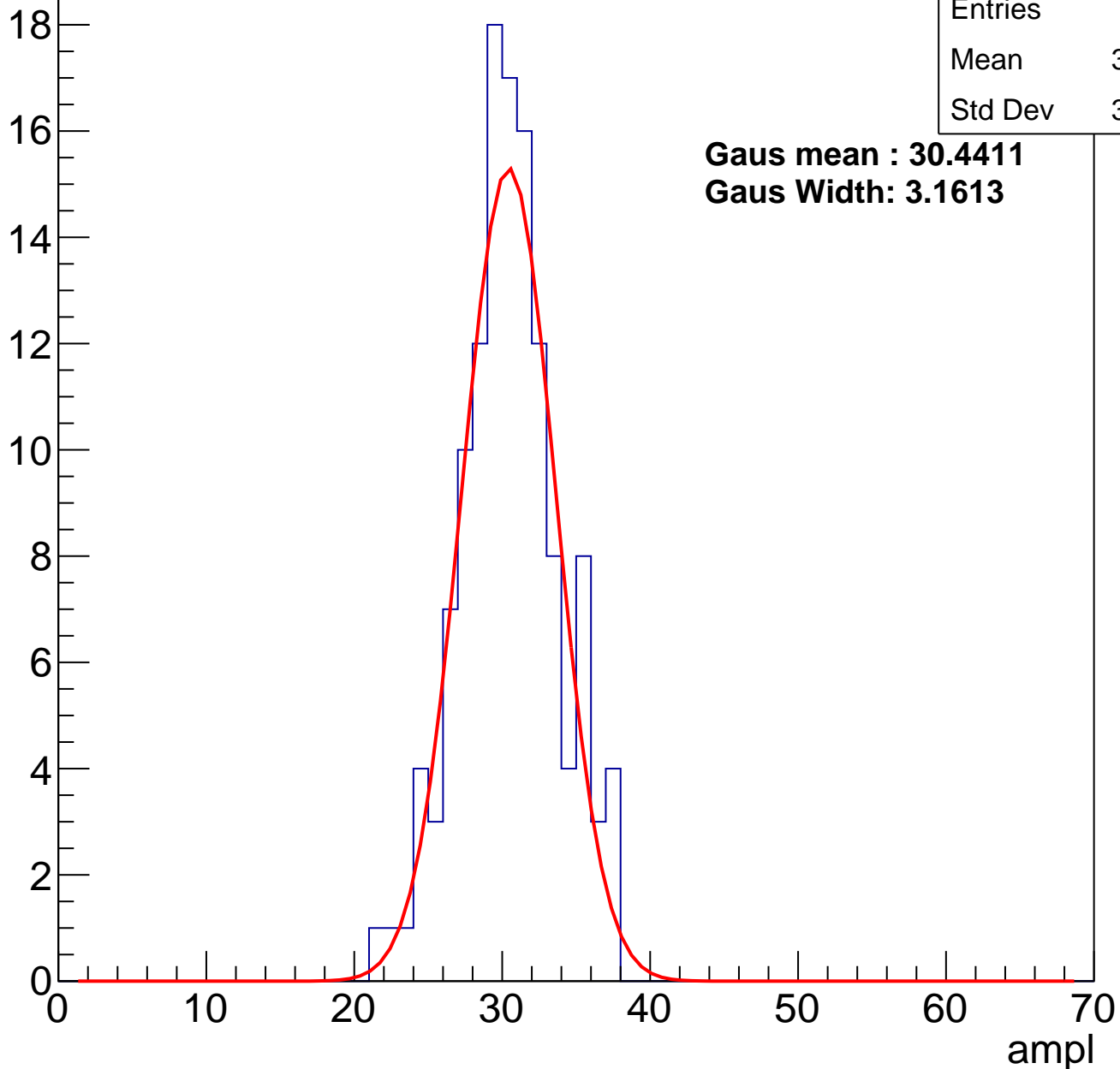
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	30.02
Std Dev	3.277

Gaus mean : 30.4411

Gaus Width: 3.1613

Entry



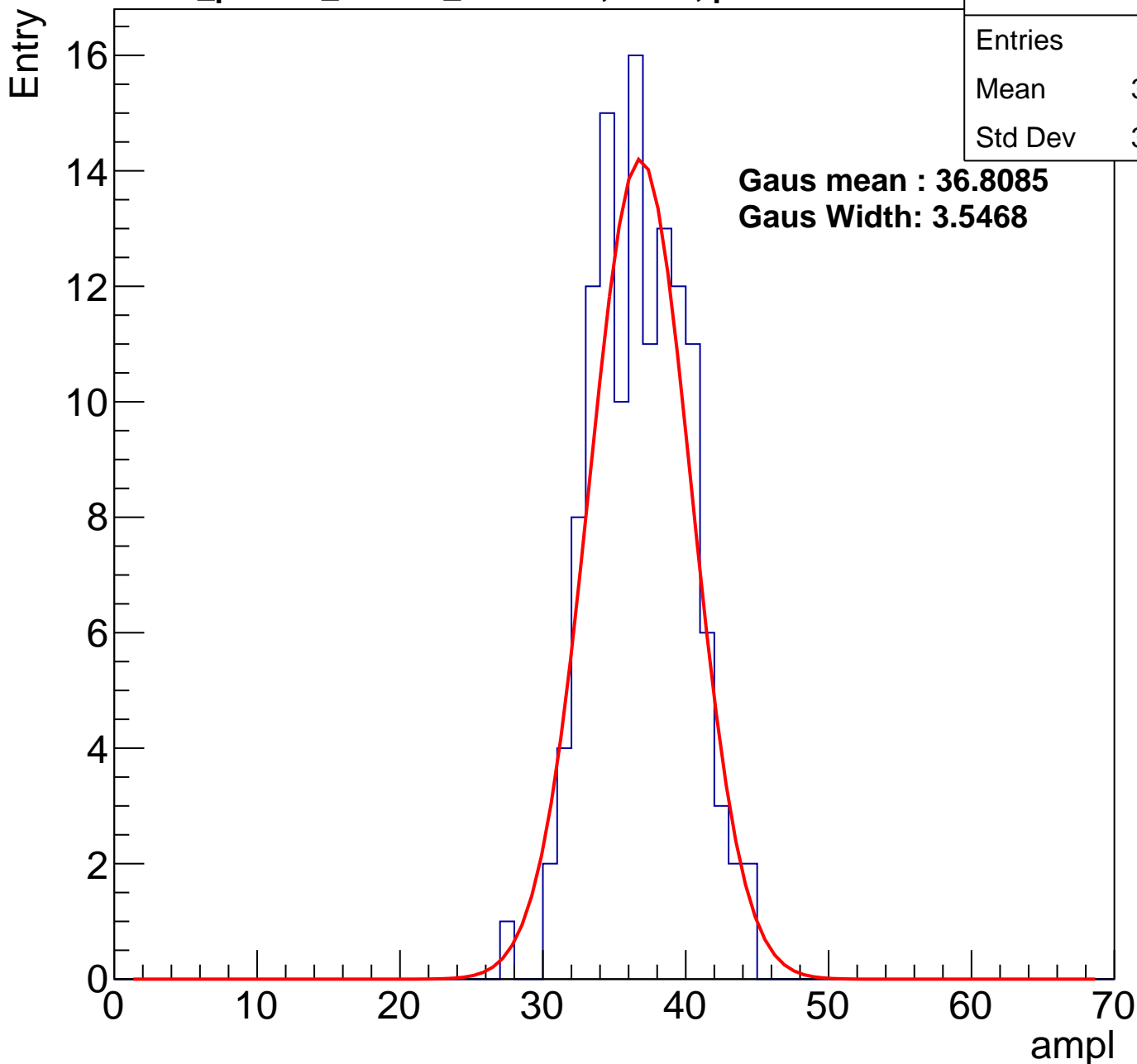
B1L001S, U24-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.36
Std Dev	3.276

Gaus mean : 36.8085

Gaus Width: 3.5468



B1L001S, U24-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	42.66
Std Dev	3.396

Gaus mean : 43.2192

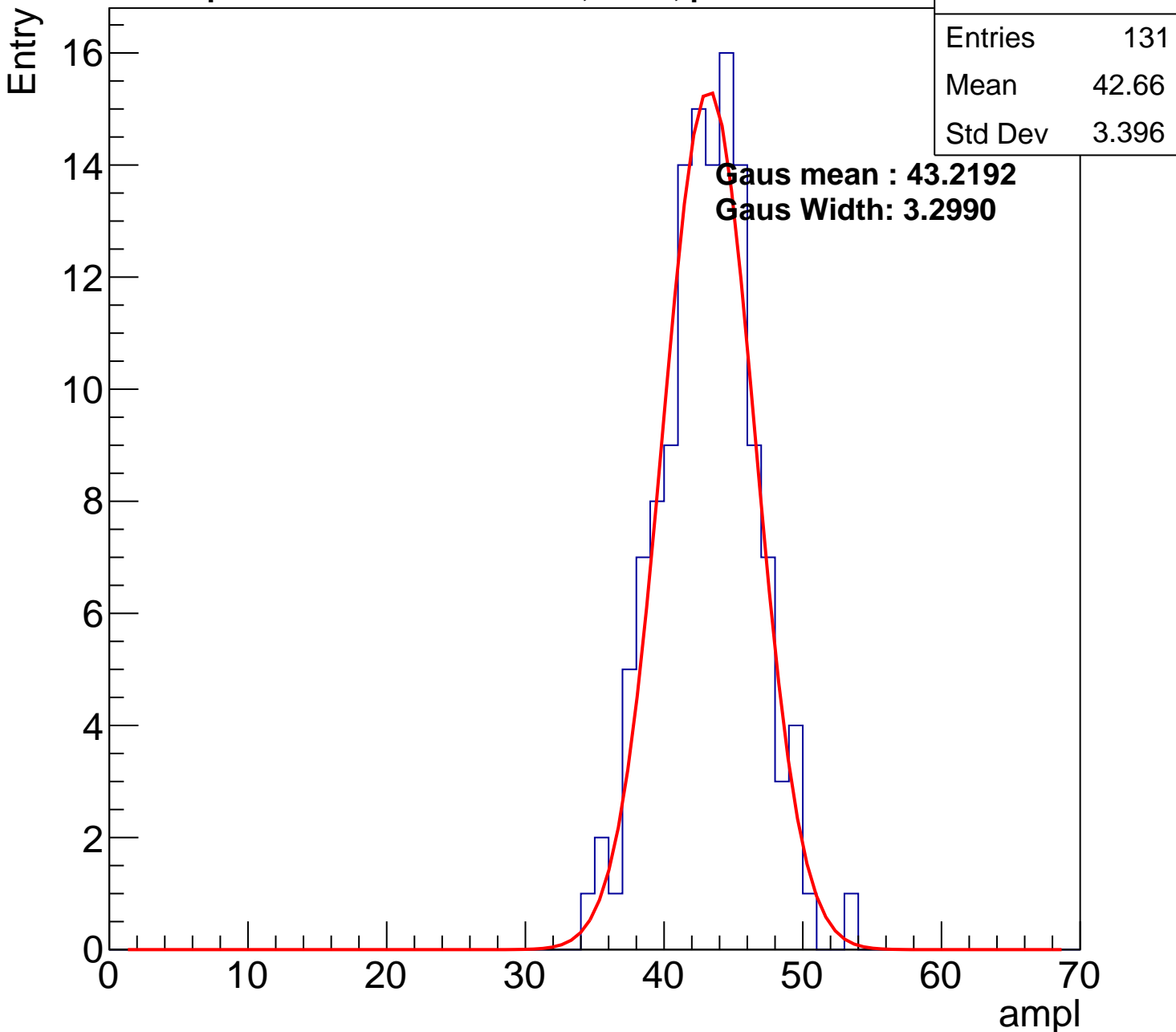
Gaus Width: 3.2990

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch63, adc3

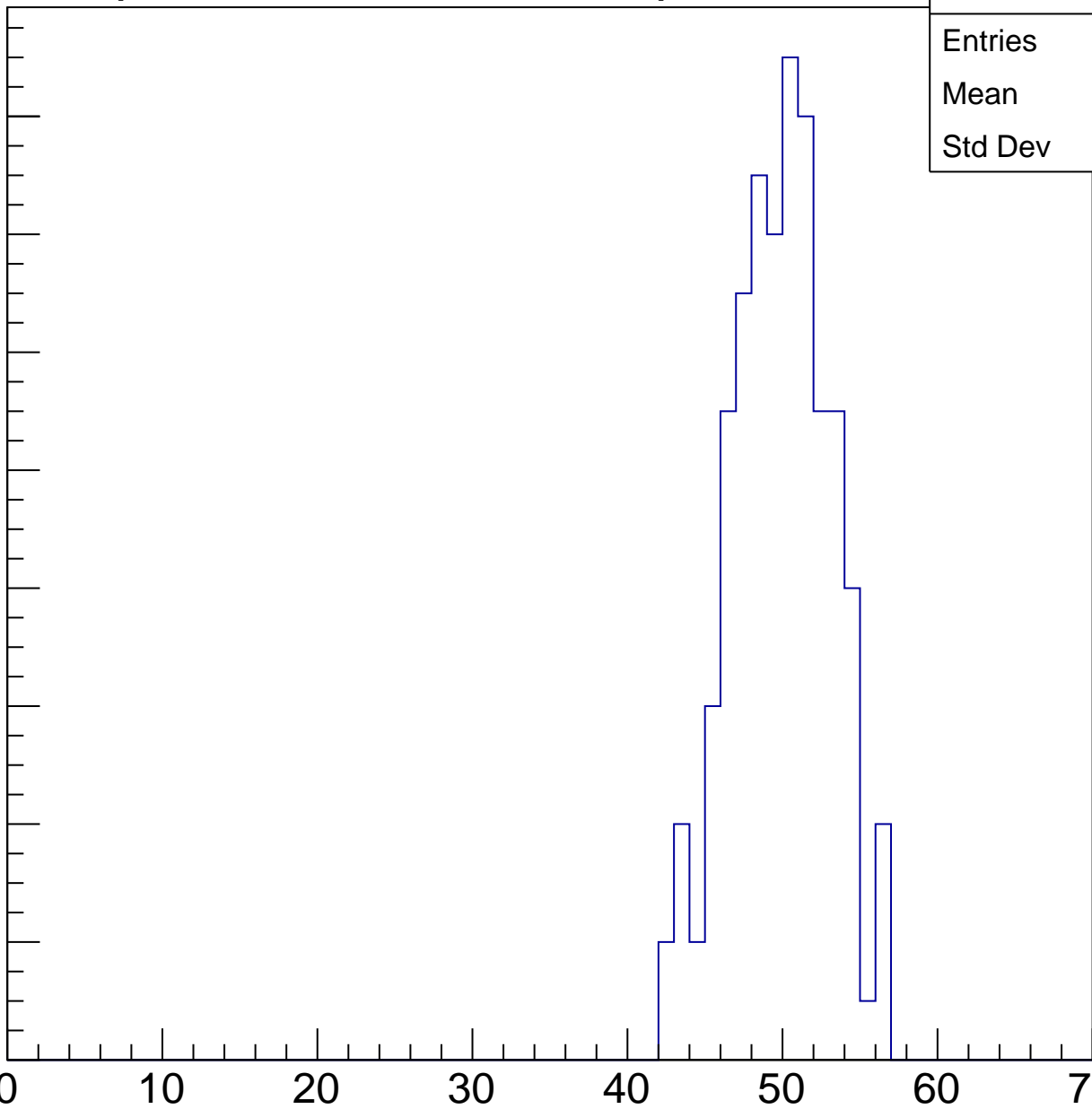
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	49.4
Std Dev	3.127

ampl



B1L001S, U24-ch63, adc4

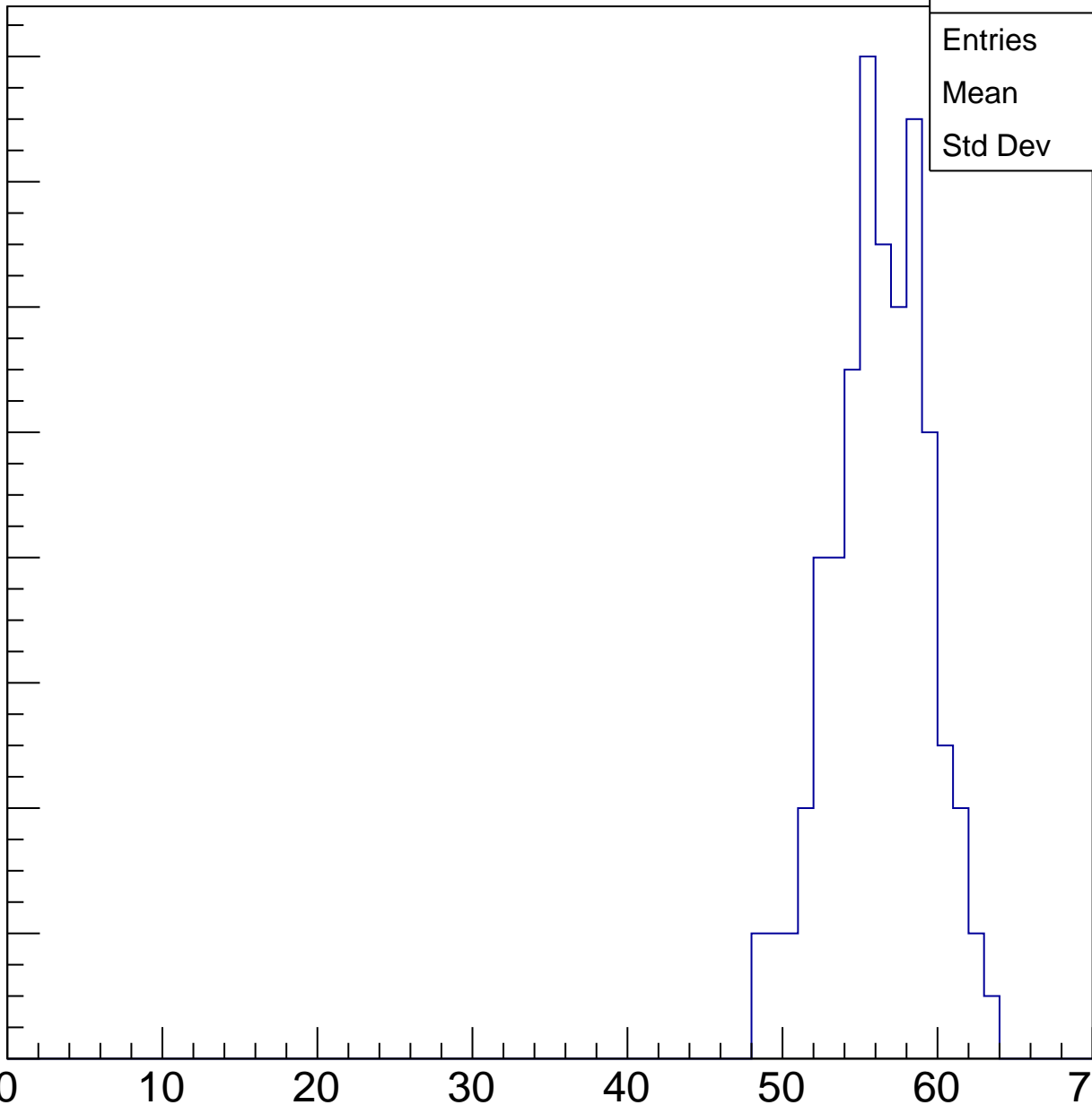
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	55.78
Std Dev	3.115

ampl



B1L001S, U24-ch63, adc5

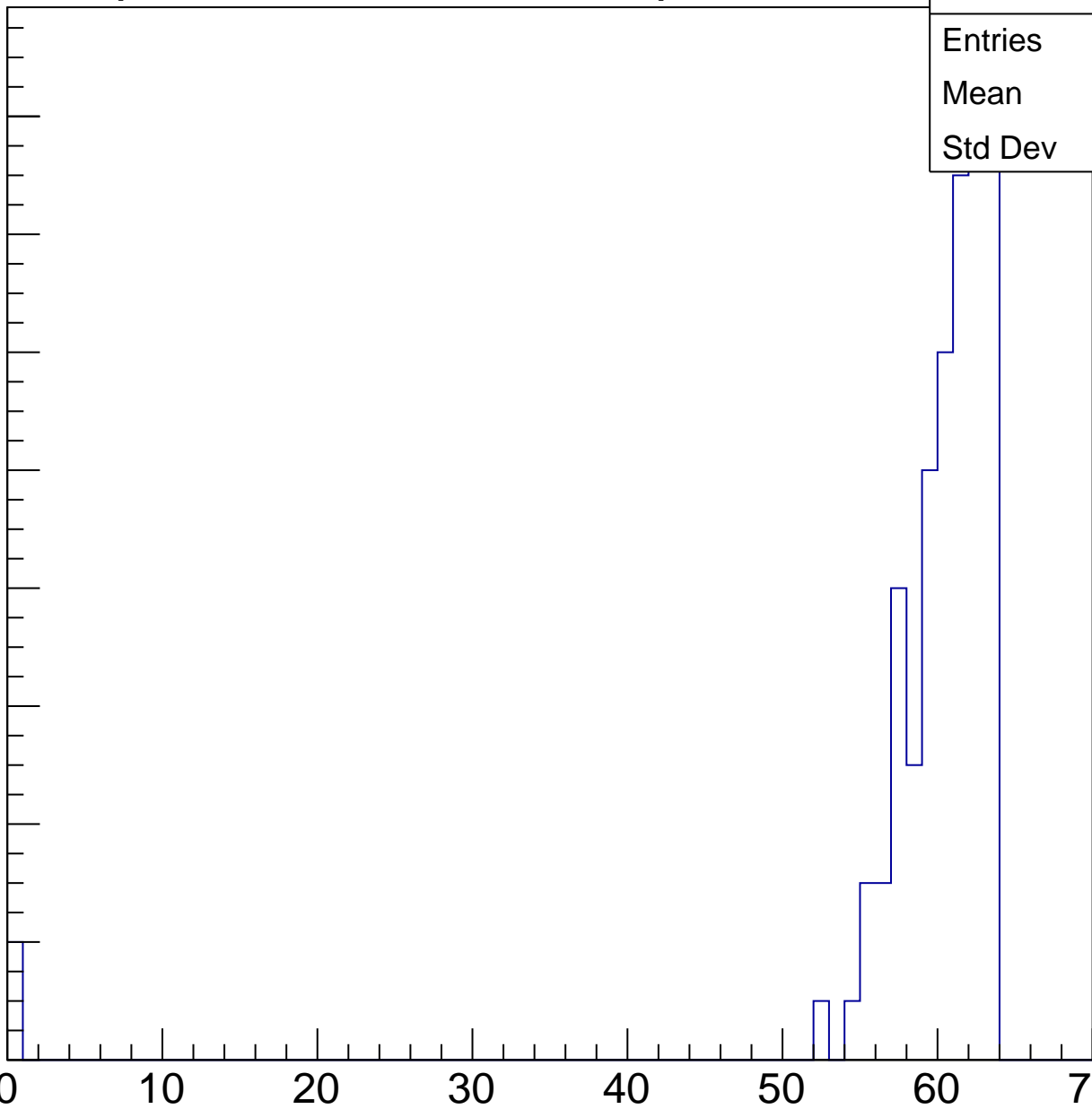
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.85
Std Dev	9.06

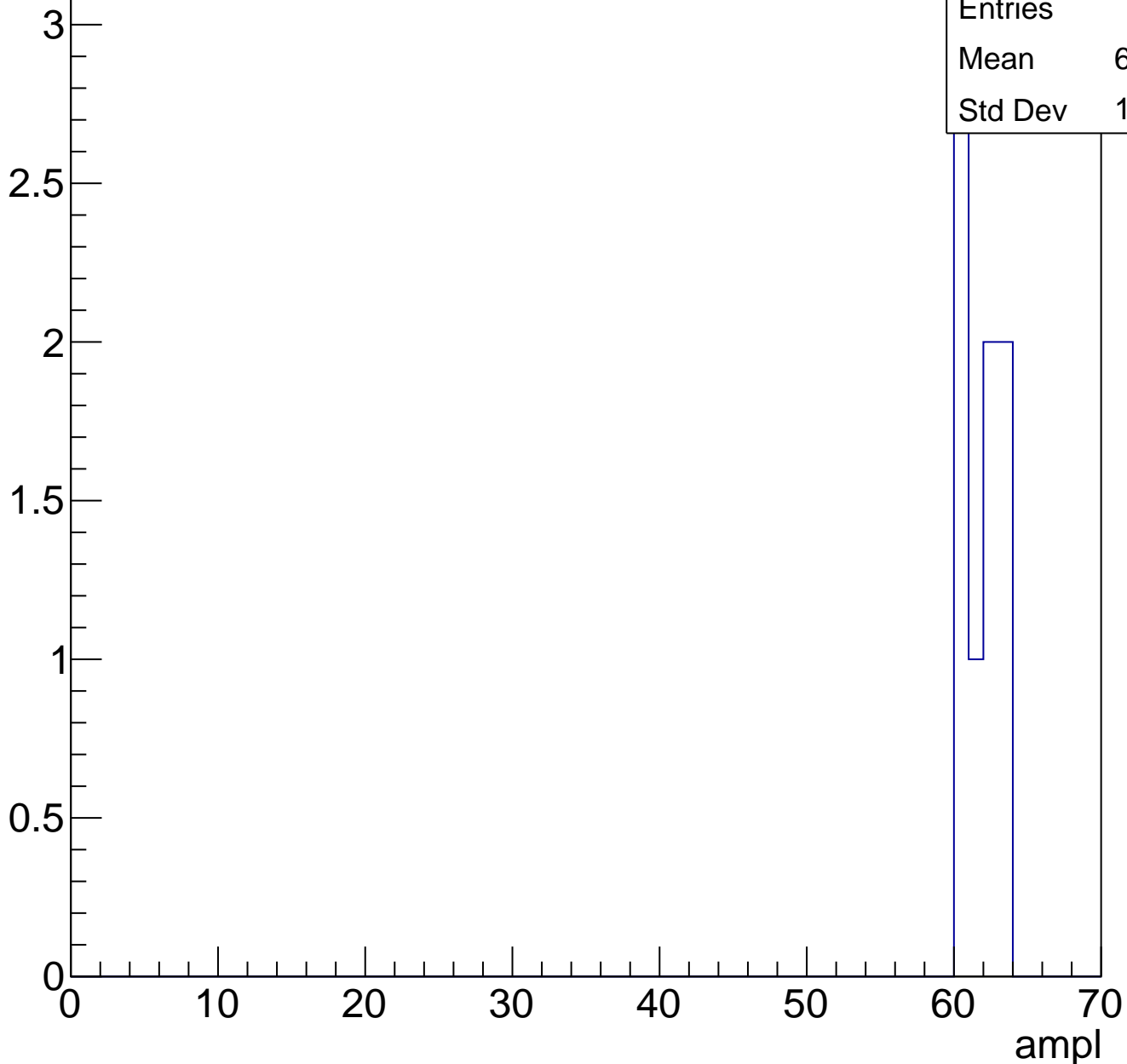
ampl



B1L001S, U24-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

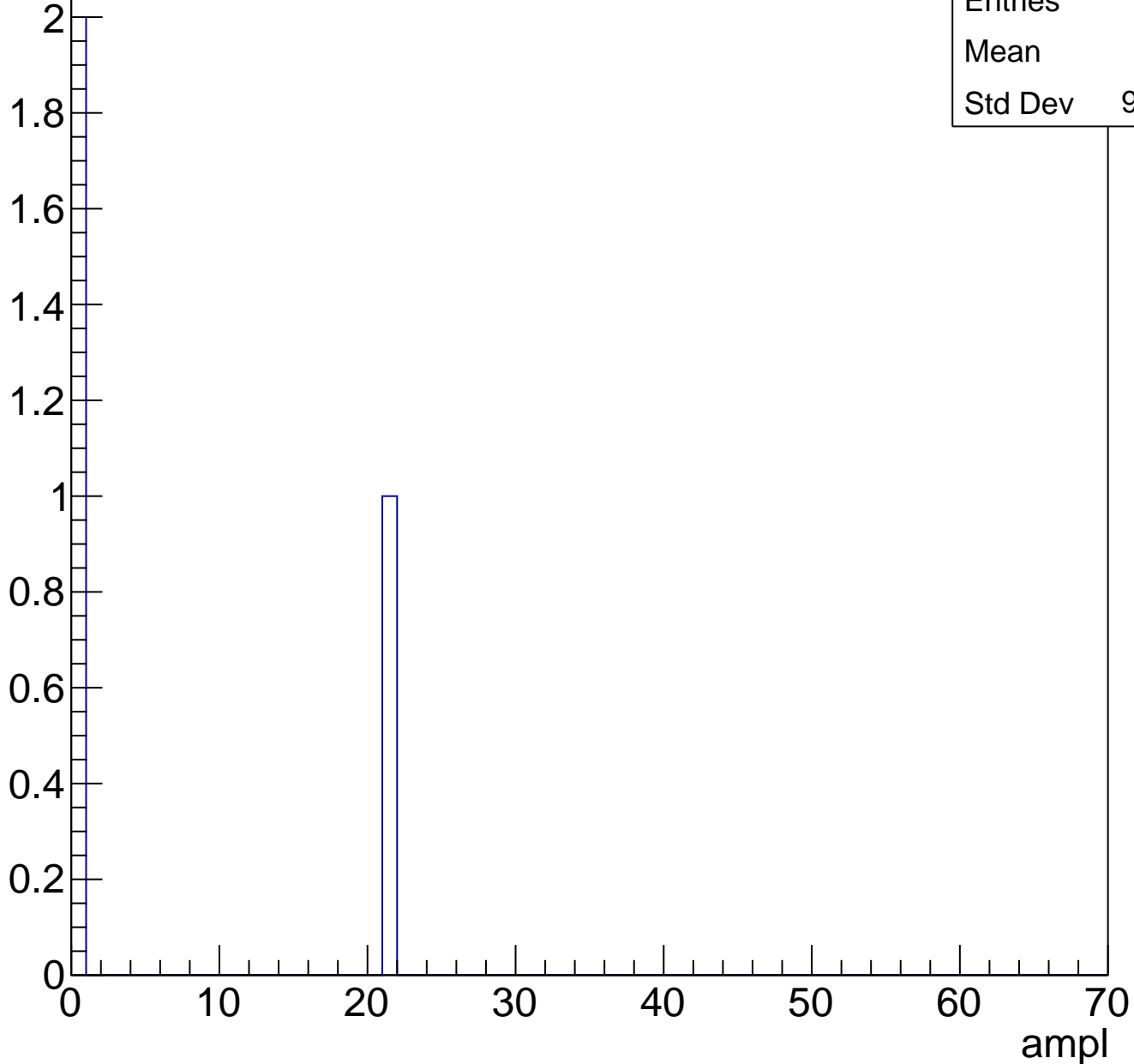
Entry



B1L001S, U24-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L001S, U24-ch64, adc0

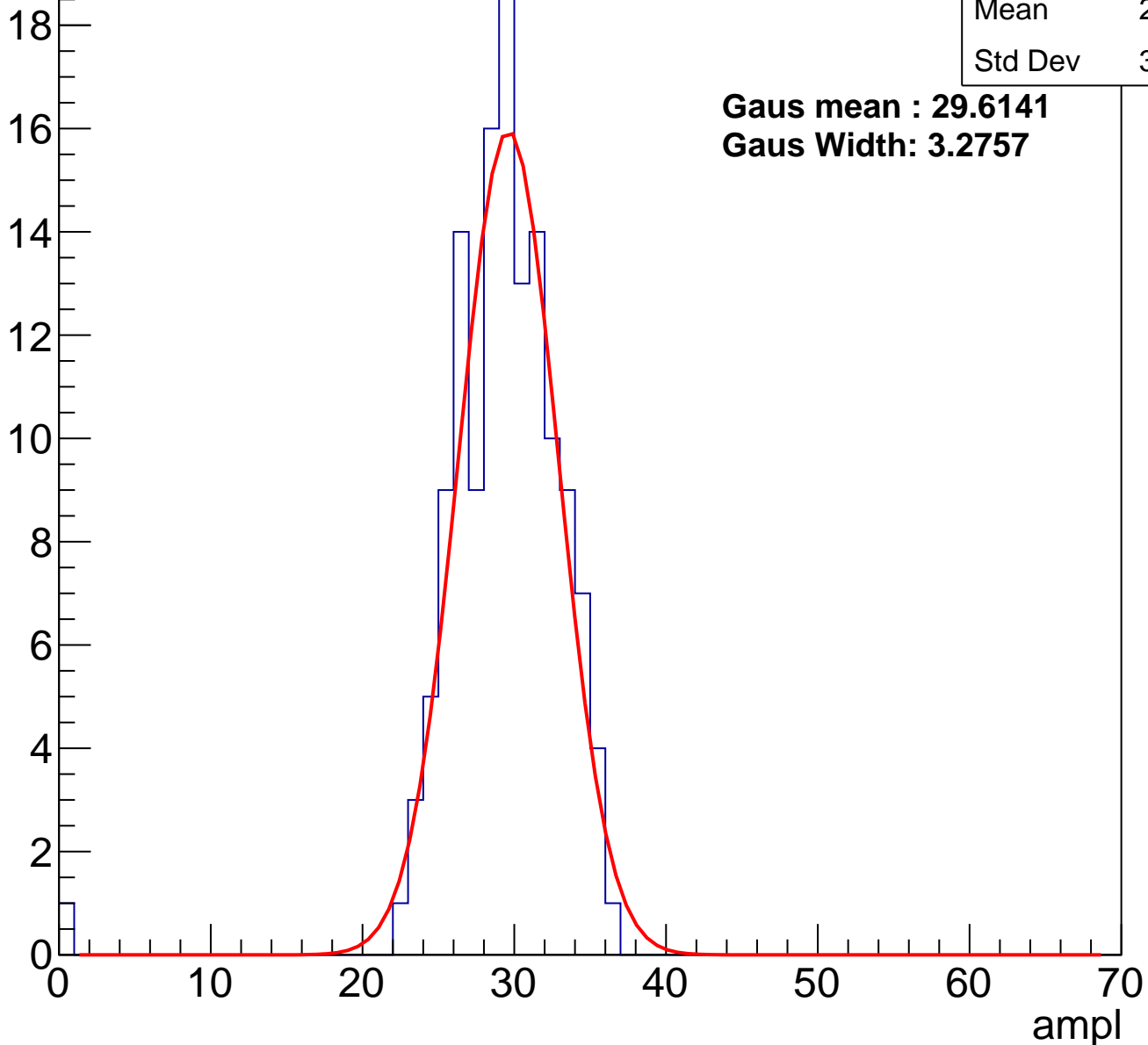
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	28.87
Std Dev	3.957

Gaus mean : 29.6141

Gaus Width: 3.2757

Entry



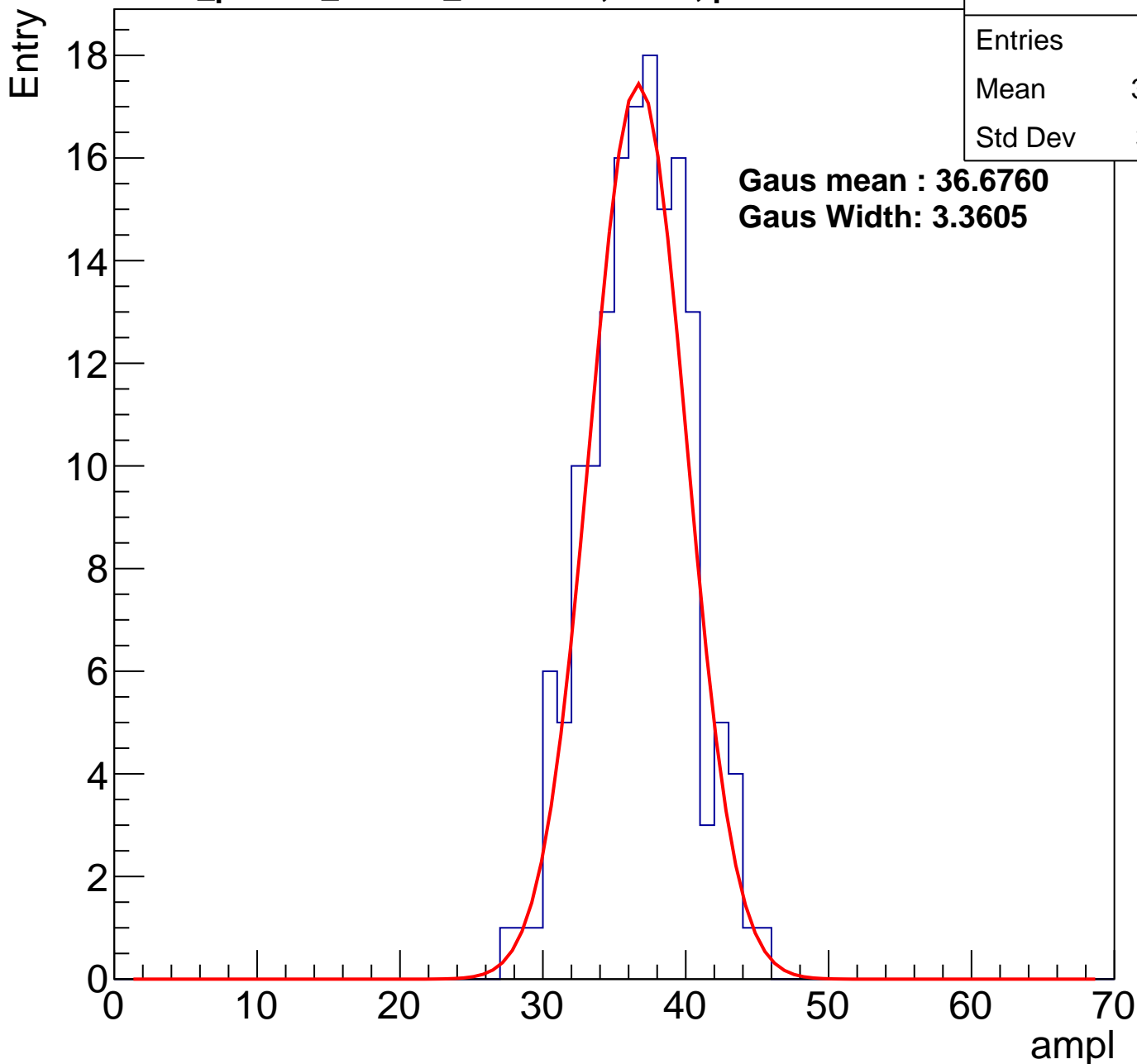
B1L001S, U24-ch64, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	36.26
Std Dev	3.461

Gaus mean : 36.6760

Gaus Width: 3.3605



B1L001S, U24-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	43.13
Std Dev	3.219

Gaus mean : 43.7995

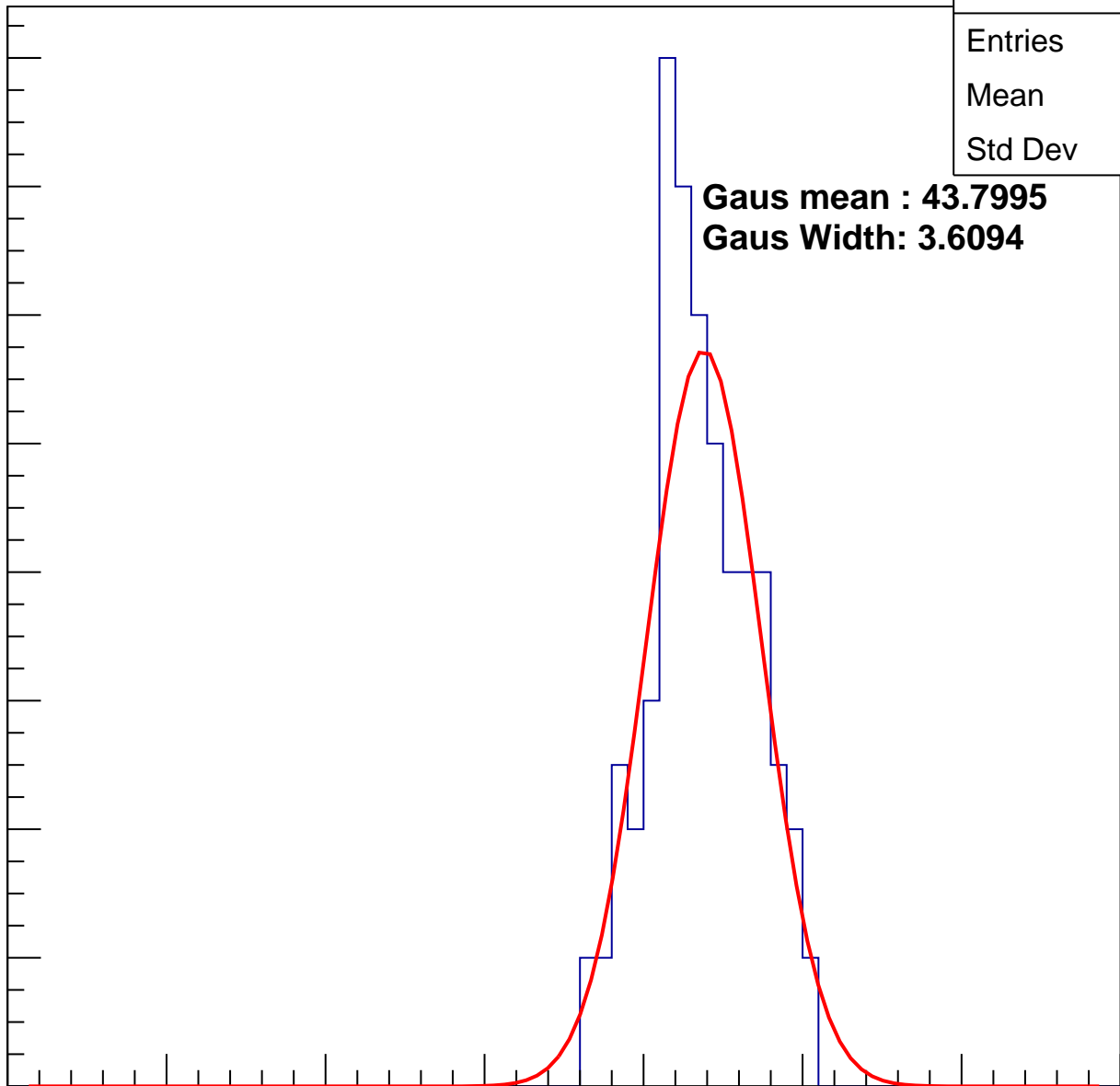
Gaus Width: 3.6094

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

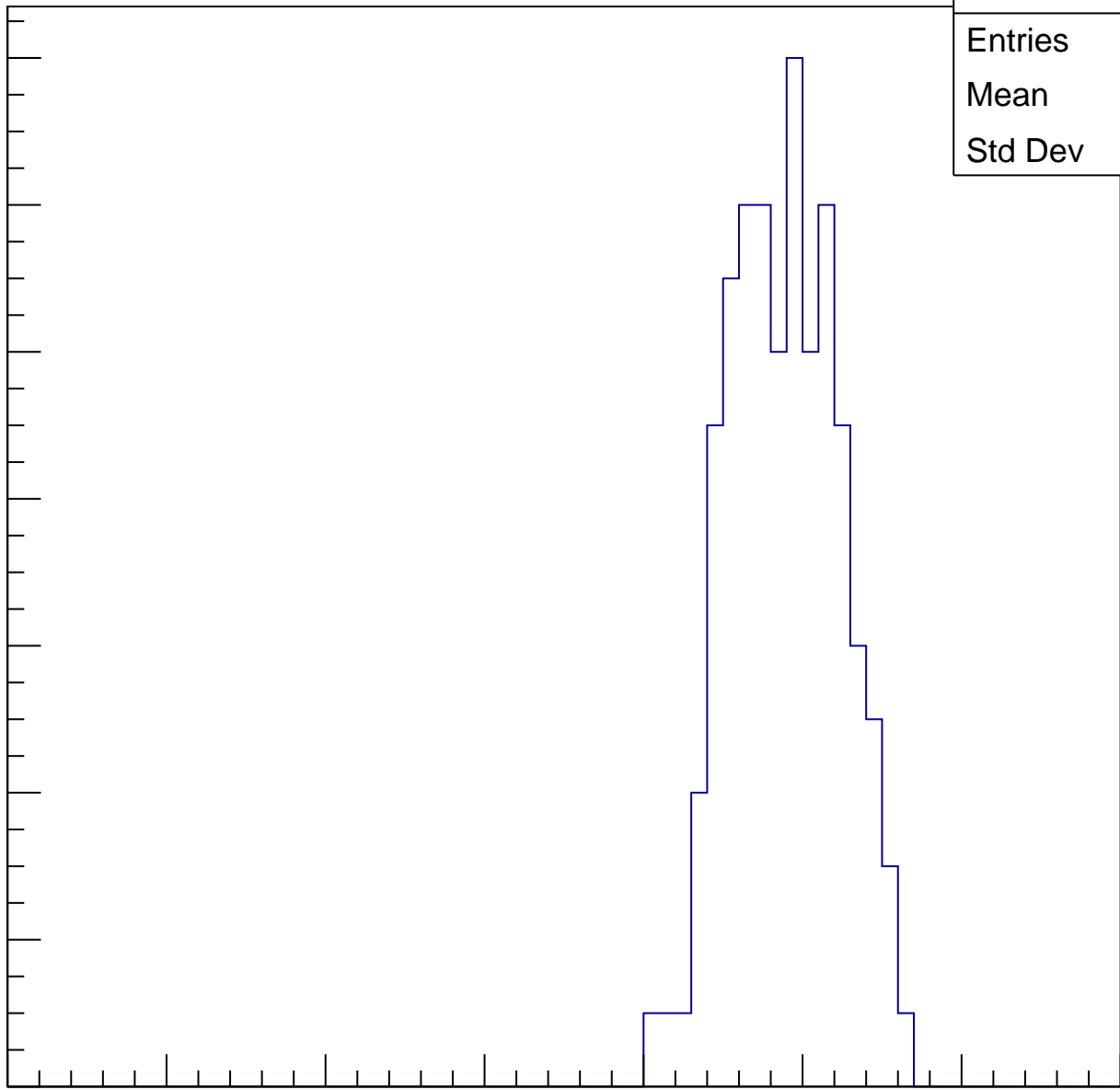
Entries	121
Mean	48.4
Std Dev	3.376

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

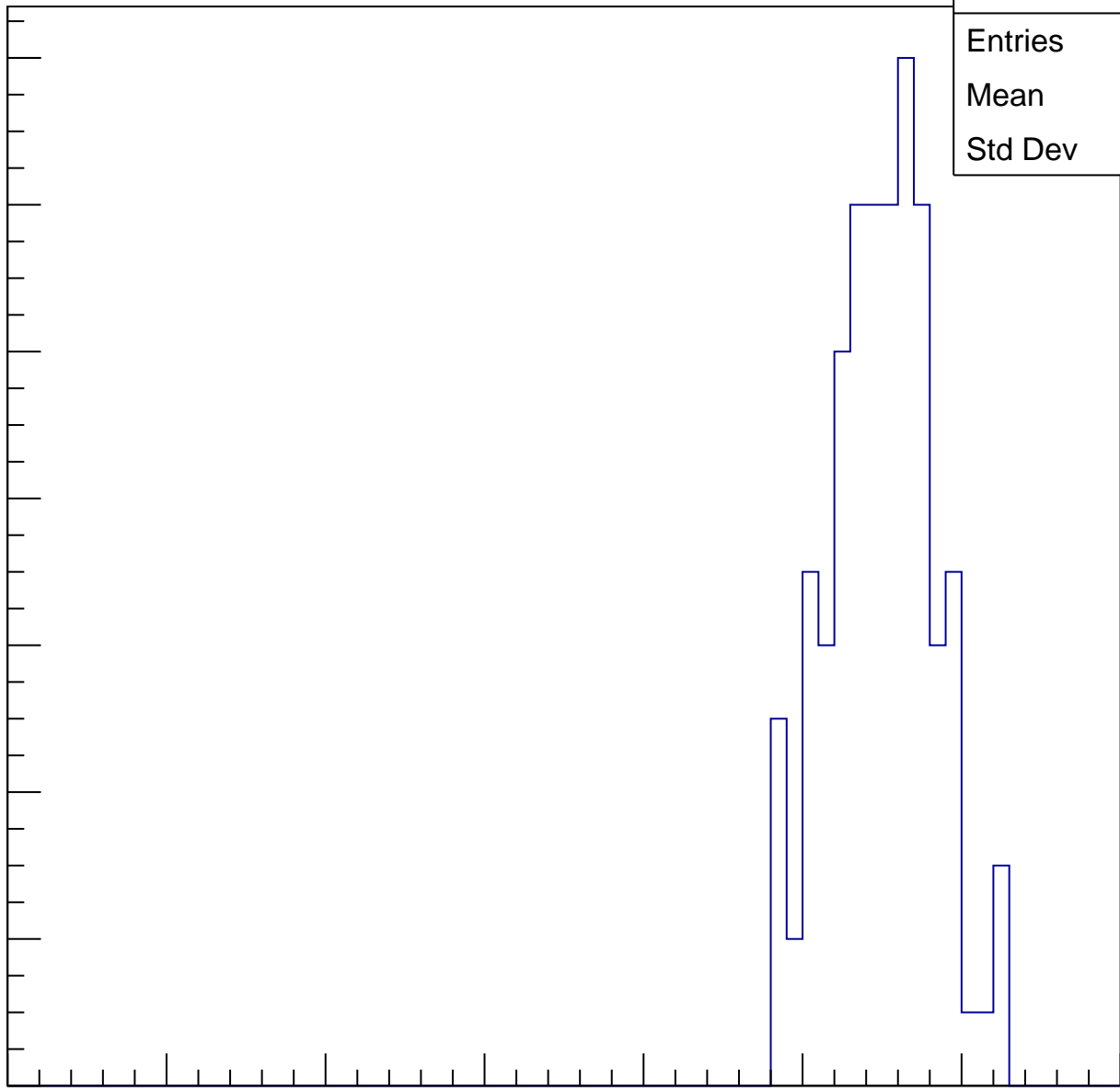
Entries	110
Mean	54.49
Std Dev	3.227

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch64, adc5

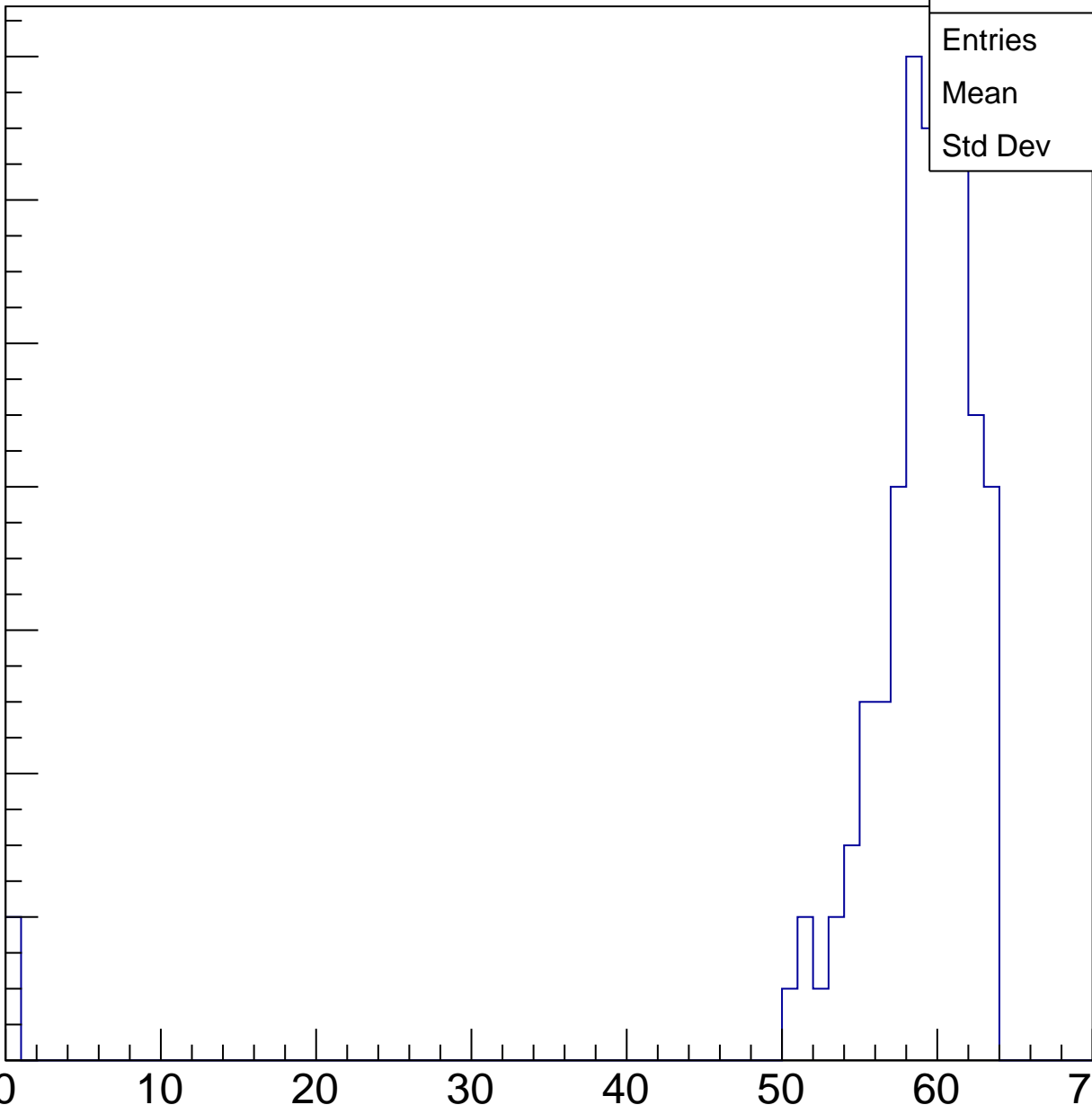
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	57.58
Std Dev	8.726

ampl



B1L001S, U24-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	30
Mean	61.8
Std Dev	1.137

B1L001S, U24-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch65, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	30.36
Std Dev	3.17

Gaus mean : 30.9138

Gaus Width: 3.2618

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch65, adc1

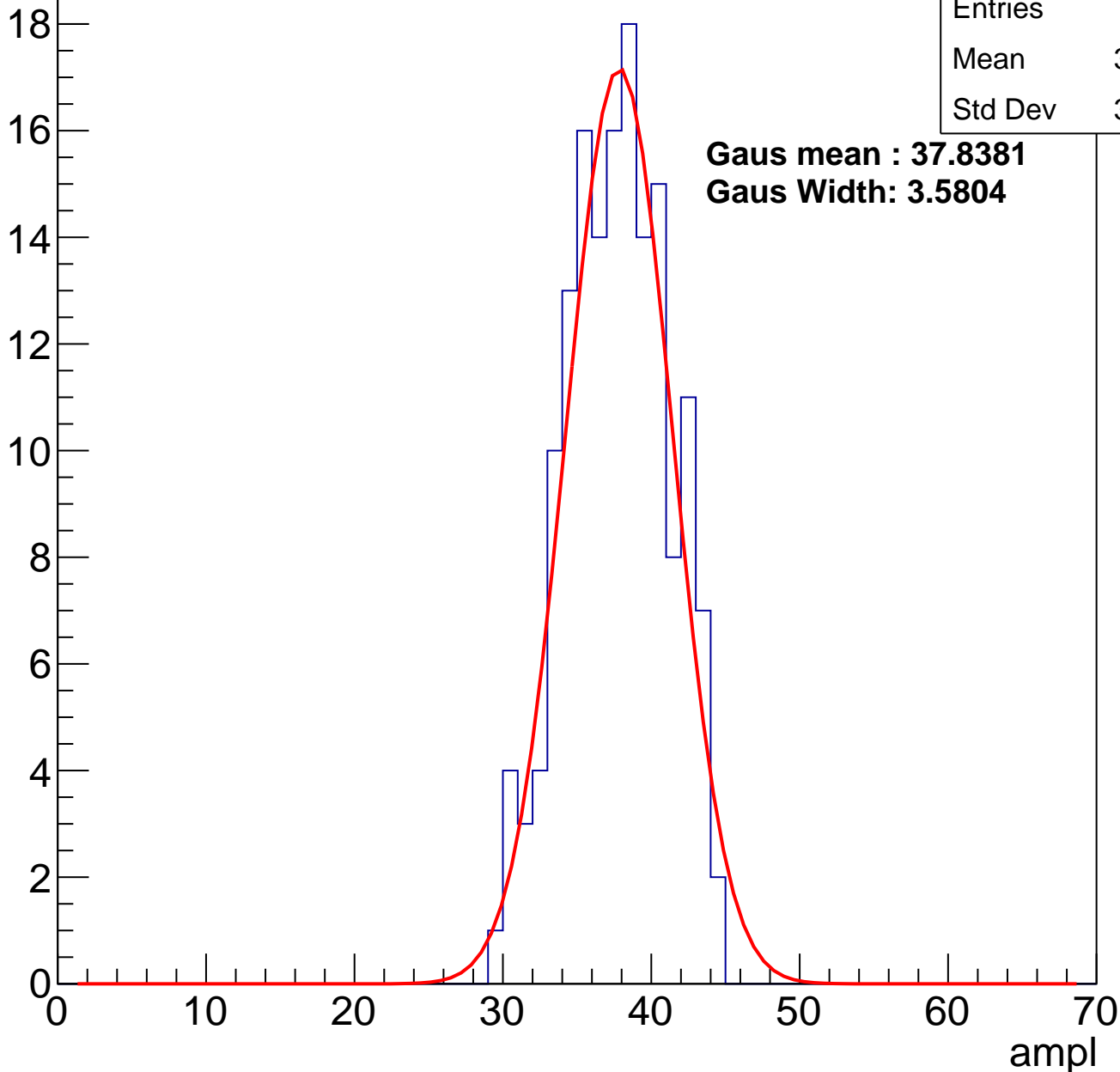
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	37.22
Std Dev	3.375

Gaus mean : 37.8381

Gaus Width: 3.5804

Entry



B1L001S, U24-ch65, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

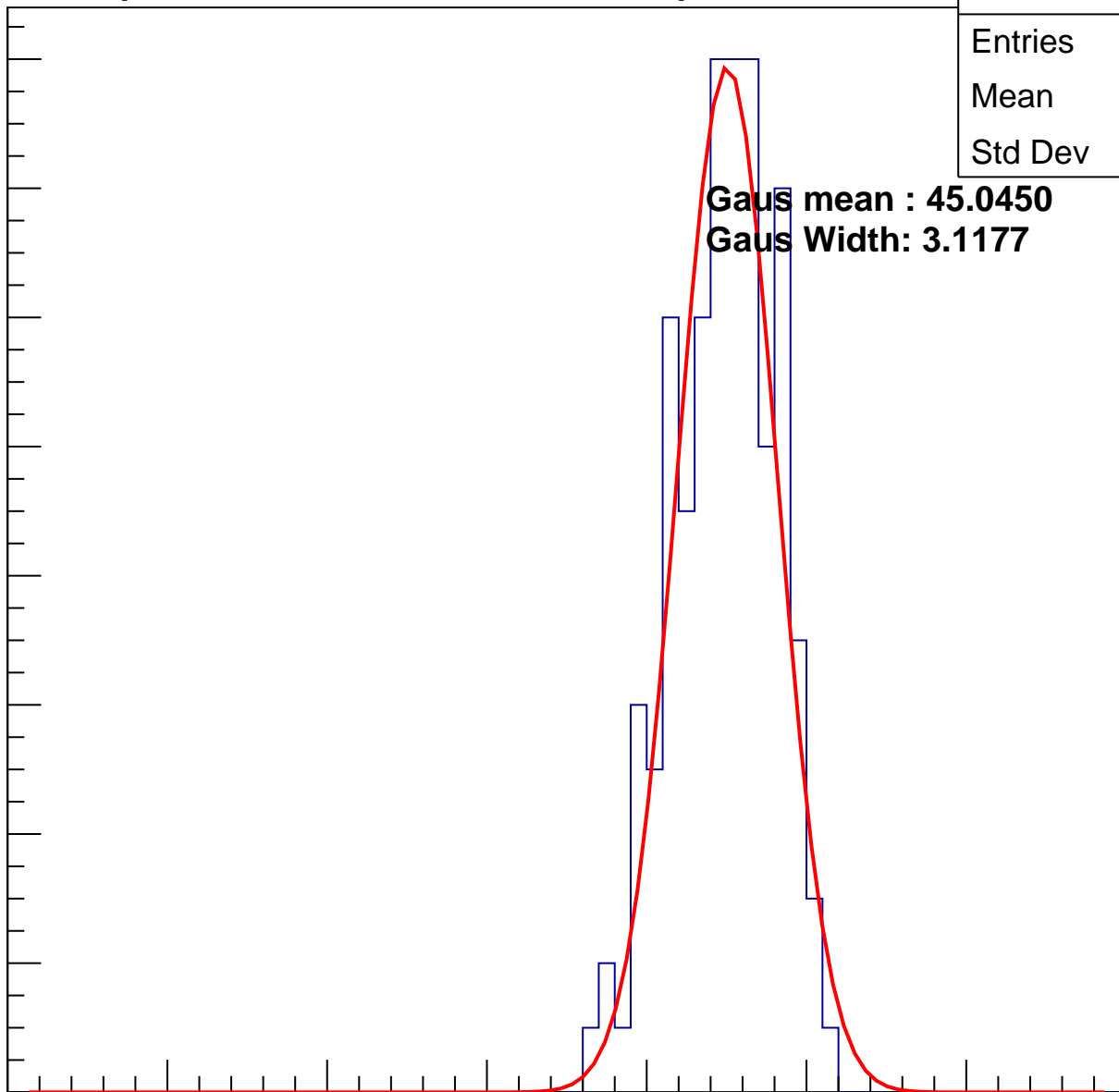
16
14
12
10
8
6
4
2
0

Entries	131
Mean	44.38
Std Dev	3.139

Gaus mean : 45.0450
Gaus Width: 3.1177

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	51.08
Std Dev	3.203

Entry

16

14

12

10

8

6

4

2

0

0

10

20

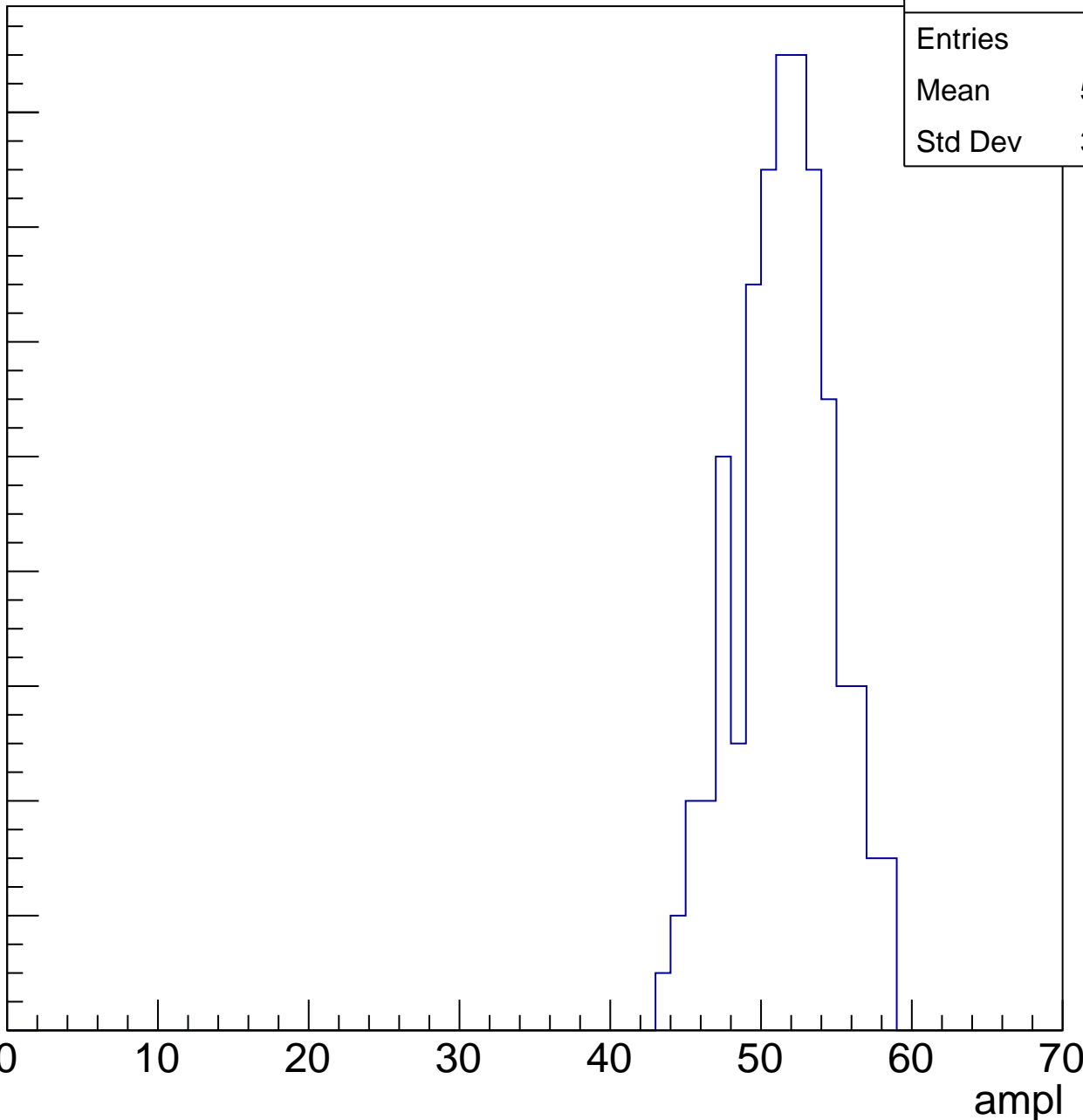
30

40

50

60

ampl



B1L001S, U24-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

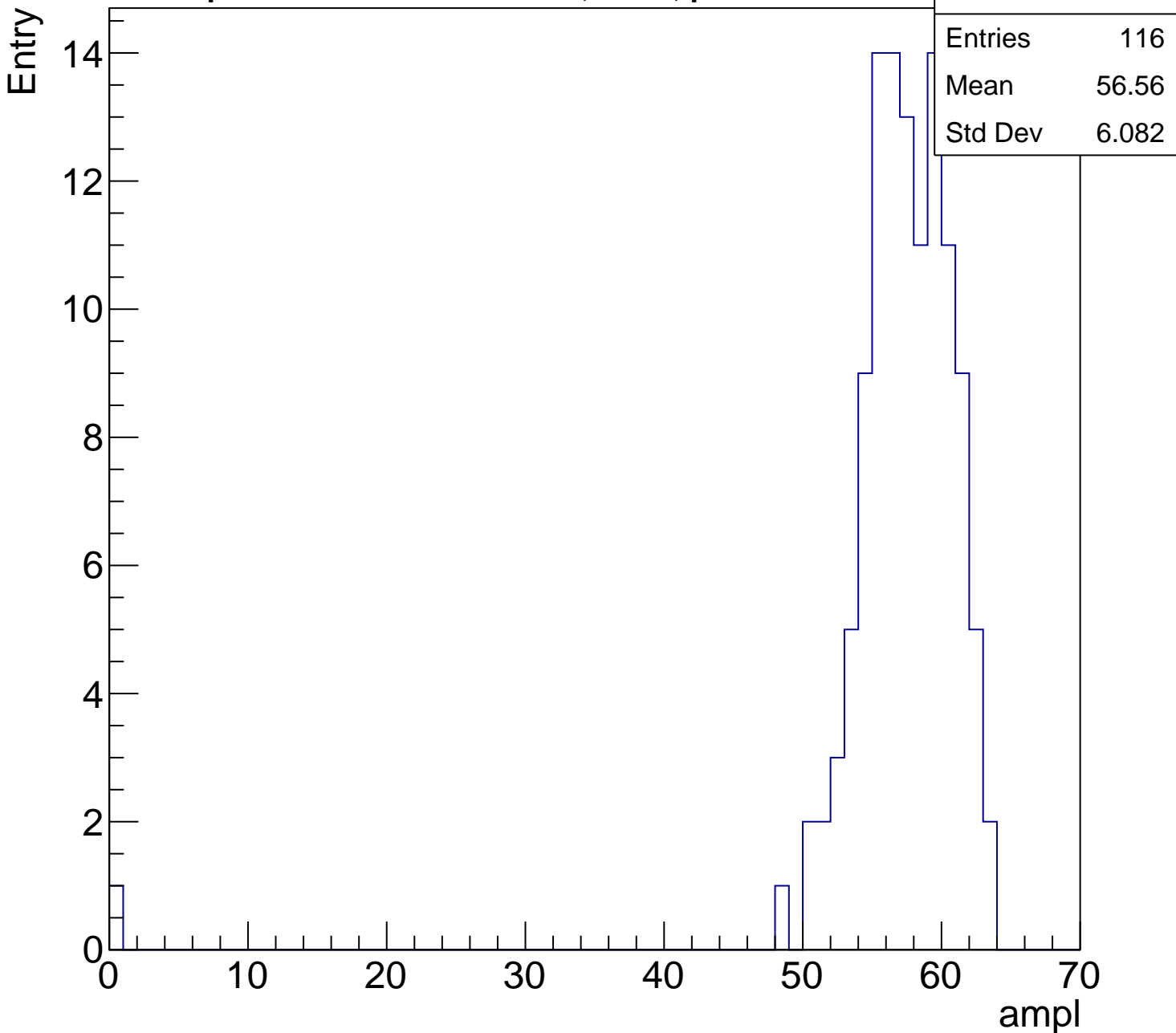
Entries	116
Mean	56.56
Std Dev	6.082

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch65, adc5

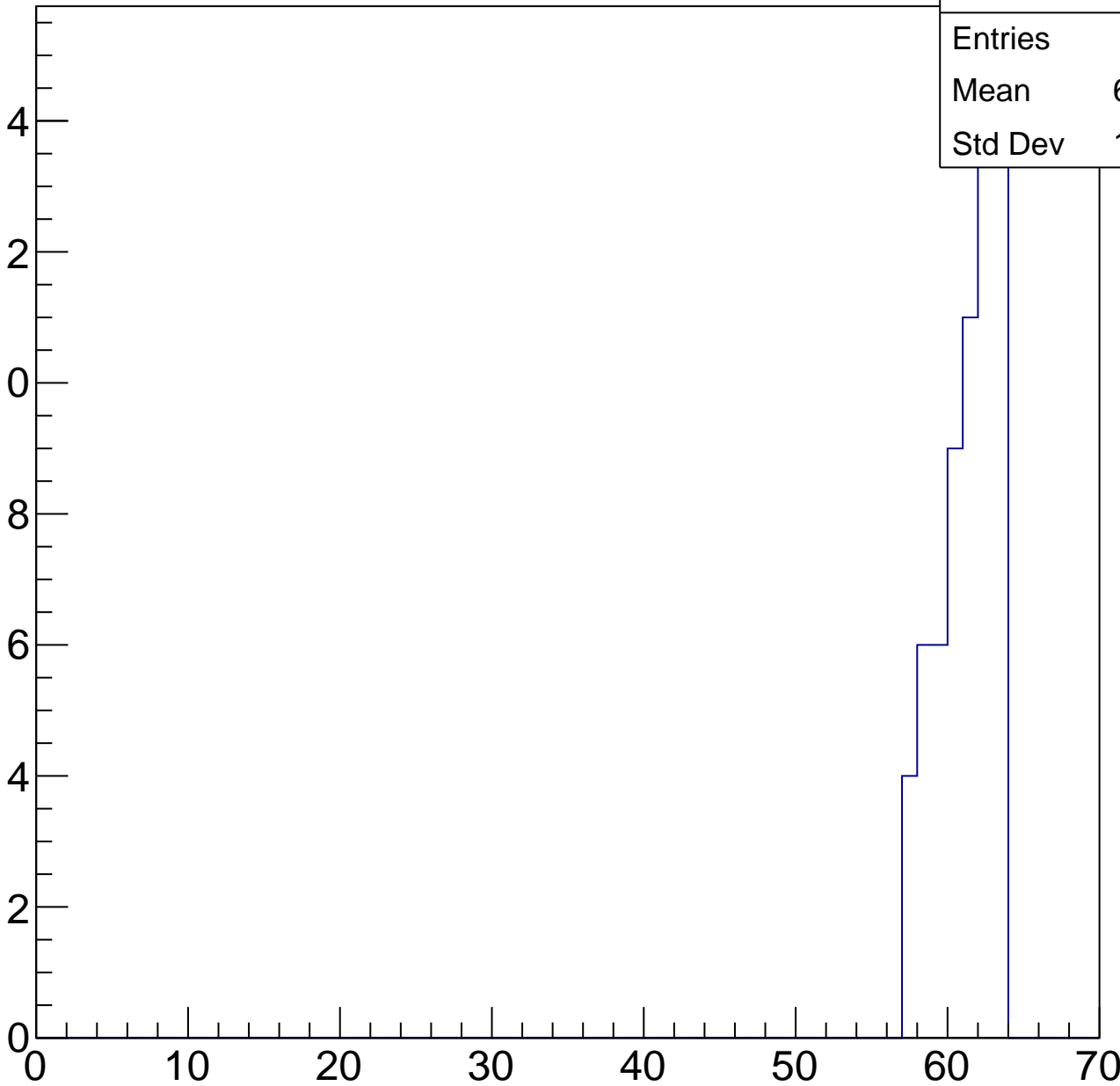
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	60.85
Std Dev	1.844

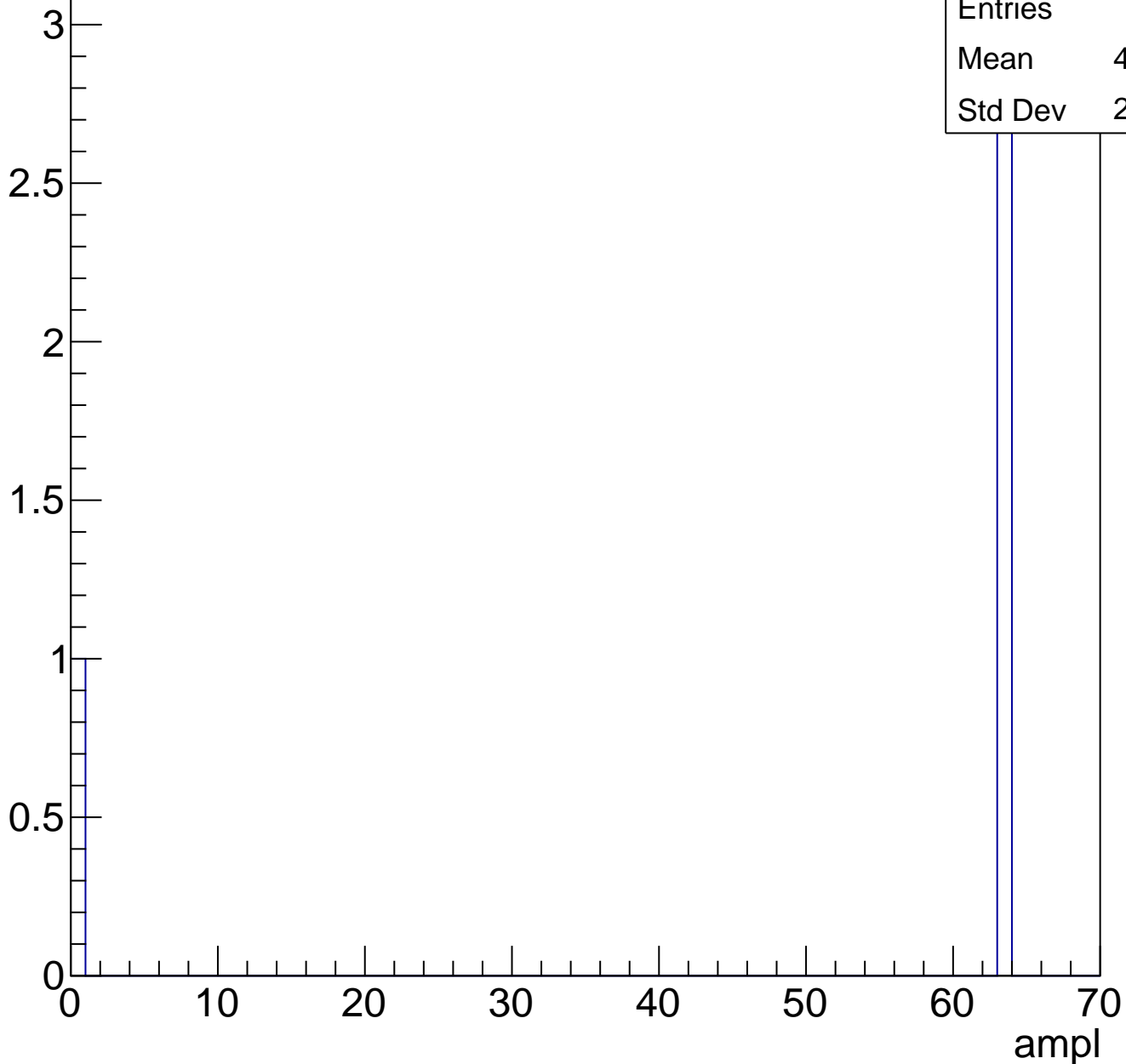
ampl



B1L001S, U24-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

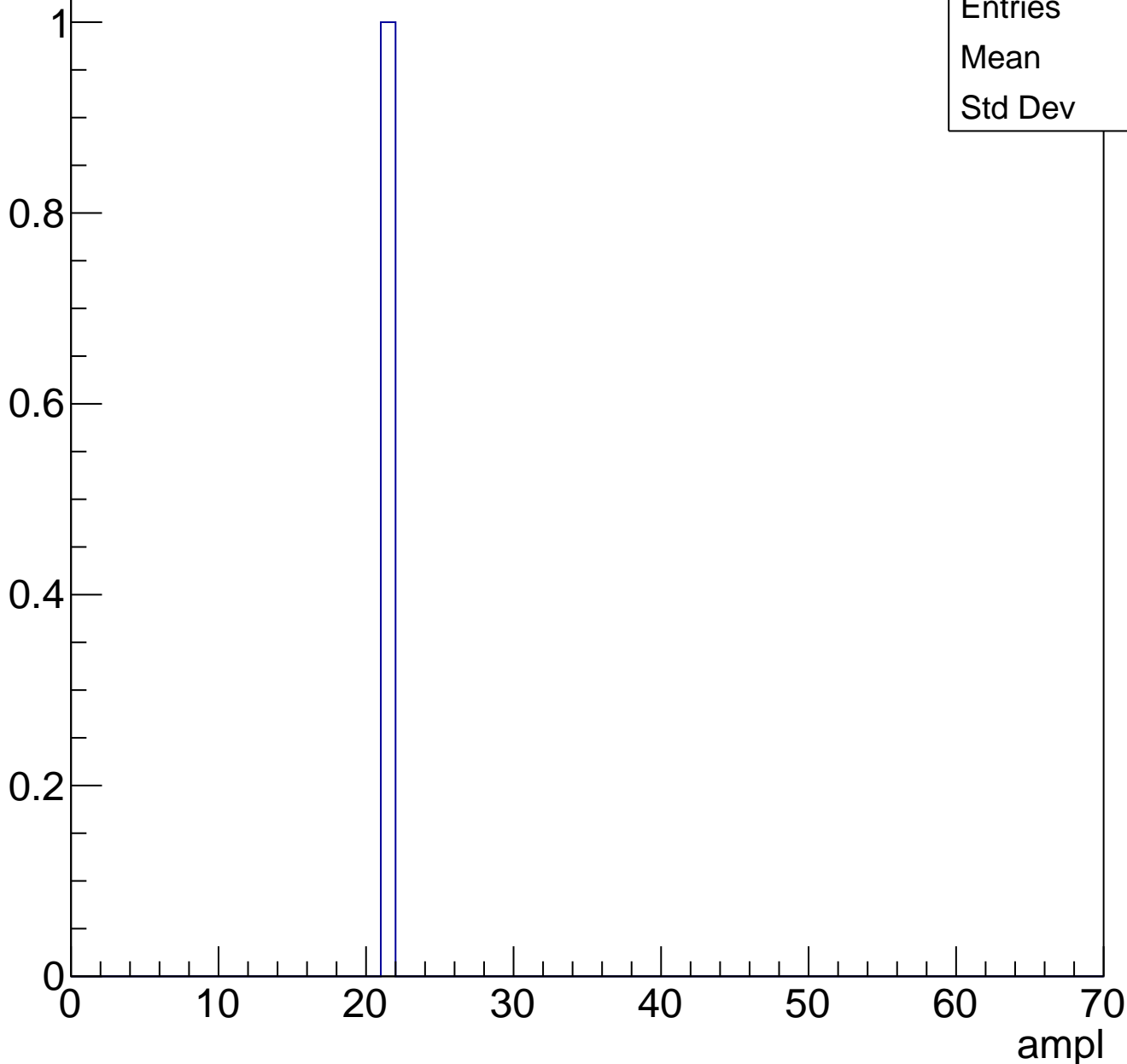
Entry



B1L001S, U24-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch66, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.96
Std Dev	3.199

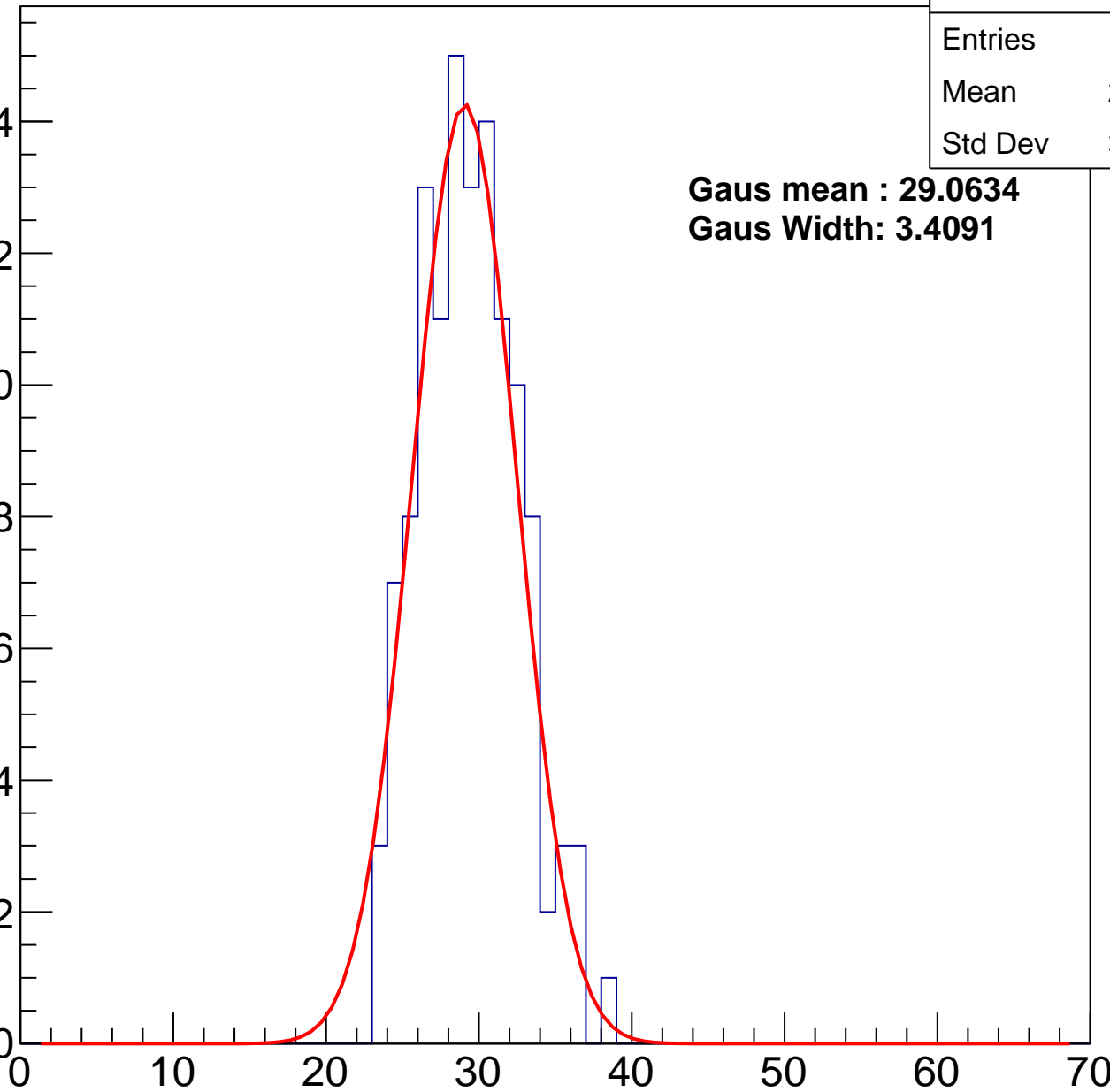
Gaus mean : 29.0634

Gaus Width: 3.4091

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.52
Std Dev	3.388

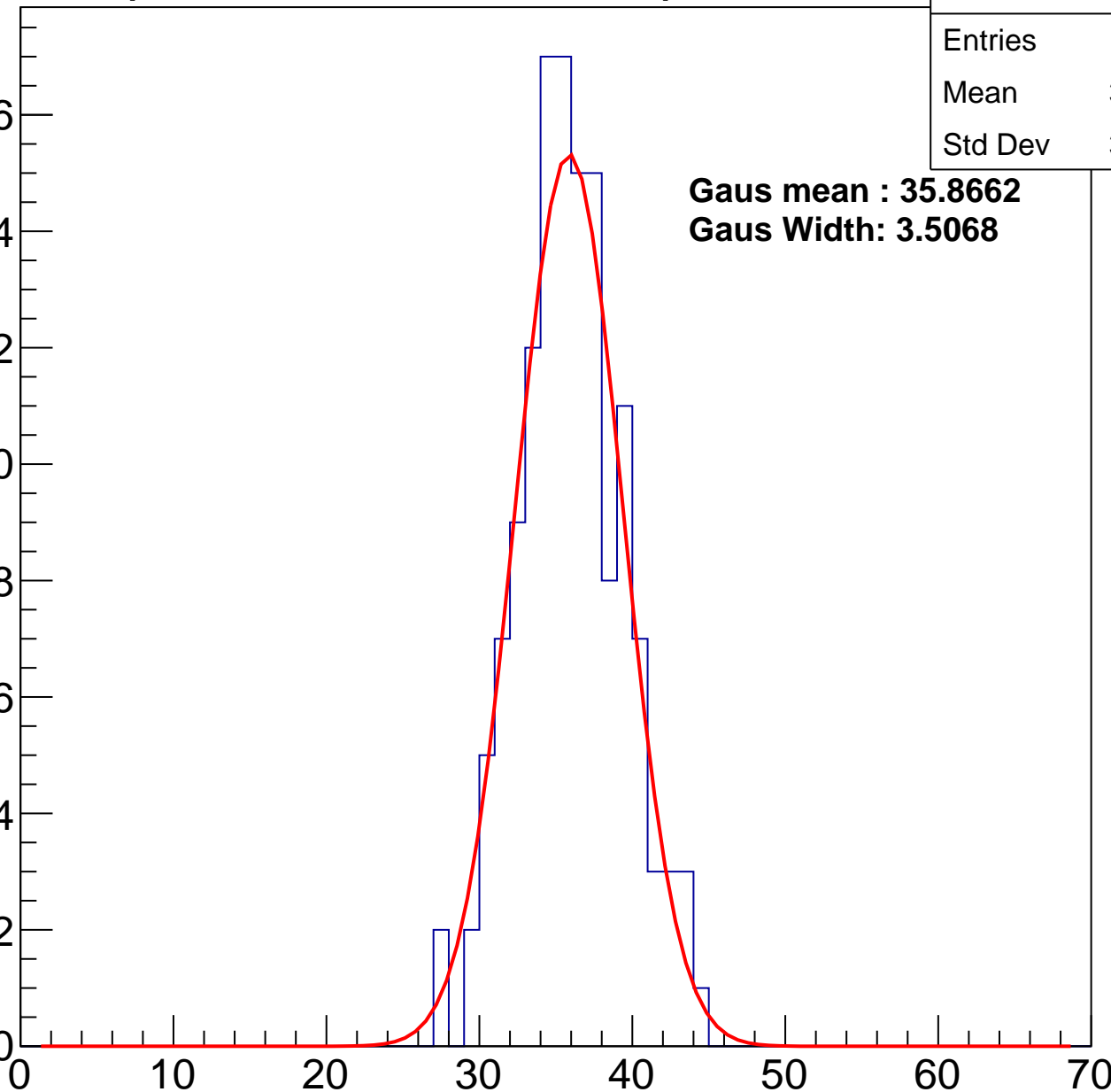
Gaus mean : 35.8662

Gaus Width: 3.5068

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch66, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

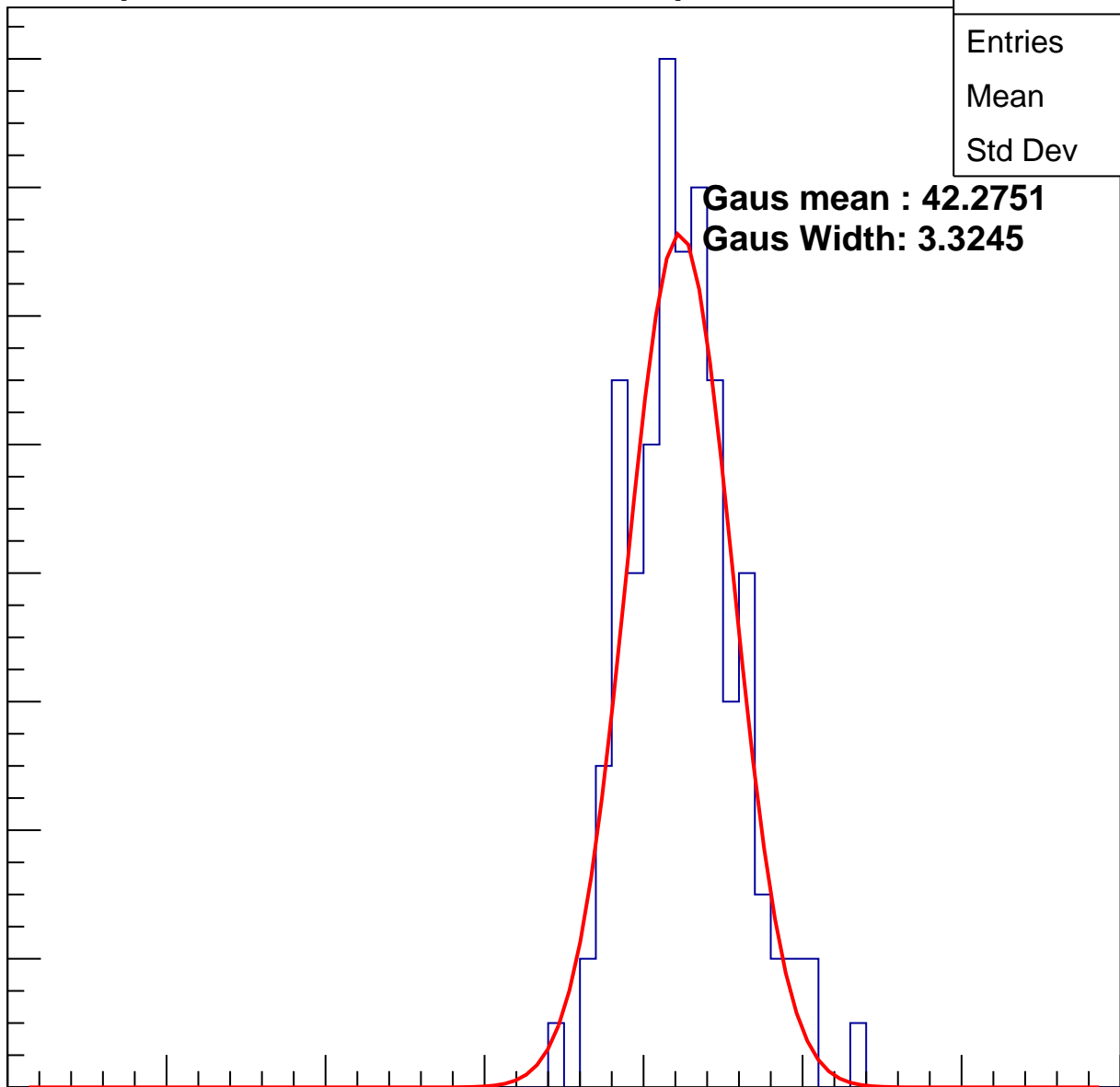
Entries	115
Mean	42.04
Std Dev	3.361

Gaus mean : 42.2751

Gaus Width: 3.3245

0 10 20 30 40 50 60 70

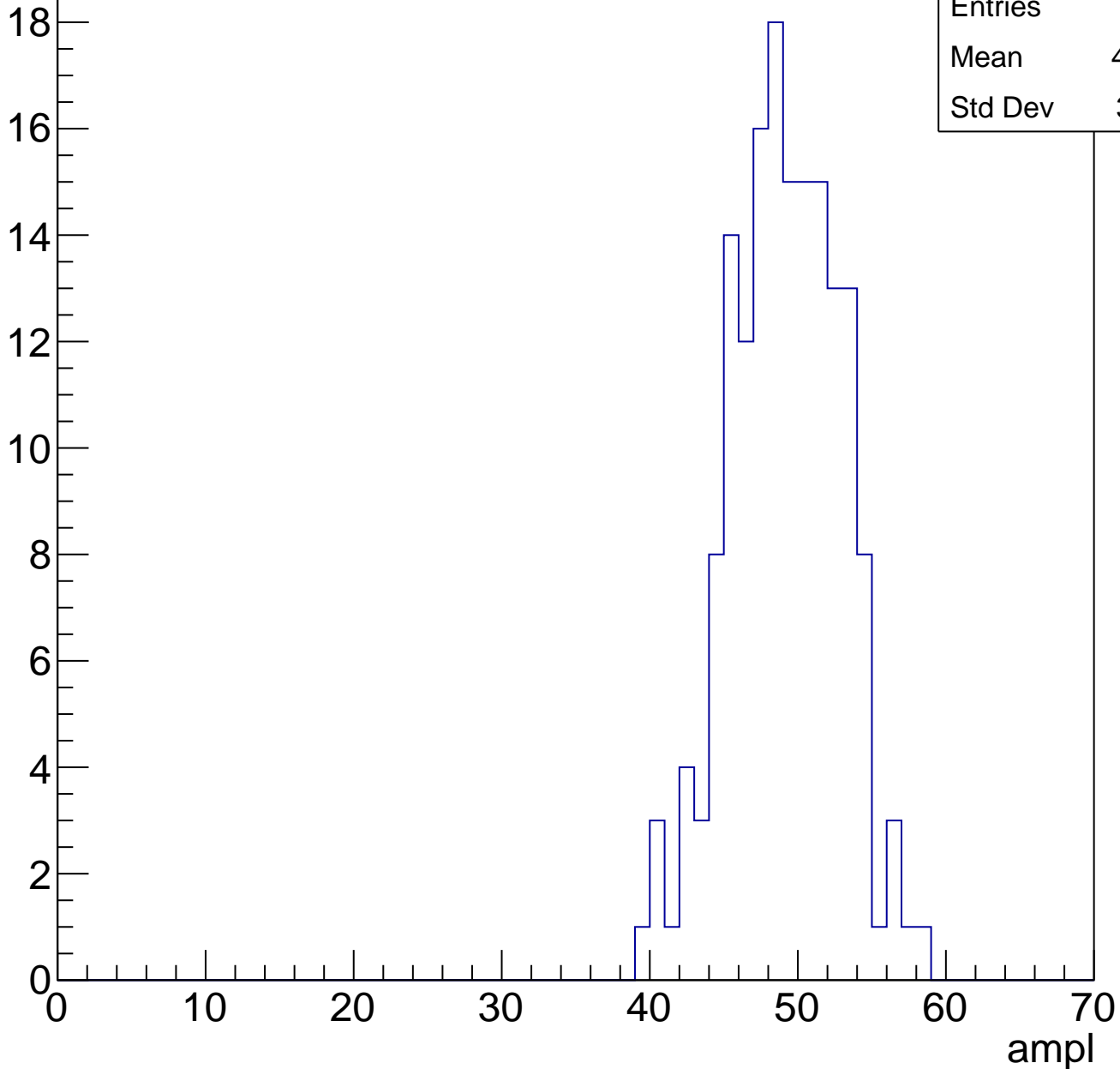
ampl



B1L001S, U24-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	165
Mean	48.68
Std Dev	3.681

B1L001S, U24-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	55.55
Std Dev	6.137

Entry

16

14

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch66, adc5

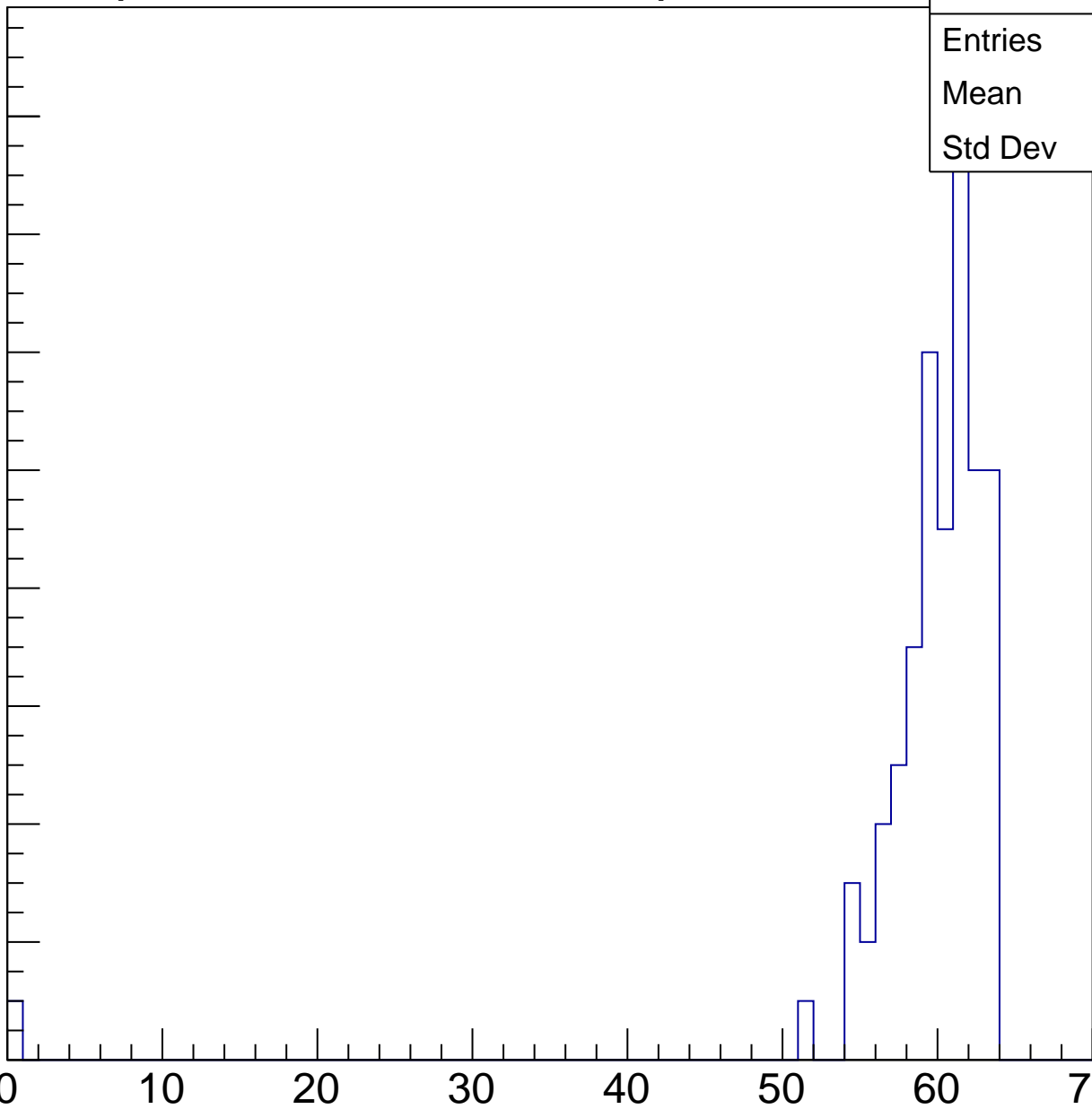
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.93
Std Dev	7.064

ampl



B1L001S, U24-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	16
Mean	62.12
Std Dev	1.452

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

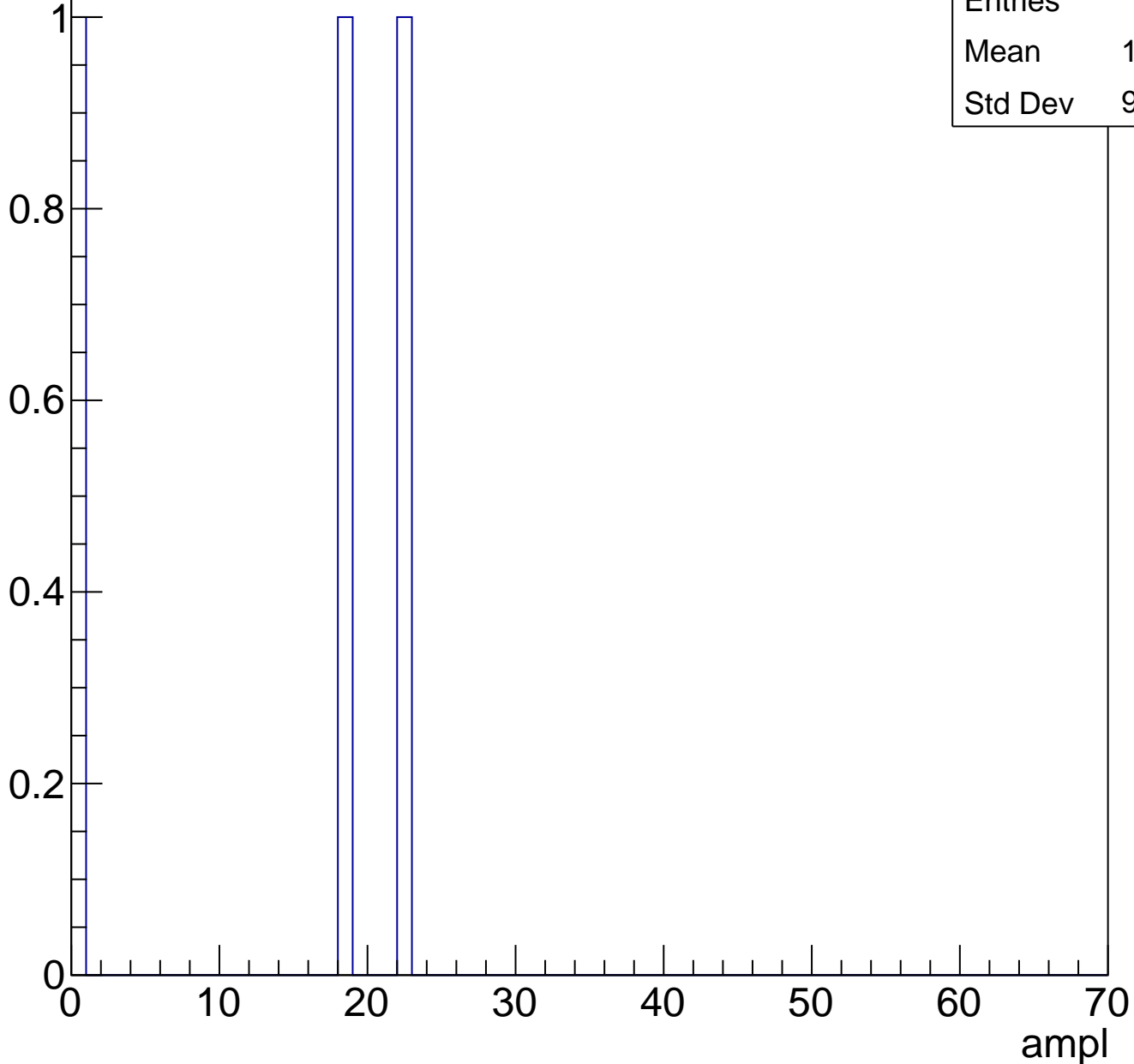
8

9

B1L001S, U24-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



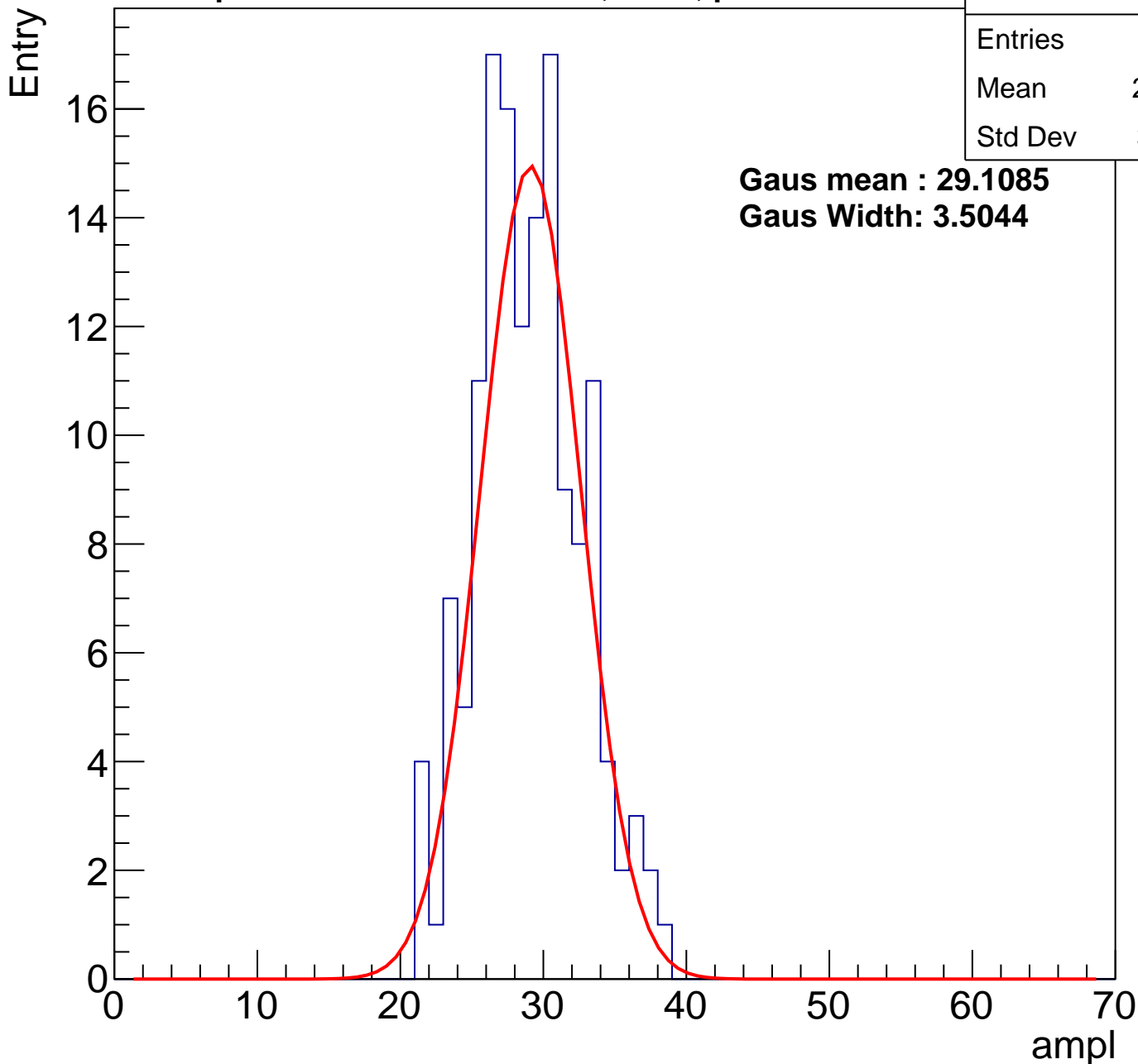
B1L001S, U24-ch67, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	28.56
Std Dev	3.621

Gaus mean : 29.1085

Gaus Width: 3.5044



B1L001S, U24-ch67, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

122

Mean

34.43

Std Dev

3.416

Gaus mean : 34.9406

Gaus Width: 3.0327

0

10

20

30

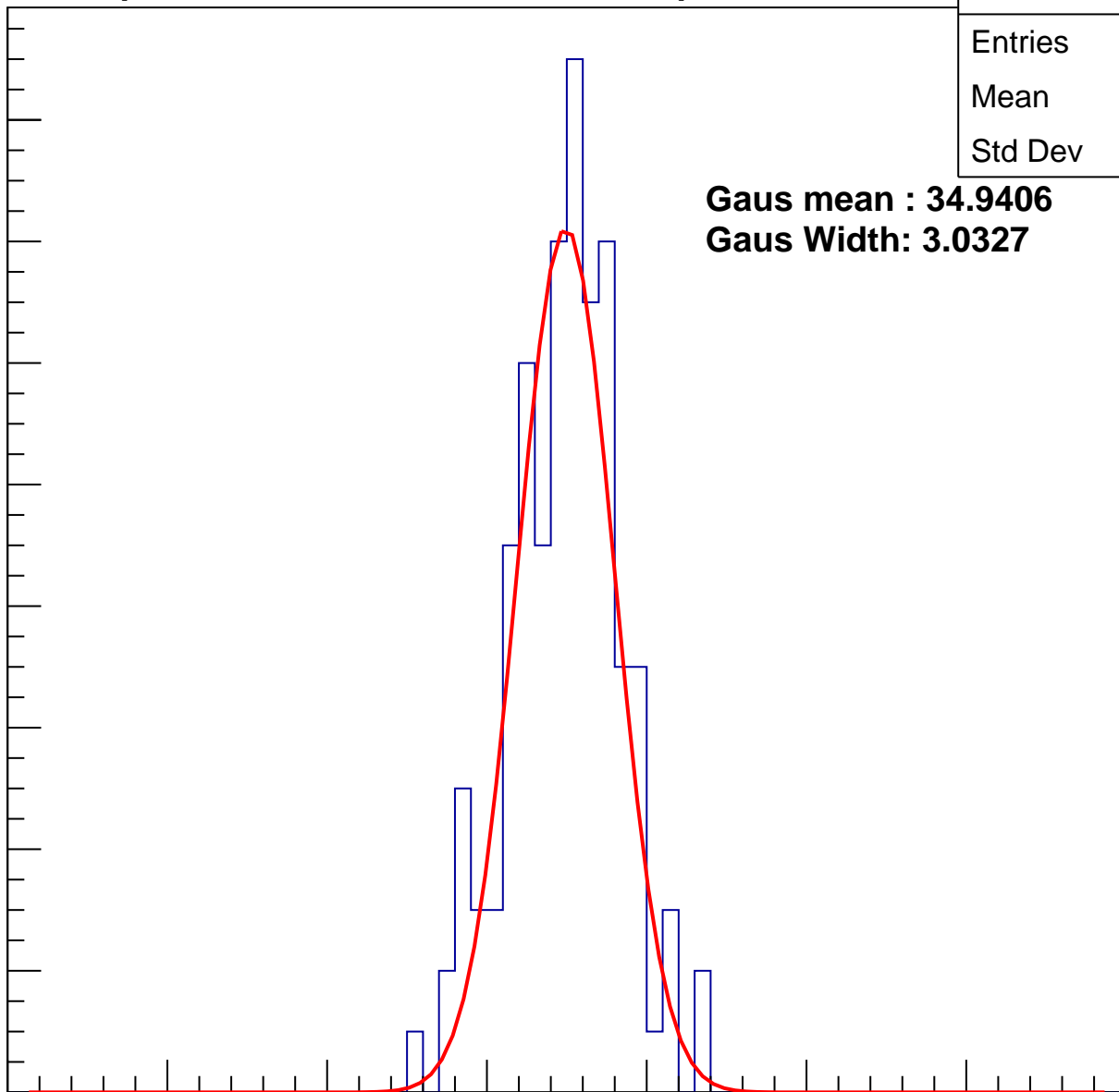
40

50

60

70

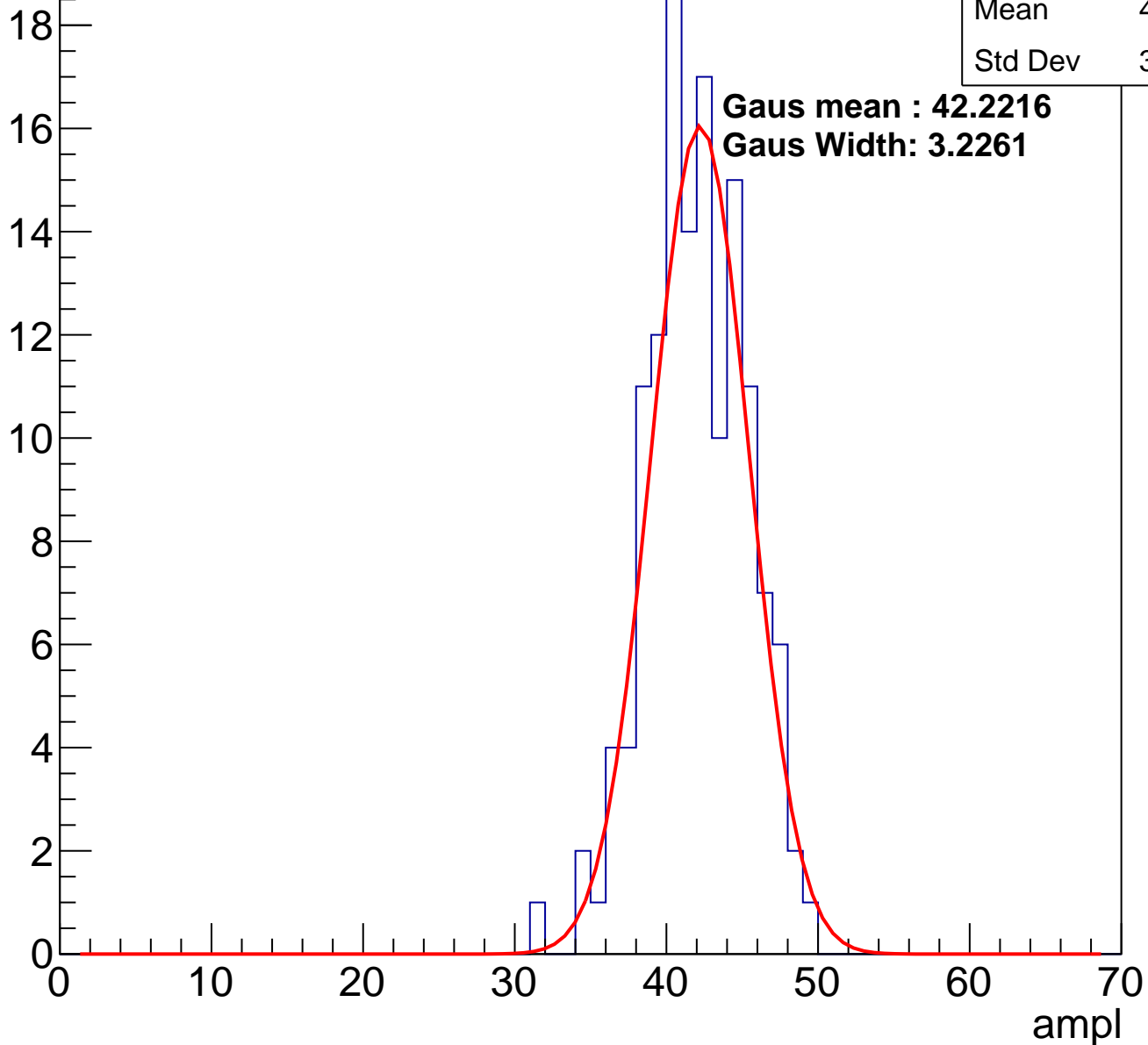
ampl



B1L001S, U24-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	47.67
Std Dev	3.307

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

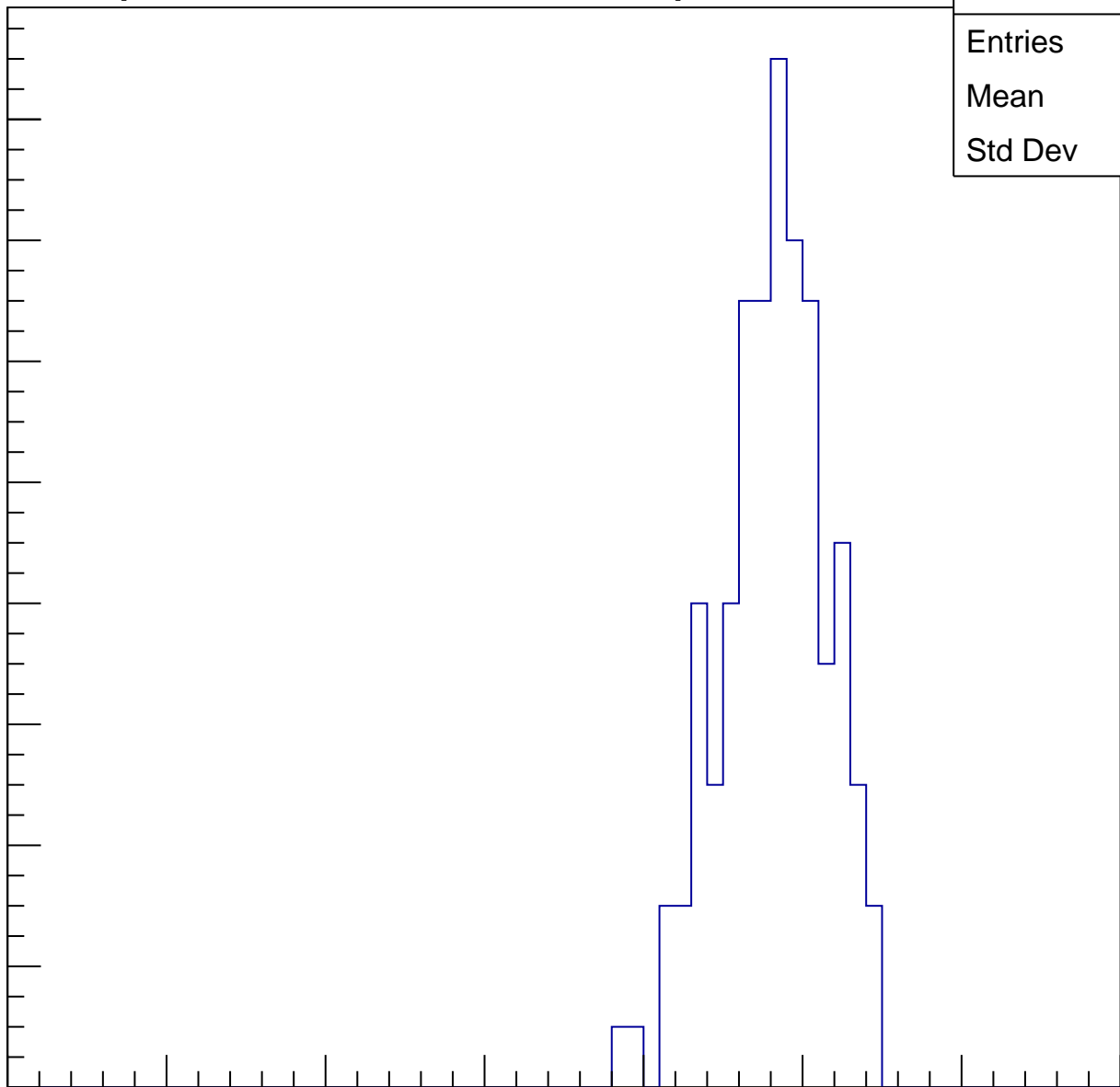
40

50

60

70

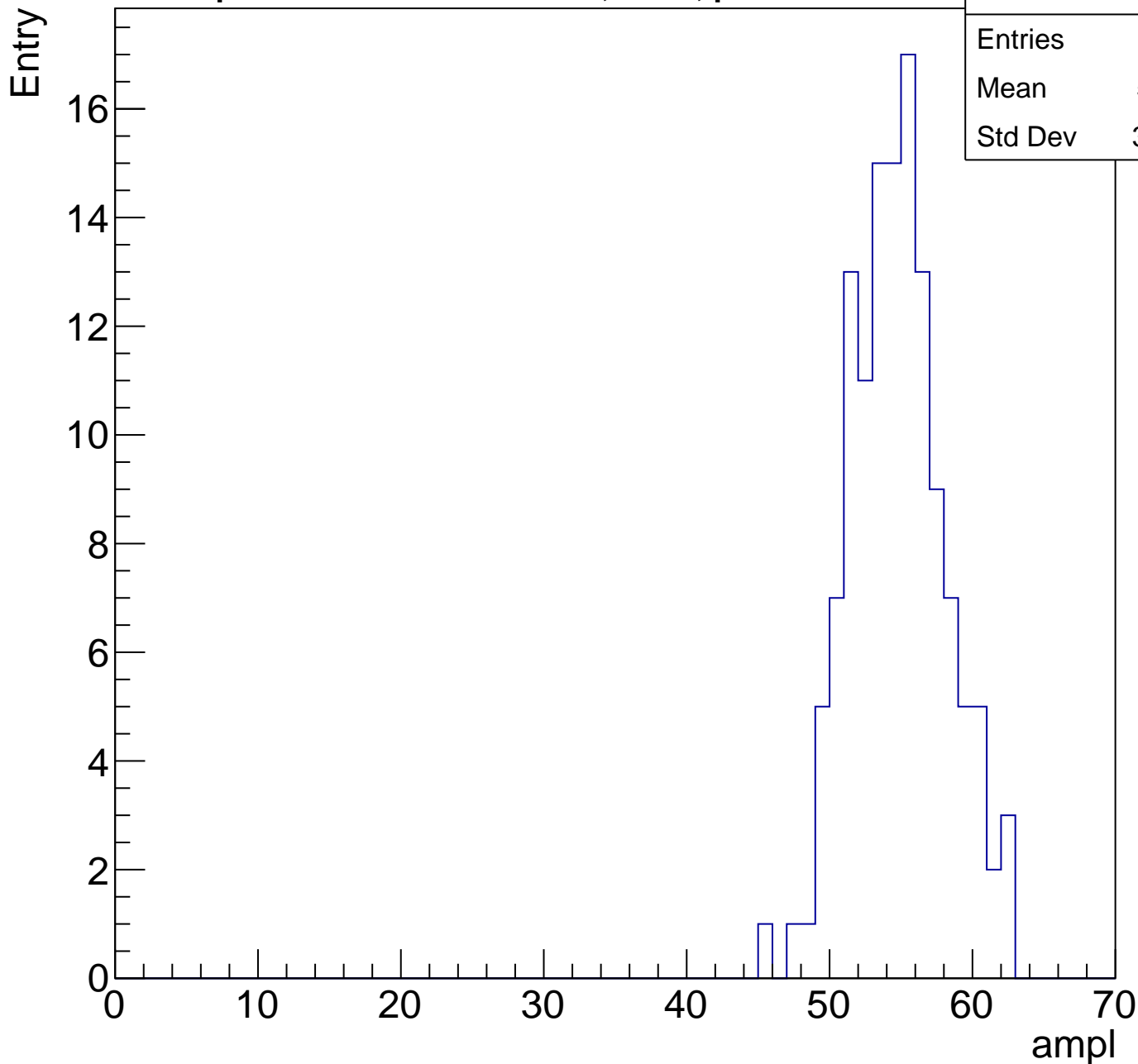
ampl



B1L001S, U24-ch67, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	54.31
Std Dev	3.309



B1L001S, U24-ch67, adc5

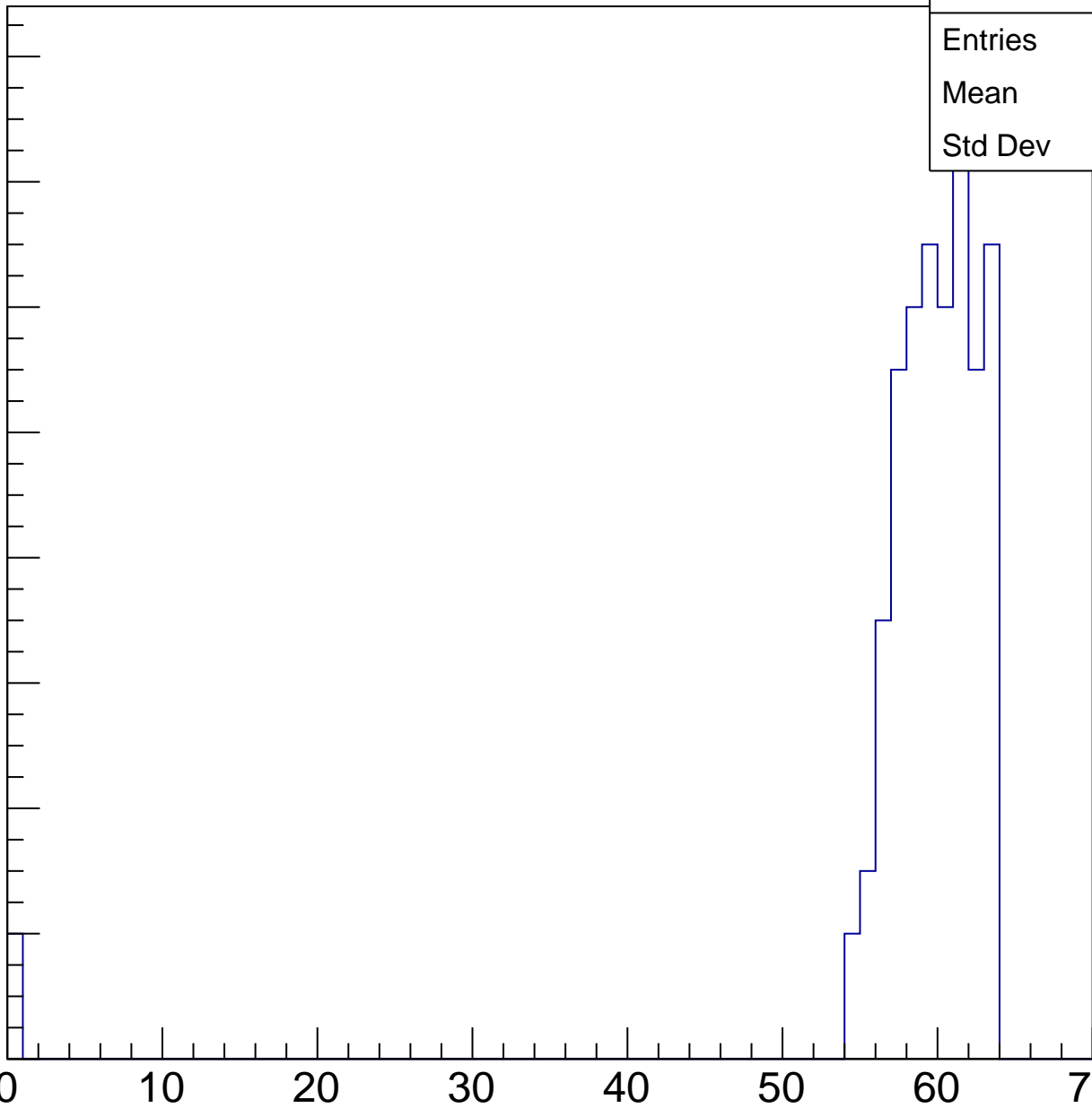
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.35
Std Dev	8.586

ampl

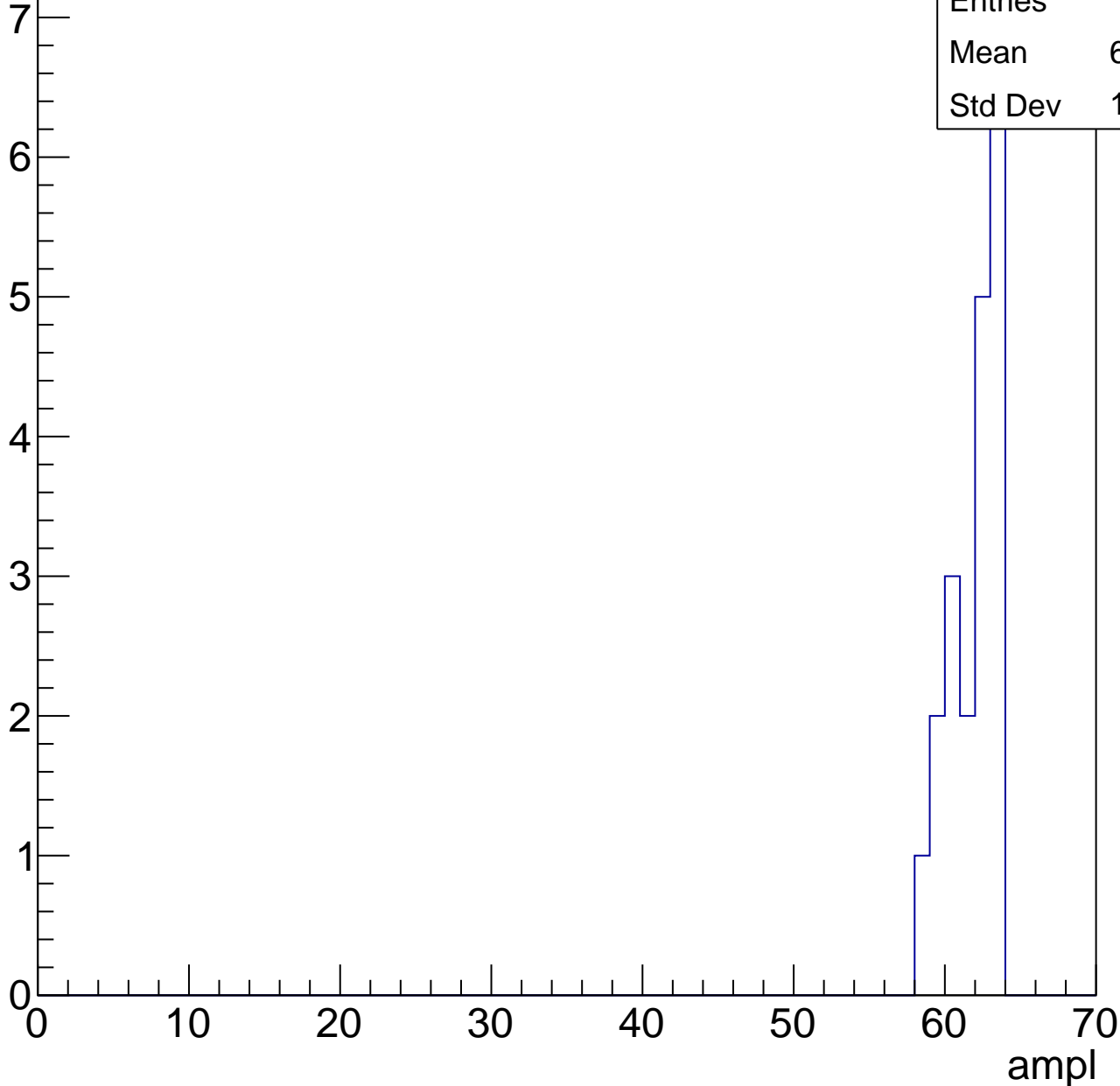


B1L001S, U24-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

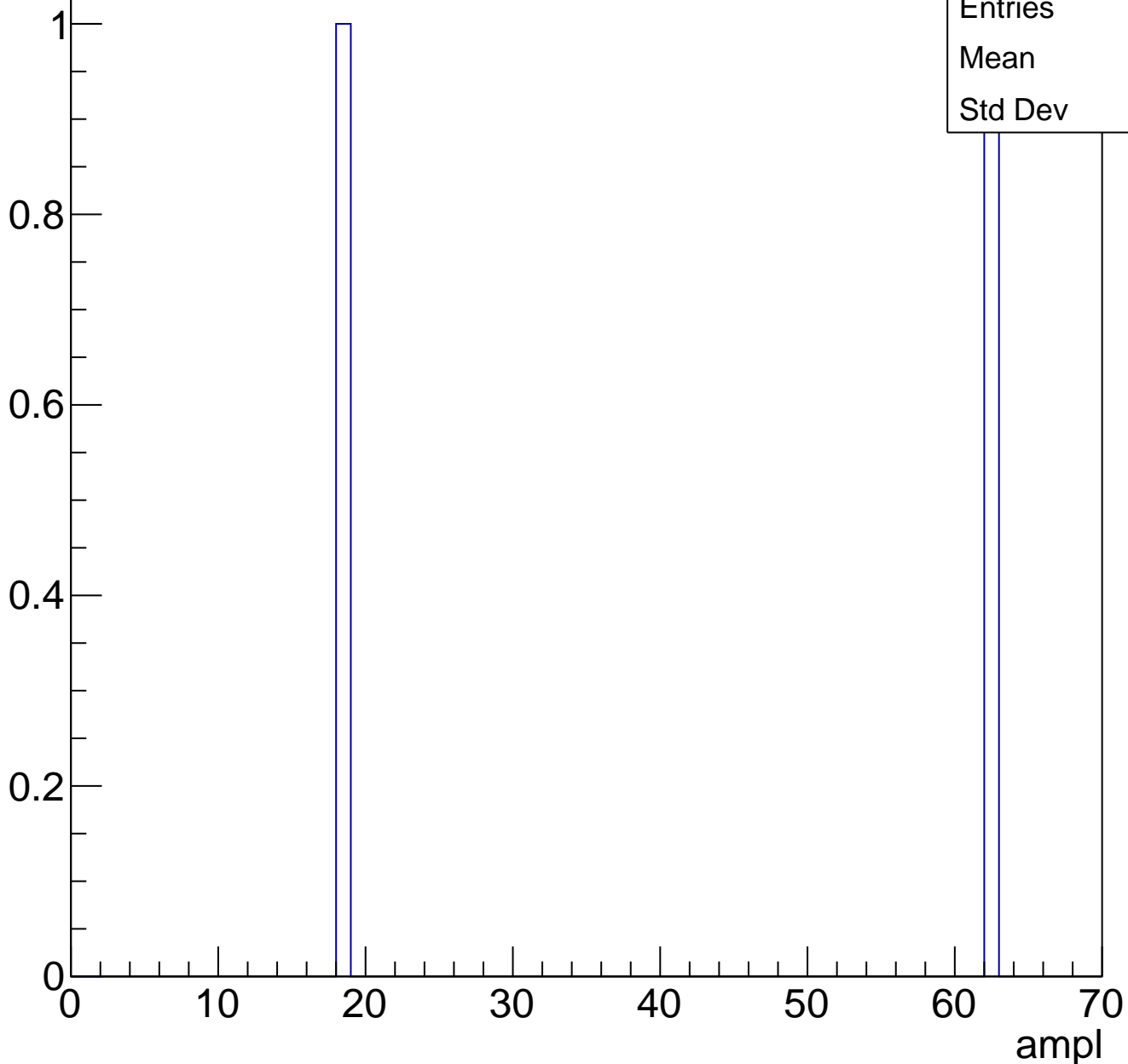
Entries	20
Mean	61.45
Std Dev	1.564



B1L001S, U24-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	40
Std Dev	22

B1L001S, U24-ch68, adc0

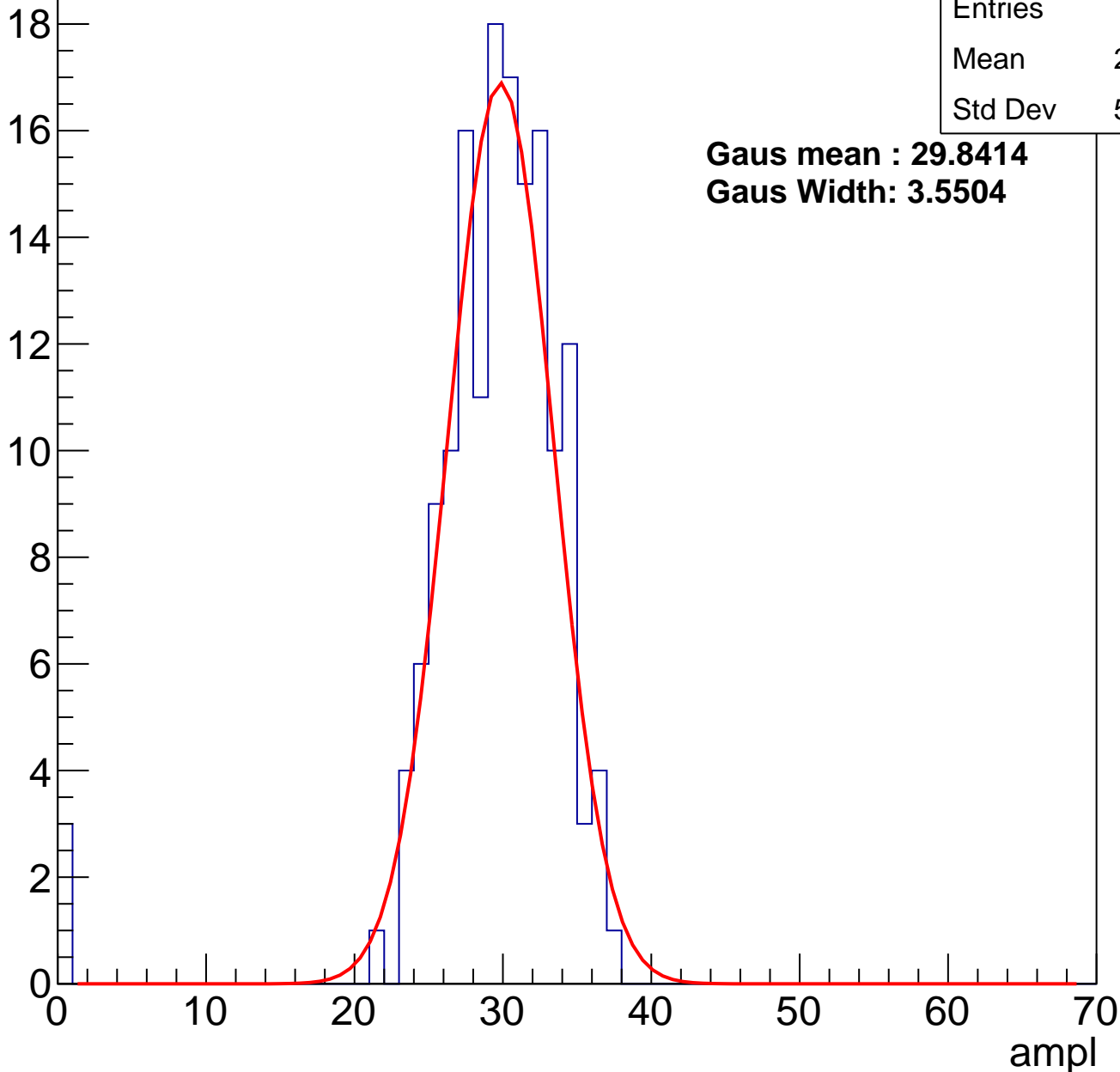
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	28.94
Std Dev	5.212

Gaus mean : 29.8414

Gaus Width: 3.5504

Entry



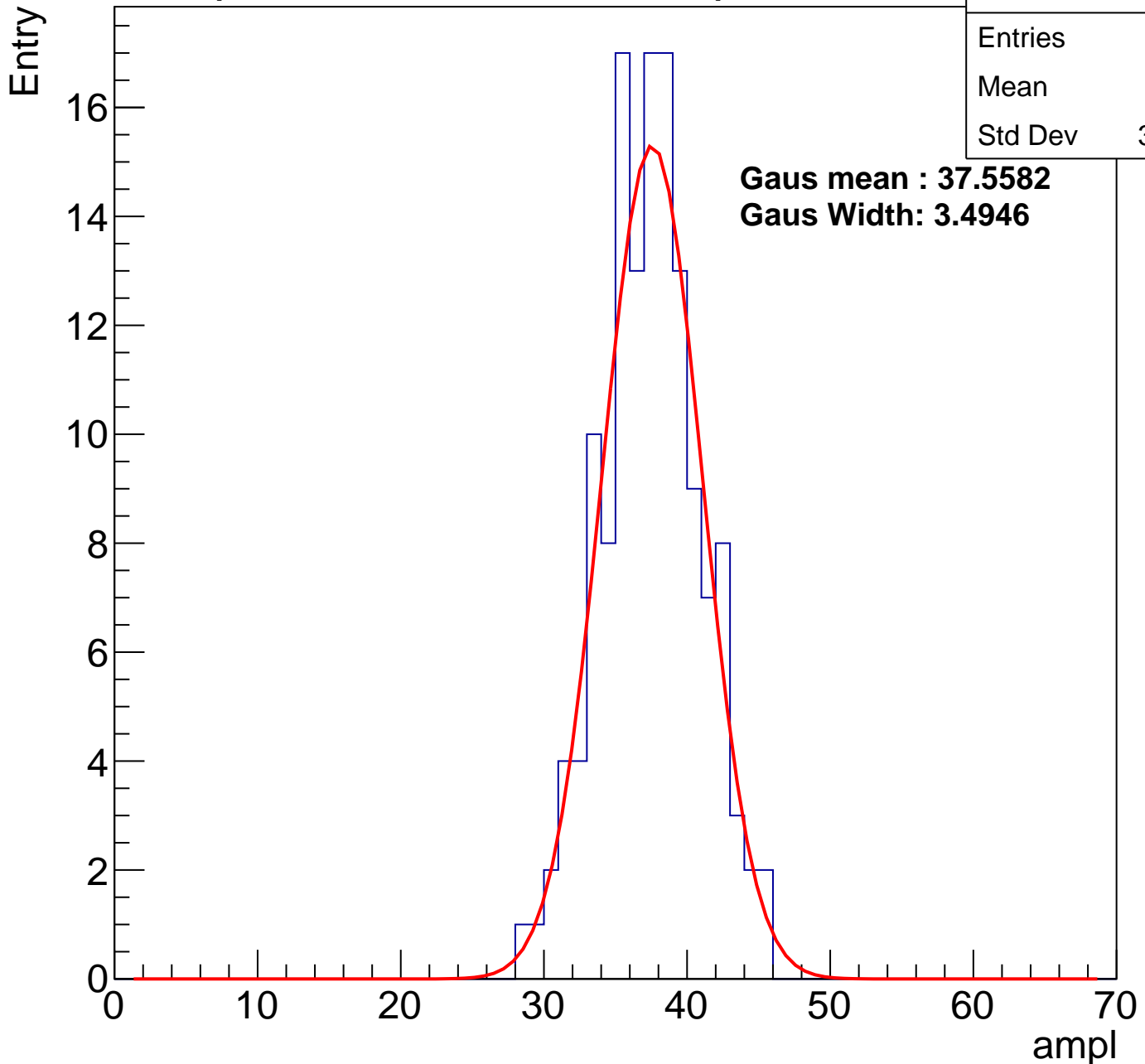
B1L001S, U24-ch68, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	37
Std Dev	3.401

Gaus mean : 37.5582

Gaus Width: 3.4946



B1L001S, U24-ch68, adc2

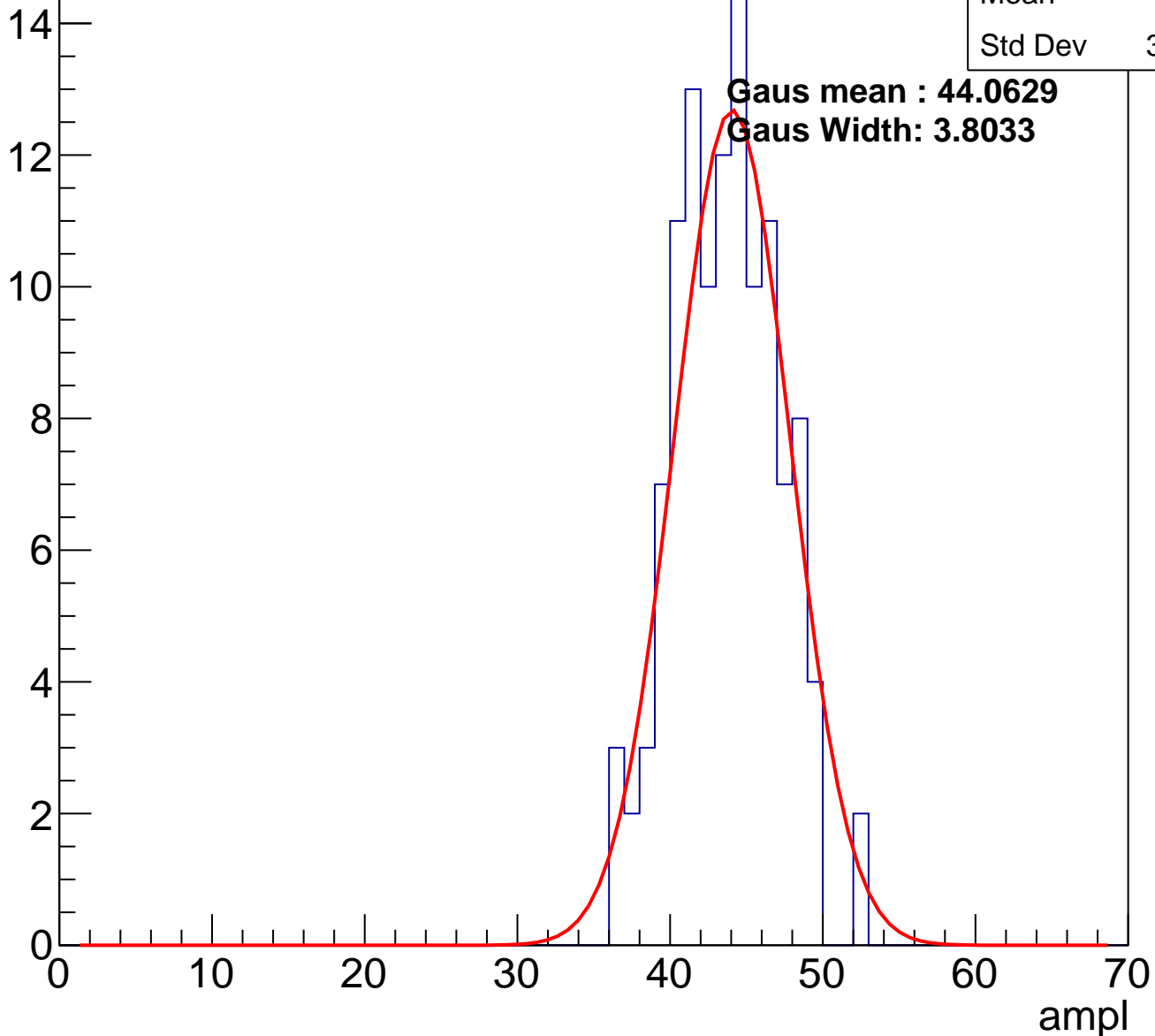
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	43.28
Std Dev	3.357

Gaus mean : 44.0629

Gaus Width: 3.8033

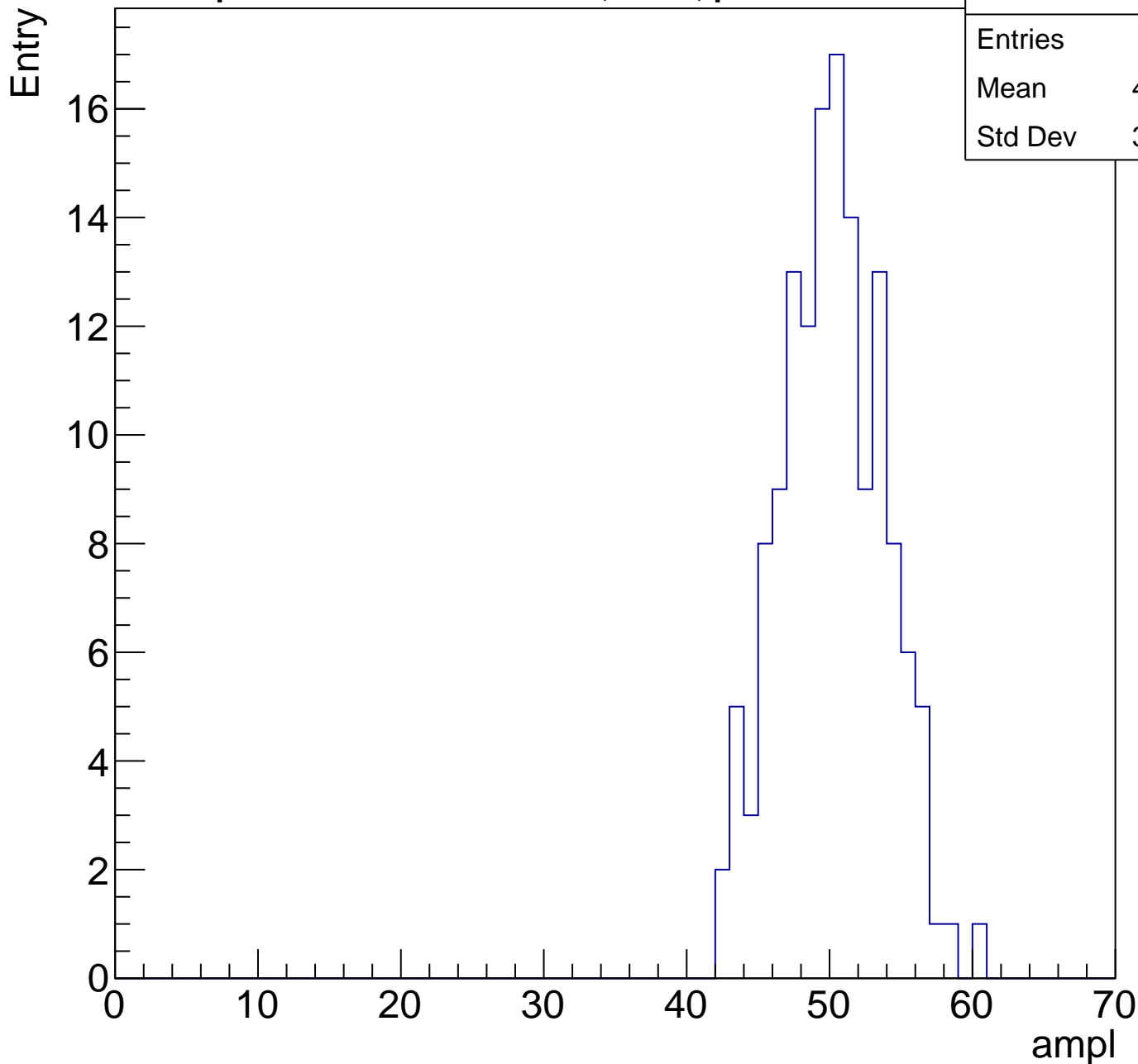
Entry



B1L001S, U24-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	49.75
Std Dev	3.582

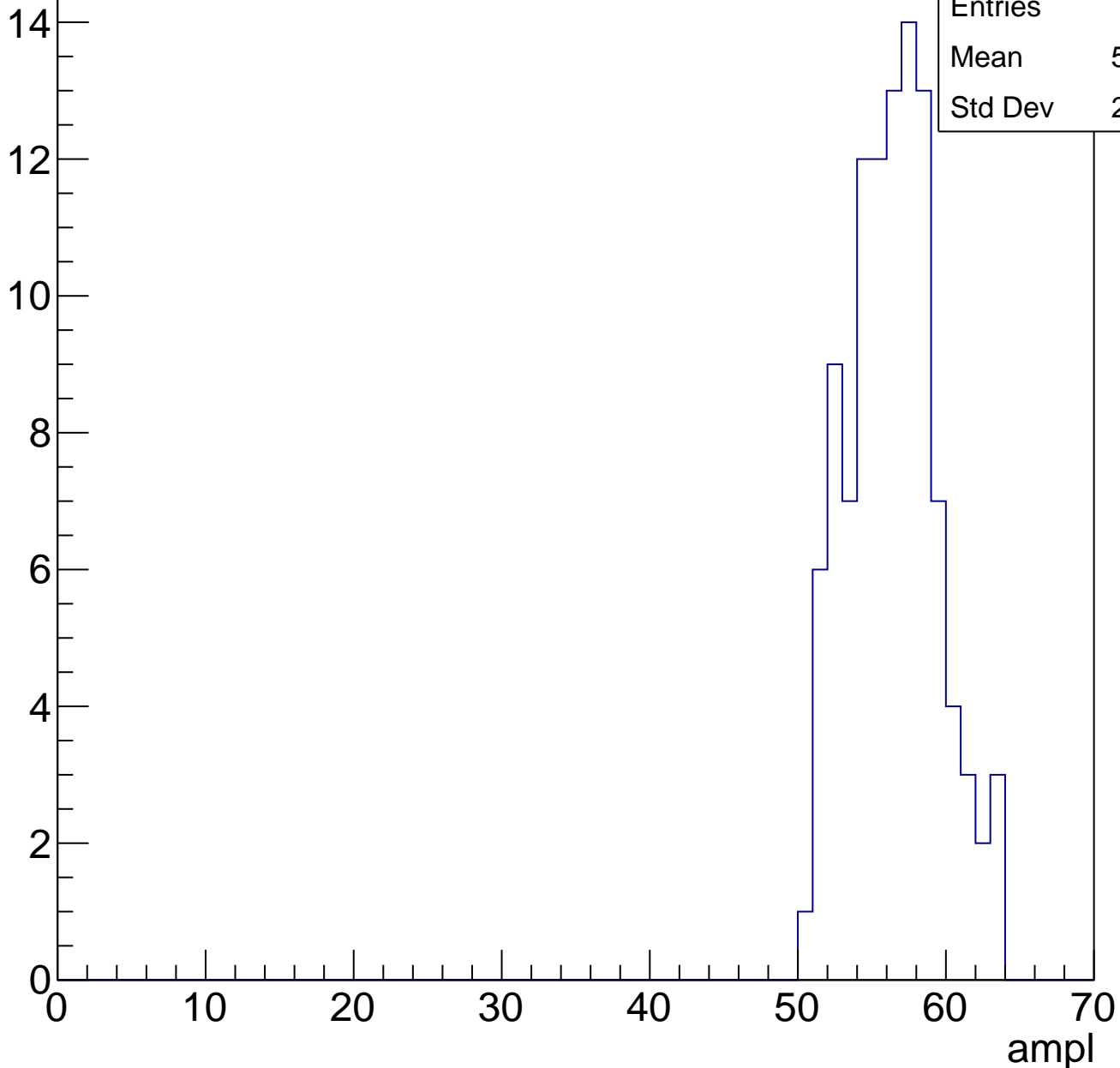


B1L001S, U24-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	55.96
Std Dev	2.978

Entry



B1L001S, U24-ch68, adc5

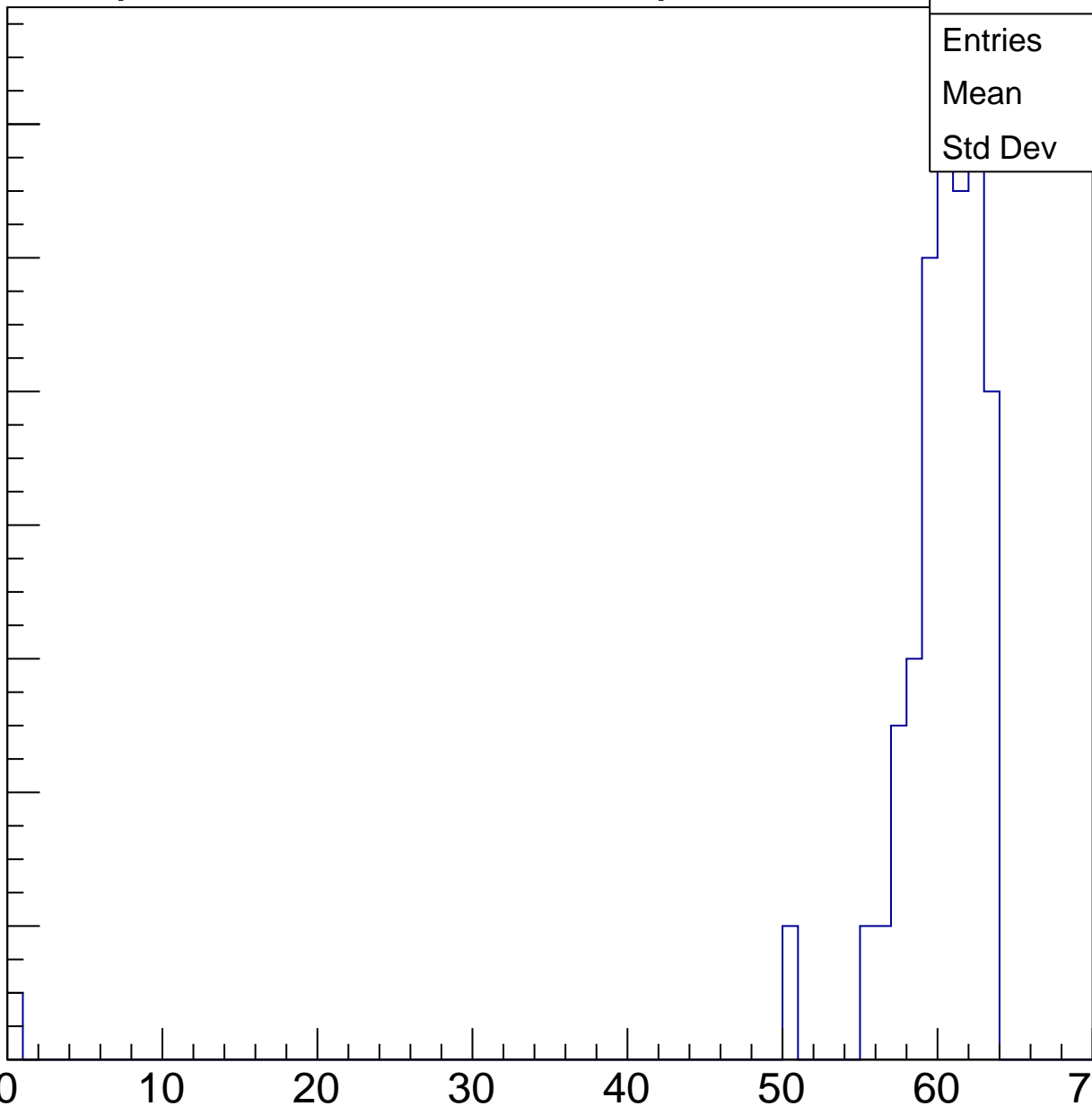
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	59.23
Std Dev	7.008

ampl

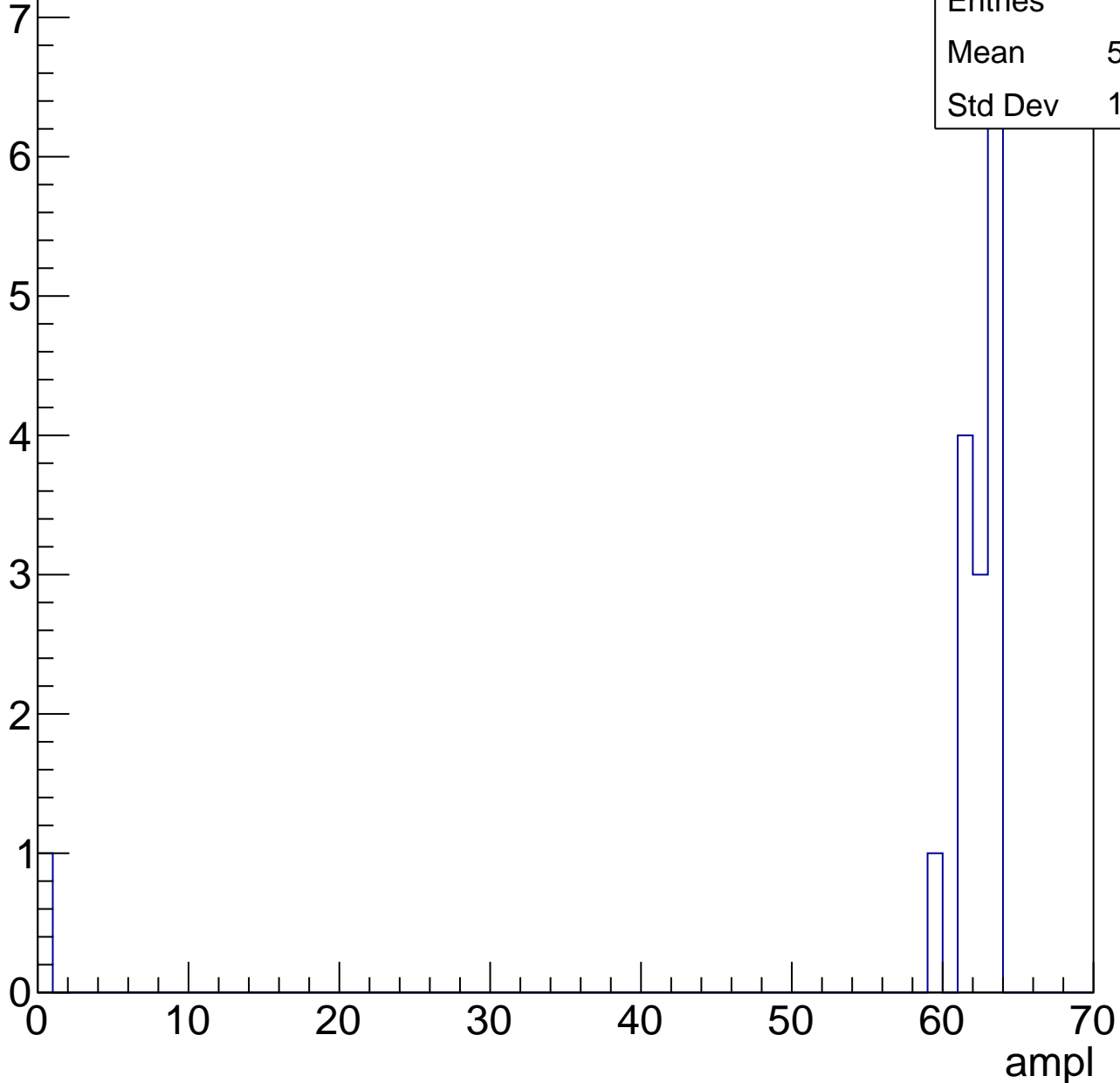


B1L001S, U24-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	58.12
Std Dev	15.05



B1L001S, U24-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L001S, U24-ch69, adc0

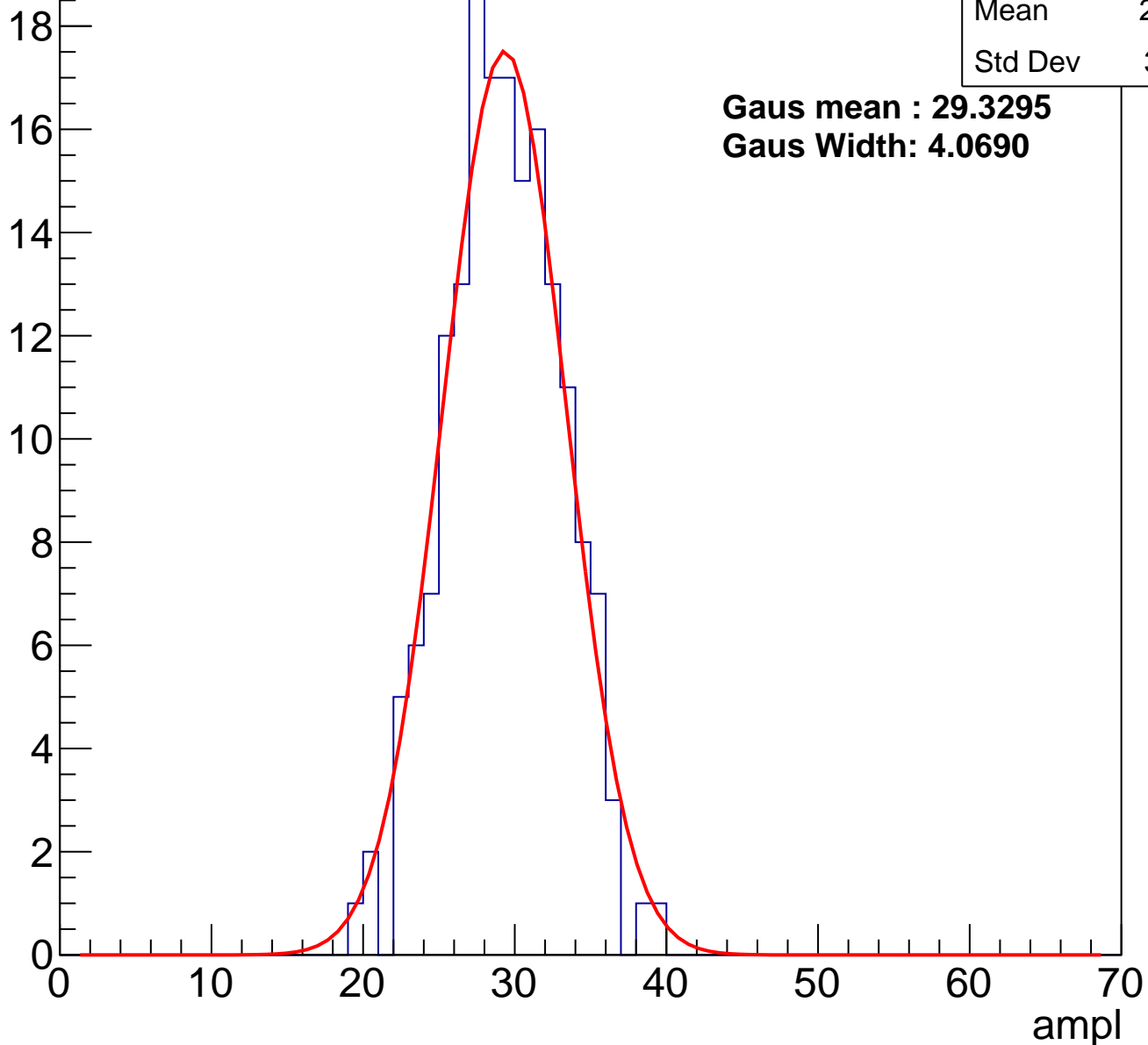
calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	28.86
Std Dev	3.751

Gaus mean : 29.3295

Gaus Width: 4.0690

Entry



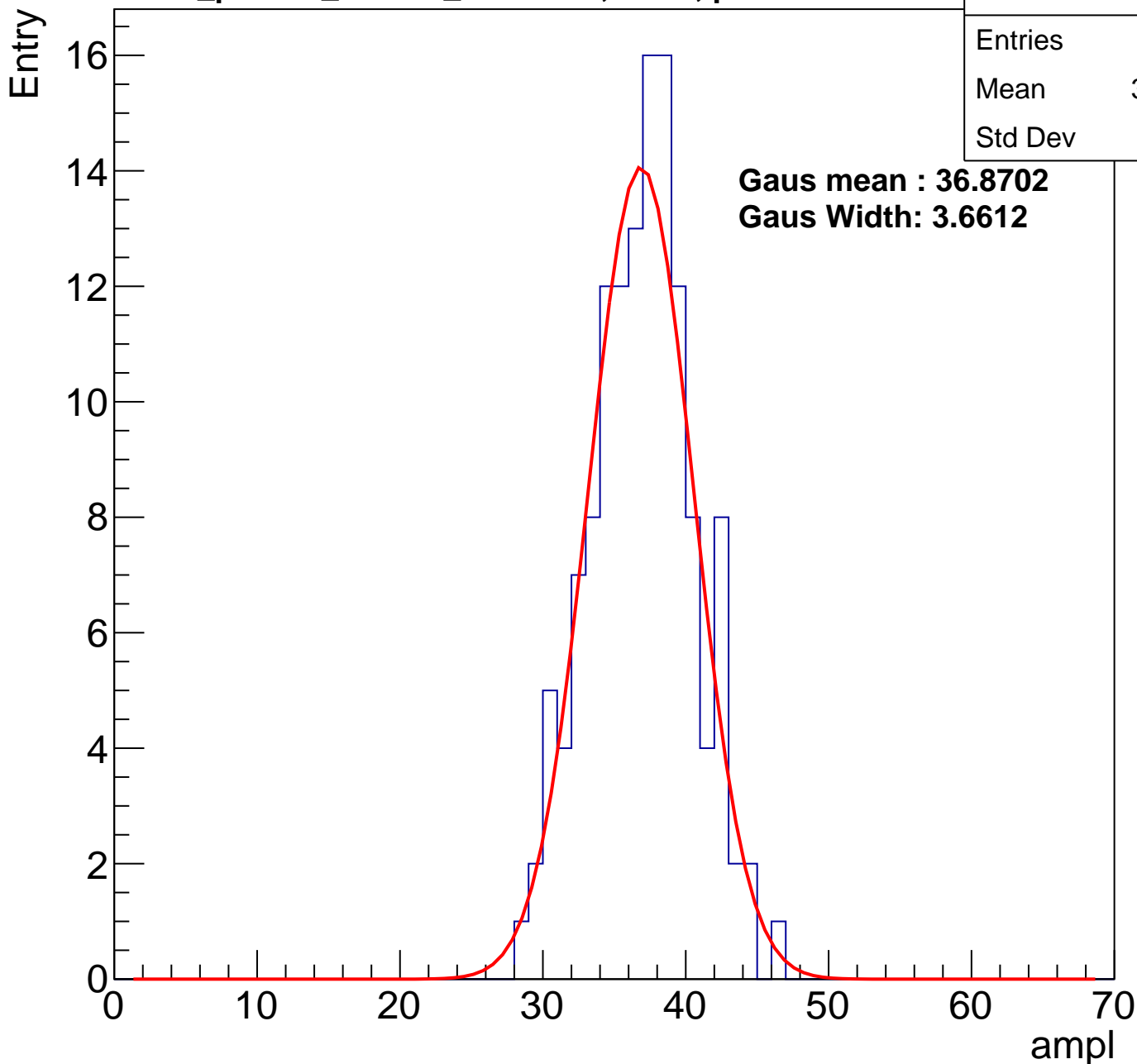
B1L001S, U24-ch69, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.48
Std Dev	3.56

Gaus mean : 36.8702

Gaus Width: 3.6612



B1L001S, U24-ch69, adc2

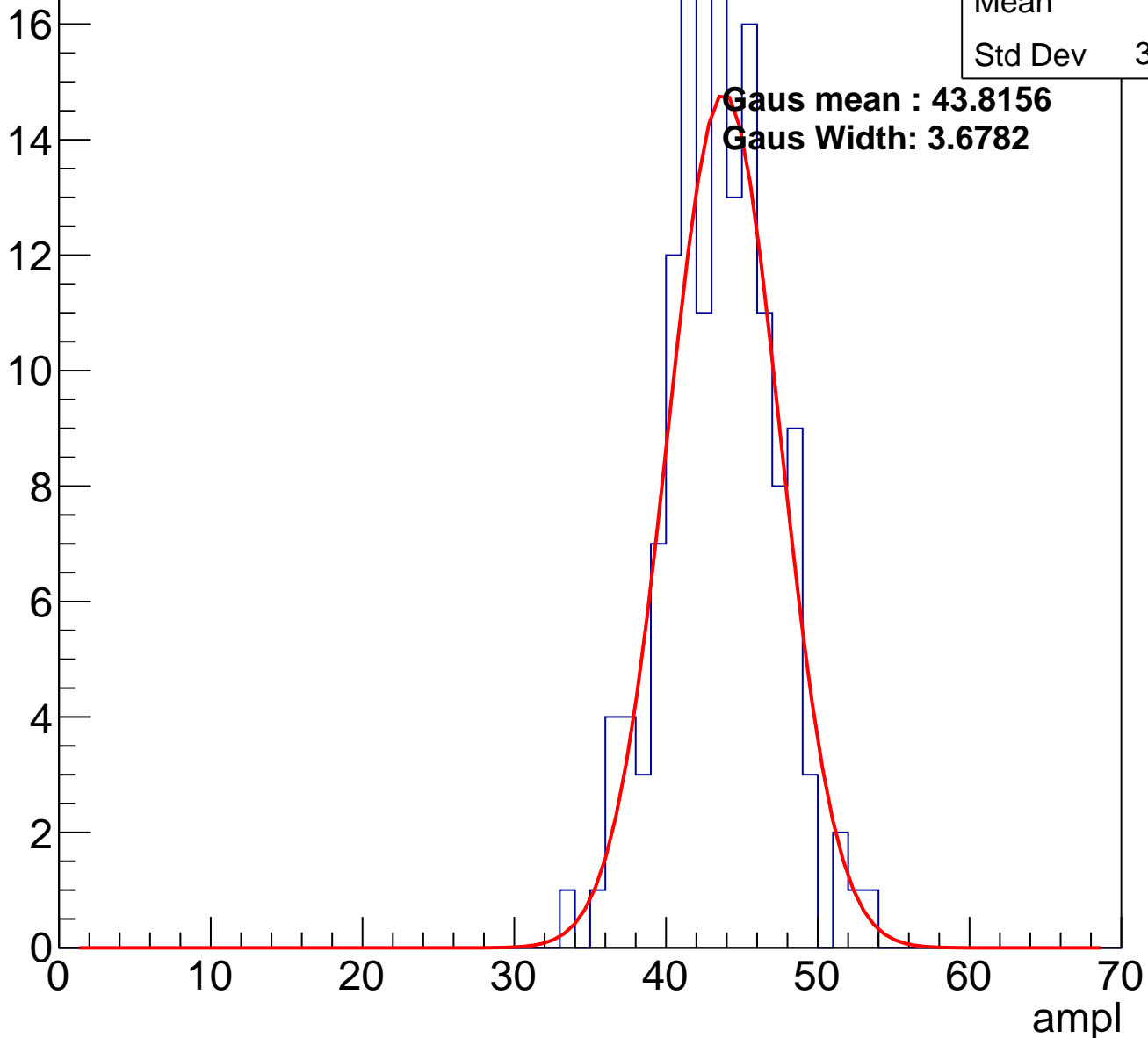
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	141
Mean	43.1
Std Dev	3.595

Gaus mean : 43.8156

Gaus Width: 3.6782

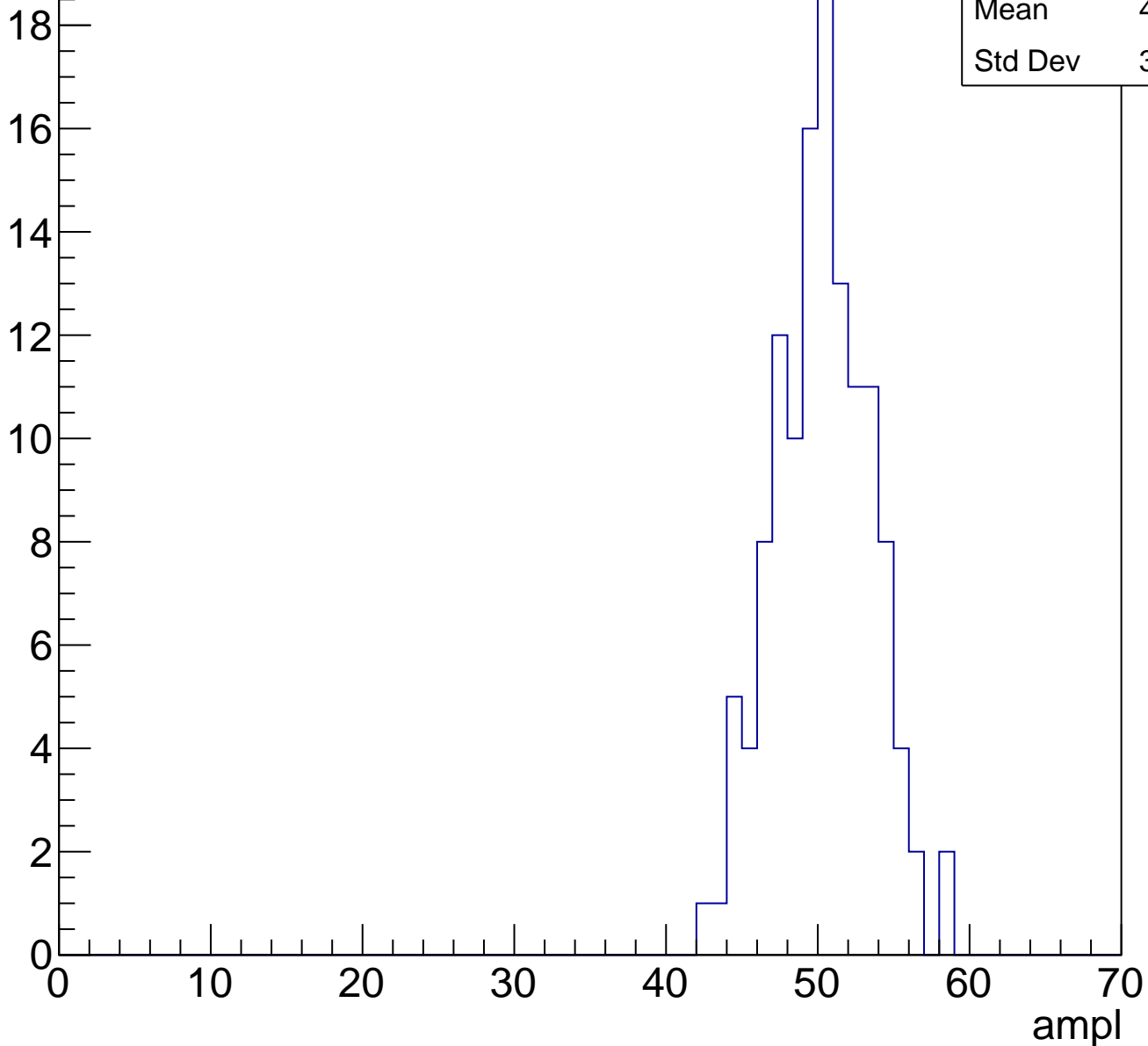


B1L001S, U24-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	49.83
Std Dev	3.157

Entry



B1L001S, U24-ch69, adc4

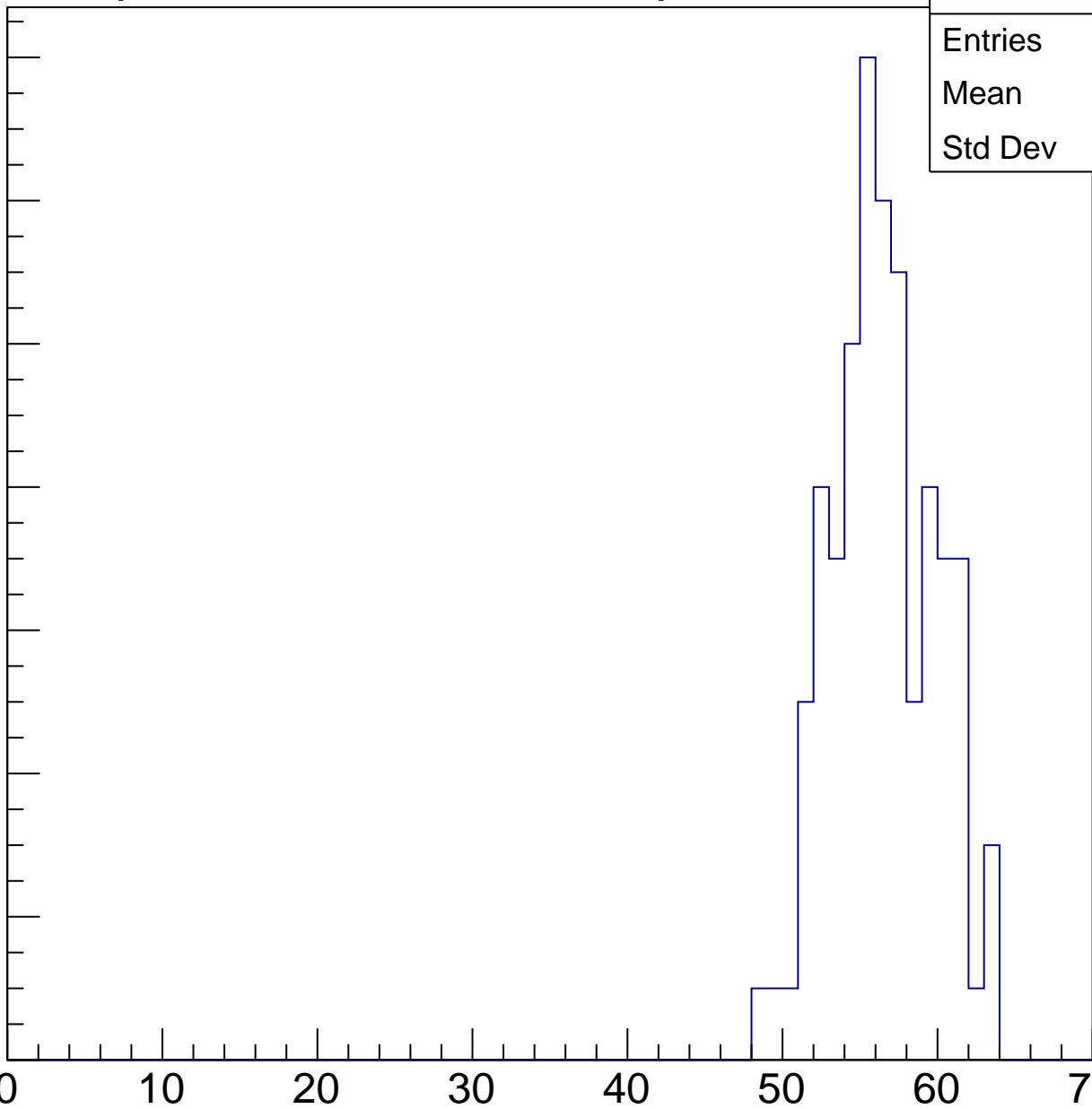
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	56.02
Std Dev	3.288

ampl



B1L001S, U24-ch69, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	88
Mean	58.82
Std Dev	6.778

ampl

0

10

20

30

40

50

60

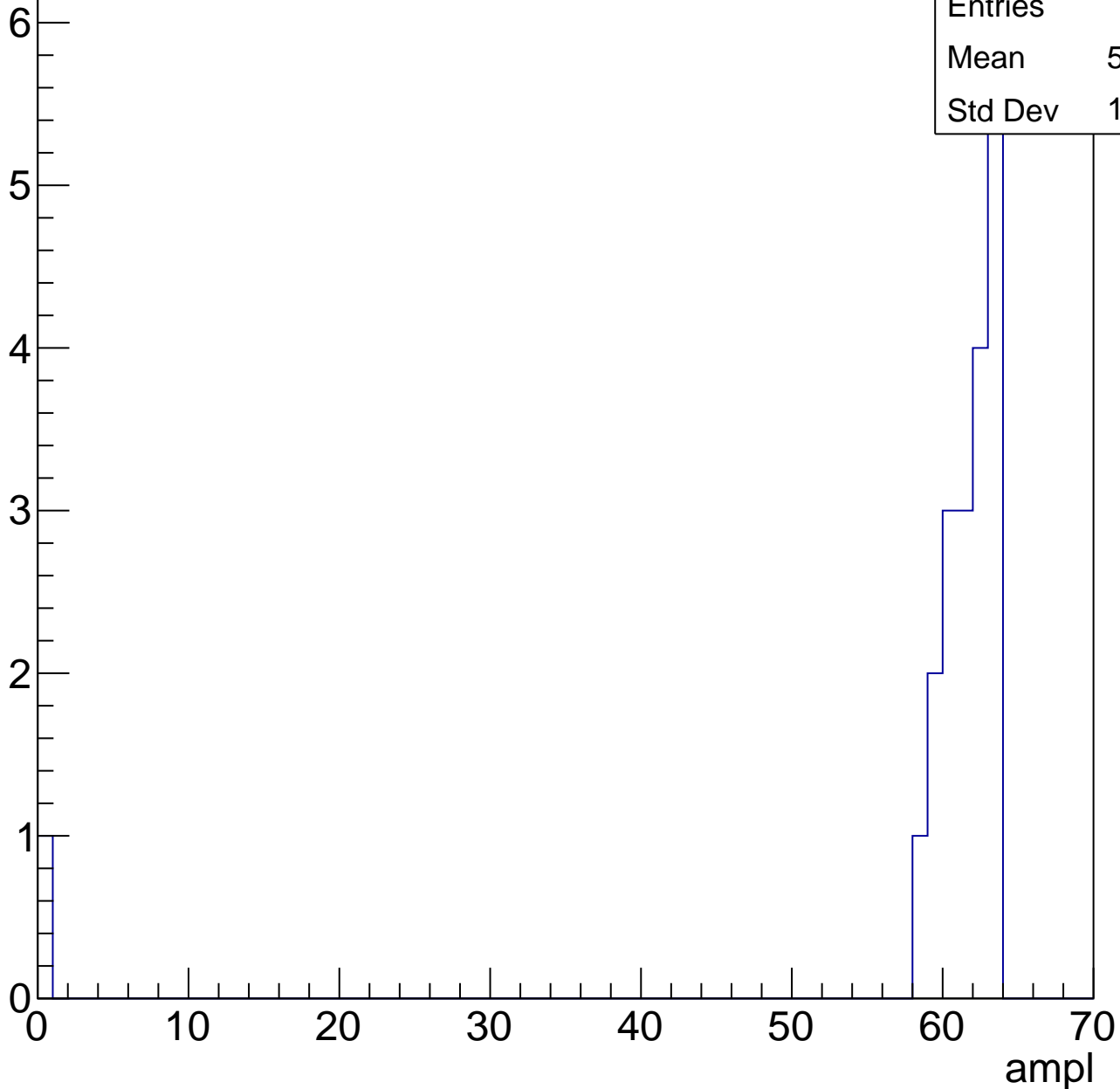
70

B1L001S, U24-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

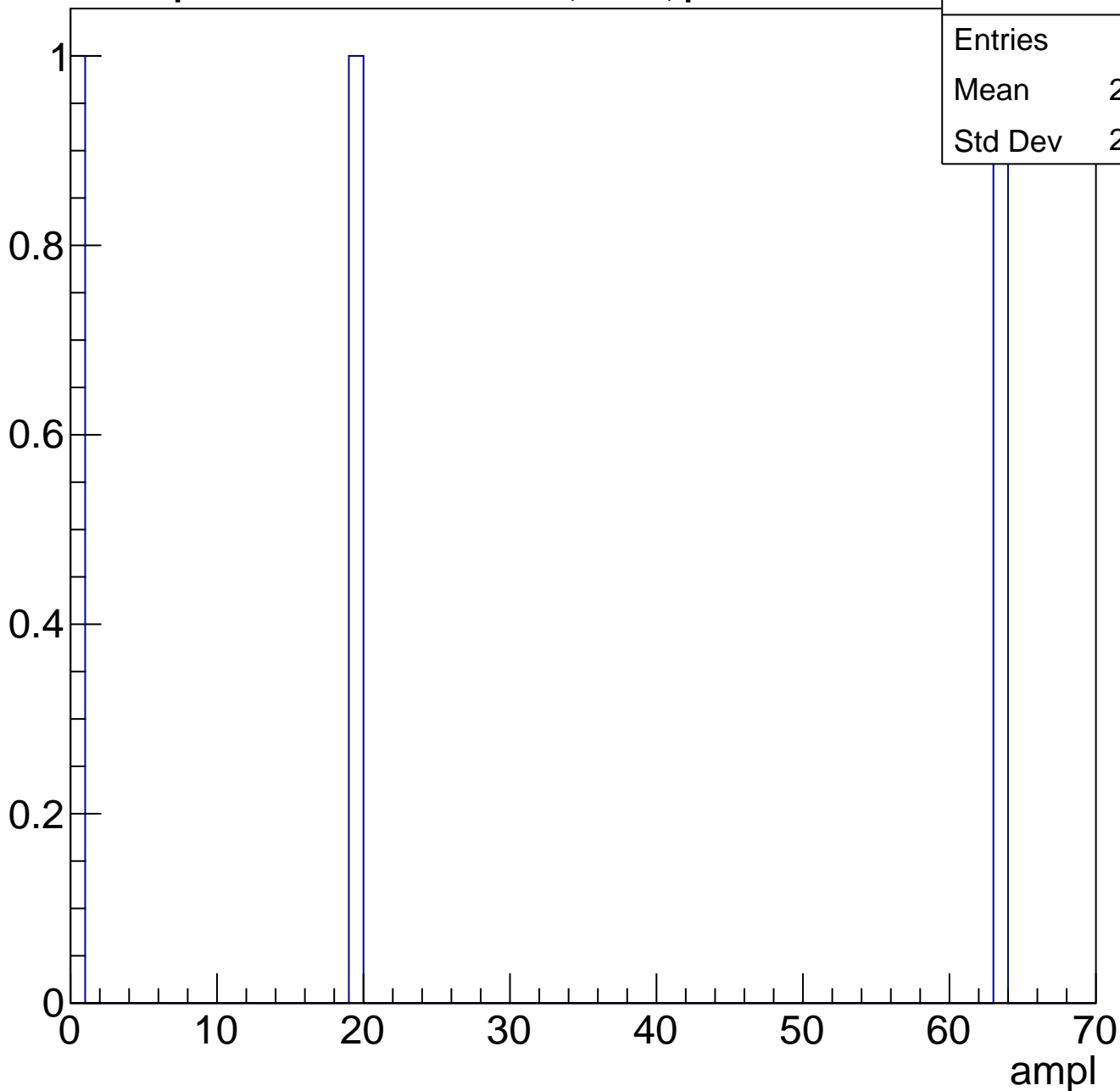
Entries	20
Mean	58.25
Std Dev	13.45



B1L001S, U24-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L001S, U24-ch70, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	30.73
Std Dev	2.798

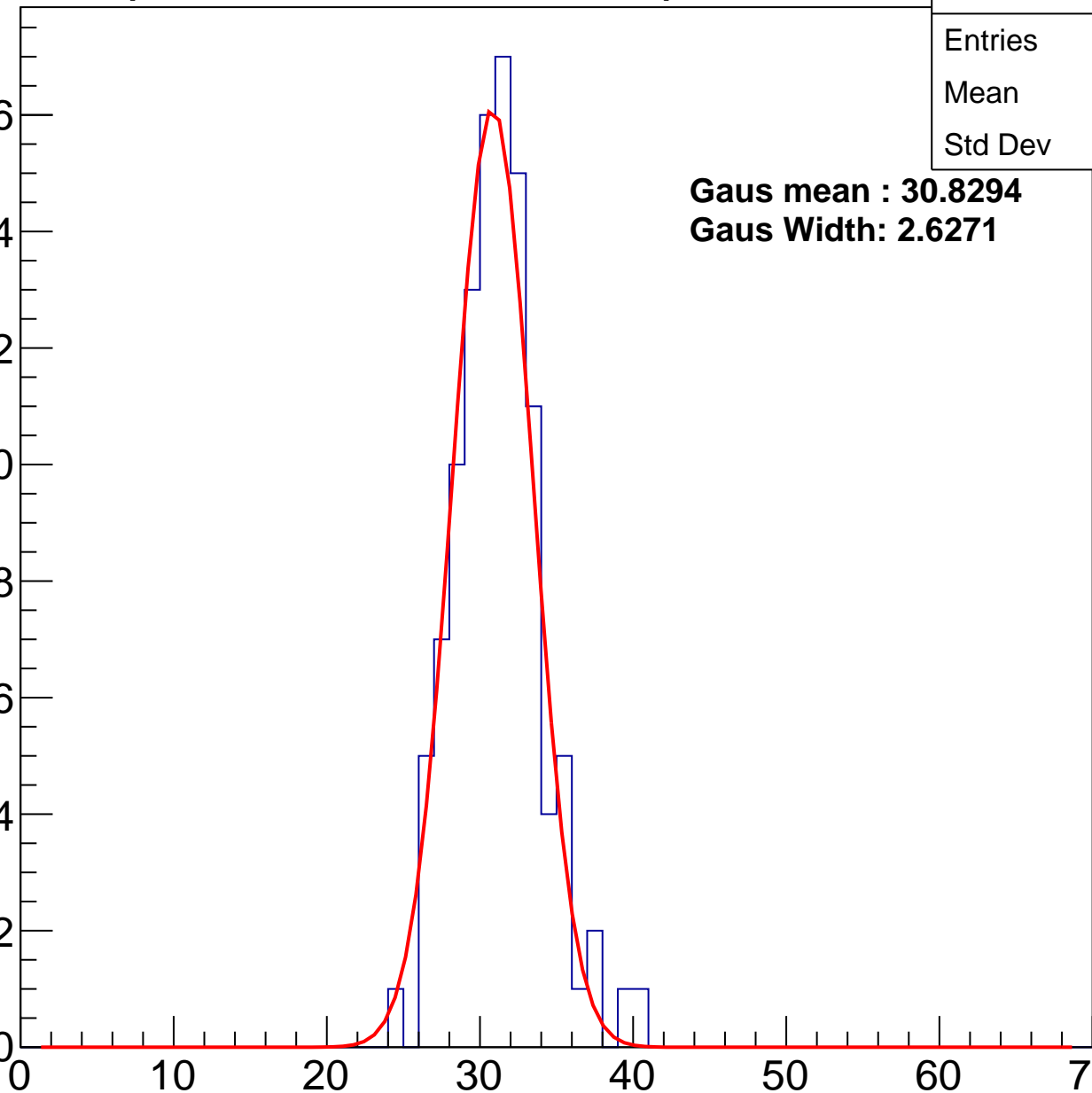
Gaus mean : 30.8294

Gaus Width: 2.6271

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch70, adc1

calib_packv5_042523_0143.root, FC#2, port C2

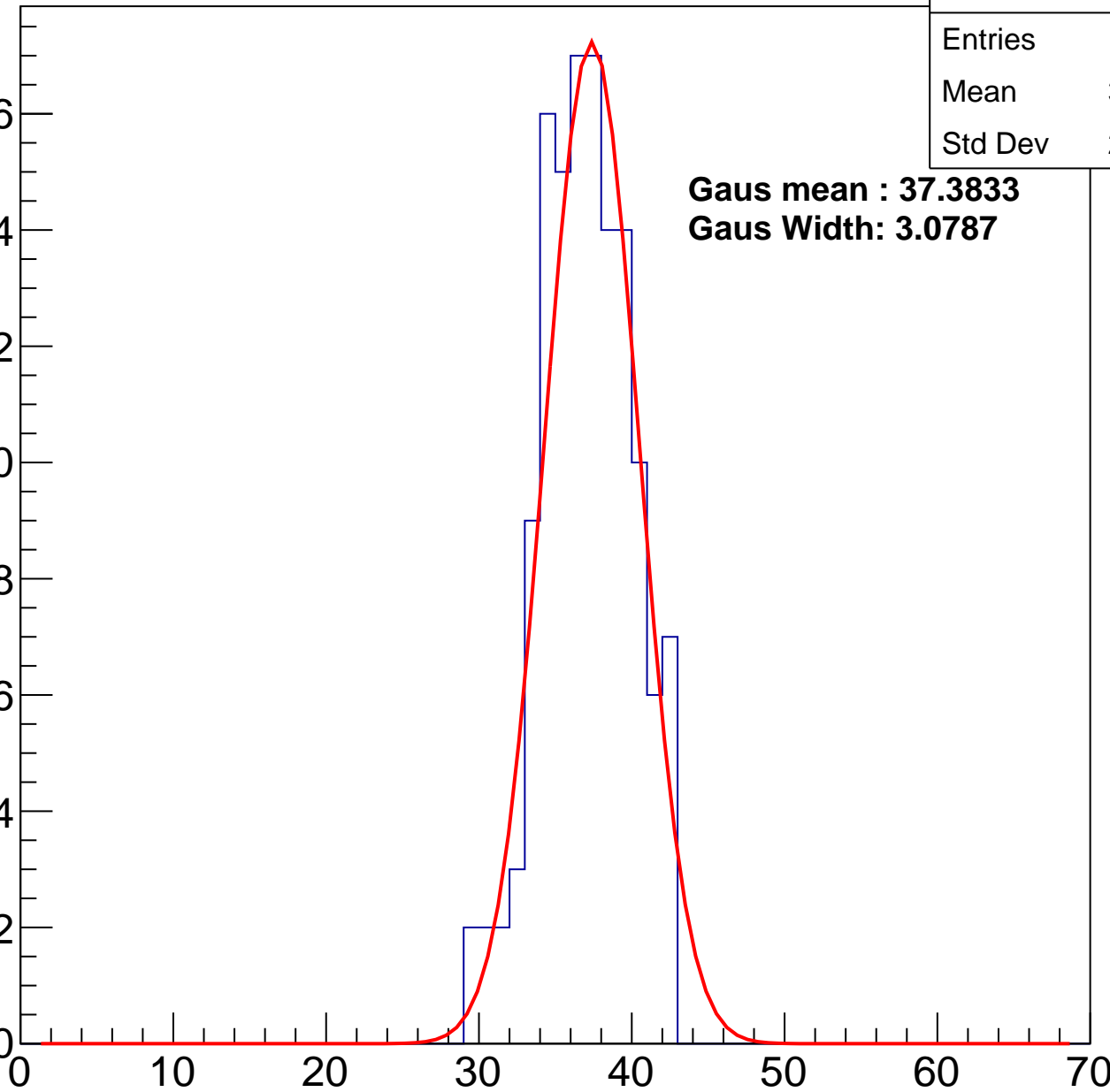
Entries	134
Mean	36.57
Std Dev	2.923

Gaus mean : 37.3833
Gaus Width: 3.0787

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch70, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	43.51
Std Dev	3.345

Gaus mean : 44.1587

Gaus Width: 3.2400

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

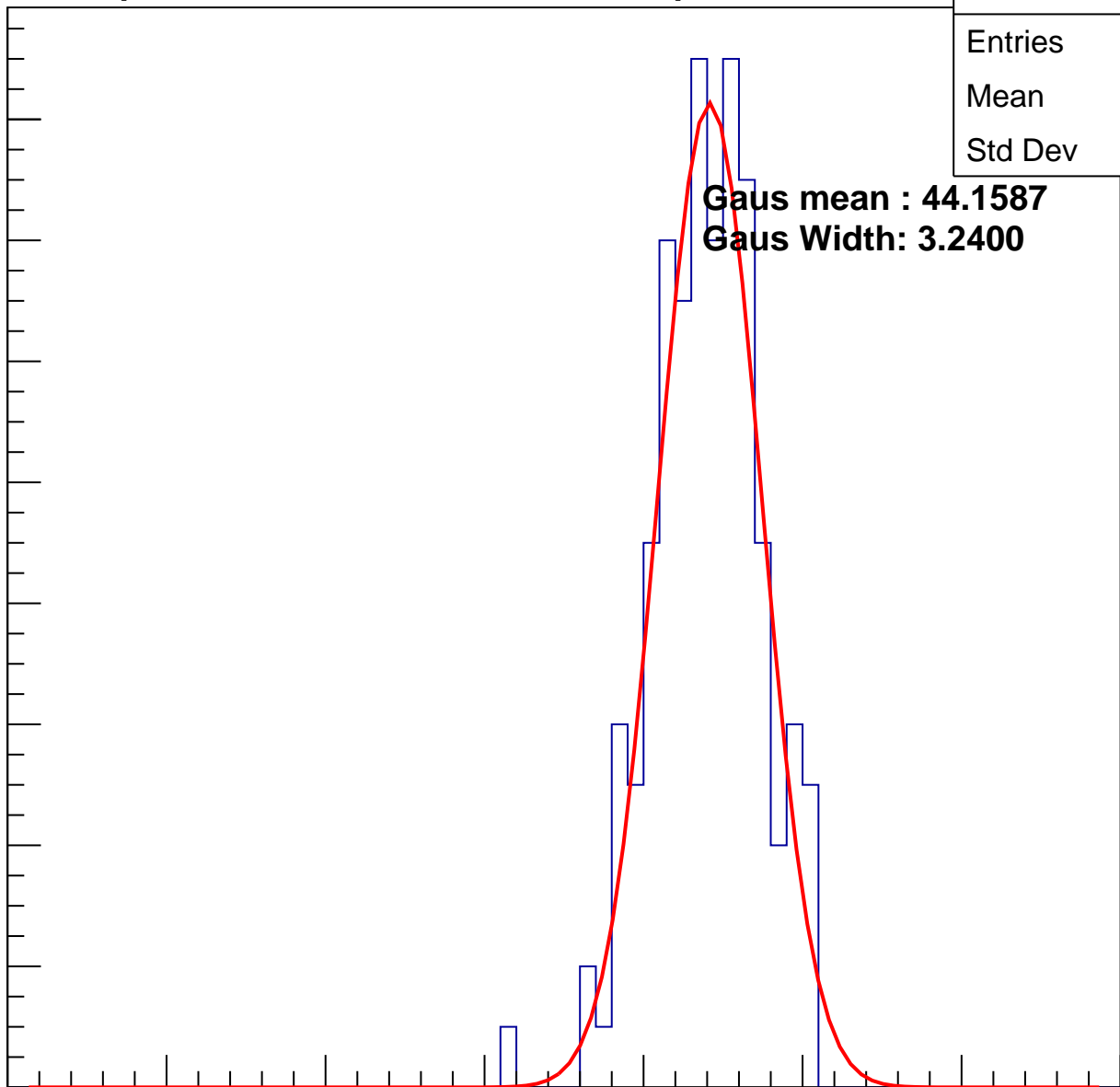
40

50

60

70

ampl



B1L001S, U24-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

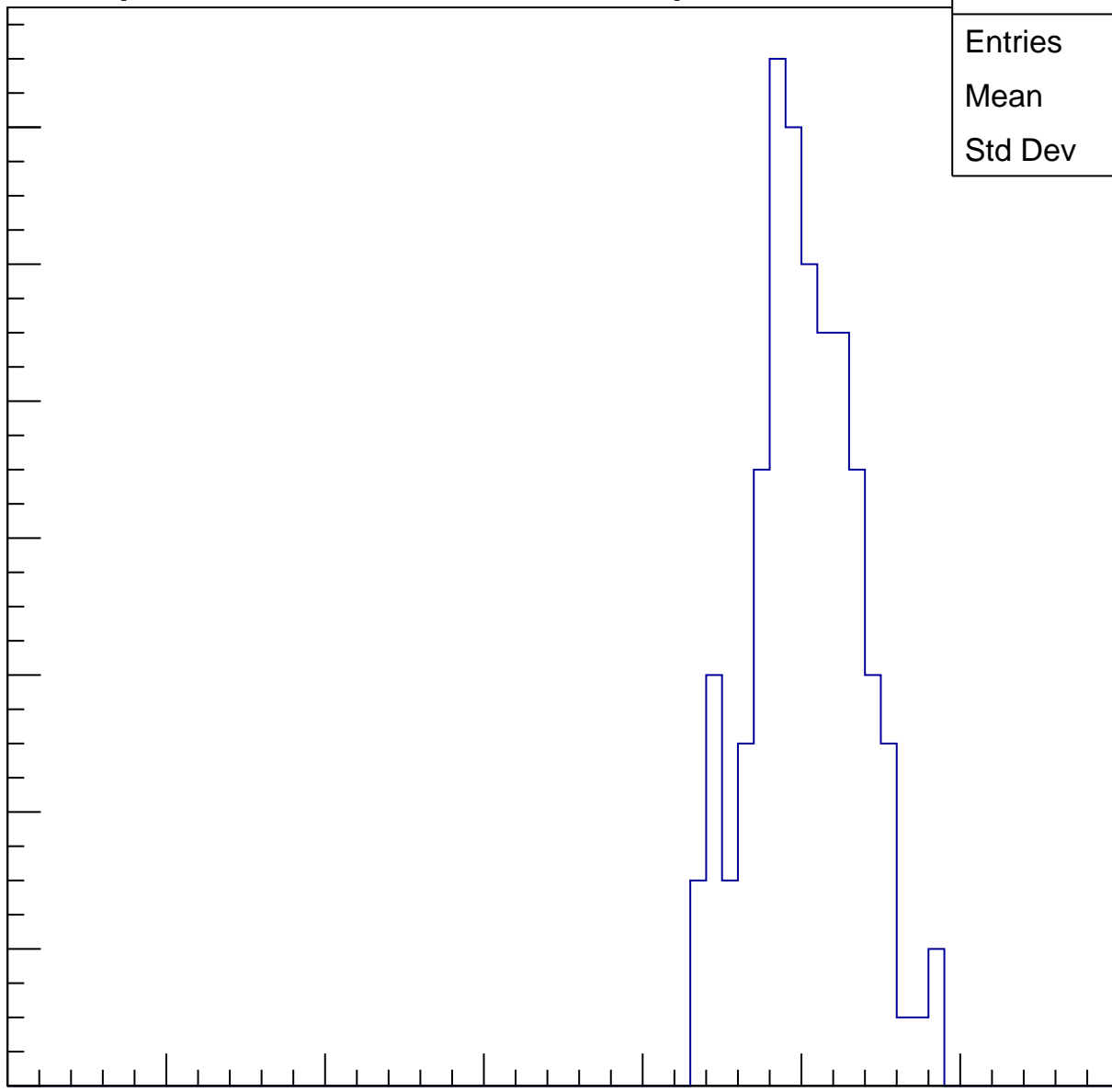
Entries	113
Mean	49.78
Std Dev	3.309

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch70, adc4

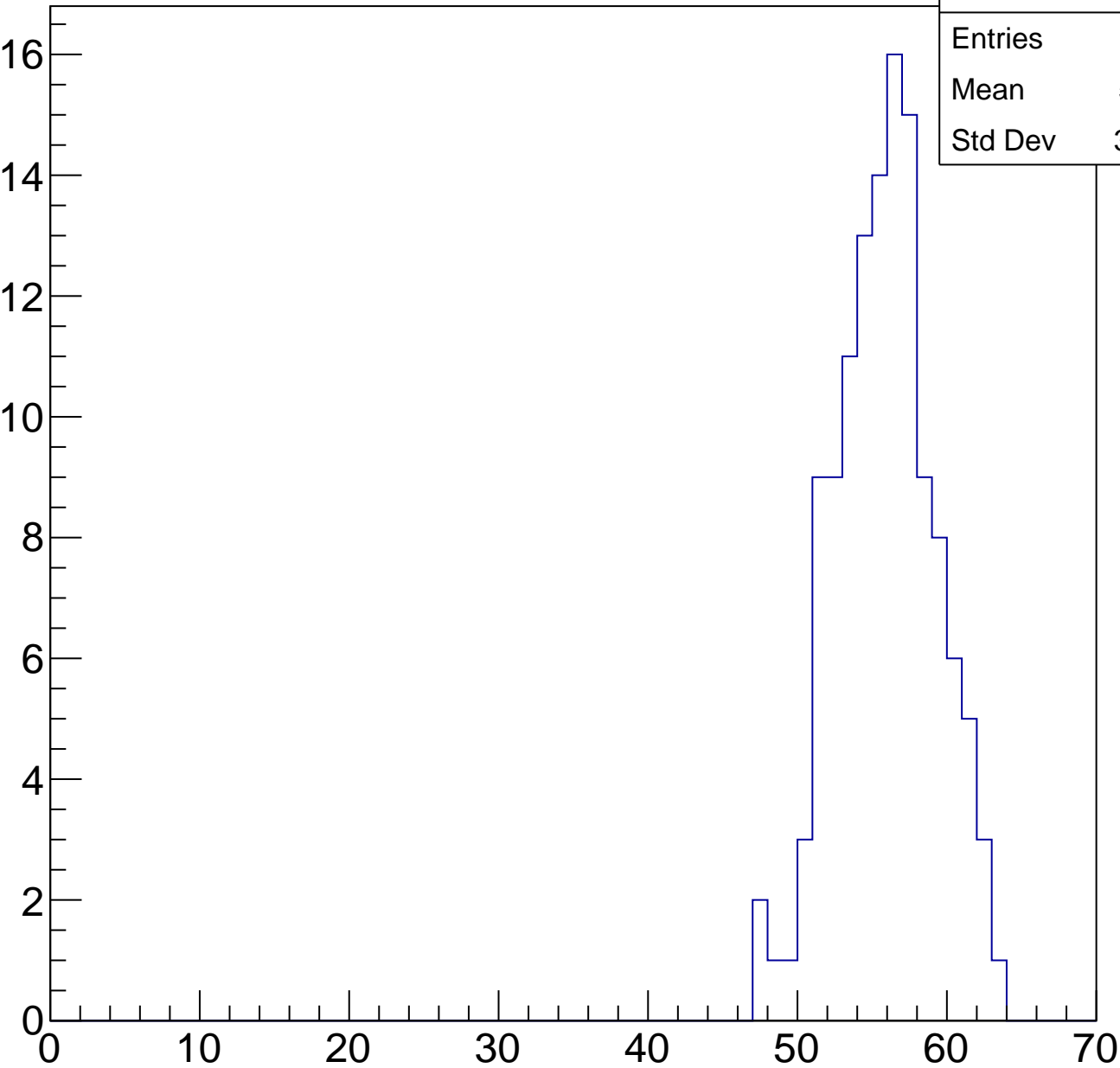
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	55.41
Std Dev	3.303

ampl



B1L001S, U24-ch70, adc5

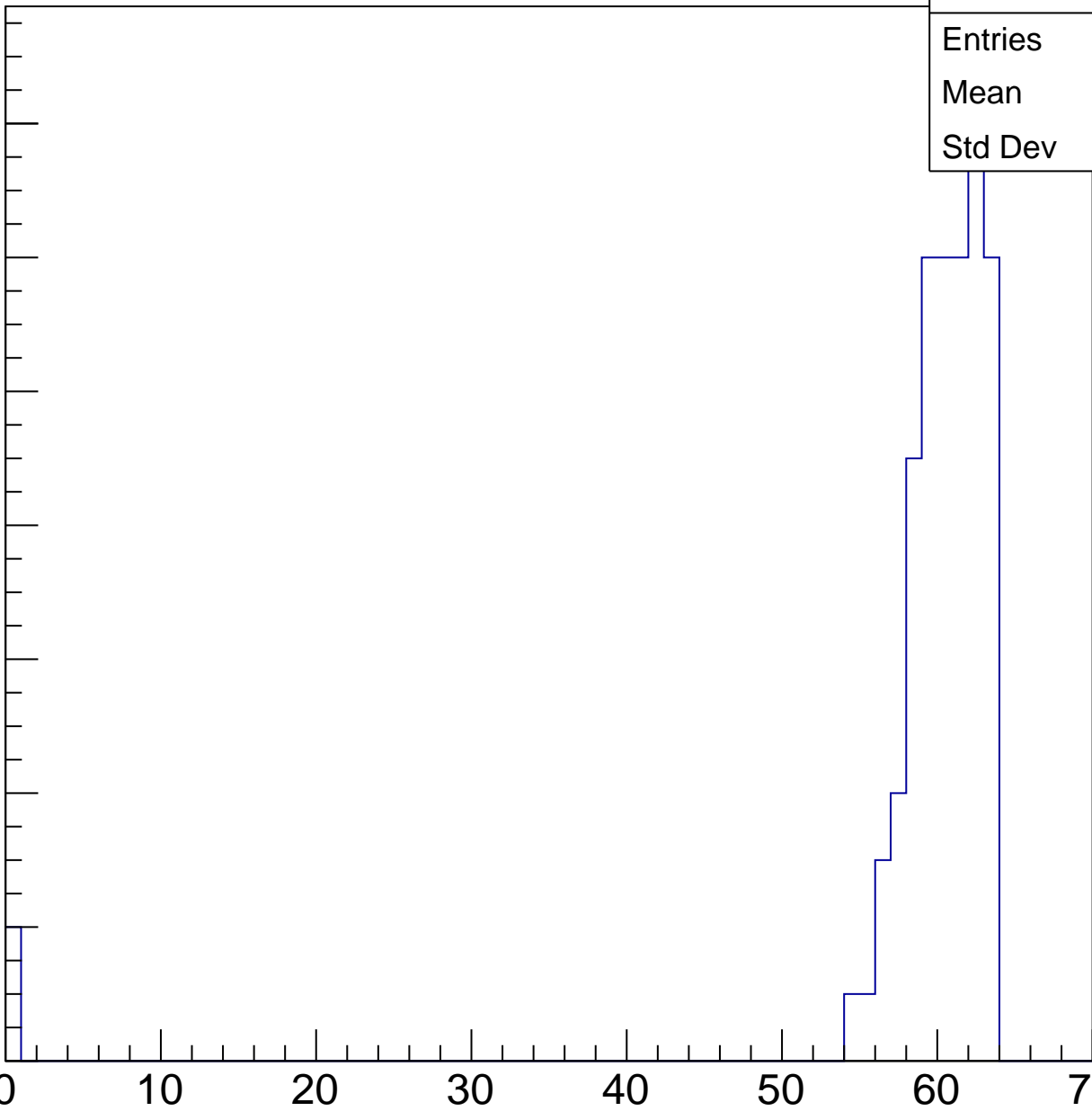
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.71
Std Dev	9.465

ampl

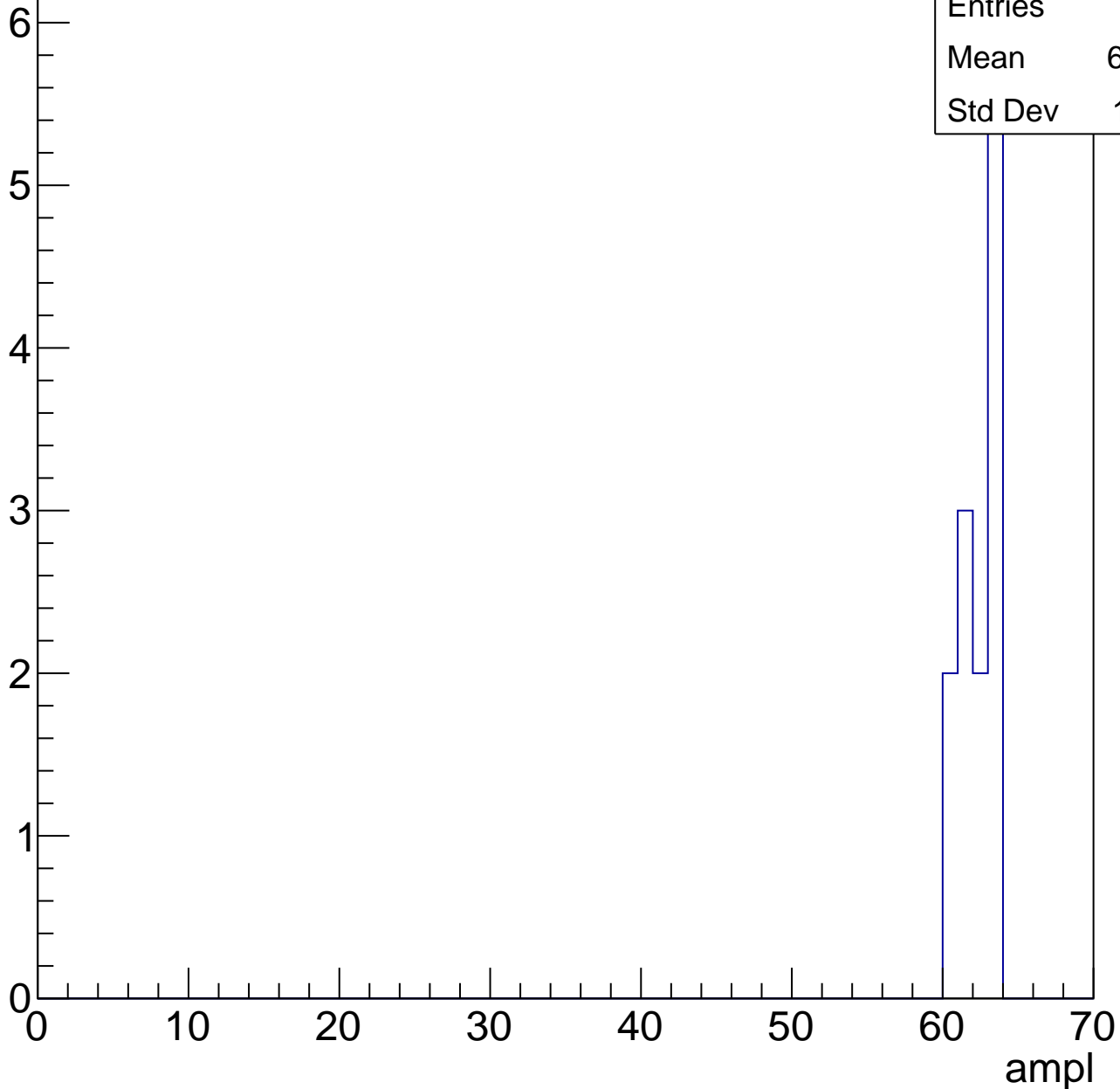


B1L001S, U24-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

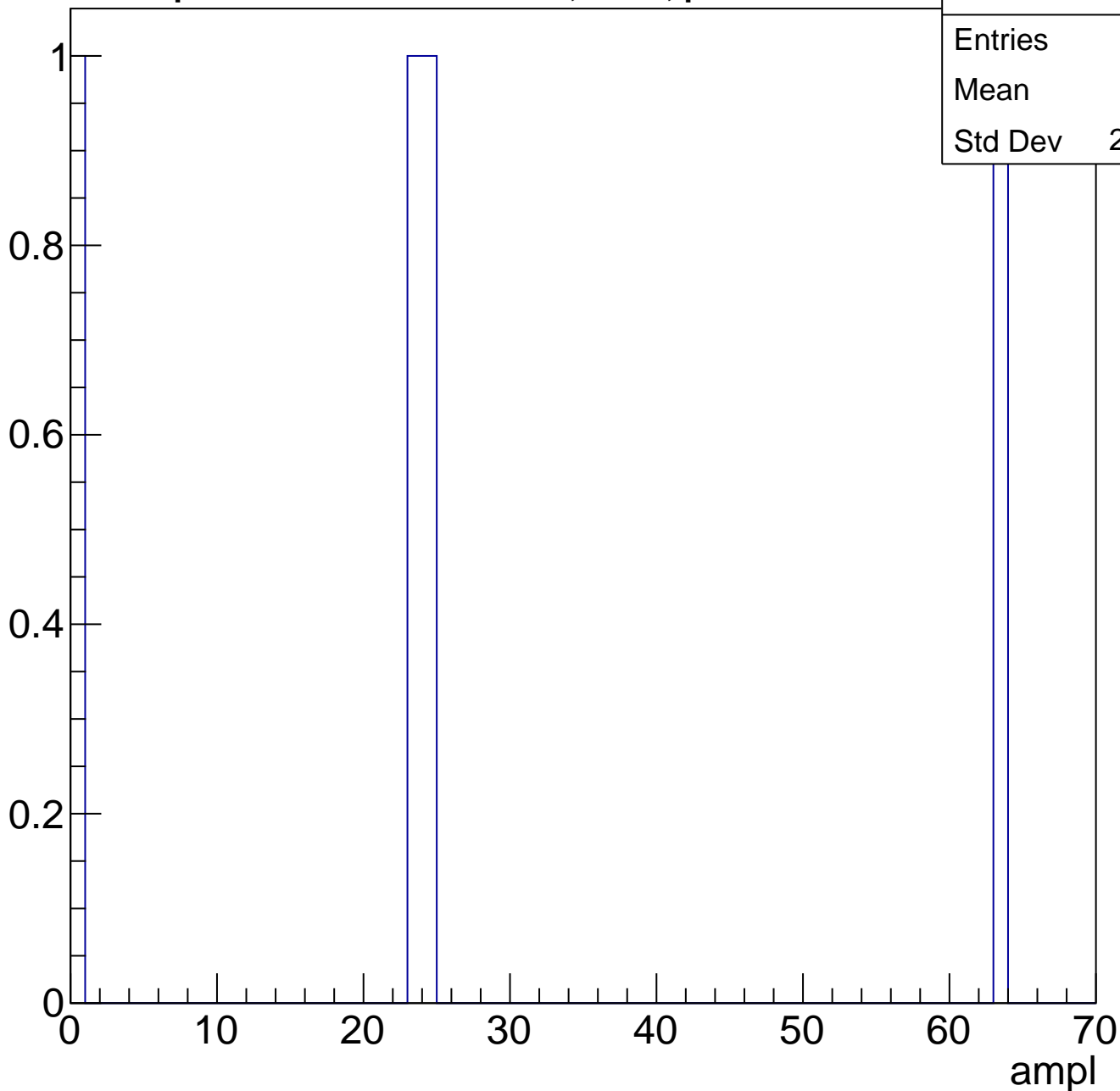
Entries	13
Mean	61.92
Std Dev	1.141



B1L001S, U24-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	27.5
Std Dev	22.63

B1L001S, U24-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	172
Mean	29.03
Std Dev	4.515

Gaus mean : 29.6894

Gaus Width: 3.8579

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

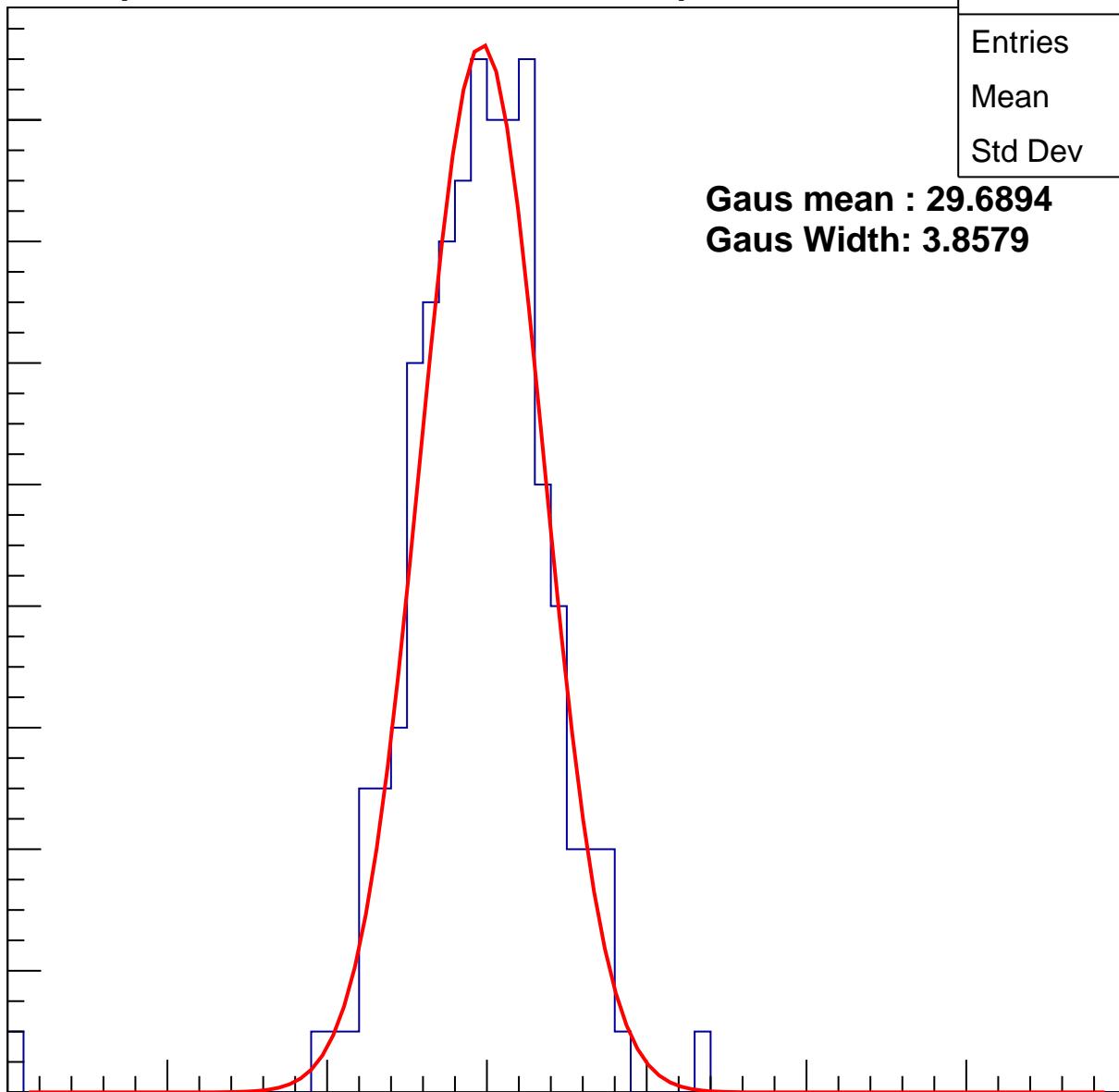
40

50

60

70

ampl



B1L001S, U24-ch71, adc1

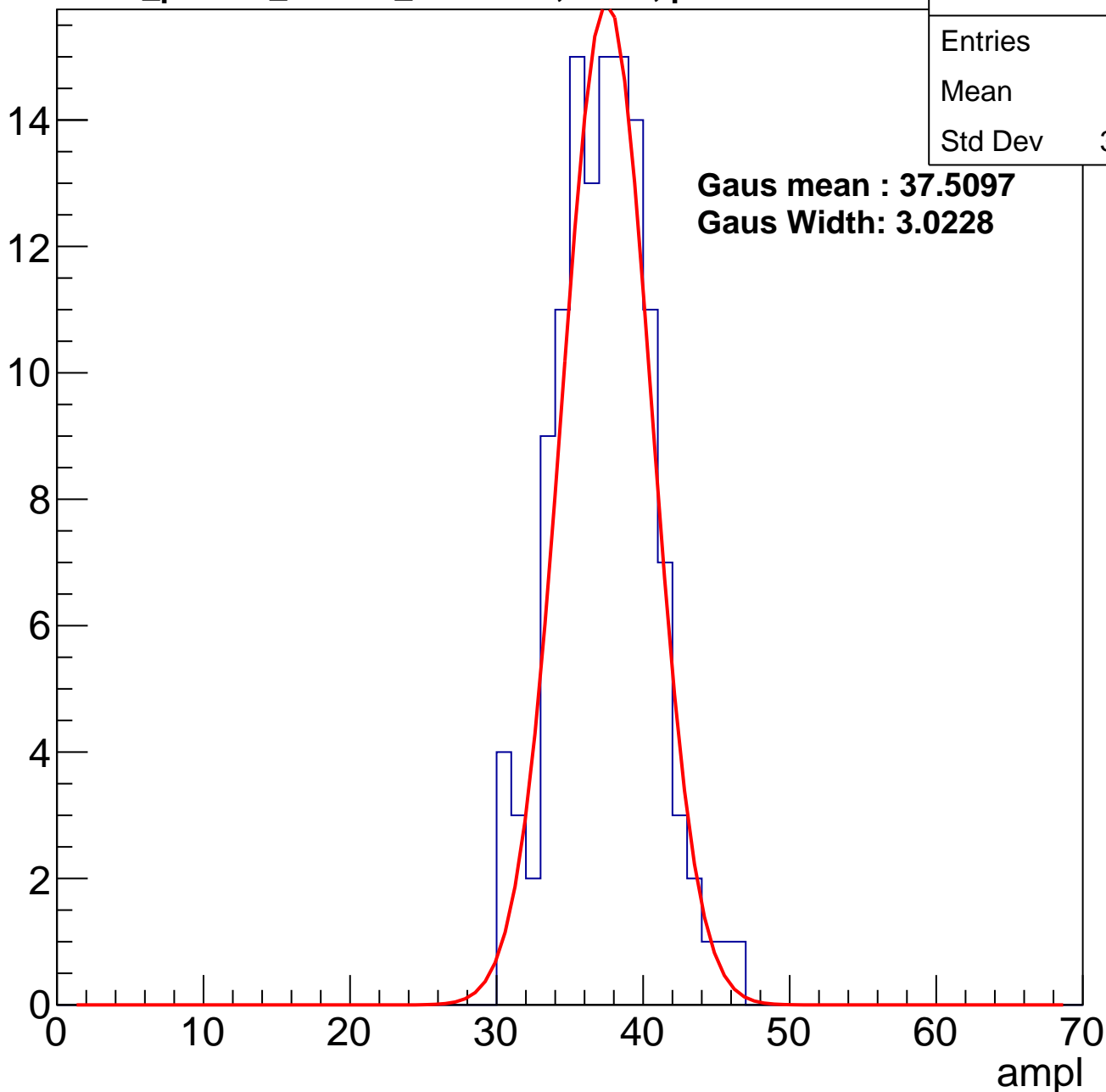
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.9
Std Dev	3.189

Gaus mean : 37.5097

Gaus Width: 3.0228

Entry



B1L001S, U24-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	42.53
Std Dev	3.366

Gaus mean : 43.3761

Gaus Width: 2.8568

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

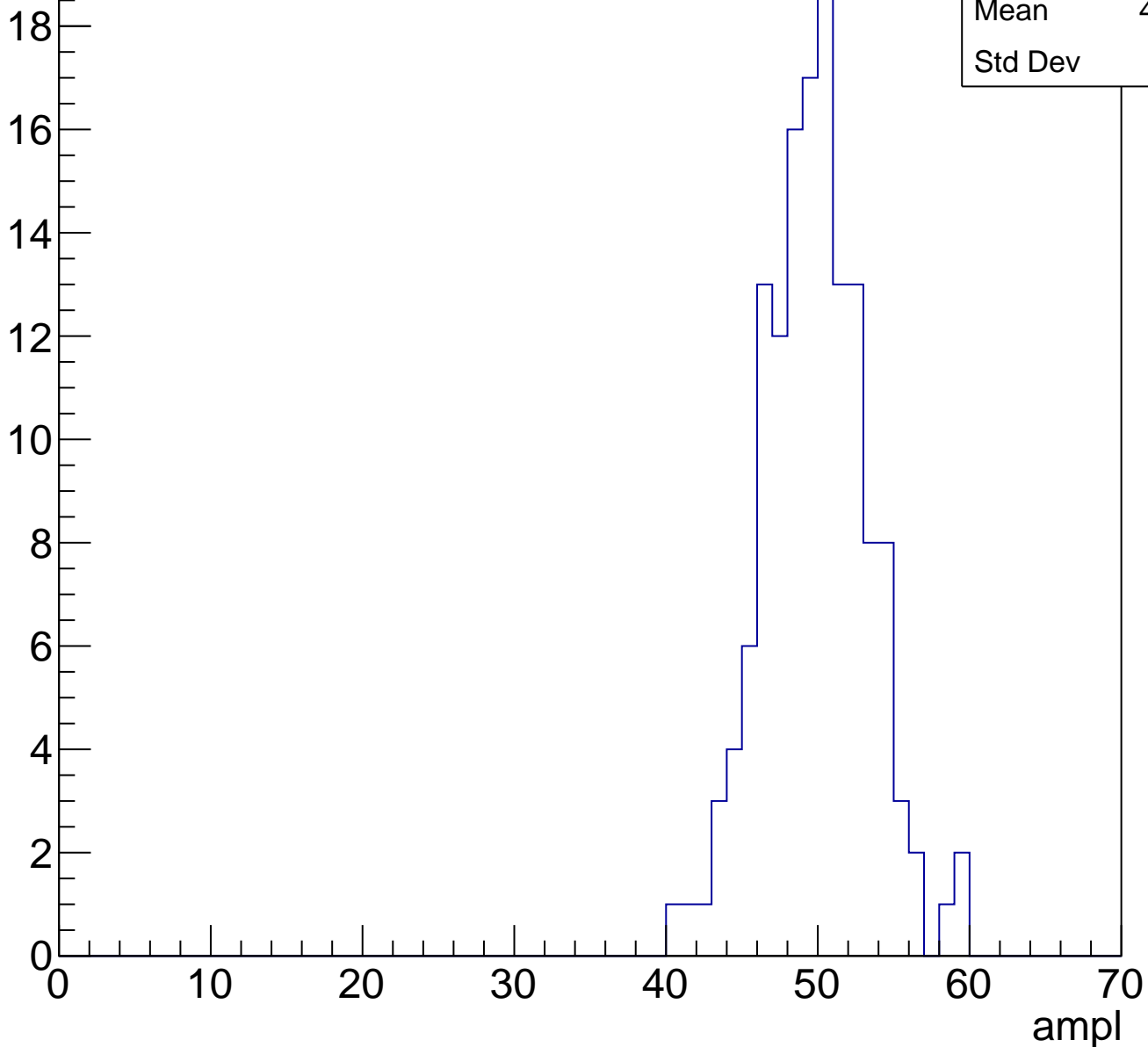
70

B1L001S, U24-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	49.36
Std Dev	3.41

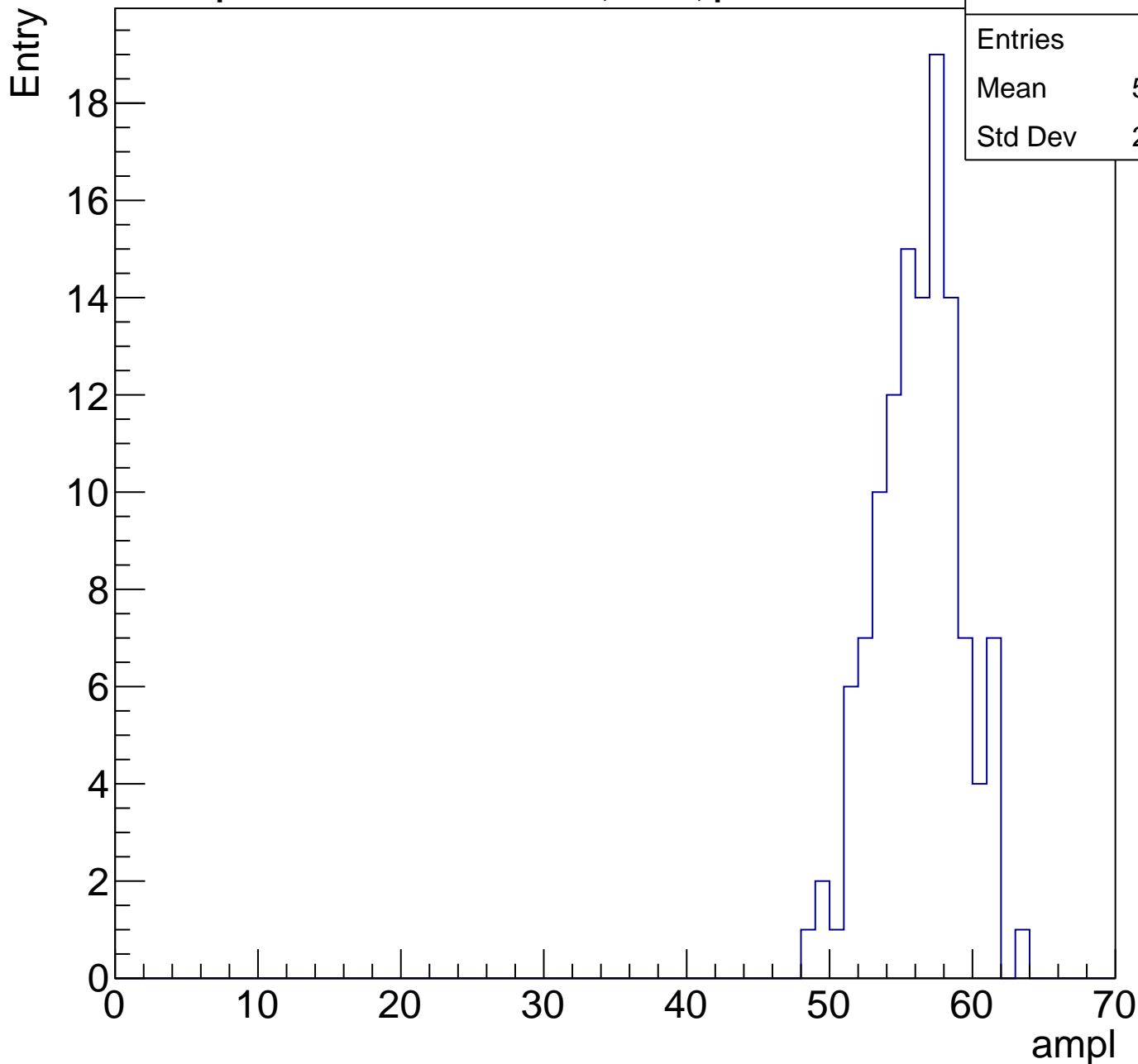
Entry



B1L001S, U24-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	55.76
Std Dev	2.933



B1L001S, U24-ch71, adc5

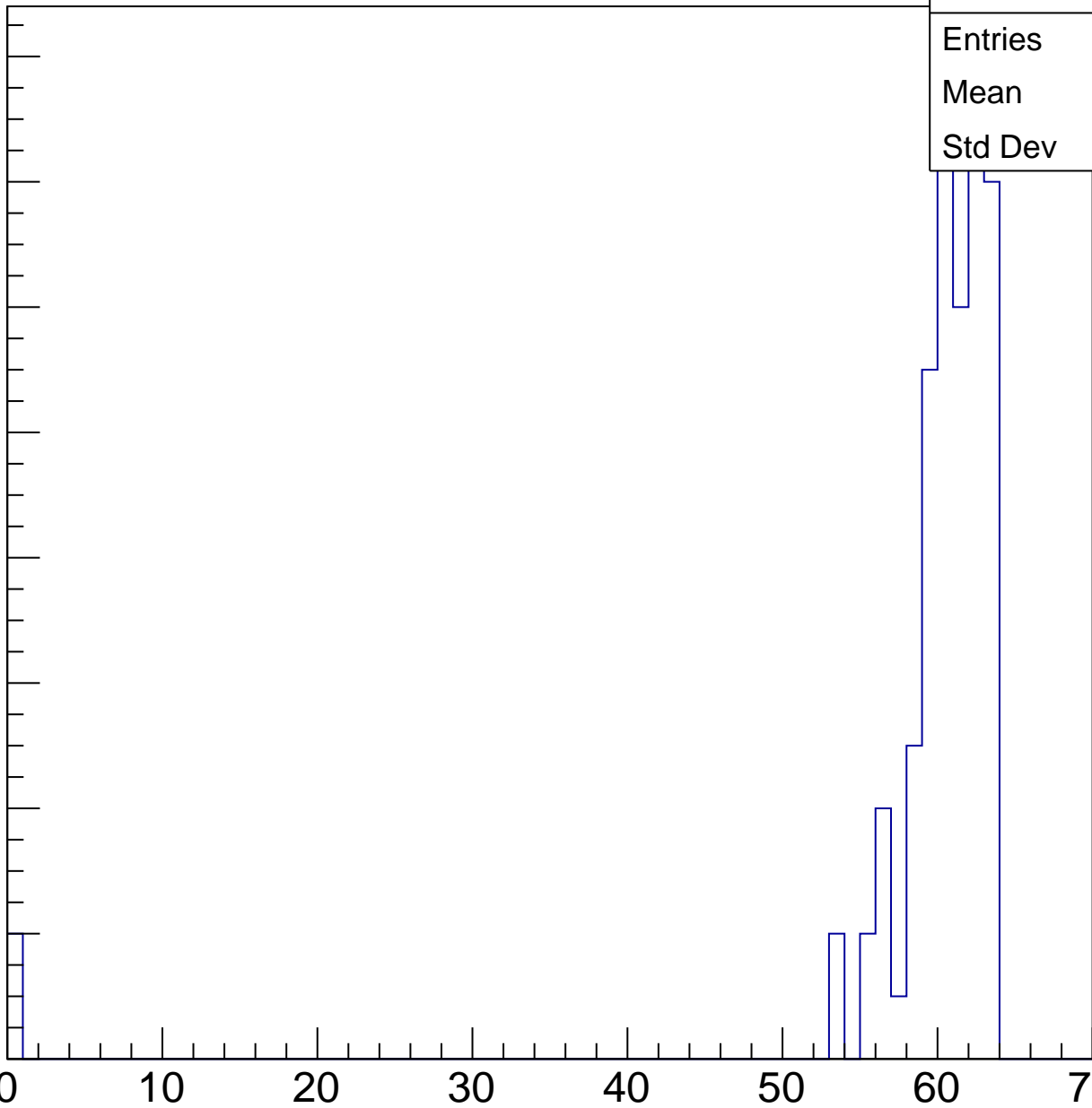
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.85
Std Dev	9.419

ampl

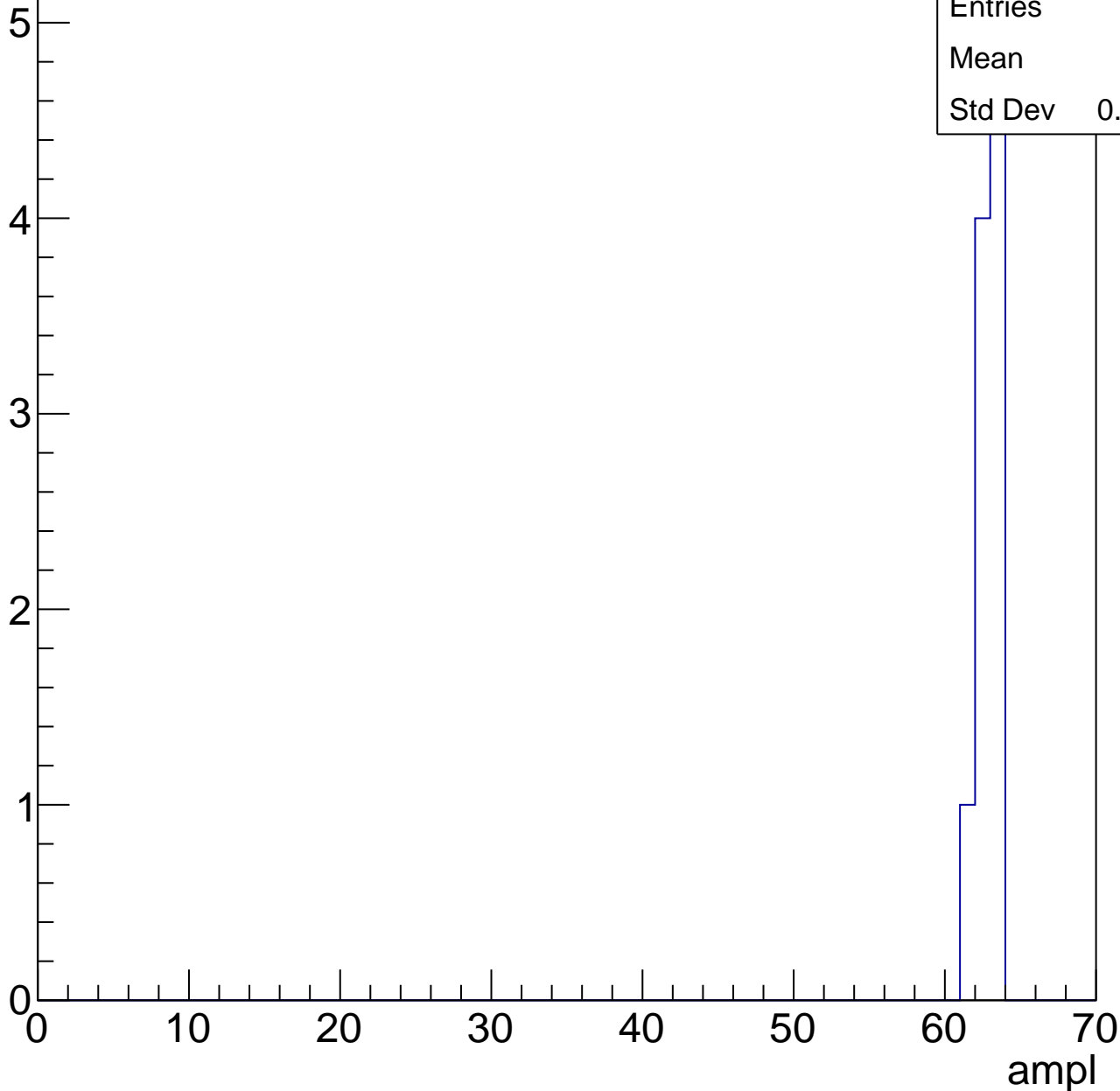


B1L001S, U24-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

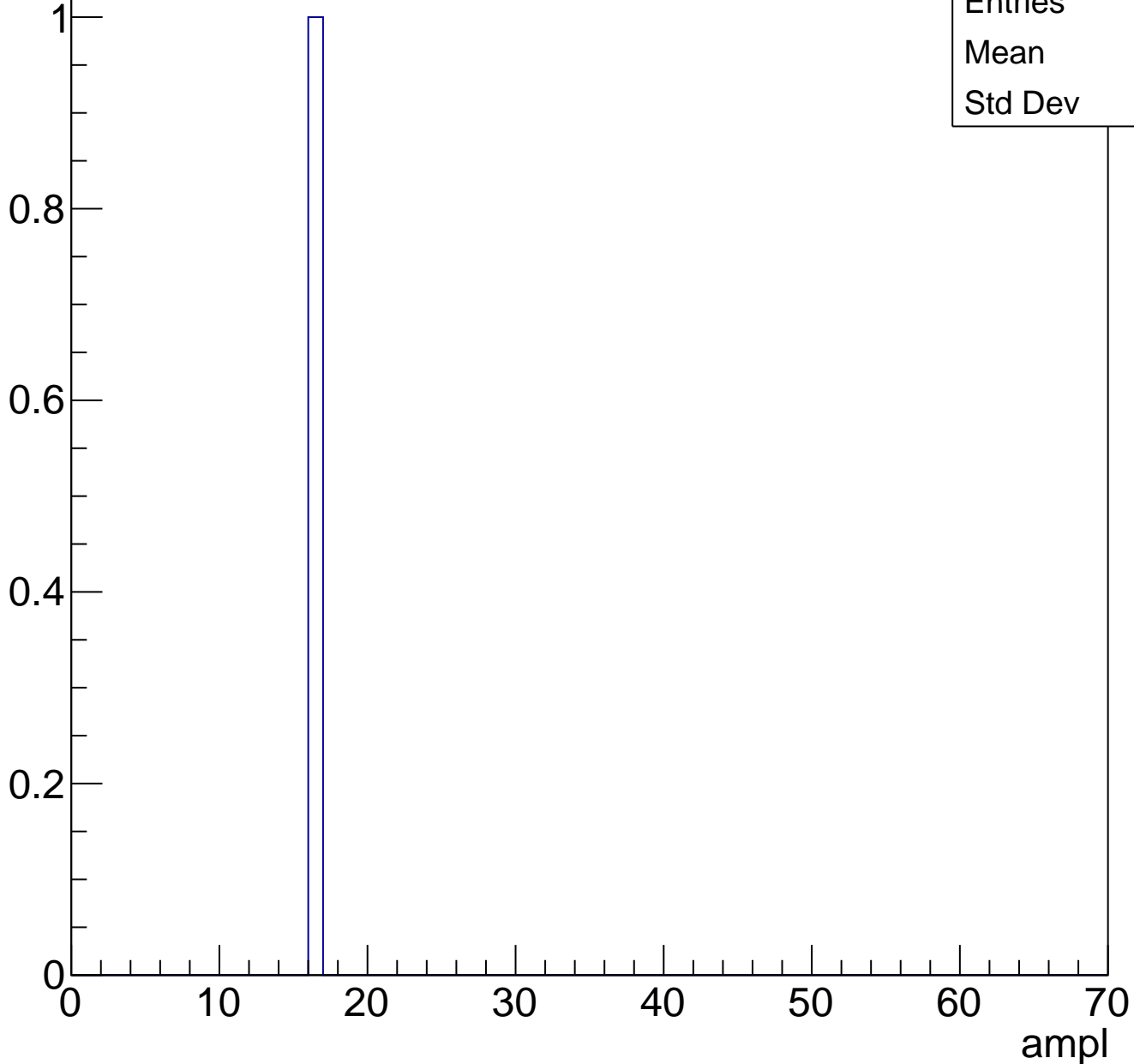
Entries	10
Mean	62.4
Std Dev	0.6633



B1L001S, U24-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	16
Std Dev	0

B1L001S, U24-ch72, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	30.36
Std Dev	3.36

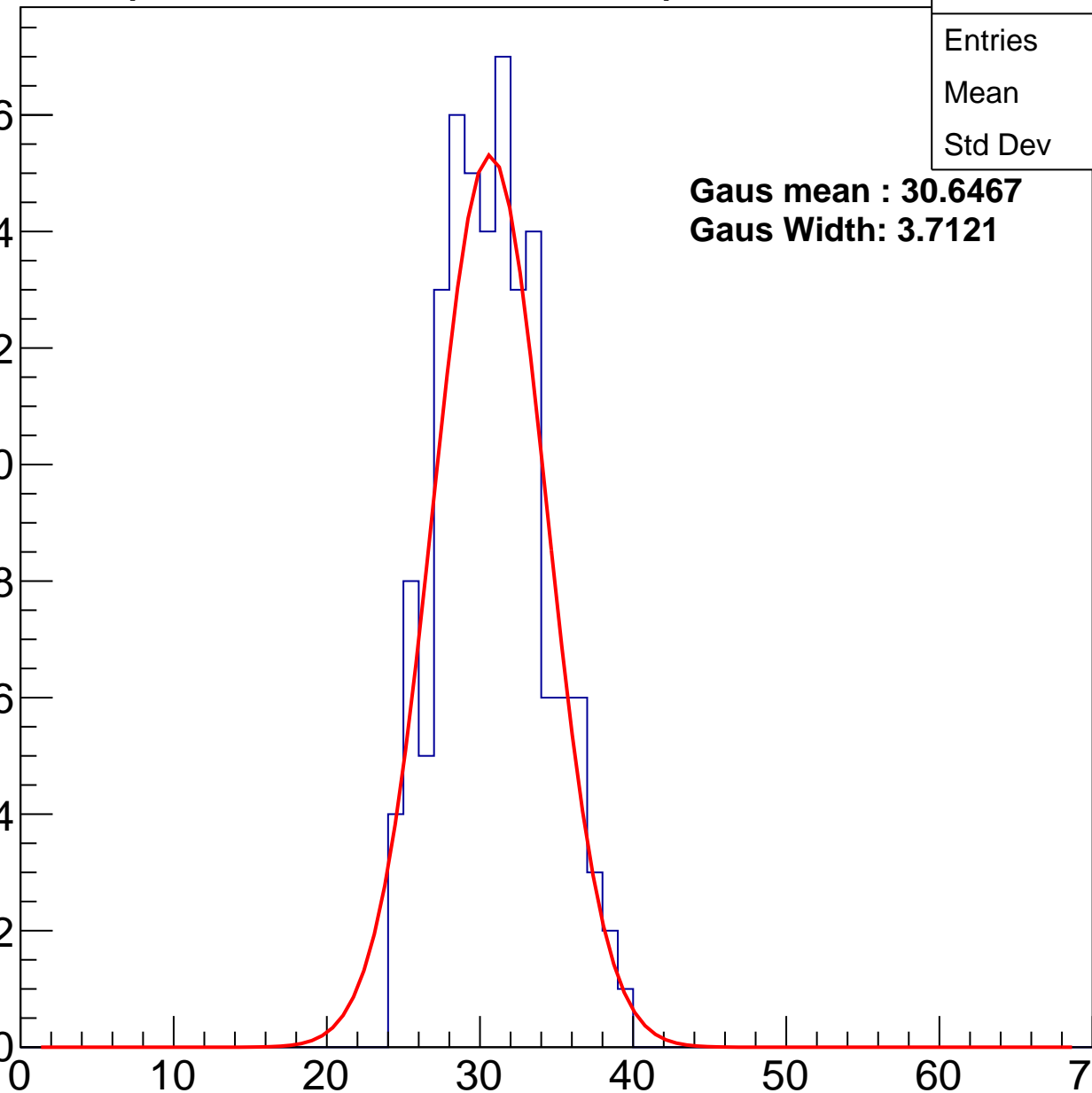
Gaus mean : 30.6467

Gaus Width: 3.7121

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.63
Std Dev	3.279

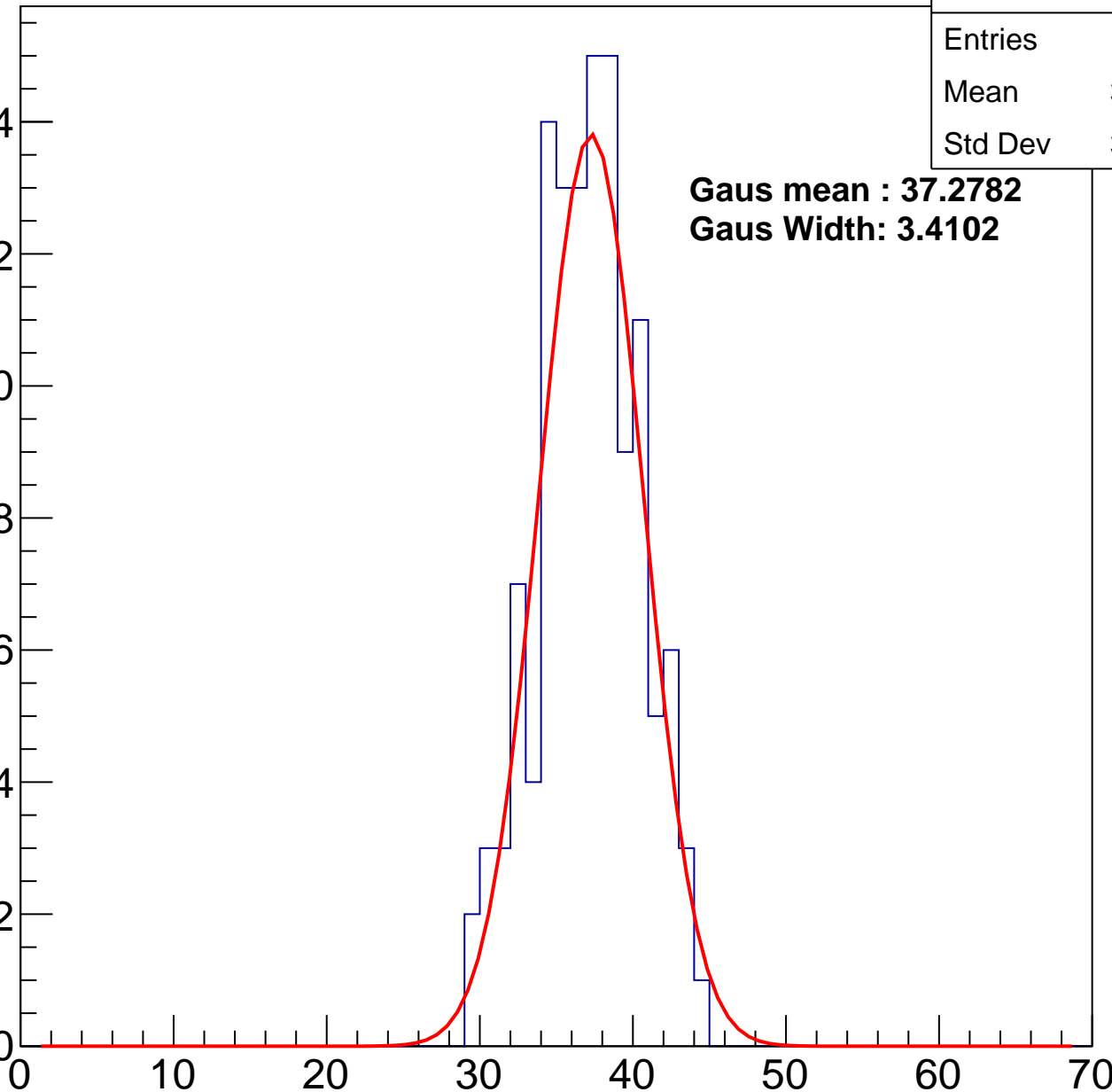
Gaus mean : 37.2782

Gaus Width: 3.4102

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch72, adc2

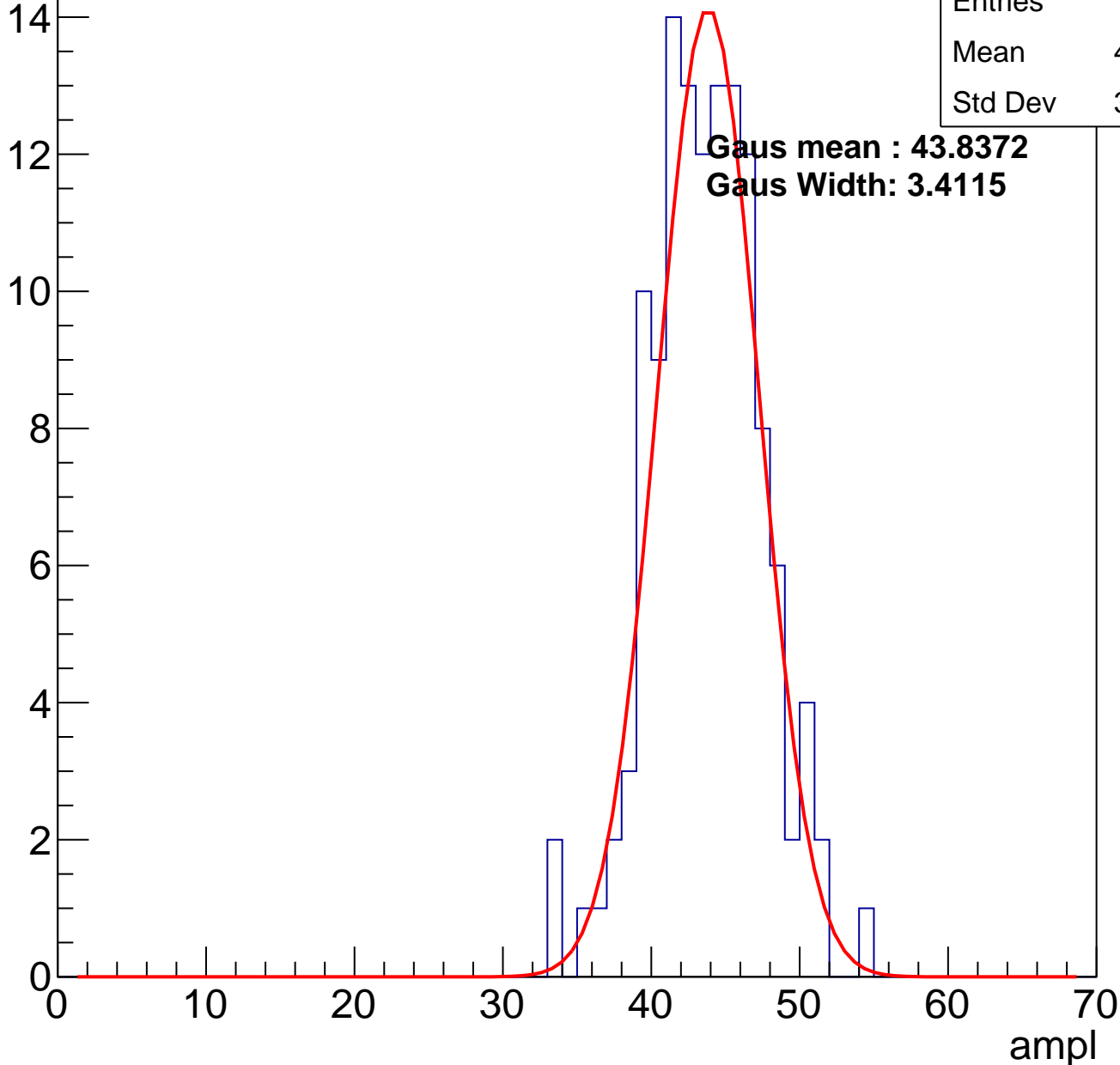
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	43.27
Std Dev	3.675

Gaus mean : 43.8372

Gaus Width: 3.4115

Entry



B1L001S, U24-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

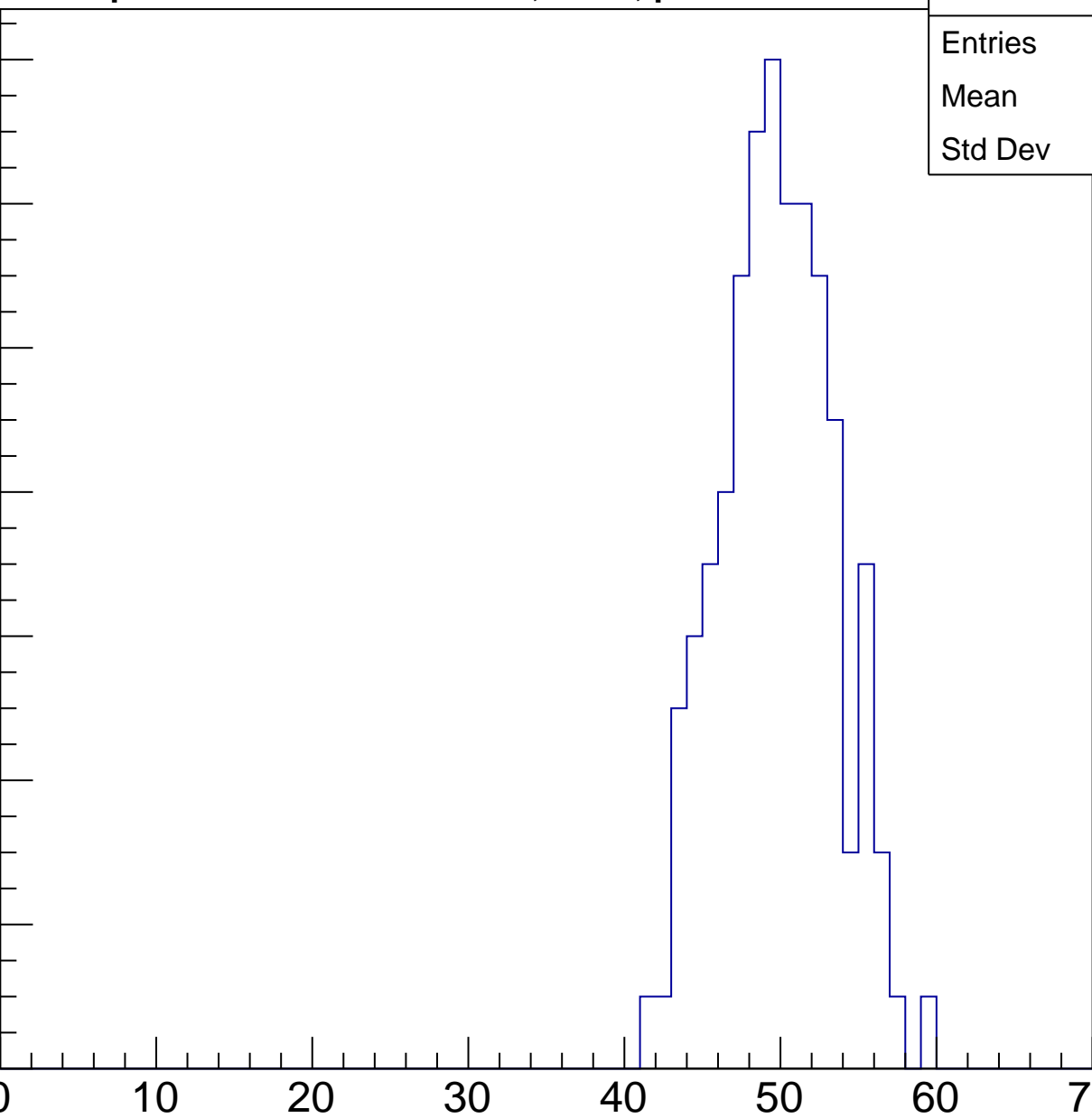
Entries	125
Mean	49.31
Std Dev	3.592

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

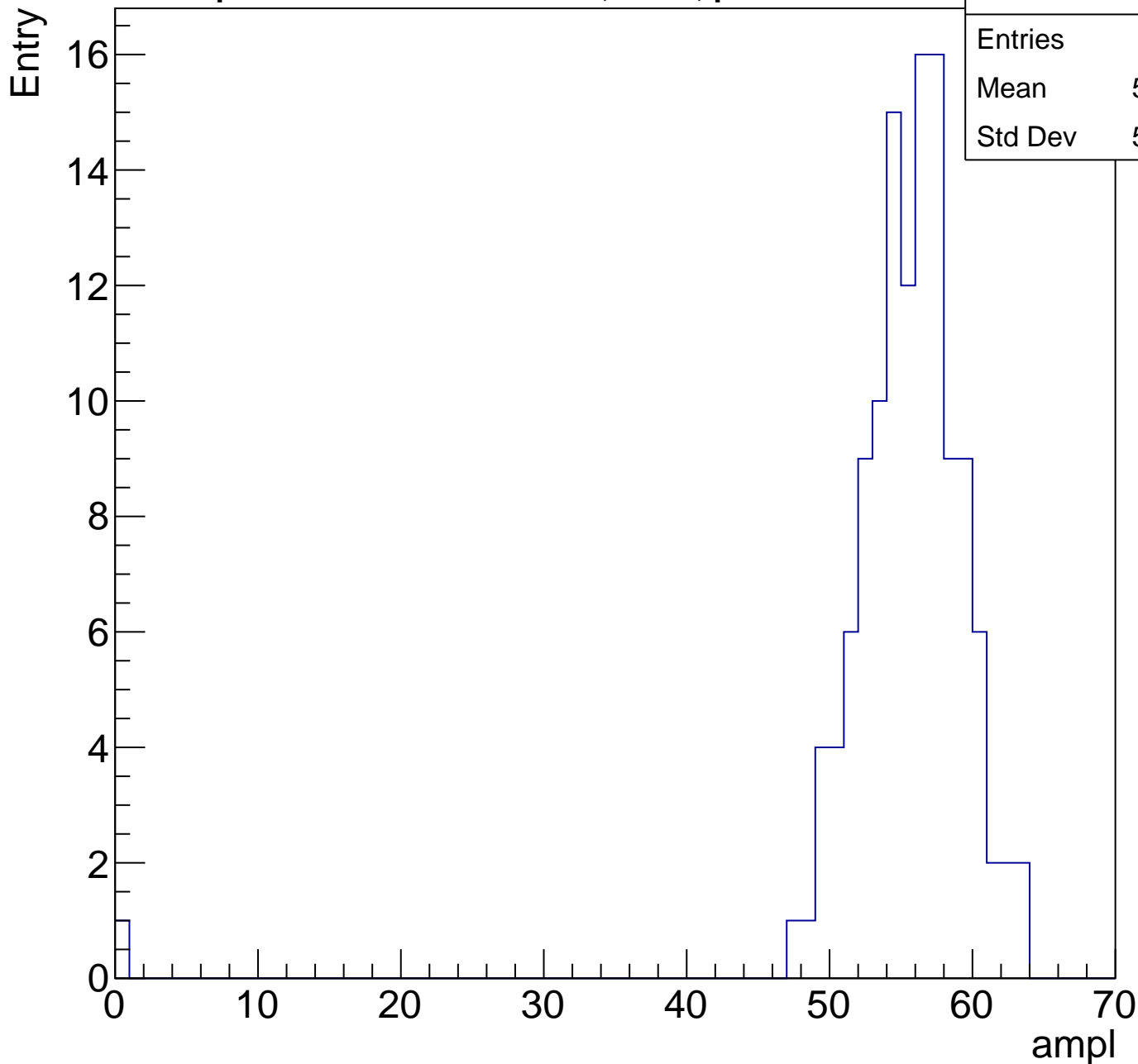
ampl



B1L001S, U24-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	54.86
Std Dev	5.909



B1L001S, U24-ch72, adc5

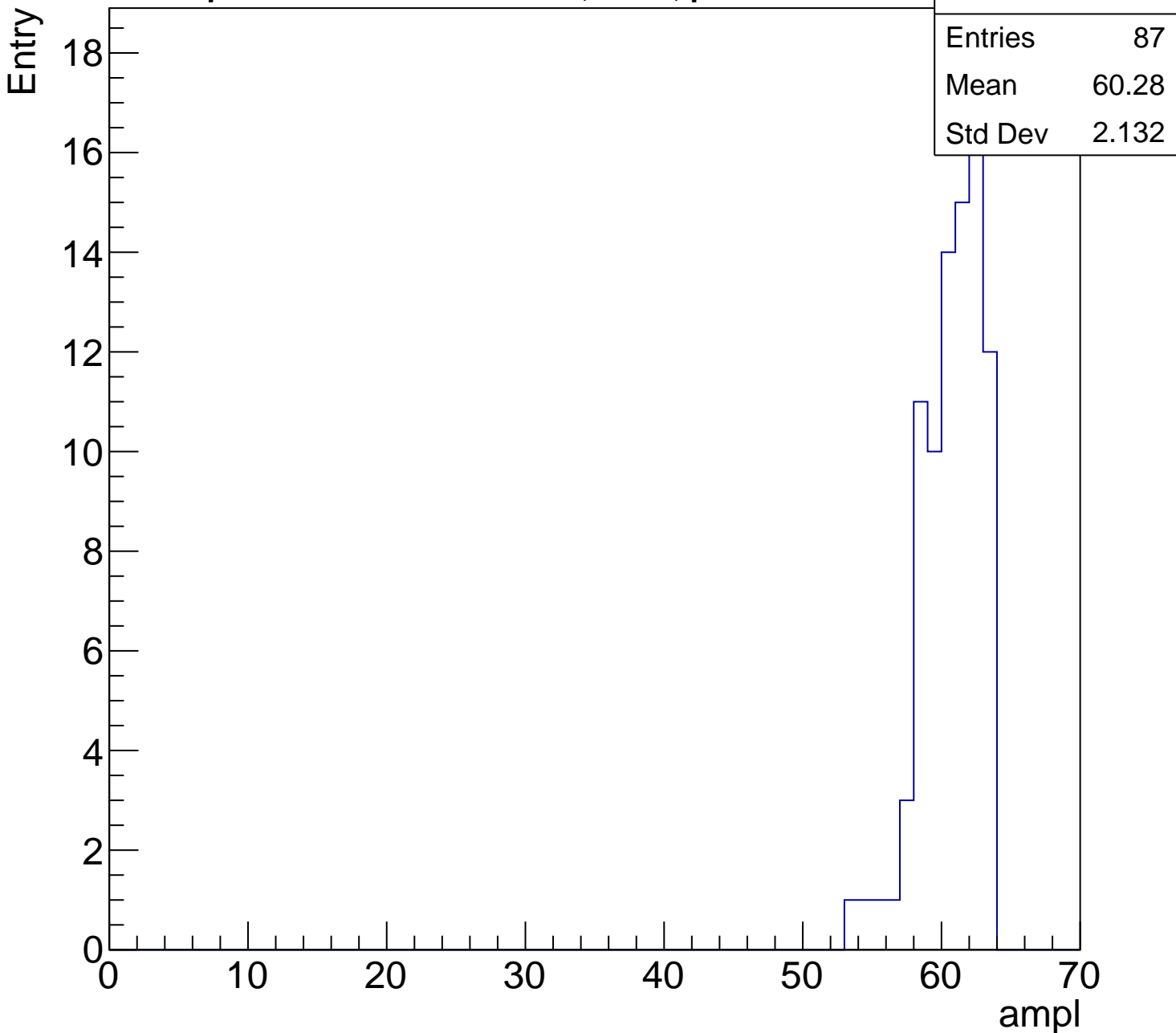
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	87
Mean	60.28
Std Dev	2.132

ampl

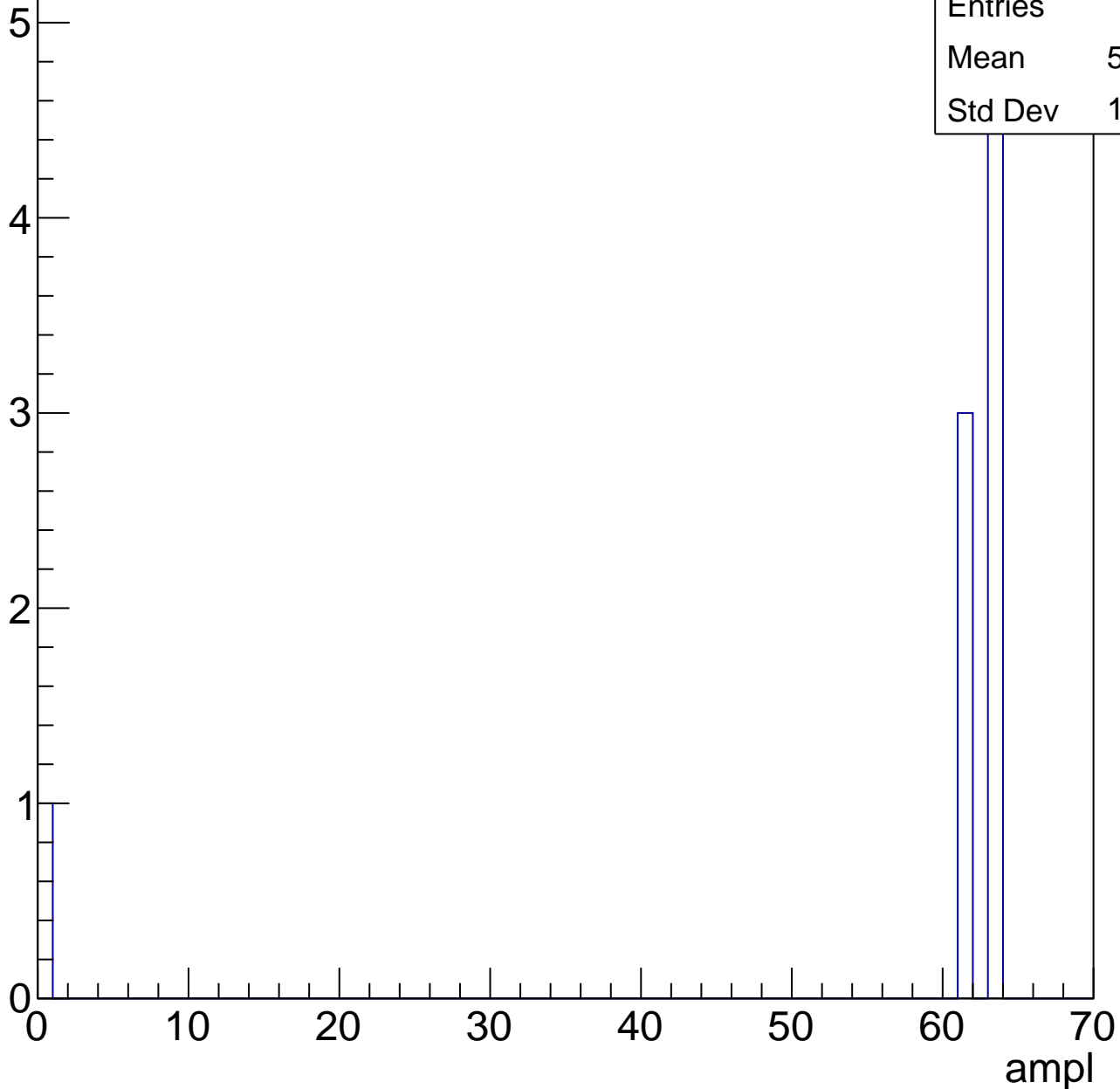


B1L001S, U24-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	55.33
Std Dev	19.58



B1L001S, U24-ch72, adc7

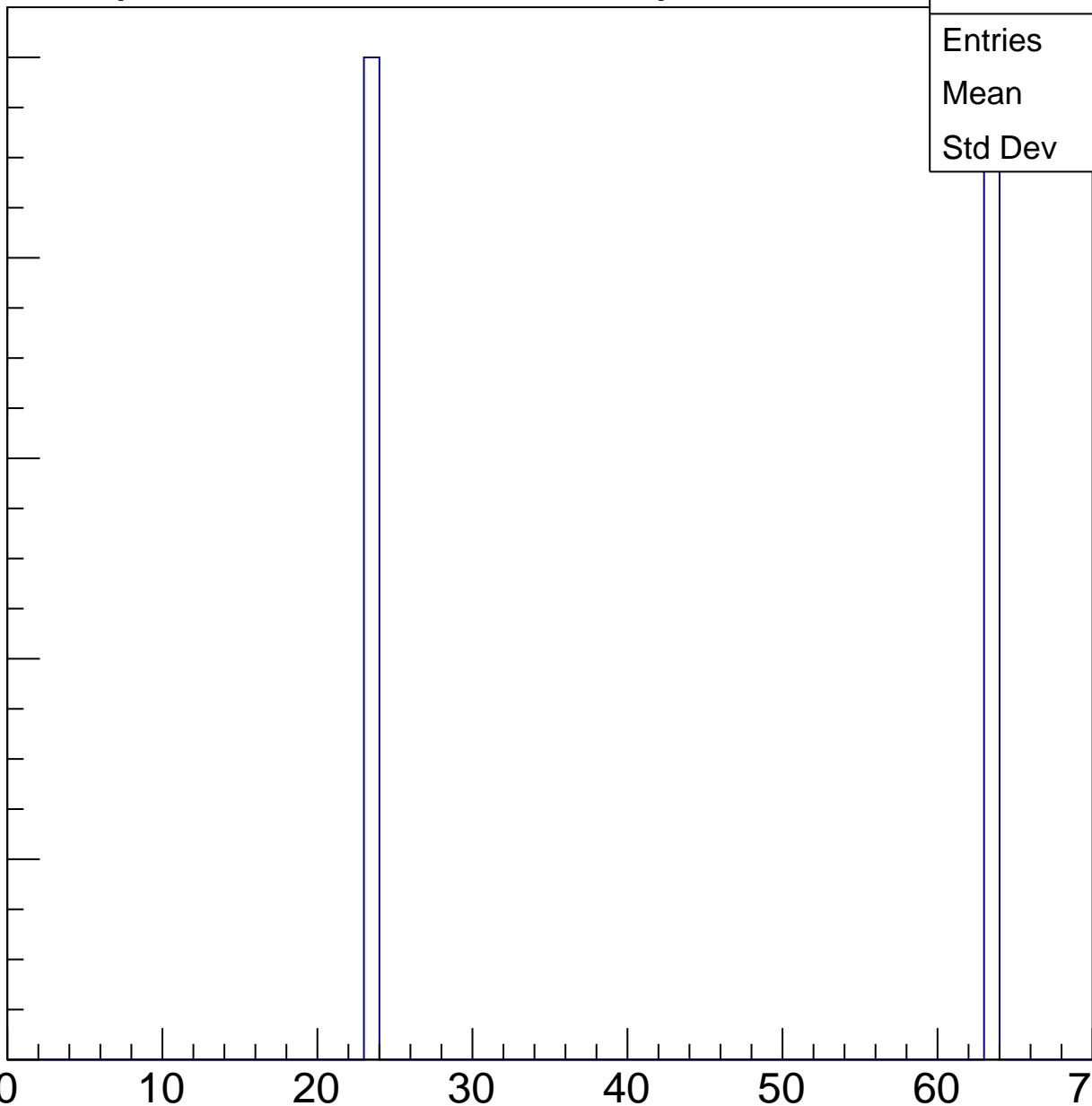
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl



B1L001S, U24-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	31.69
Std Dev	4.33

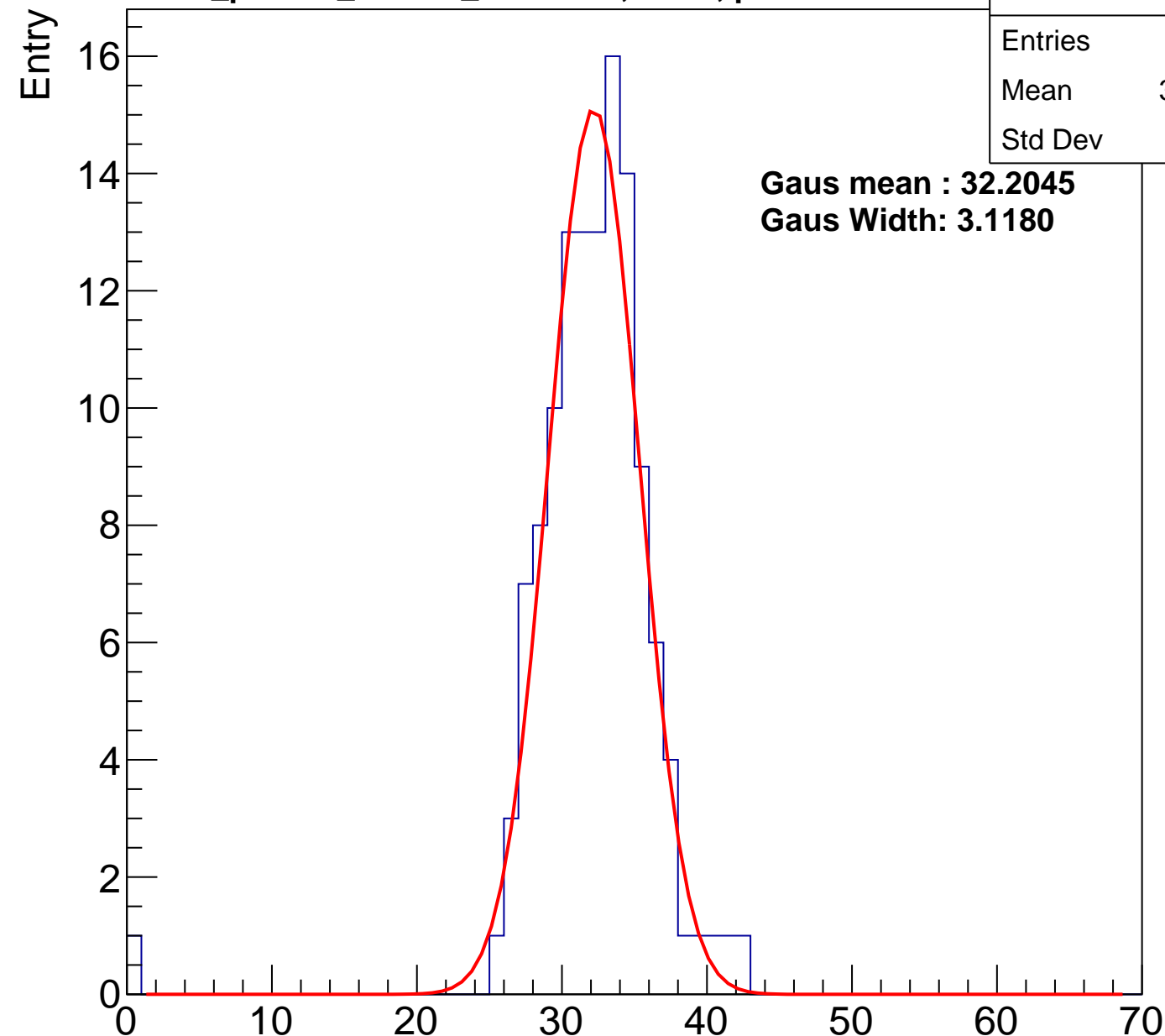
Gaus mean : 32.2045

Gaus Width: 3.1180

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch73, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	38.45
Std Dev	3.472

Gaus mean : 39.0363

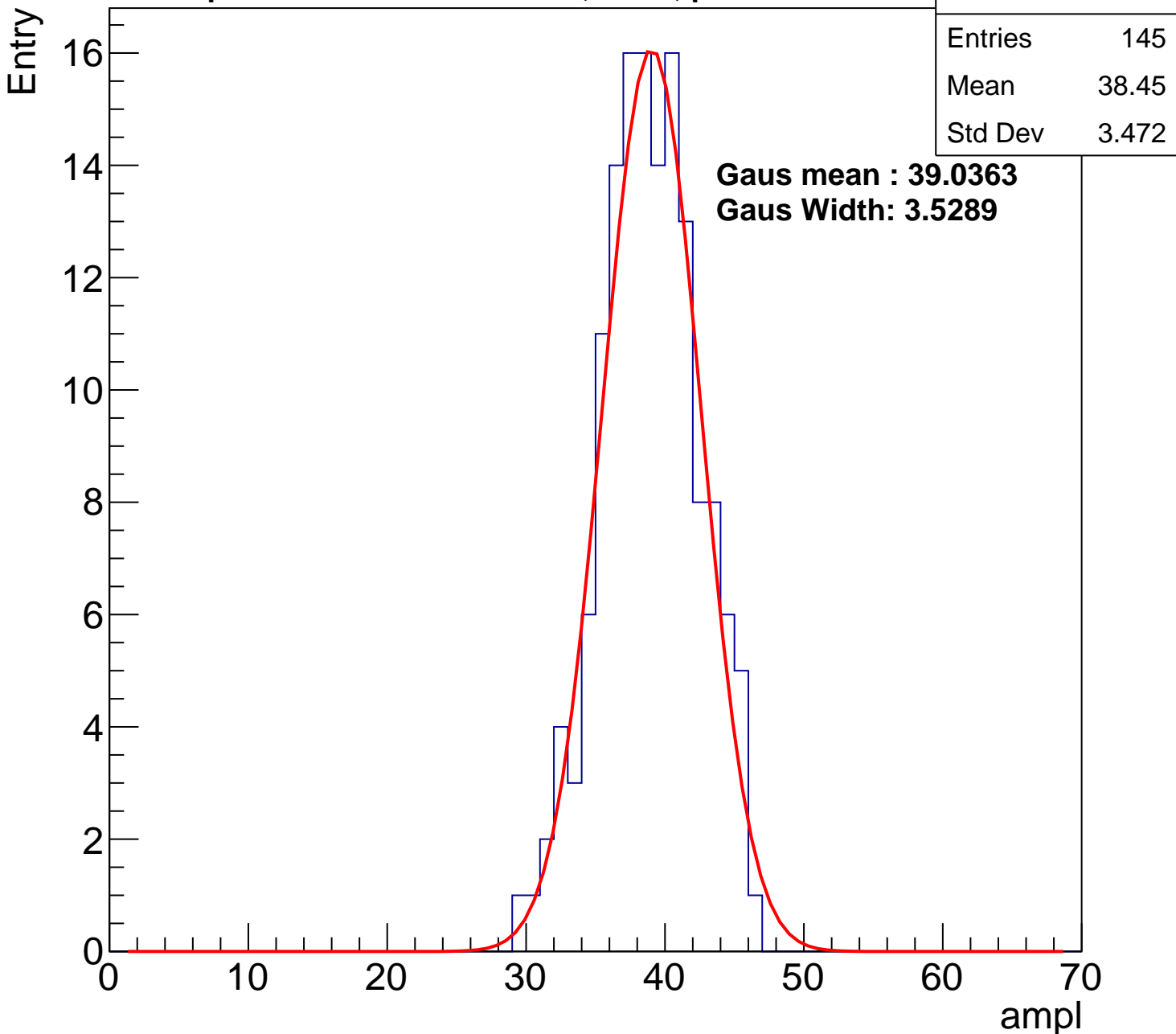
Gaus Width: 3.5289

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch73, adc2

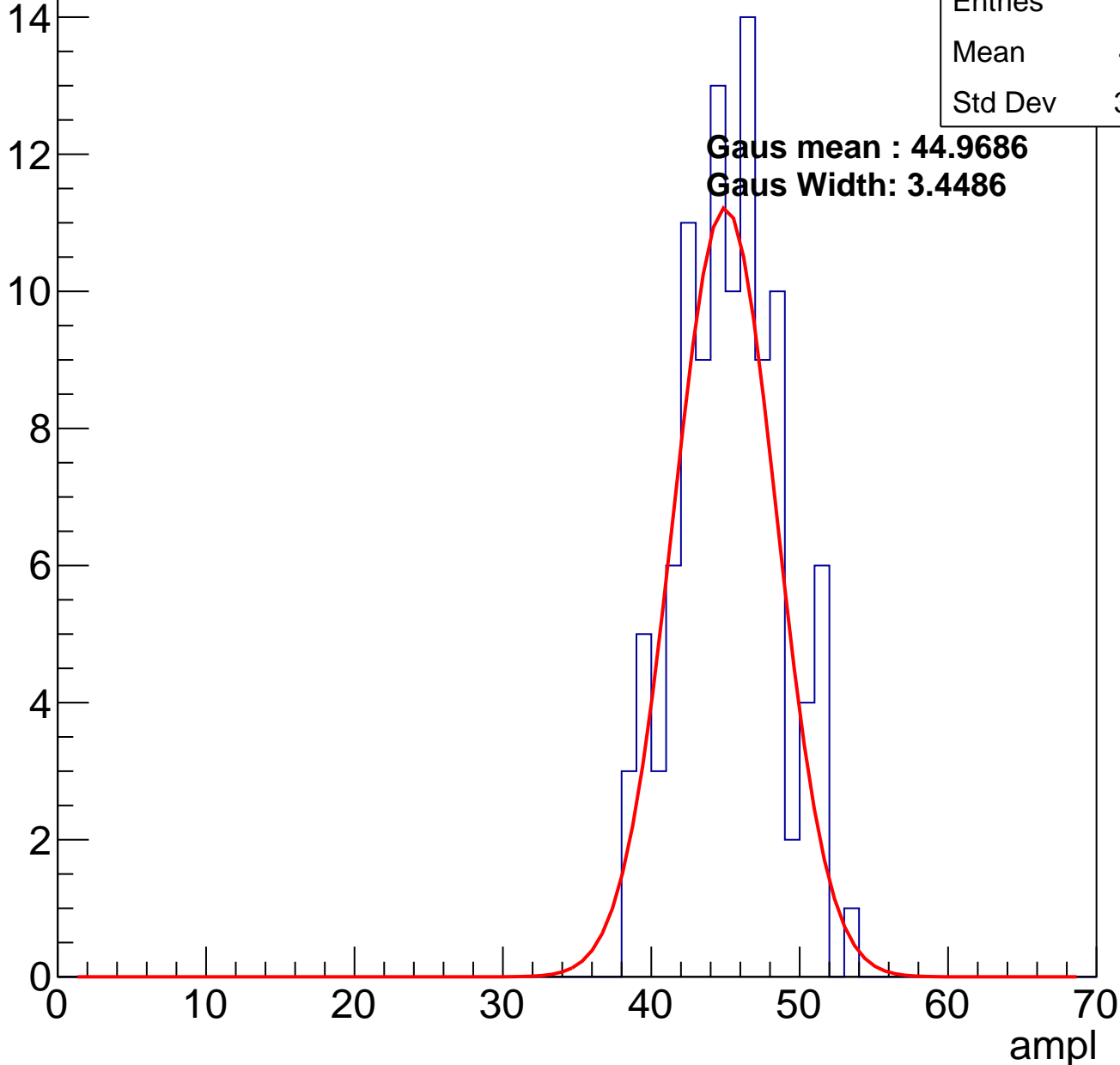
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	44.81
Std Dev	3.357

Entry

Gaus mean : 44.9686

Gaus Width: 3.4486



B1L001S, U24-ch73, adc3

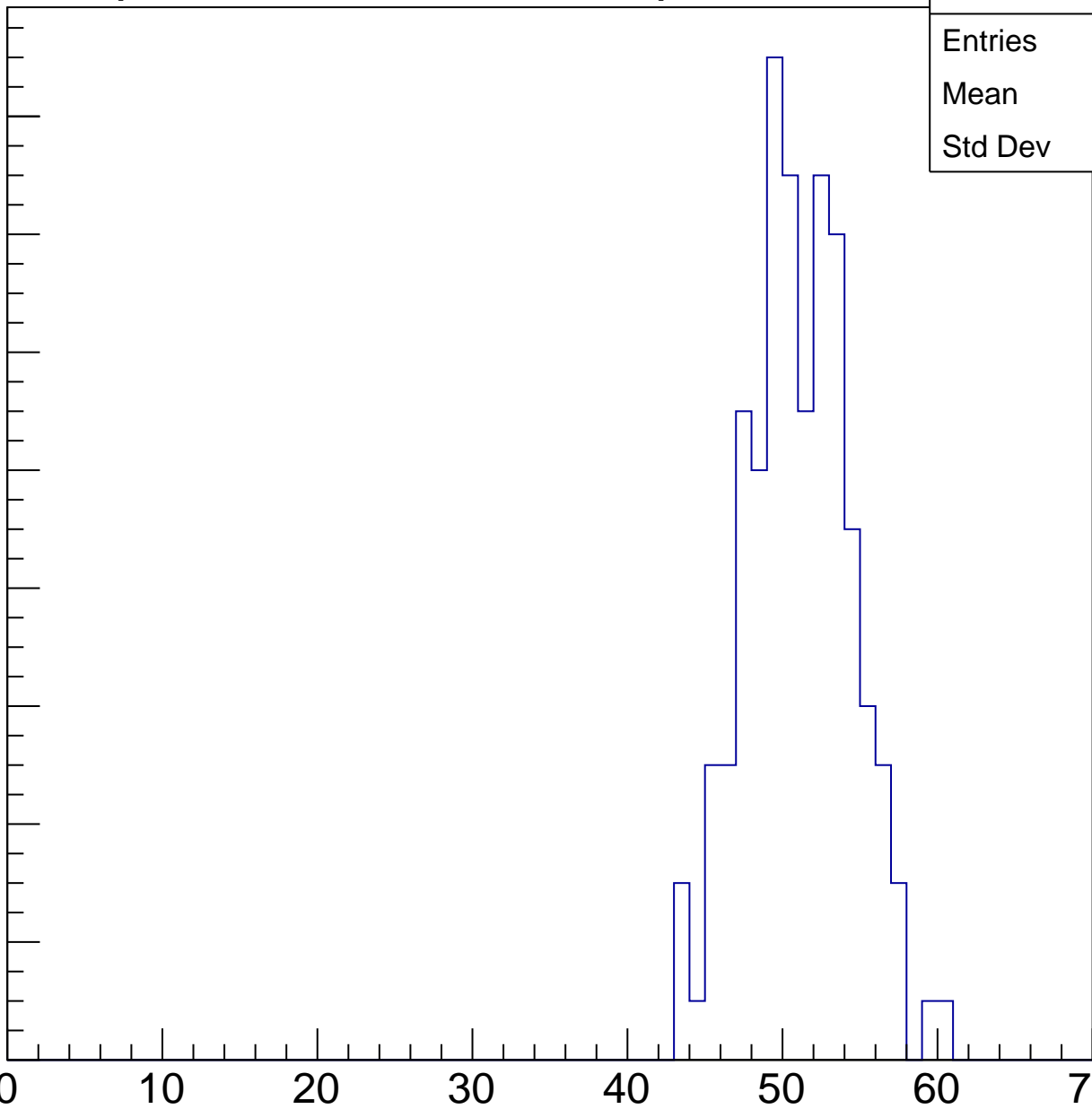
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	50.58
Std Dev	3.369

ampl



B1L001S, U24-ch73, adc4

calib_packv5_042523_0143.root, FC#2, port C2

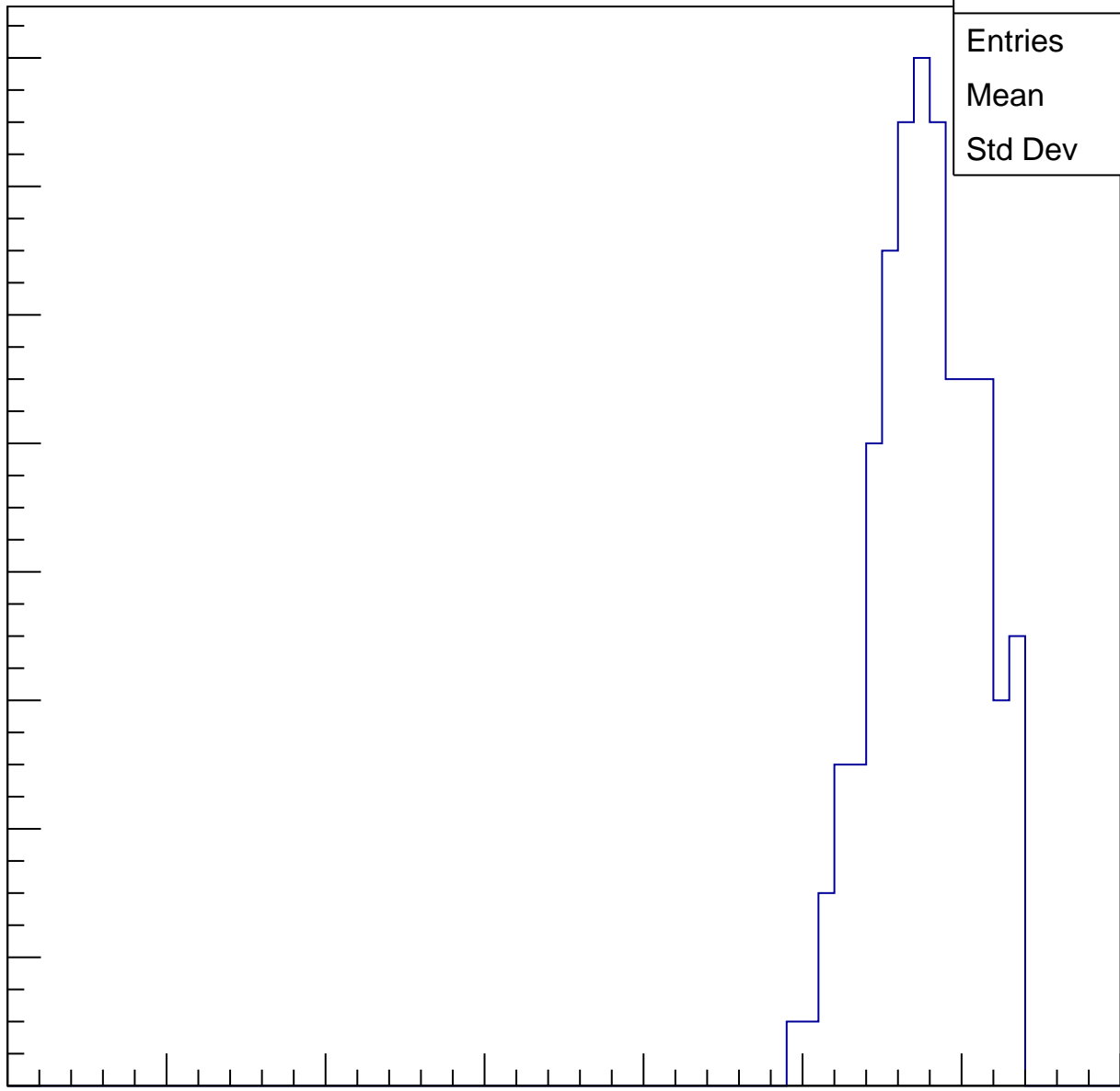
Entries	130
Mean	57.28
Std Dev	3.173

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch73, adc5

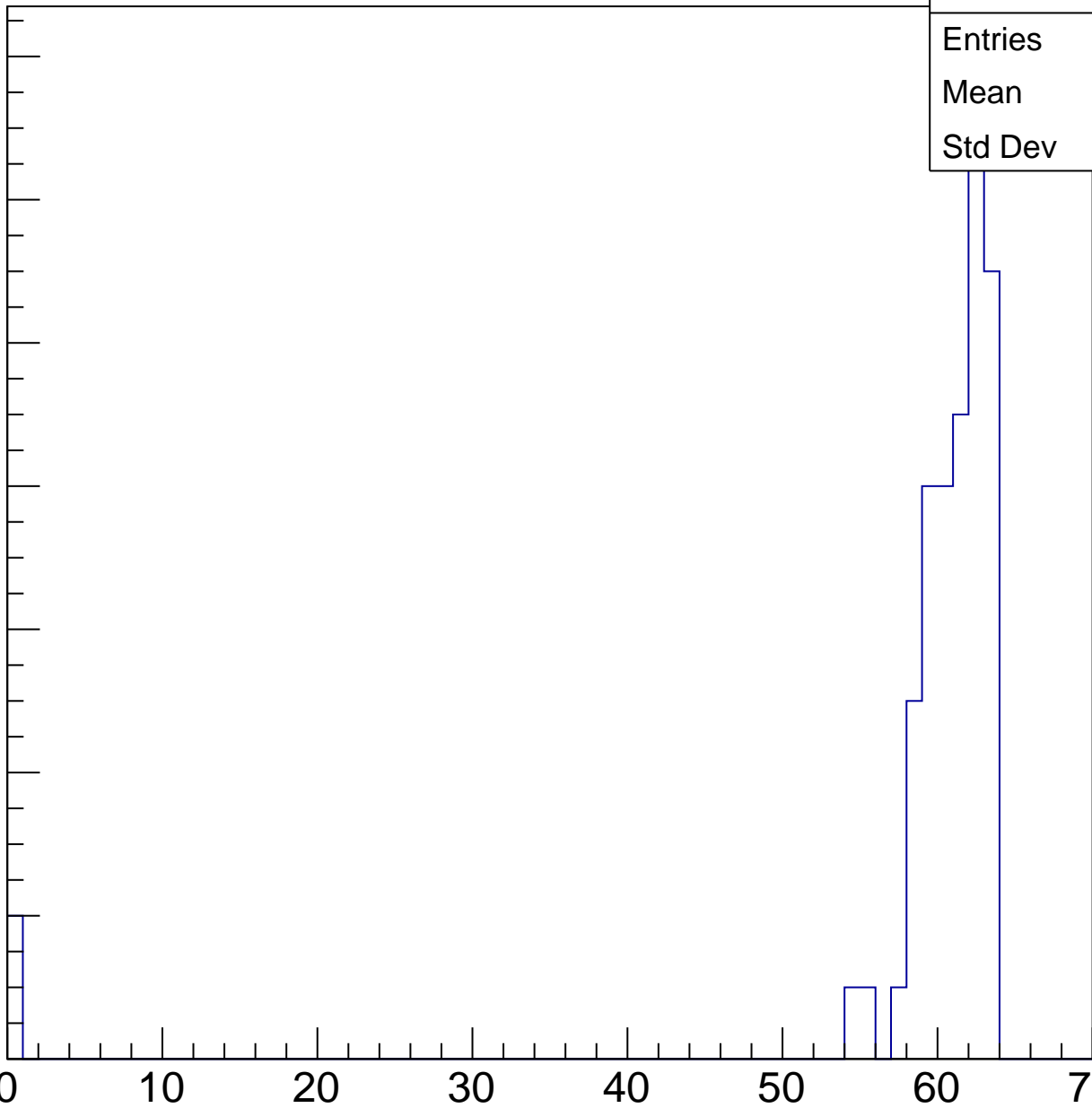
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	58.63
Std Dev	11.07

ampl



B1L001S, U24-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U24-ch73, adc7

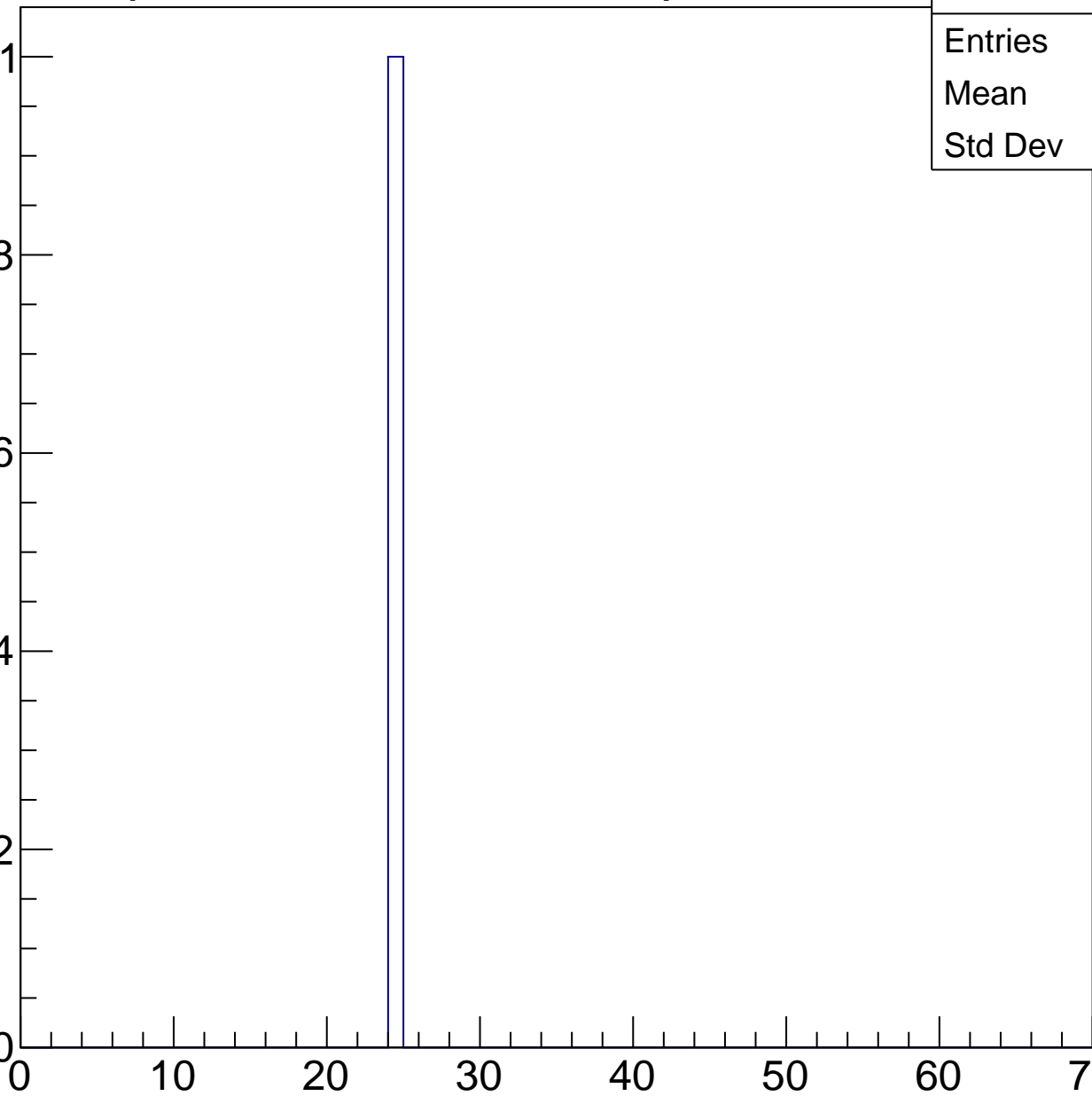
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



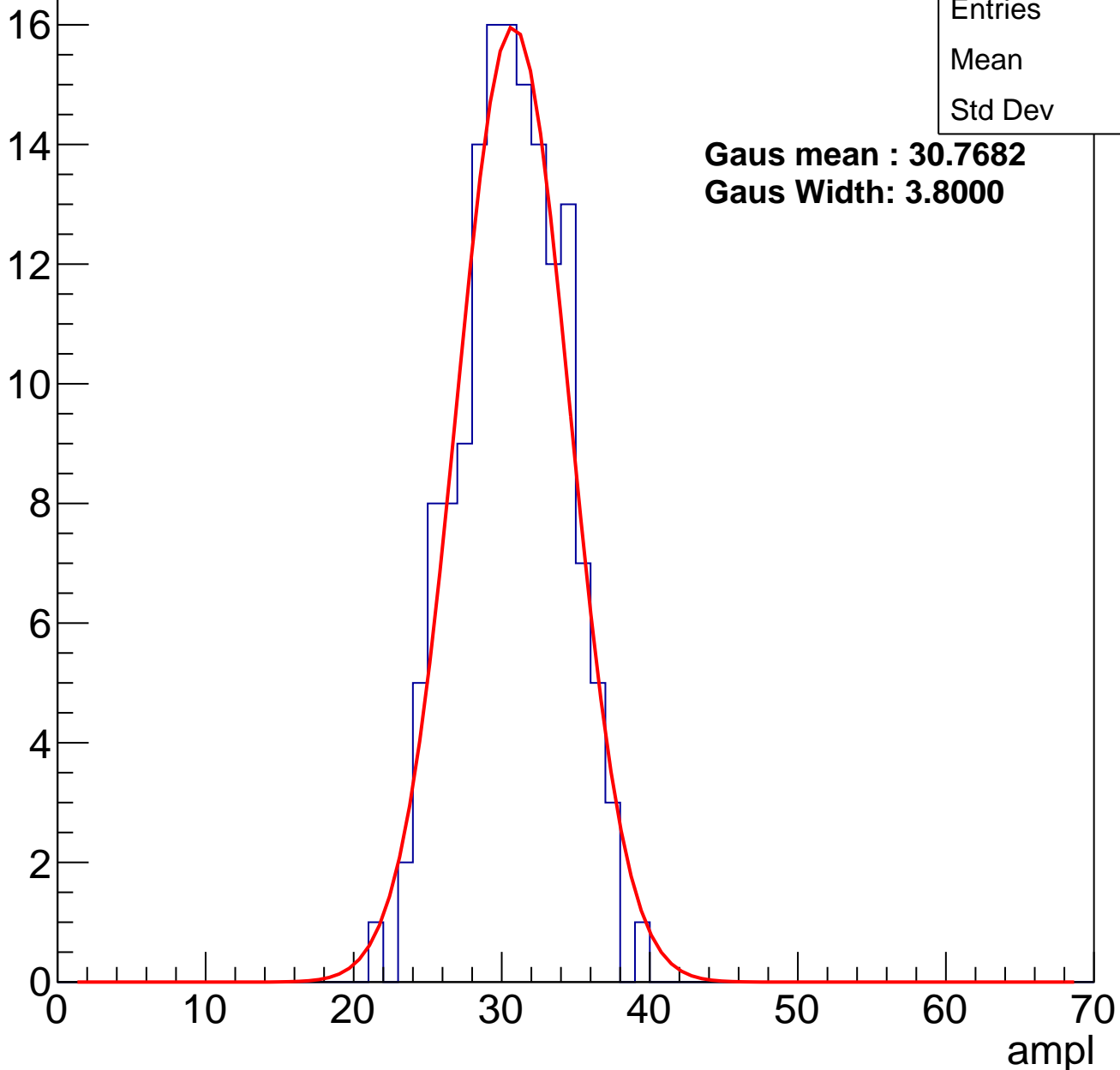
B1L001S, U24-ch74, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	30.2
Std Dev	3.48

Gaus mean : 30.7682
Gaus Width: 3.8000

Entry



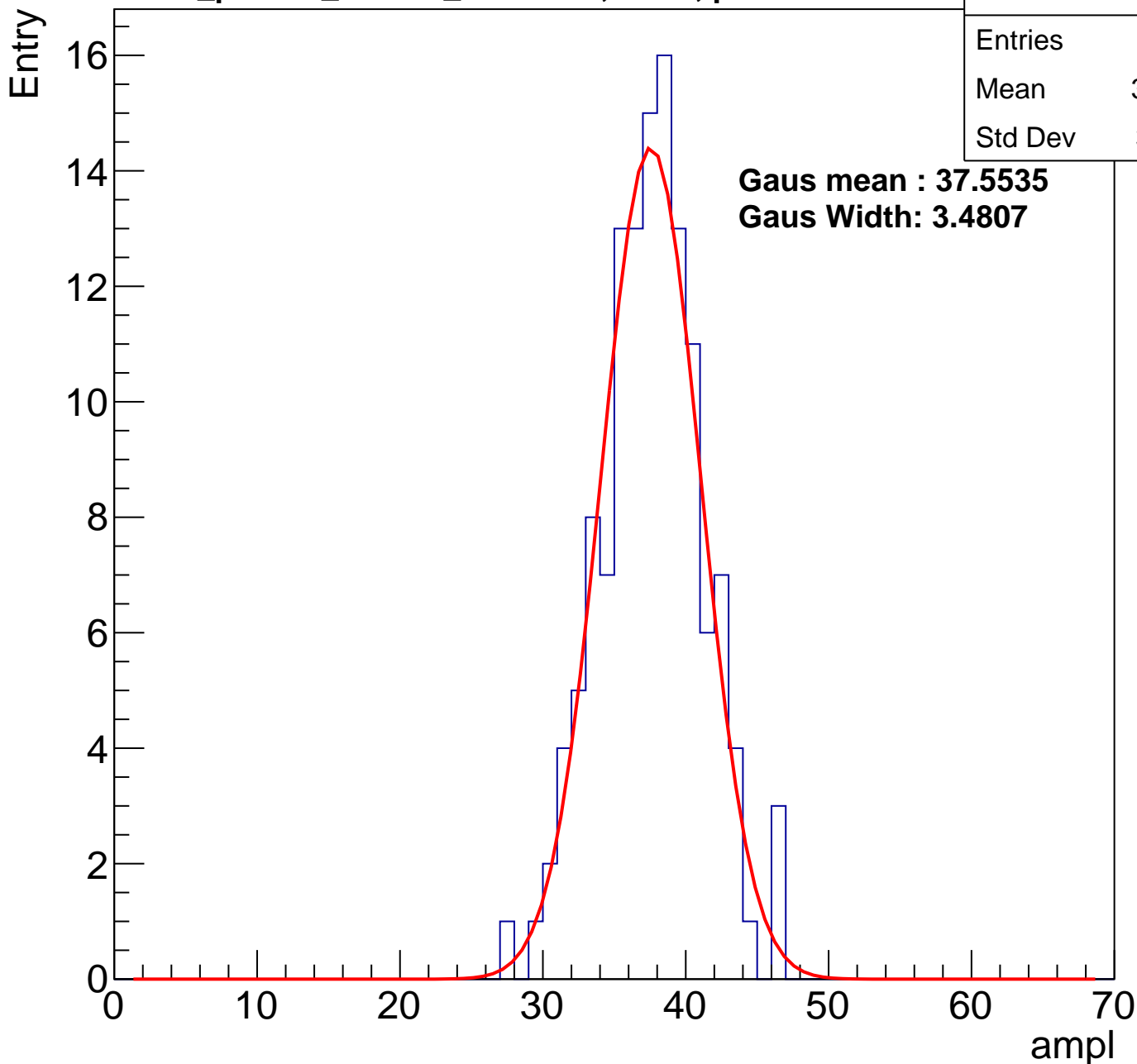
B1L001S, U24-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	37.15
Std Dev	3.571

Gaus mean : 37.5535

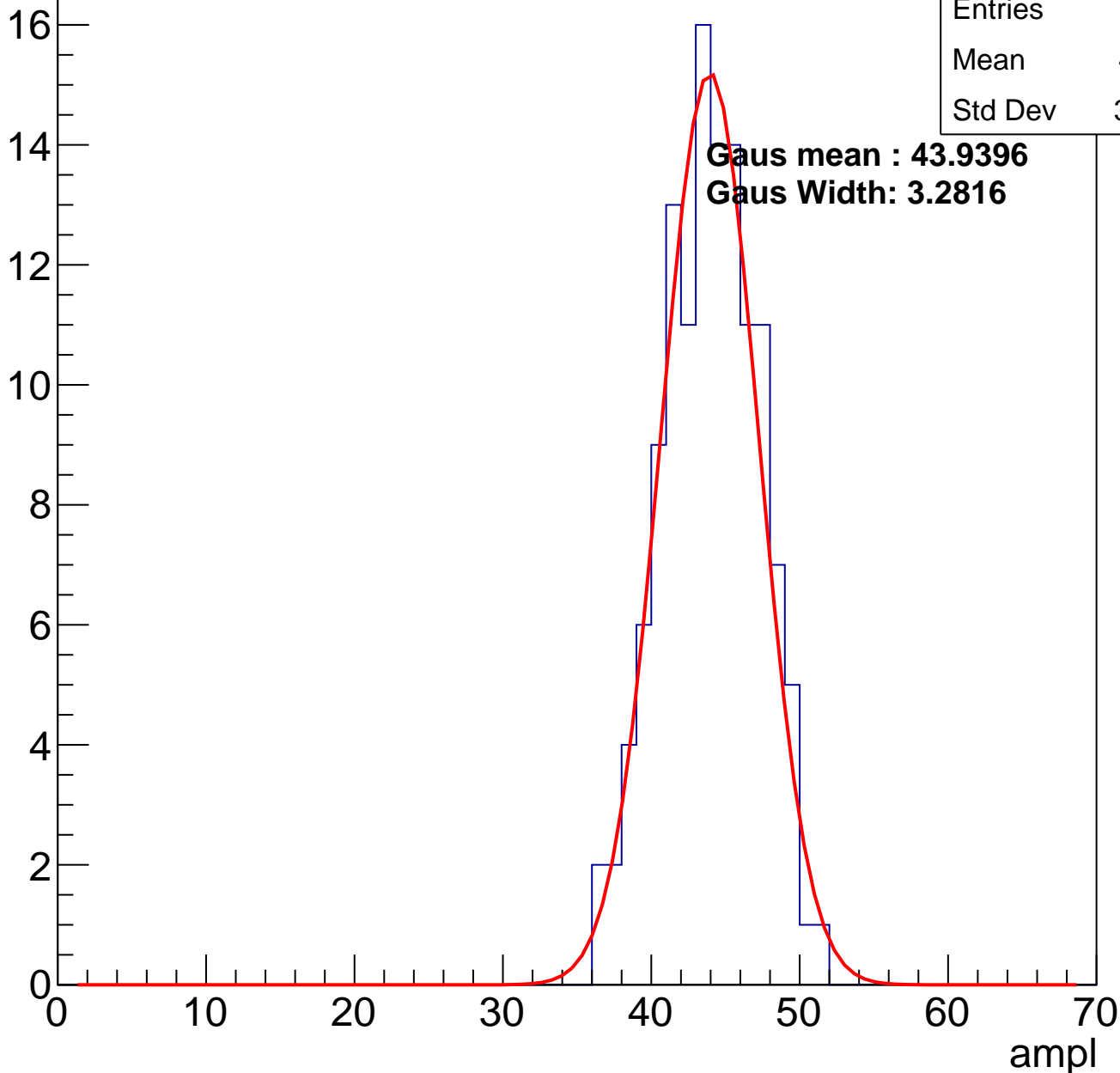
Gaus Width: 3.4807



B1L001S, U24-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

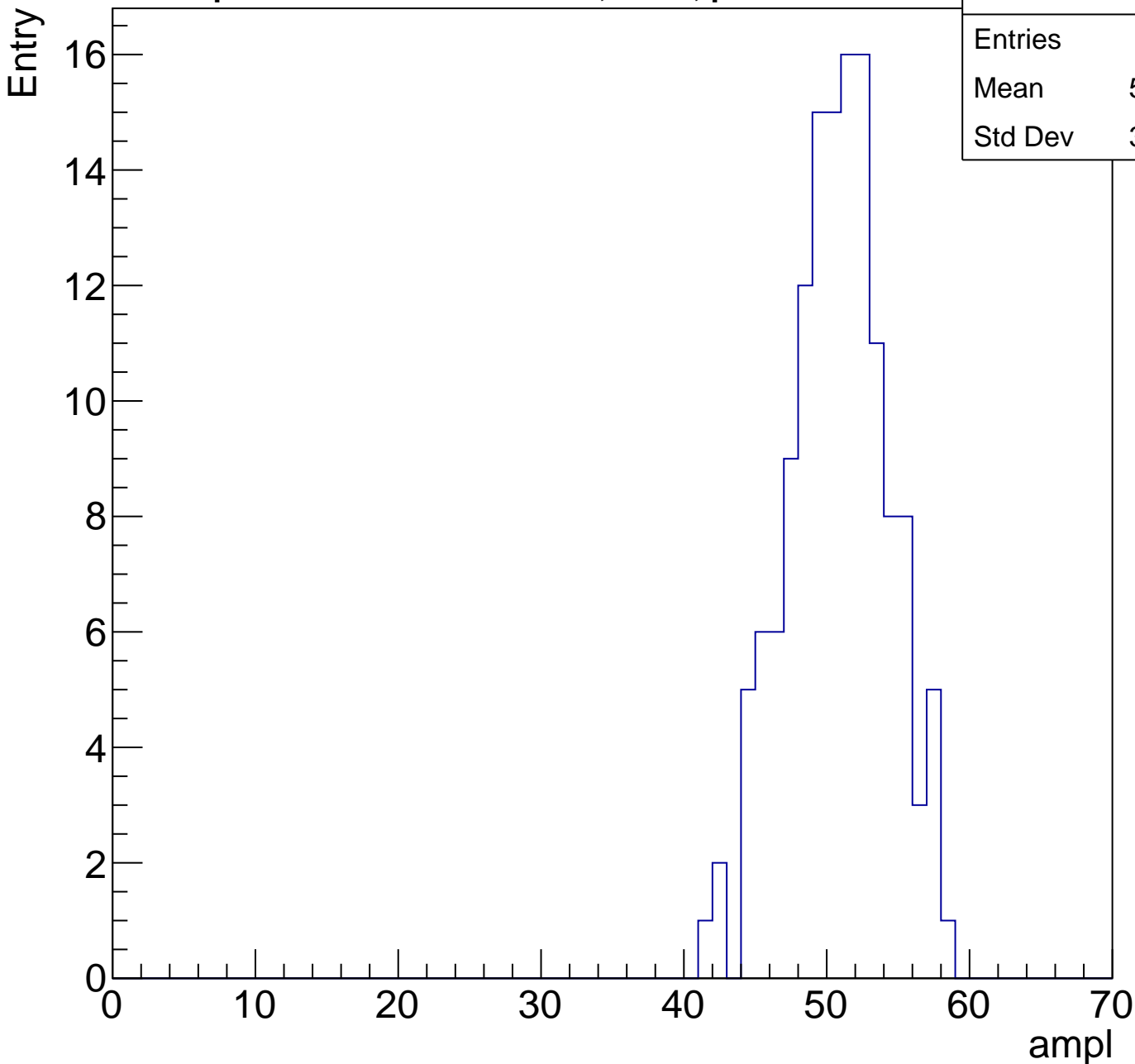


Entries	127
Mean	43.51
Std Dev	3.187

B1L001S, U24-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.28
Std Dev	3.489



B1L001S, U24-ch74, adc4

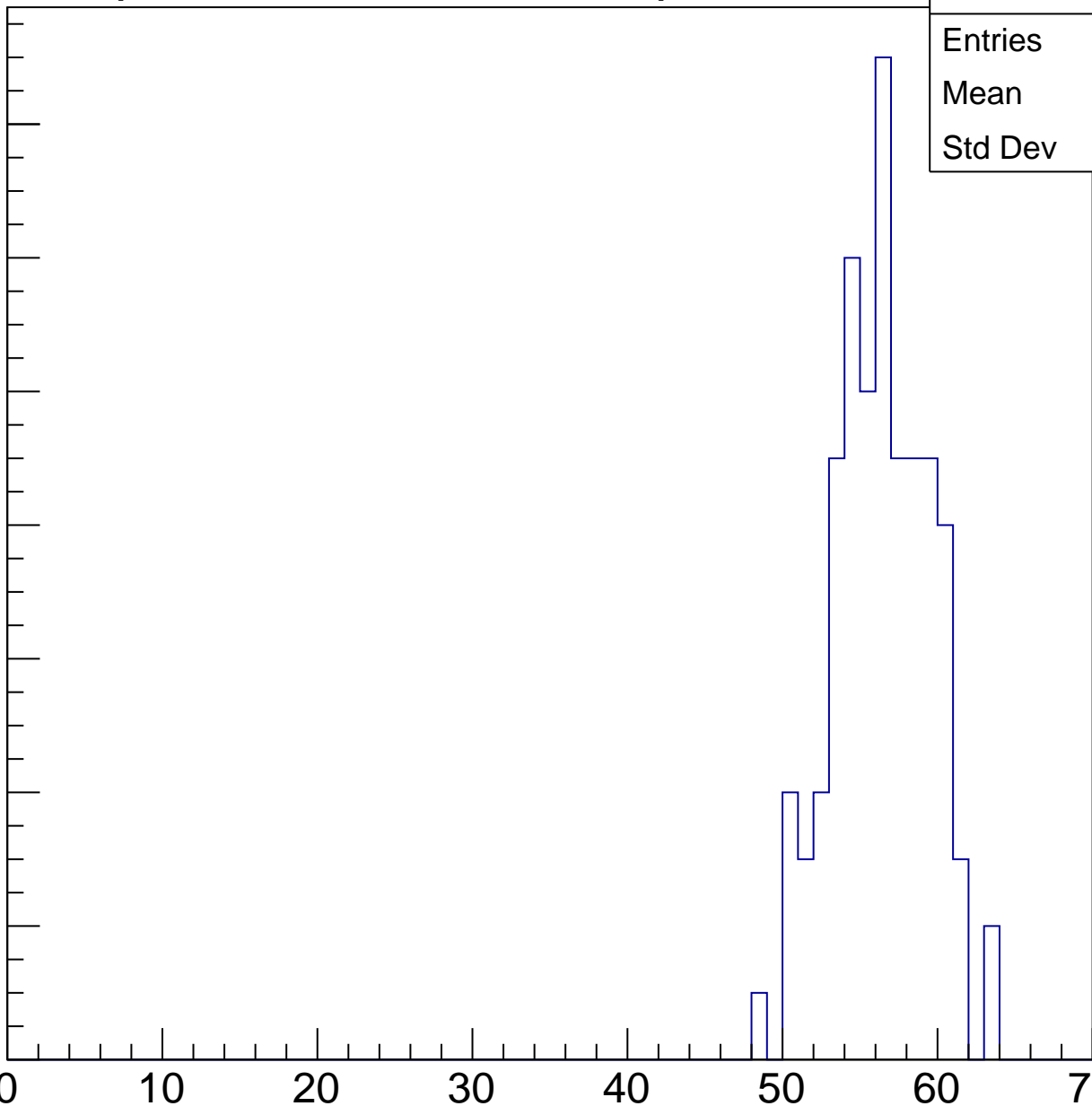
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	55.91
Std Dev	3.051

ampl



B1L001S, U24-ch74, adc5

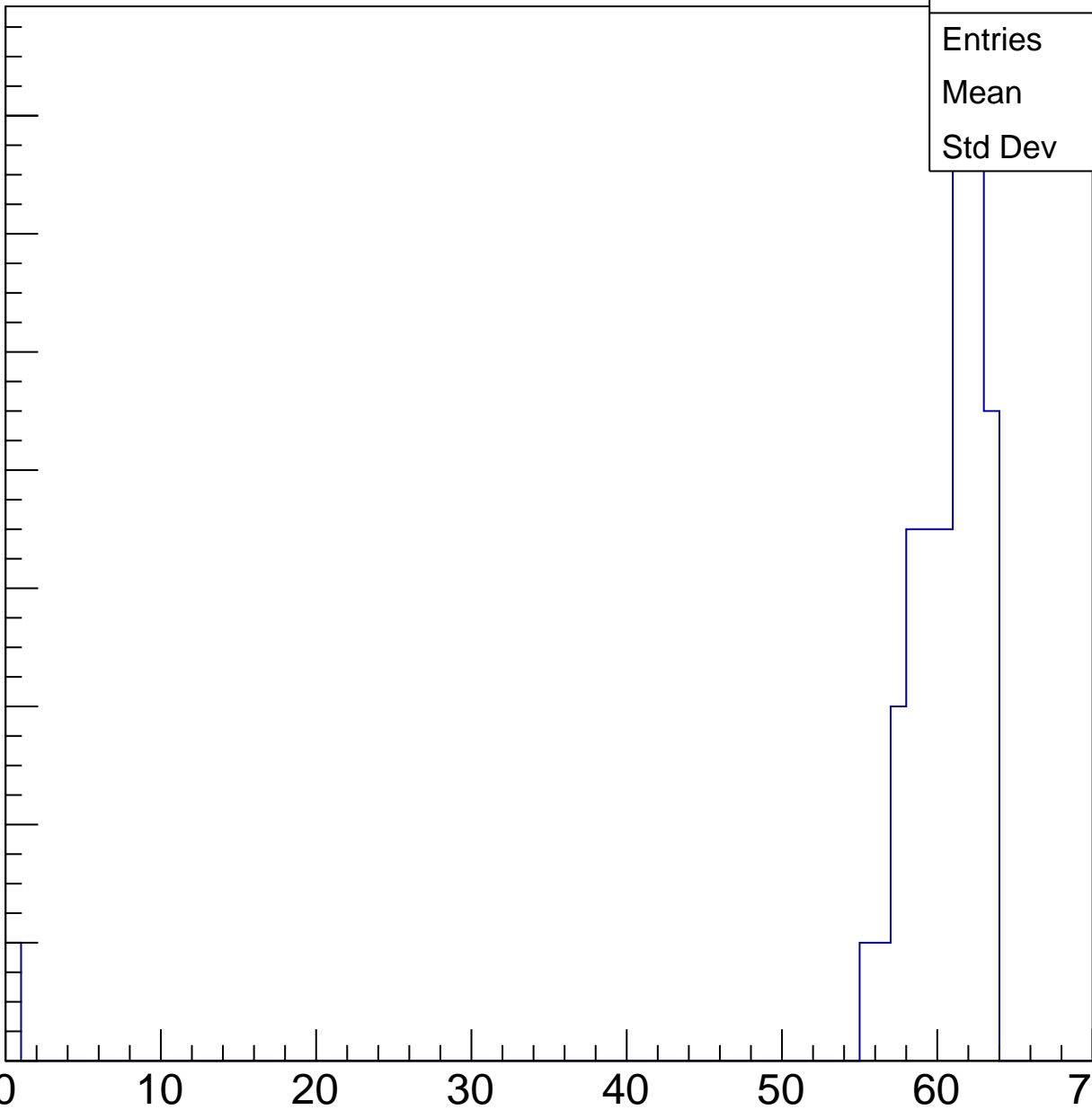
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.78
Std Dev	9.467

ampl

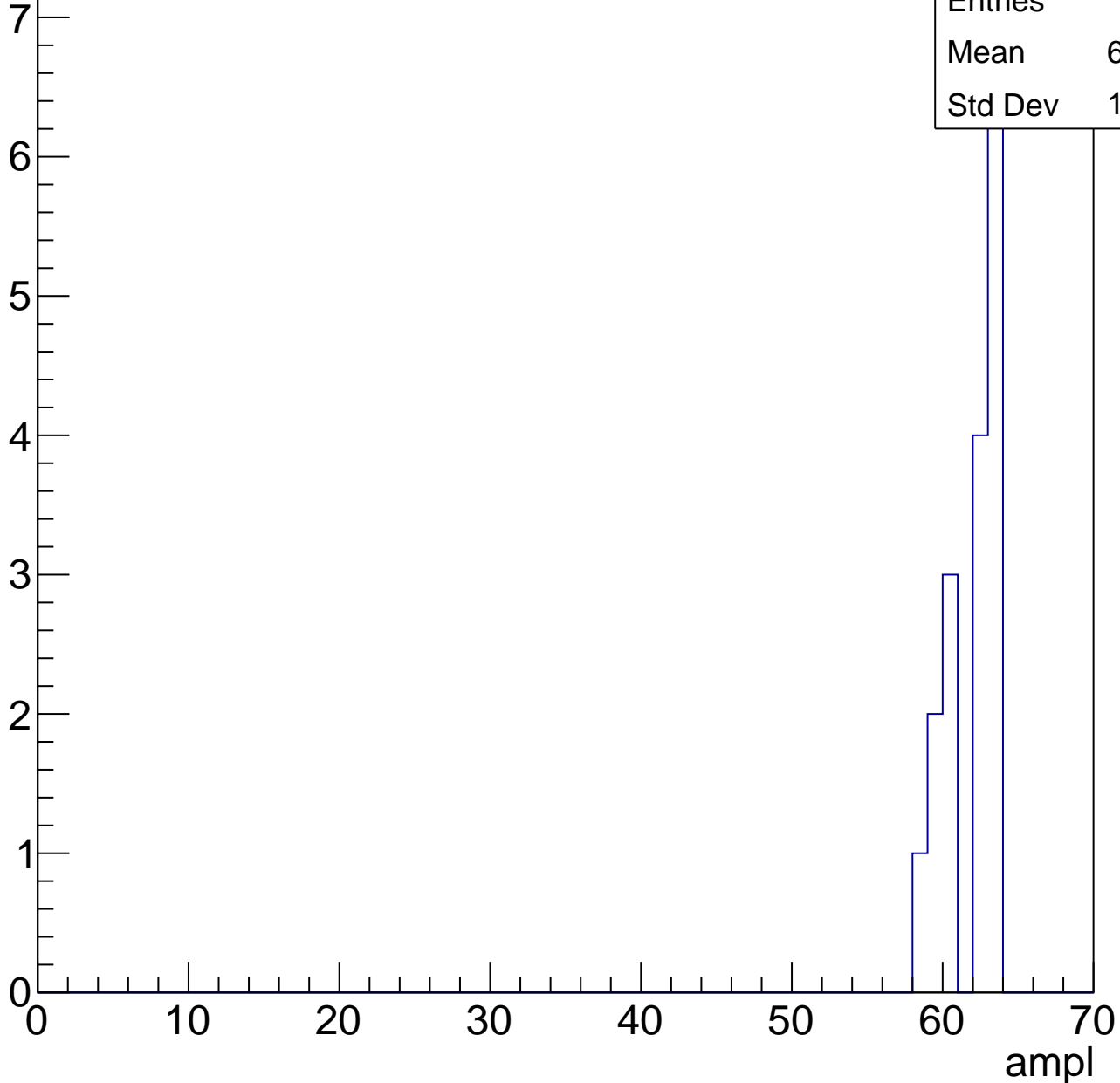


B1L001S, U24-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	61.47
Std Dev	1.684



B1L001S, U24-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L001S, U24-ch75, adc0

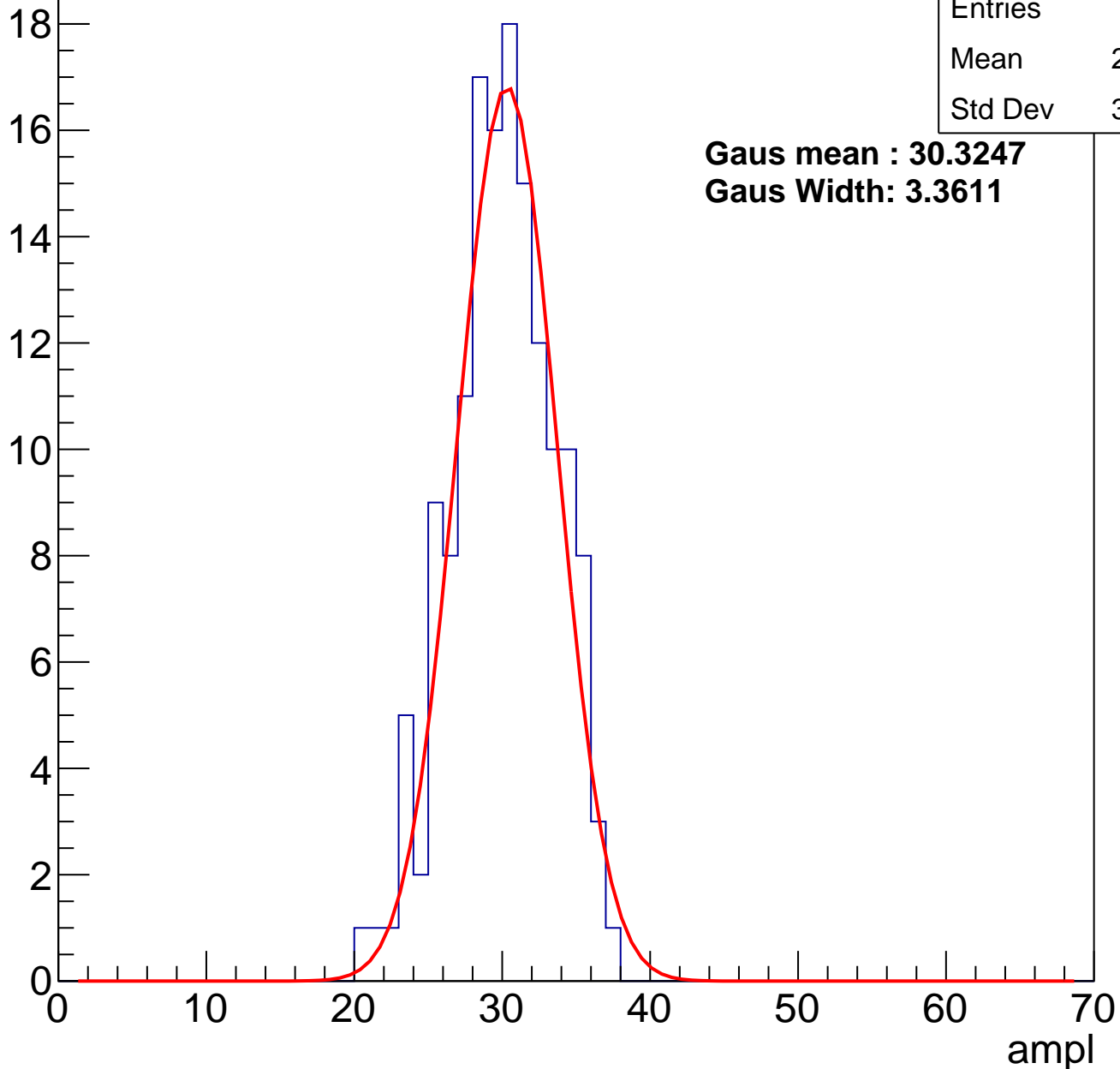
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	29.59
Std Dev	3.432

Gaus mean : 30.3247

Gaus Width: 3.3611

Entry



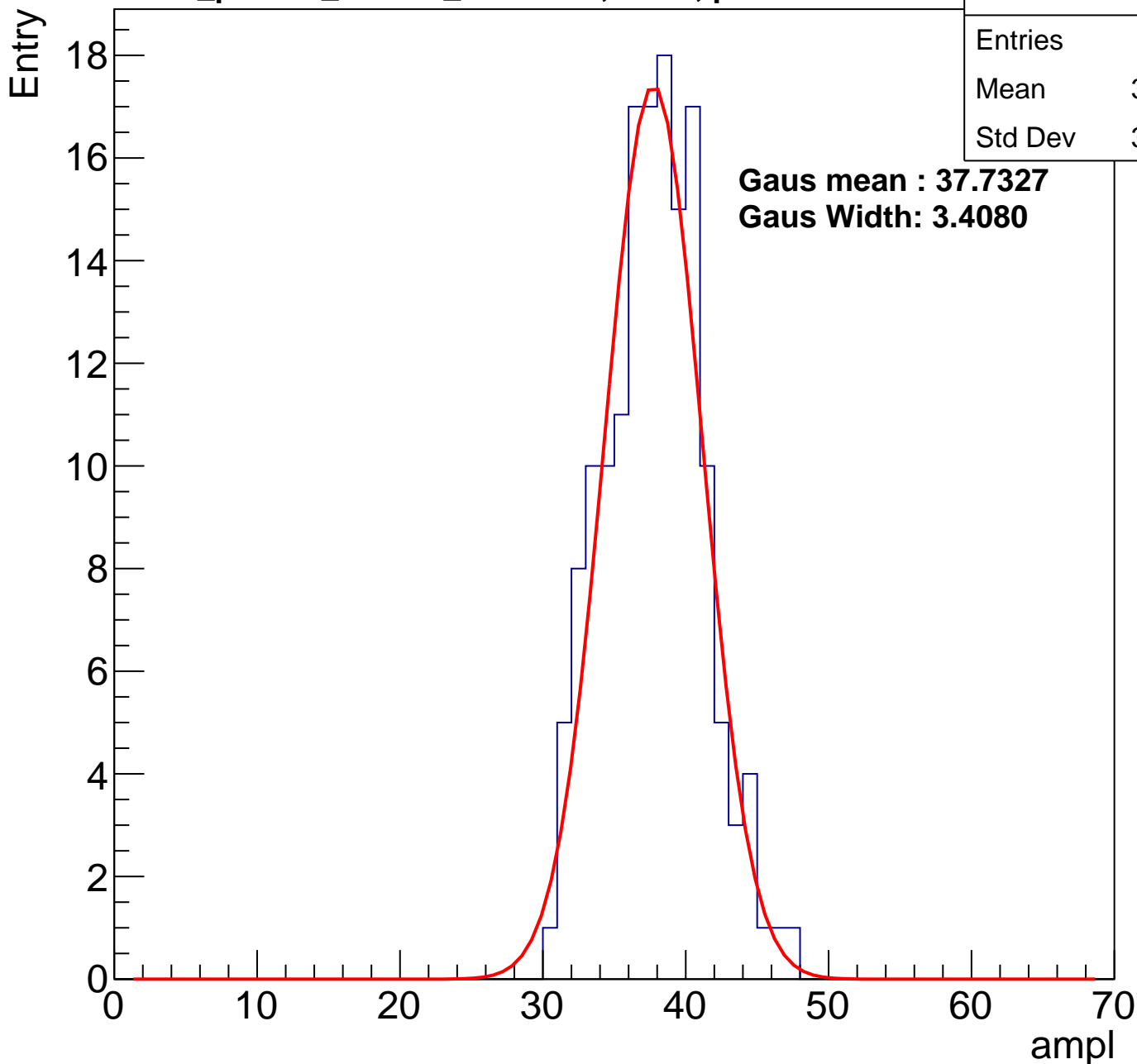
B1L001S, U24-ch75, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.33
Std Dev	3.408

Gaus mean : 37.7327

Gaus Width: 3.4080



B1L001S, U24-ch75, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

44.76

Std Dev

3.433

Gaus mean : 45.2638

Gaus Width: 3.2819

0

10

20

30

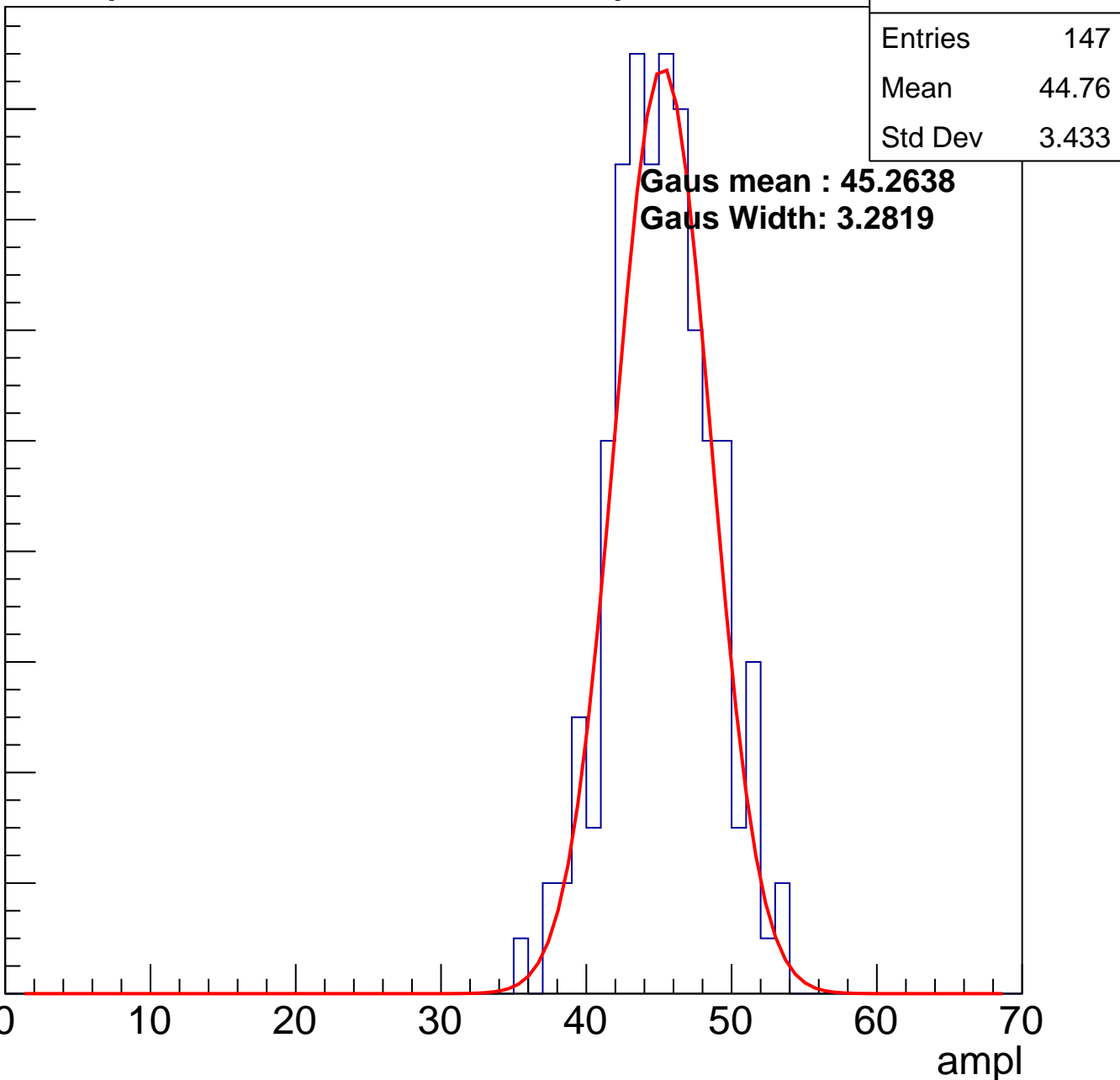
40

50

60

70

ampl



B1L001S, U24-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	50.71
Std Dev	2.93

Entry

12

10

8

6

4

2

0

0

10

20

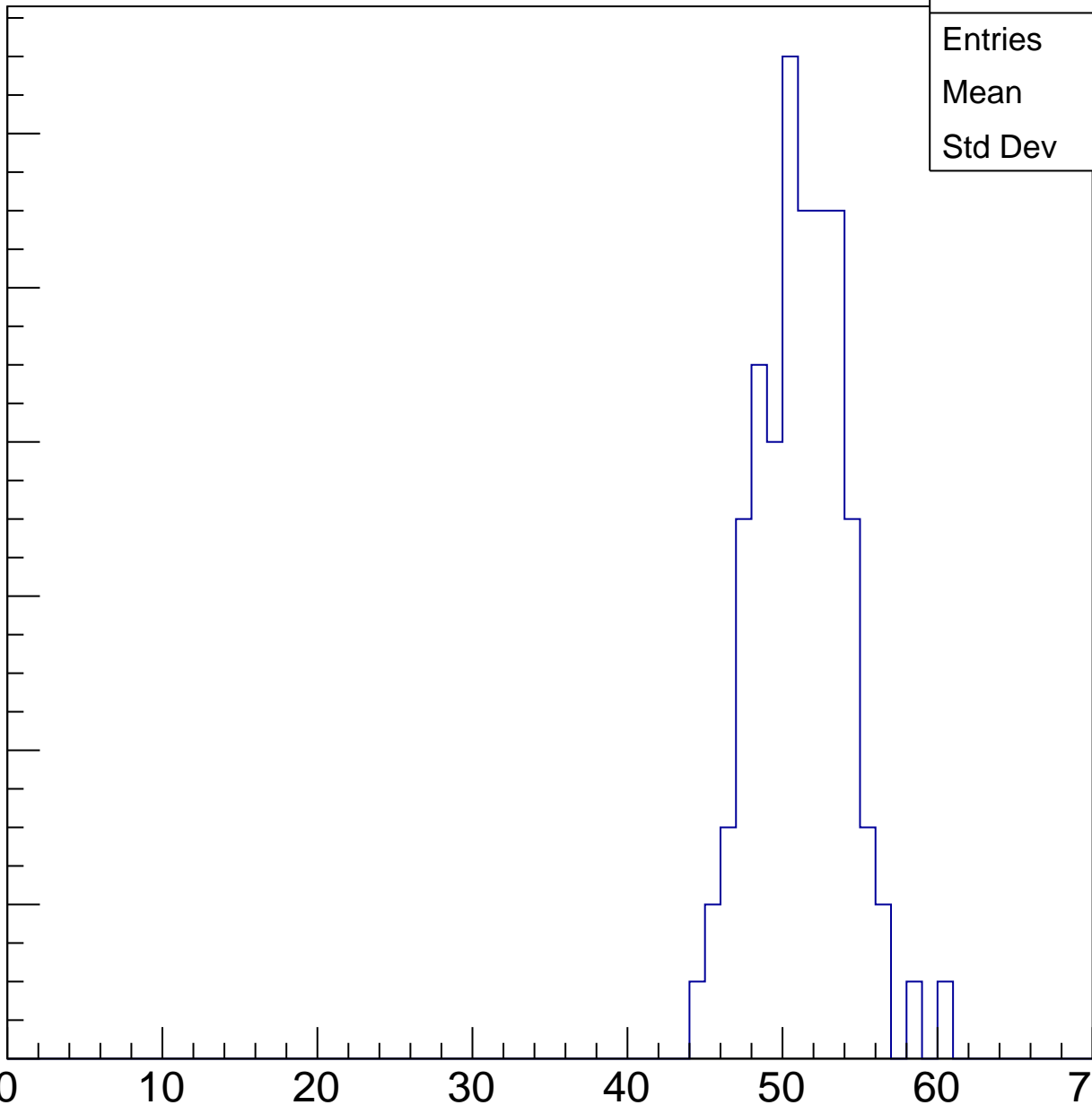
30

40

50

60

ampl



B1L001S, U24-ch75, adc4

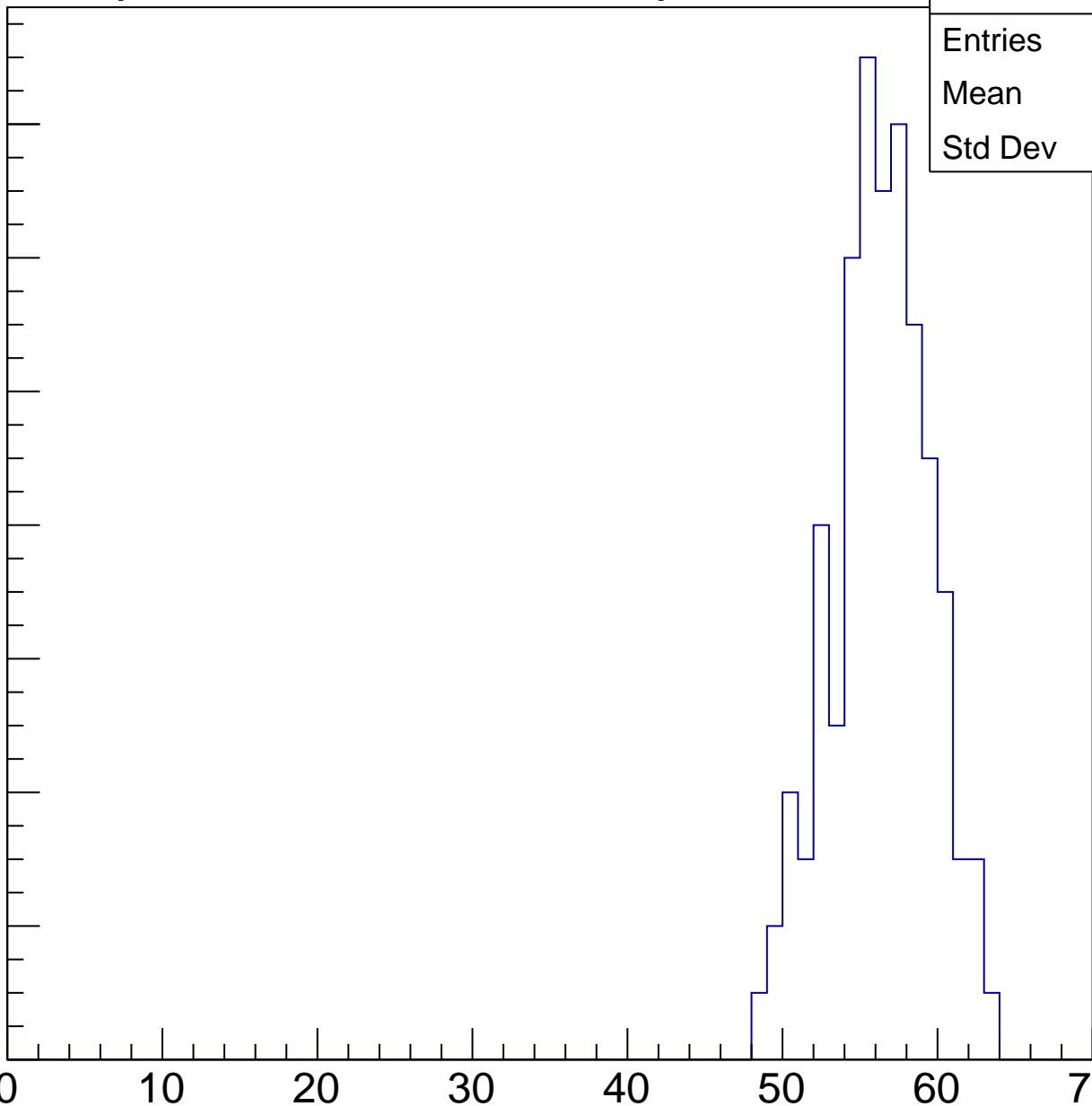
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	55.86
Std Dev	3.145

ampl



B1L001S, U24-ch75, adc5

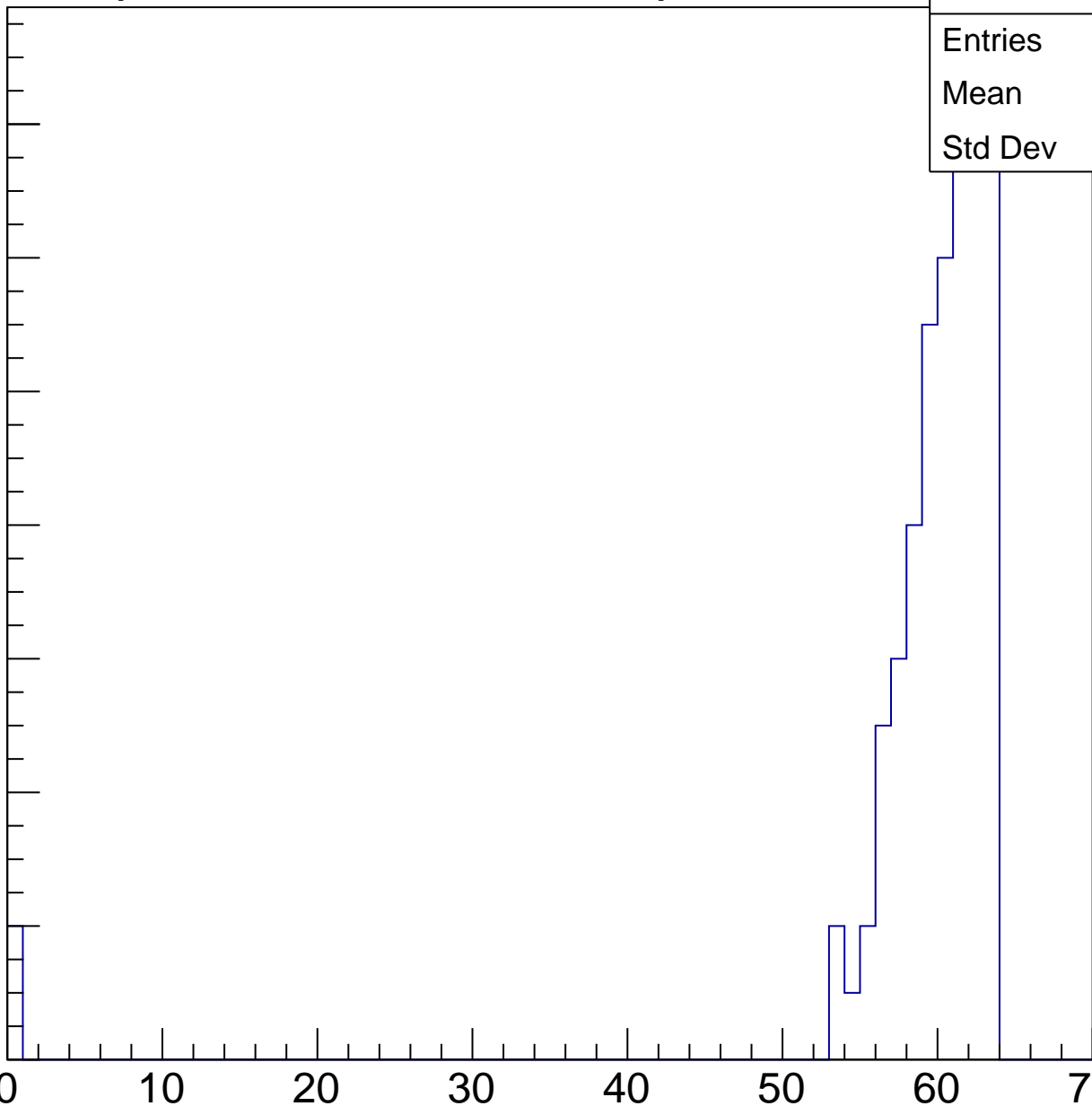
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	58.63
Std Dev	9.036

ampl



B1L001S, U24-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

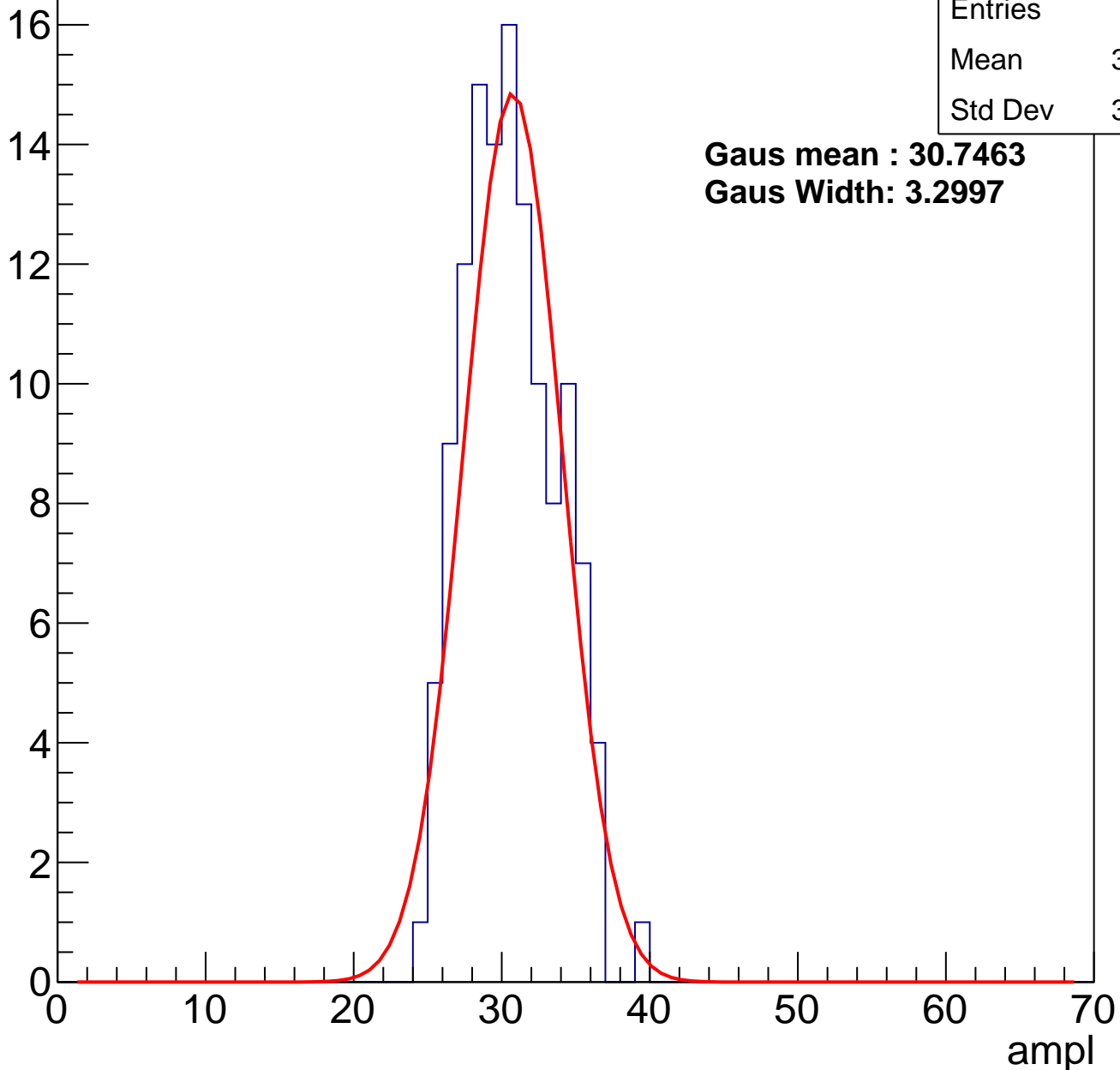


Entries	2
Mean	0
Std Dev	0

B1L001S, U24-ch76, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch76, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	36.86
Std Dev	3.485

Gaus mean : 37.1477

Gaus Width: 3.4967

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

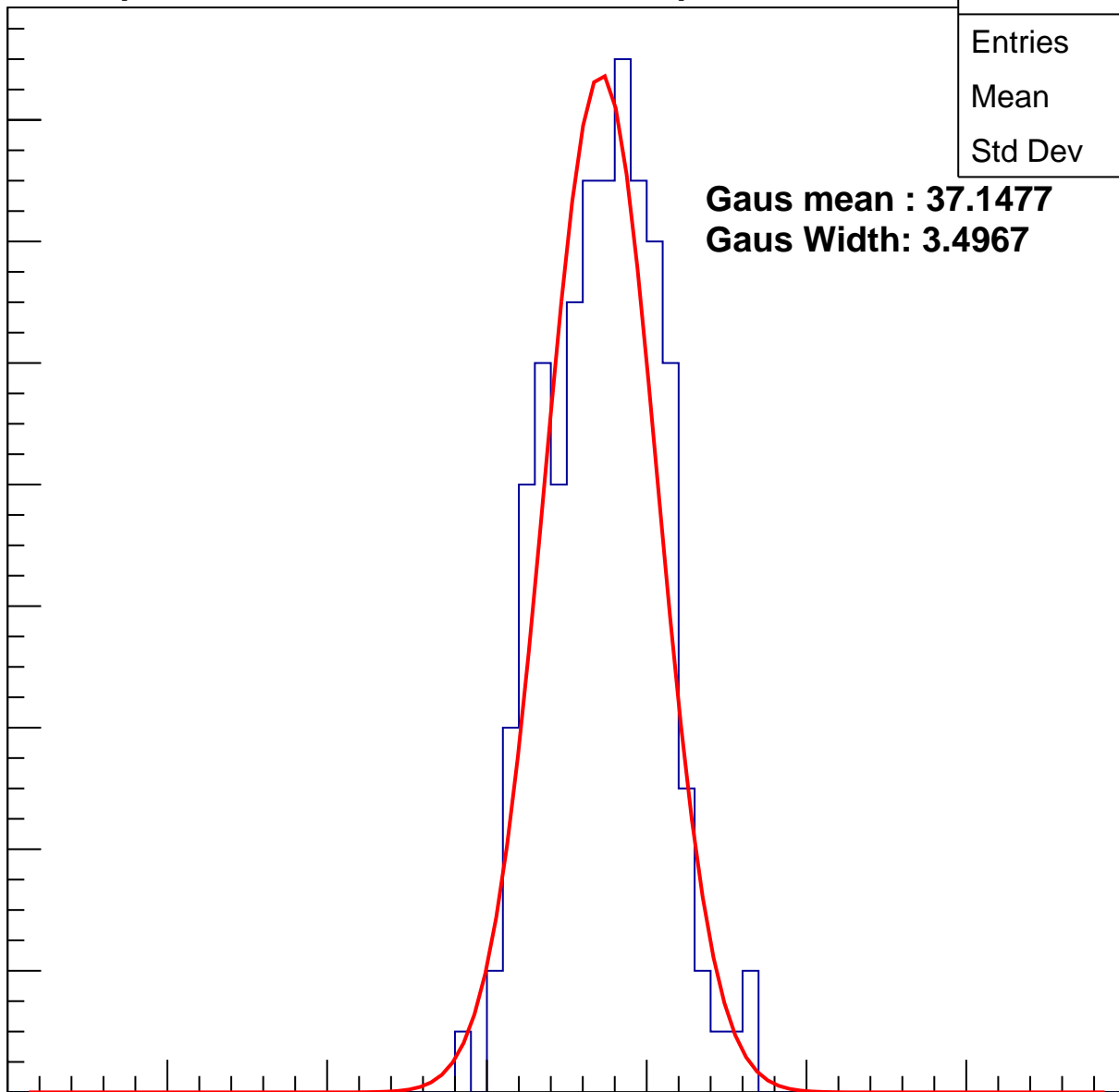
40

50

60

70

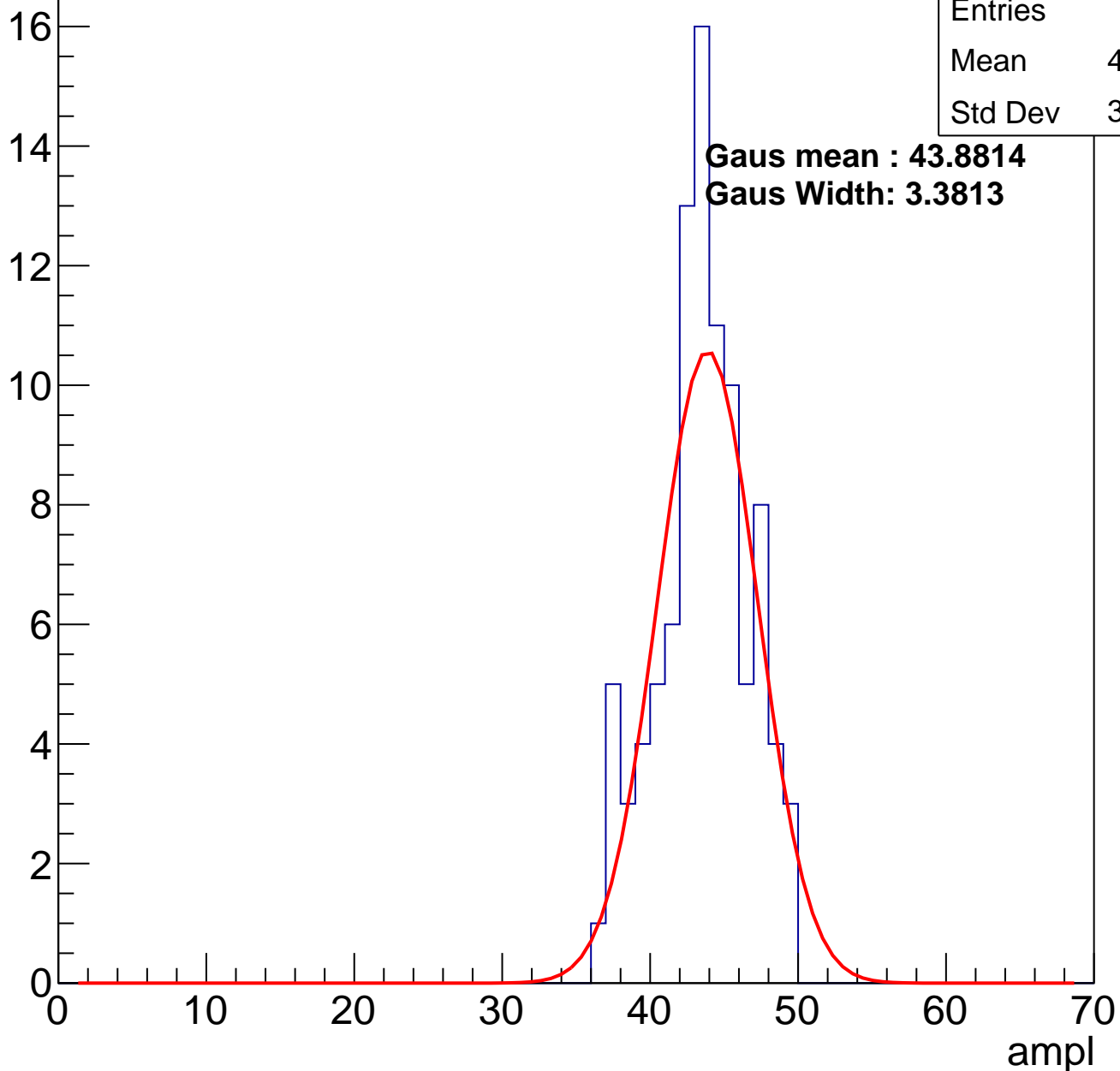
ampl



B1L001S, U24-ch76, adc2

calib_packv5_042523_0143.root, FC#2, port C2

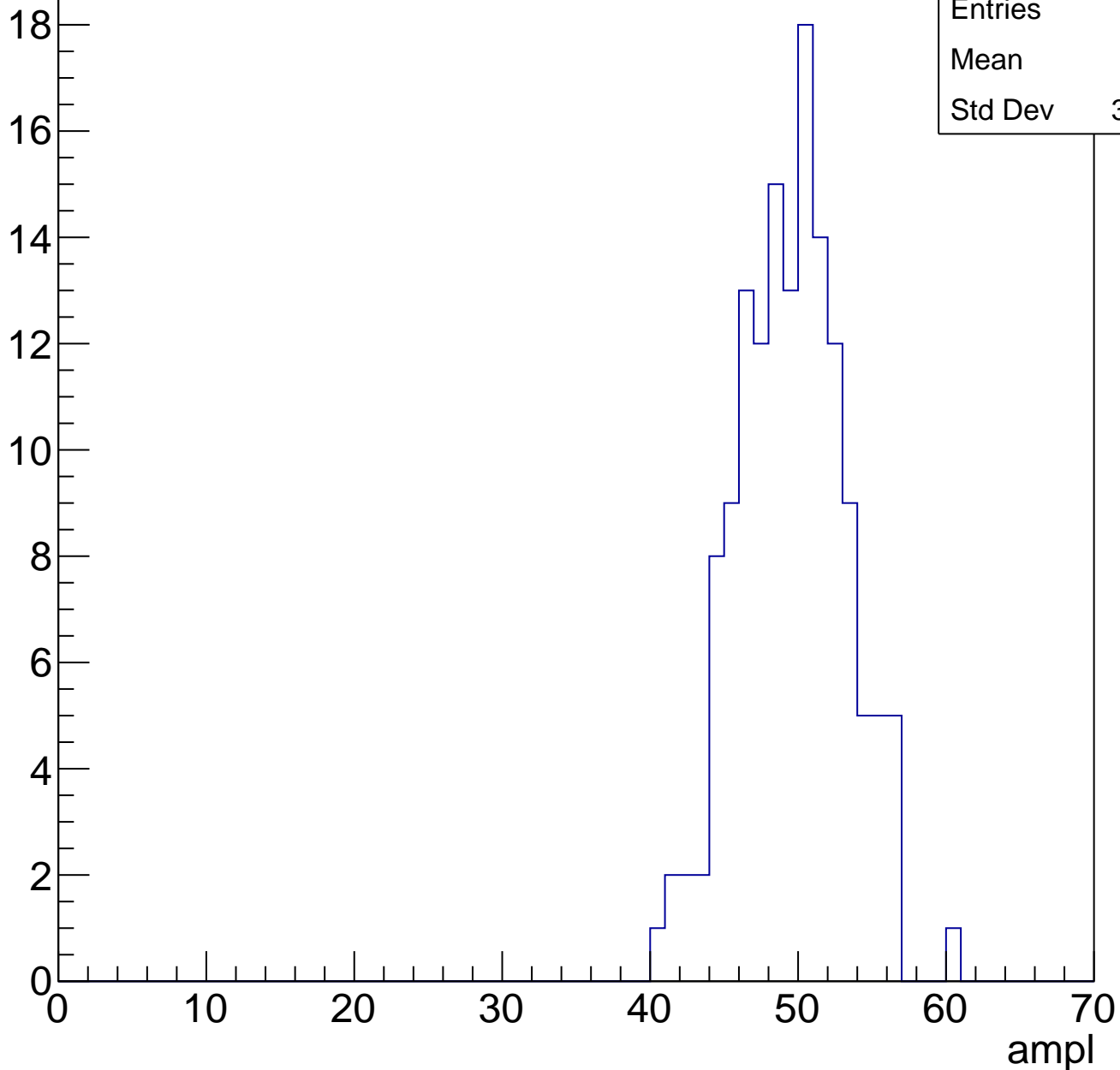
Entry



B1L001S, U24-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	90
Mean	55.18
Std Dev	3.108

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch76, adc5

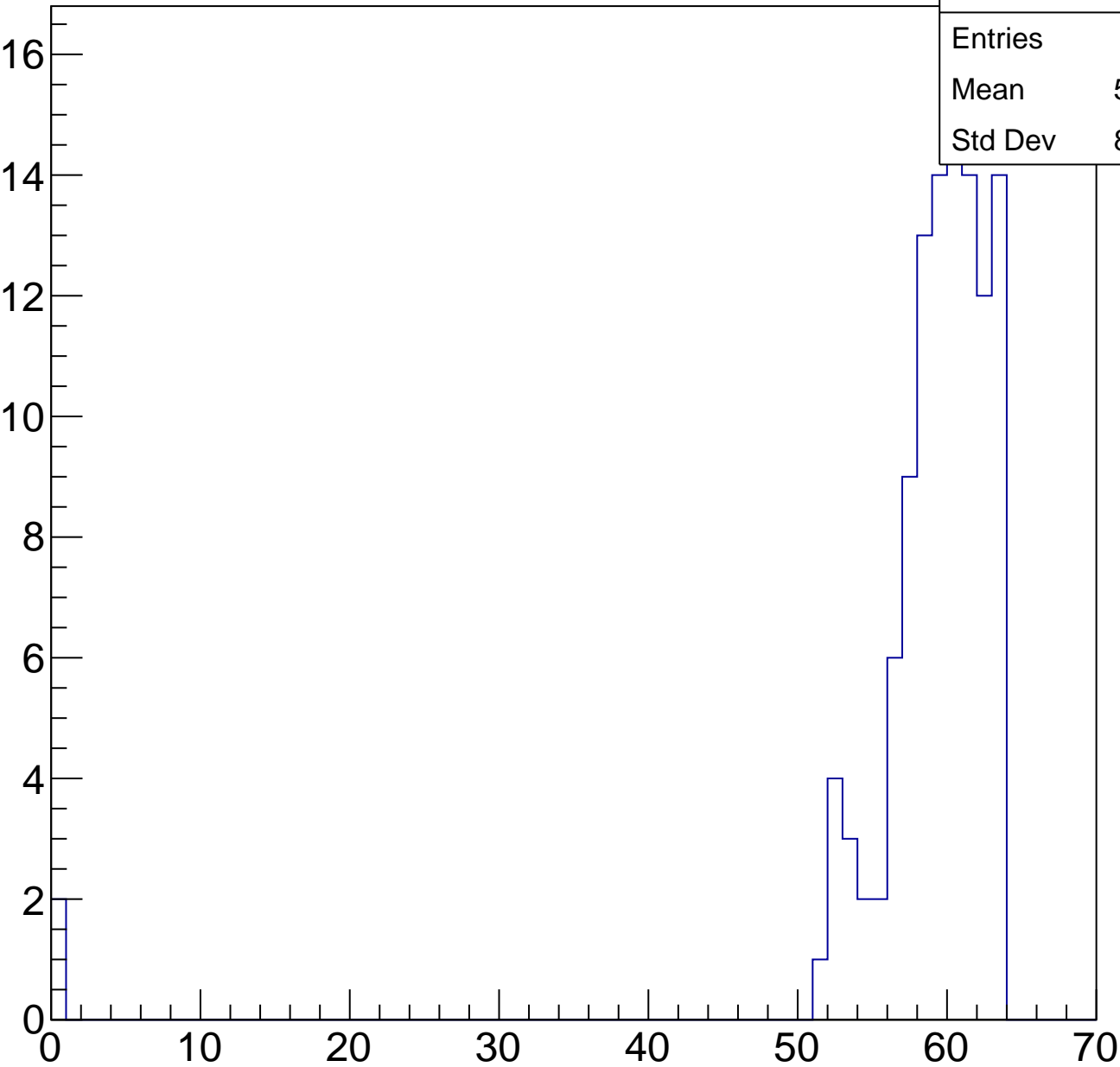
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.08
Std Dev	8.358

ampl

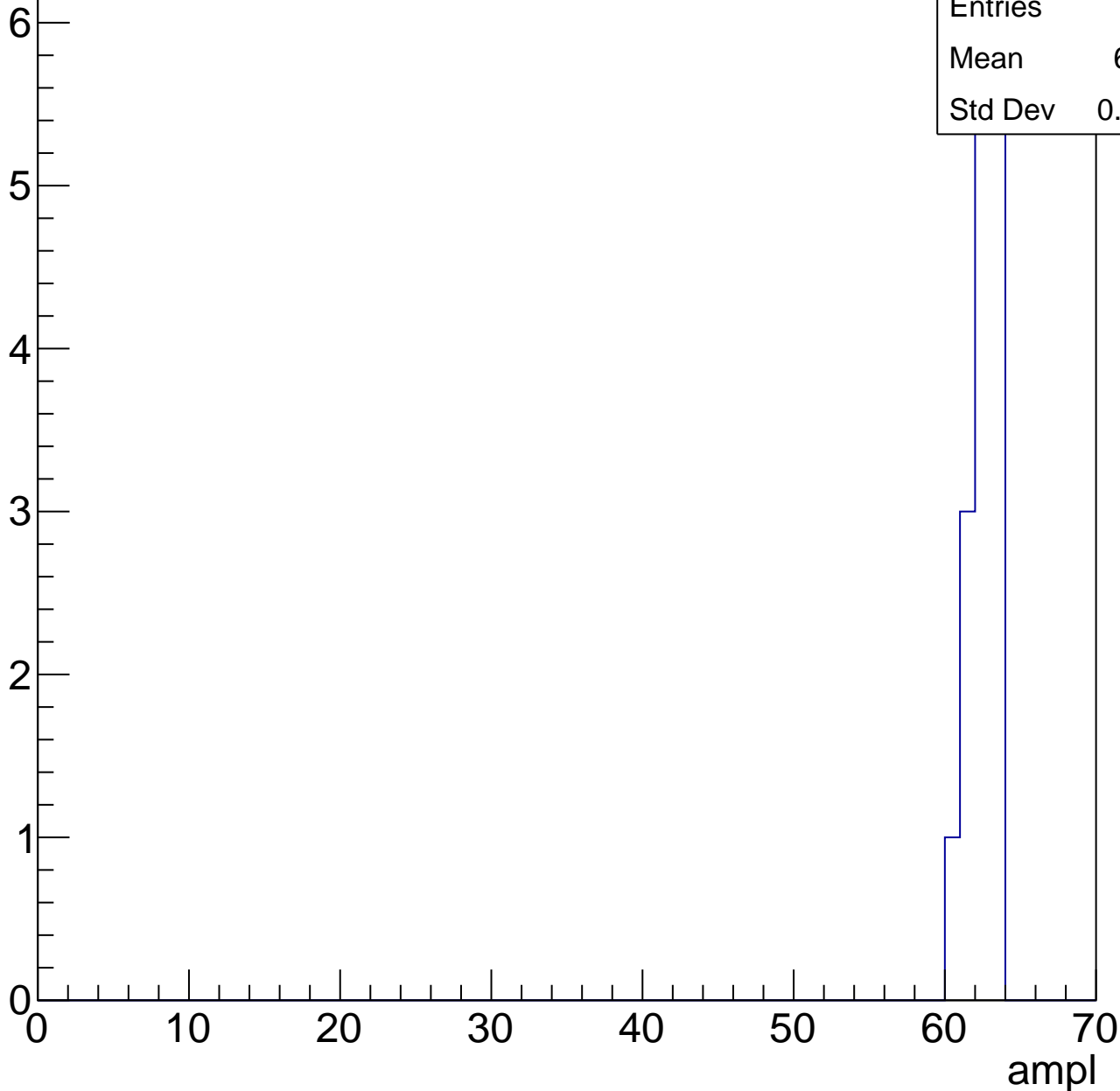


B1L001S, U24-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

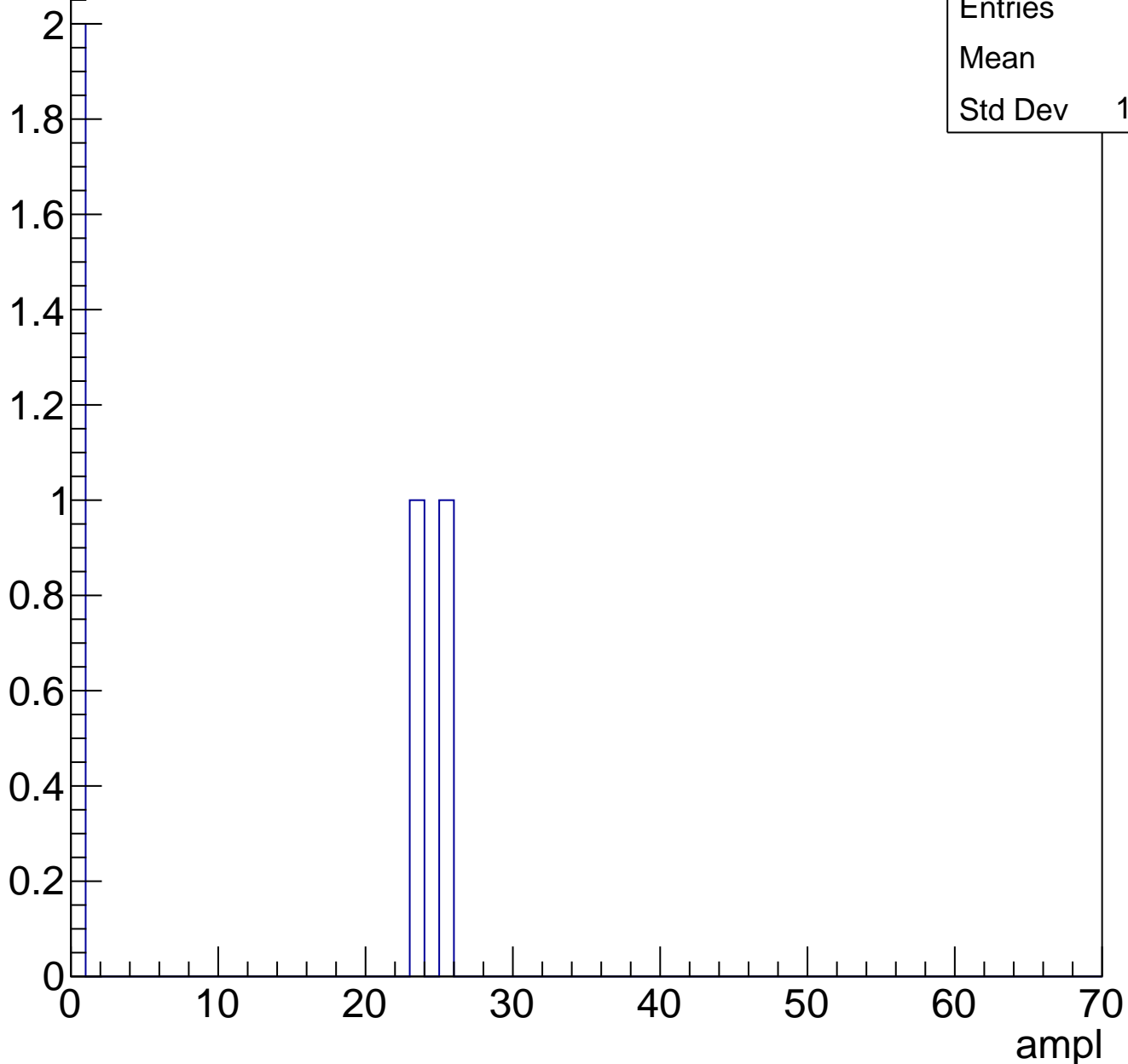
Entries	16
Mean	62.06
Std Dev	0.8992



B1L001S, U24-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	12
Std Dev	12.02

B1L001S, U24-ch77, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	30.67
Std Dev	3.165

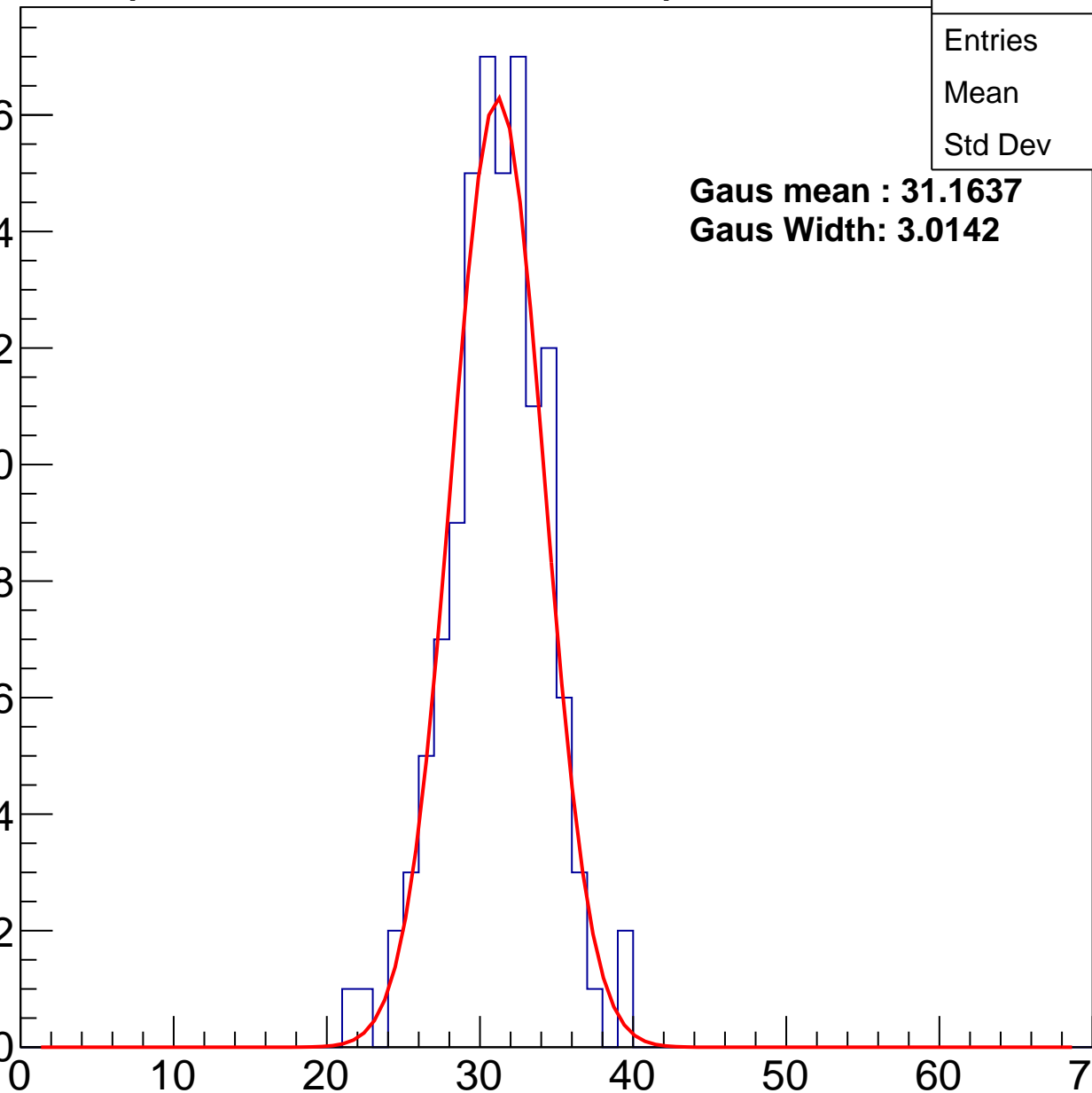
Gaus mean : 31.1637

Gaus Width: 3.0142

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch77, adc1

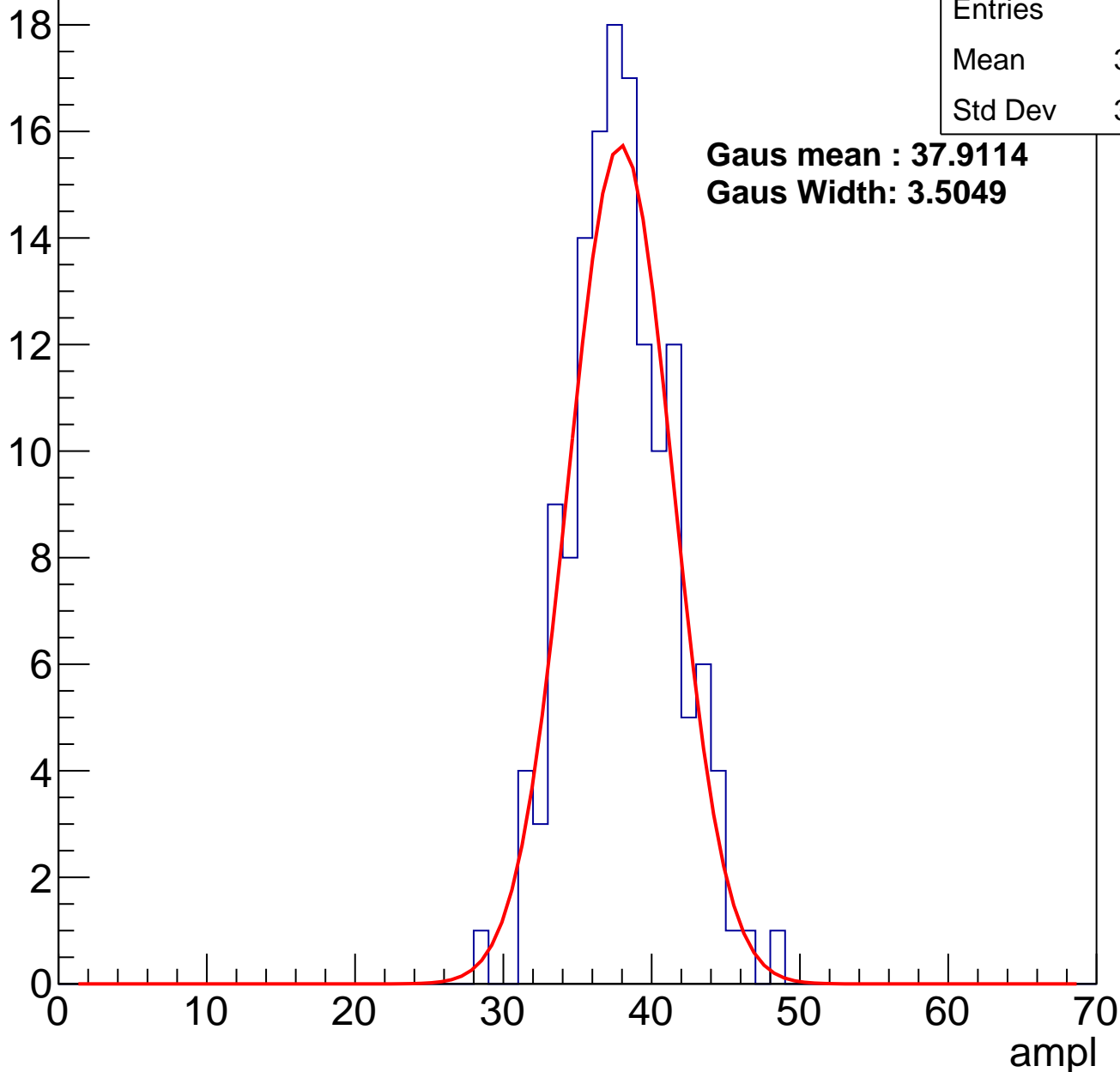
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	37.59
Std Dev	3.448

Gaus mean : 37.9114

Gaus Width: 3.5049

Entry



B1L001S, U24-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	141
Mean	44.4
Std Dev	3.507

Gaus mean : 44.8943

Gaus Width: 3.5991

0

10

20

30

40

50

60

70

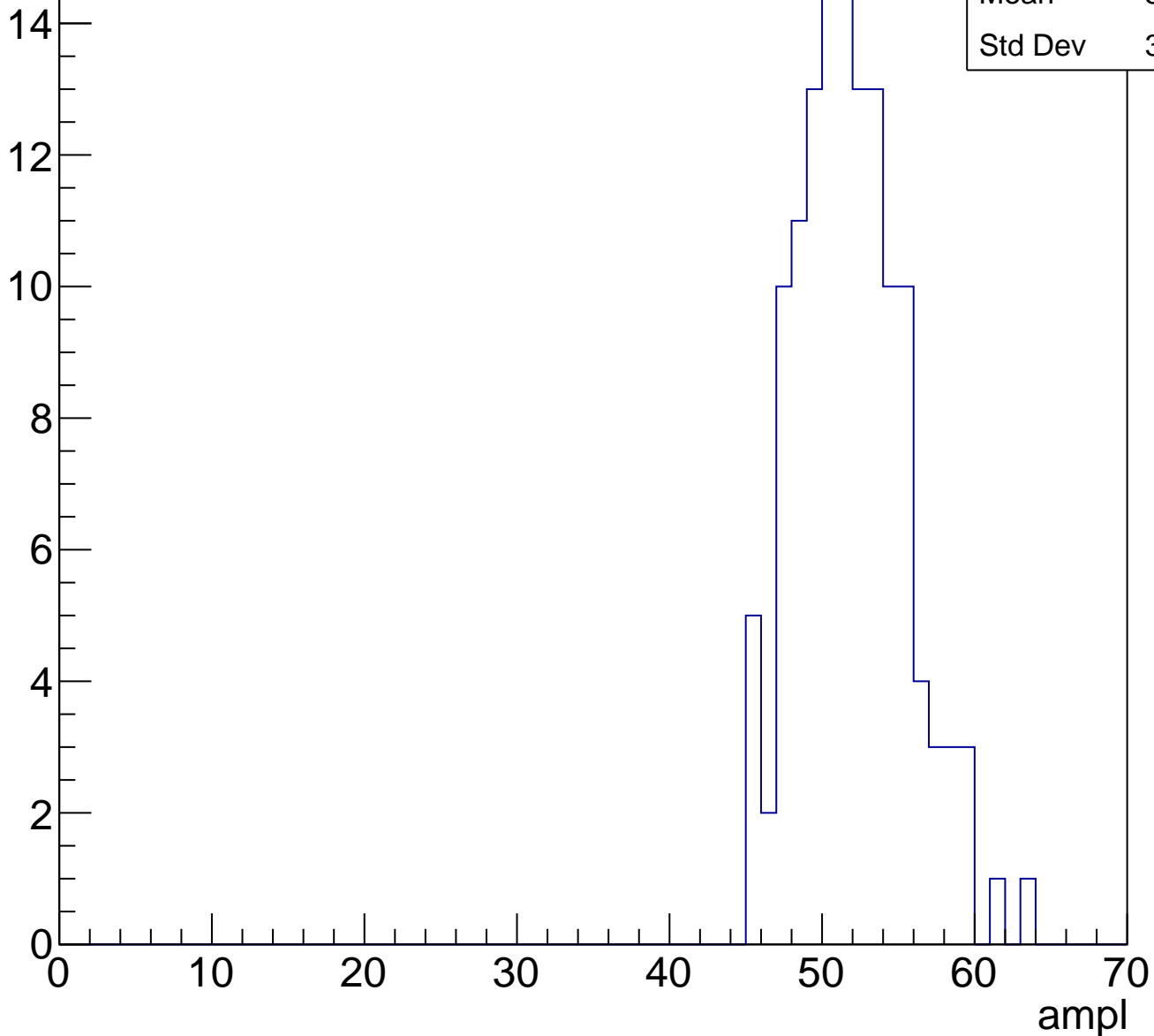
ampl

B1L001S, U24-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	51.45
Std Dev	3.519

Entry



B1L001S, U24-ch77, adc4

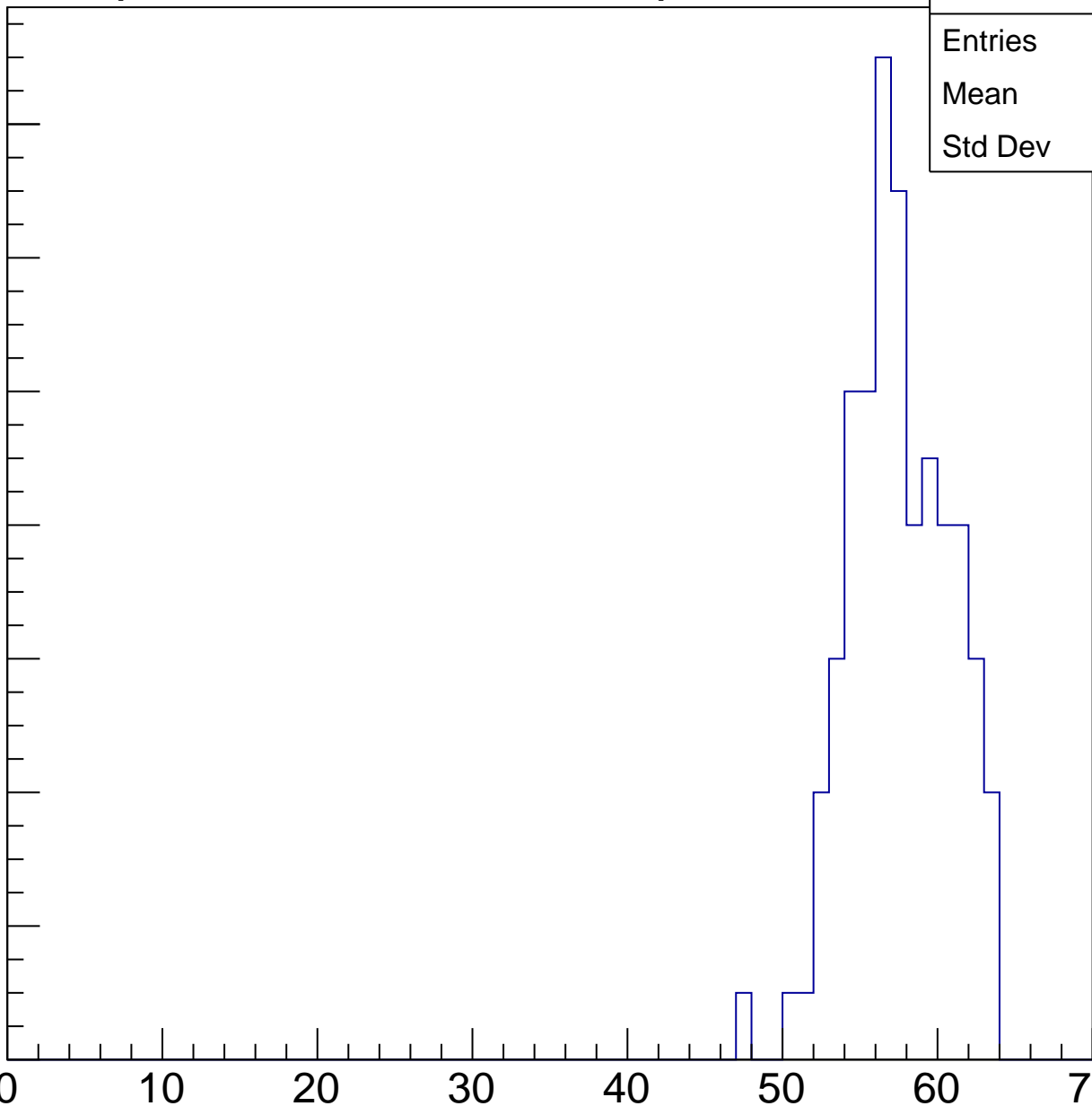
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	57.04
Std Dev	3.198

ampl



B1L001S, U24-ch77, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

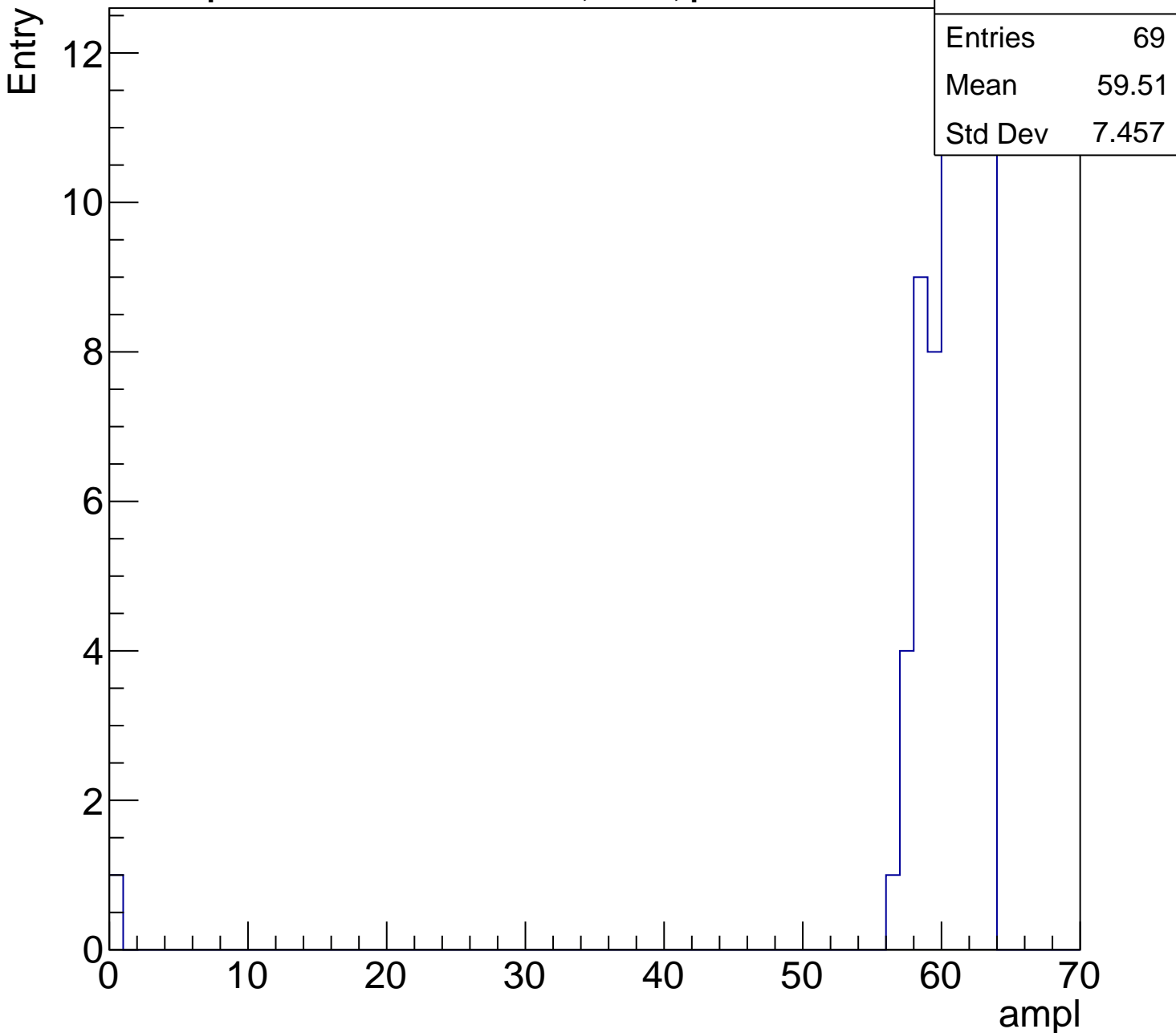
2

0

Entries	69
Mean	59.51
Std Dev	7.457

ampl

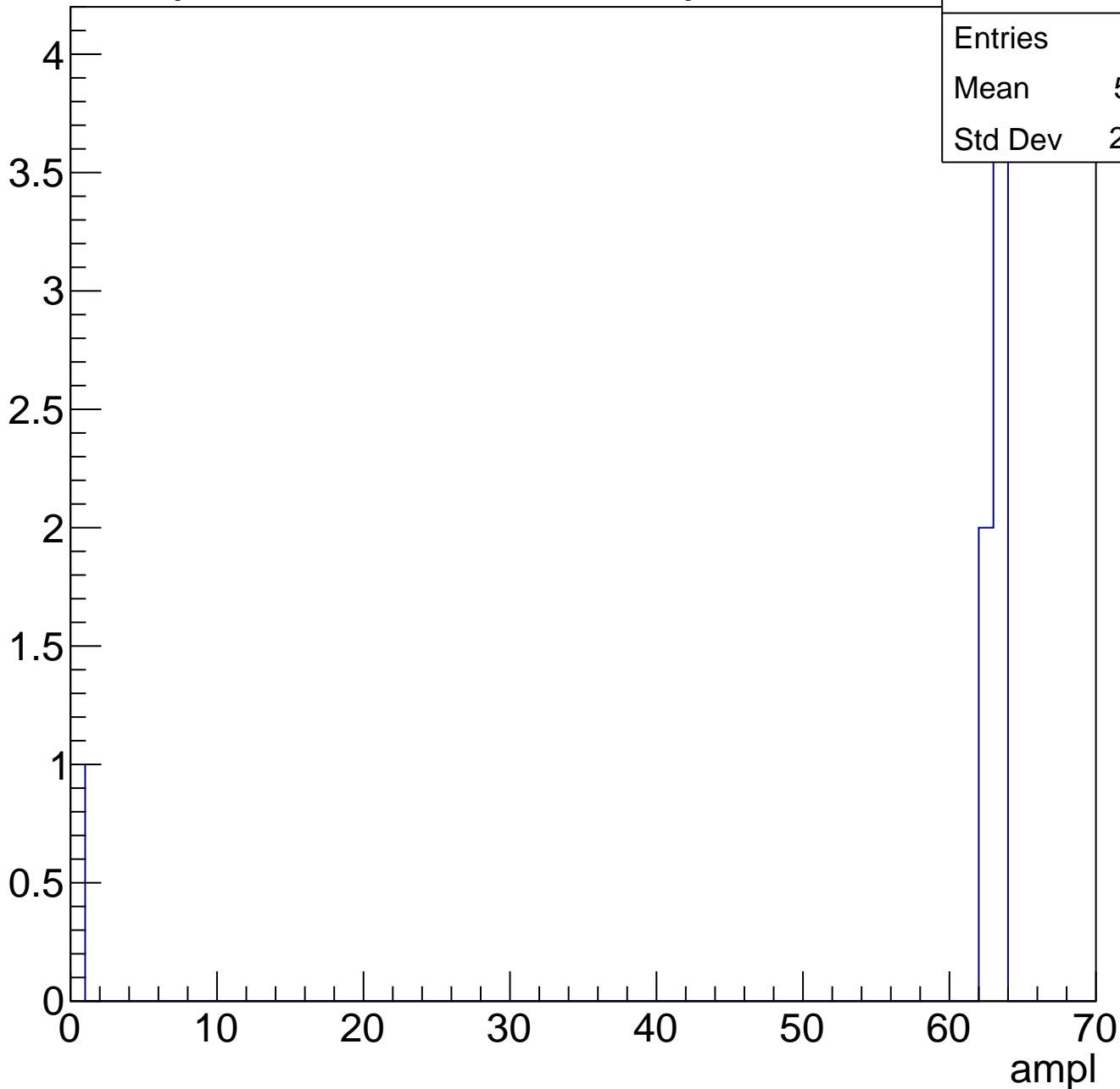
0 10 20 30 40 50 60 70



B1L001S, U24-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.02
Std Dev	4.095

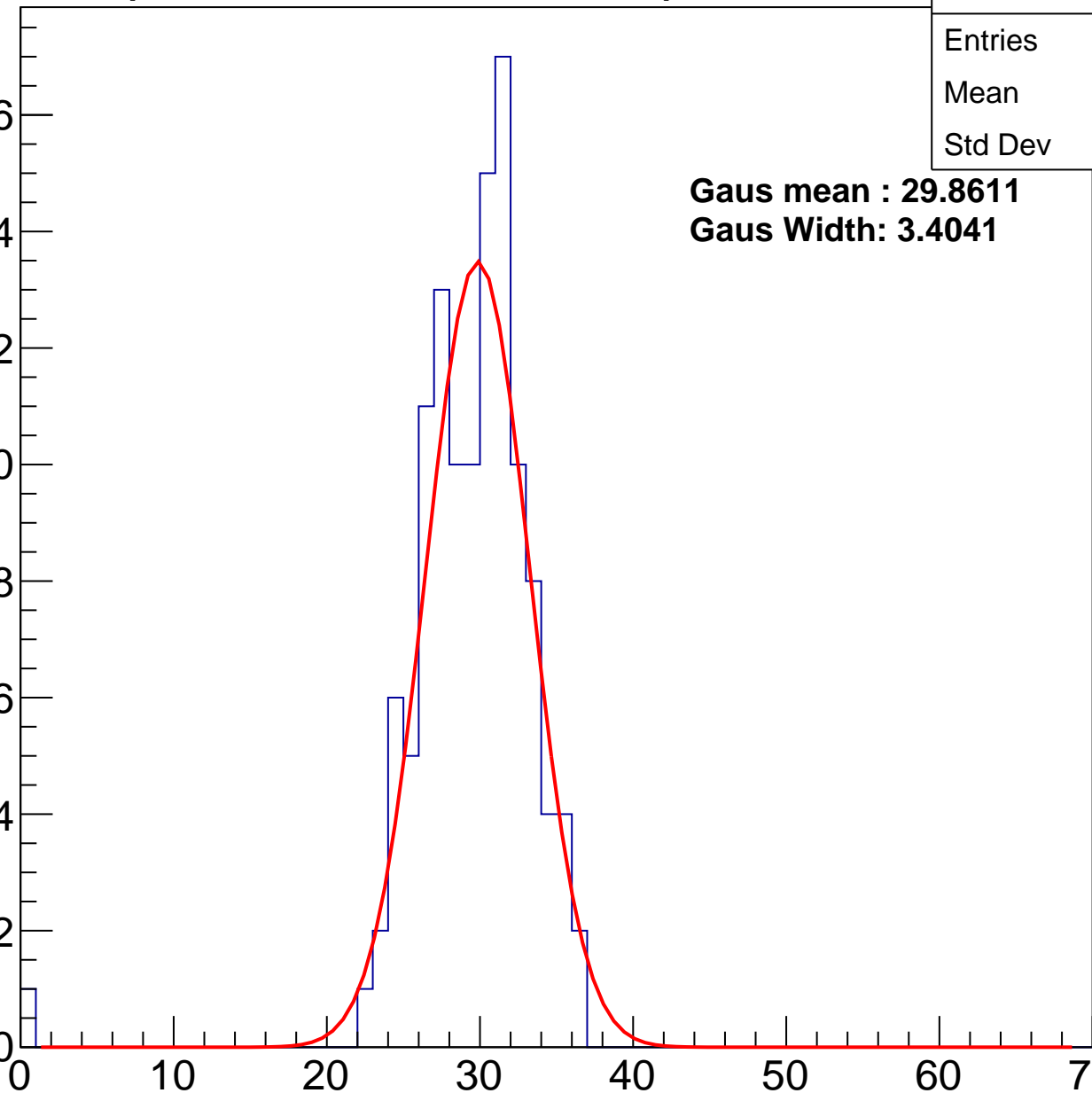
Gaus mean : 29.8611

Gaus Width: 3.4041

Entry

16
14
12
10
8
6
4
2
0

ampl



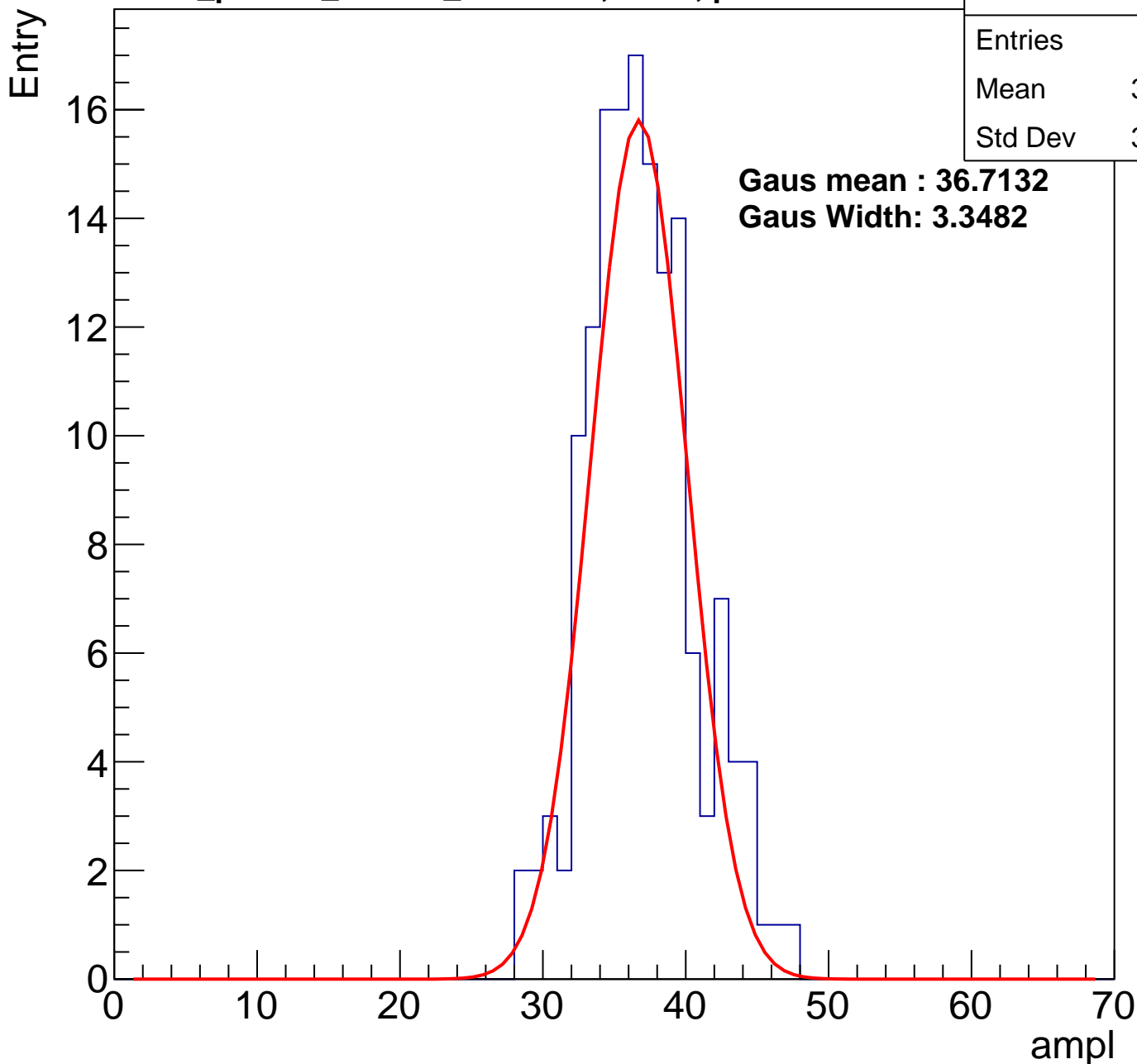
B1L001S, U24-ch78, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.48
Std Dev	3.725

Gaus mean : 36.7132

Gaus Width: 3.3482



B1L001S, U24-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	42.41
Std Dev	3.049

Gaus mean : 42.7404

Gaus Width: 3.1259

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

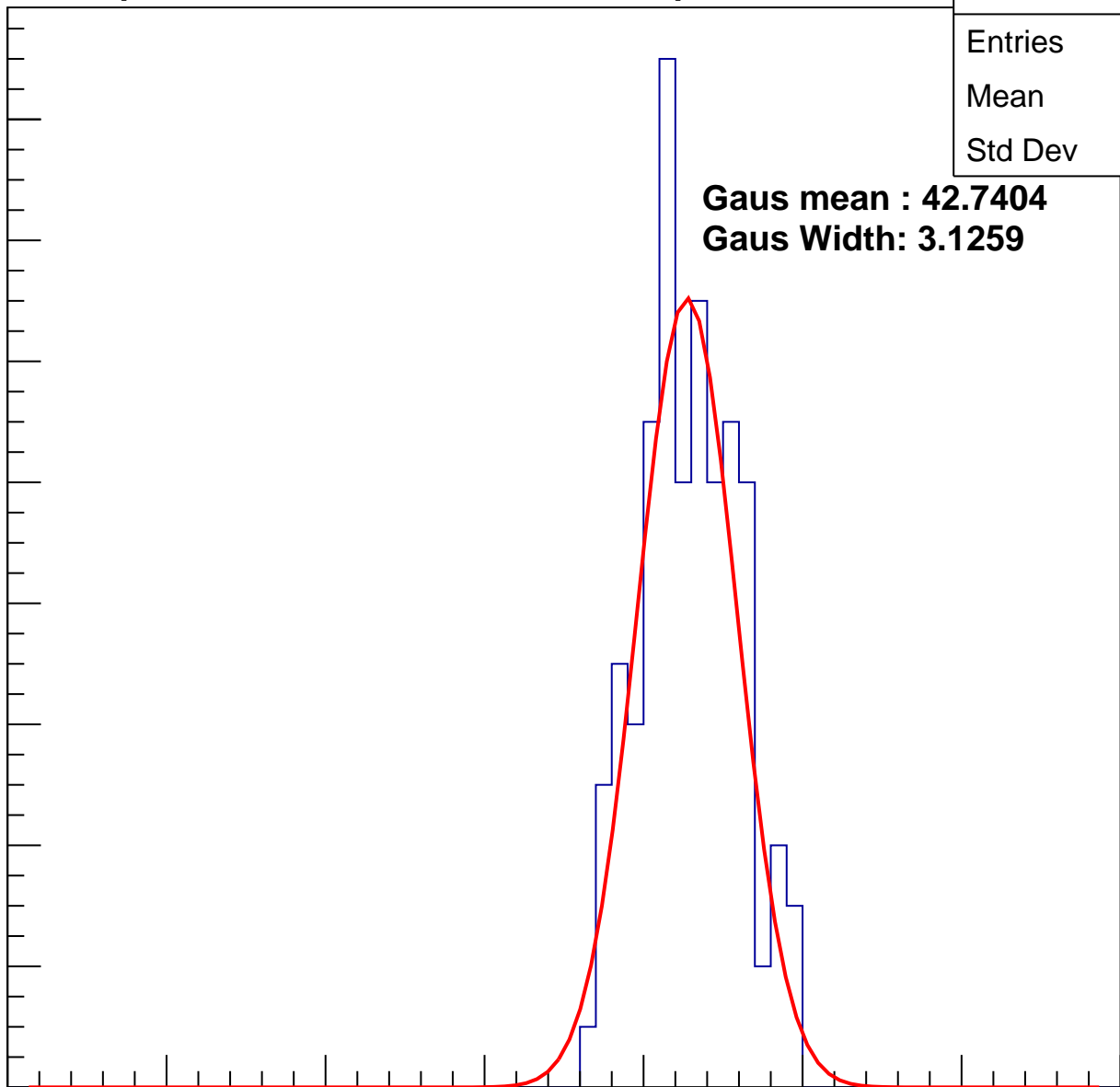
40

50

60

70

ampl

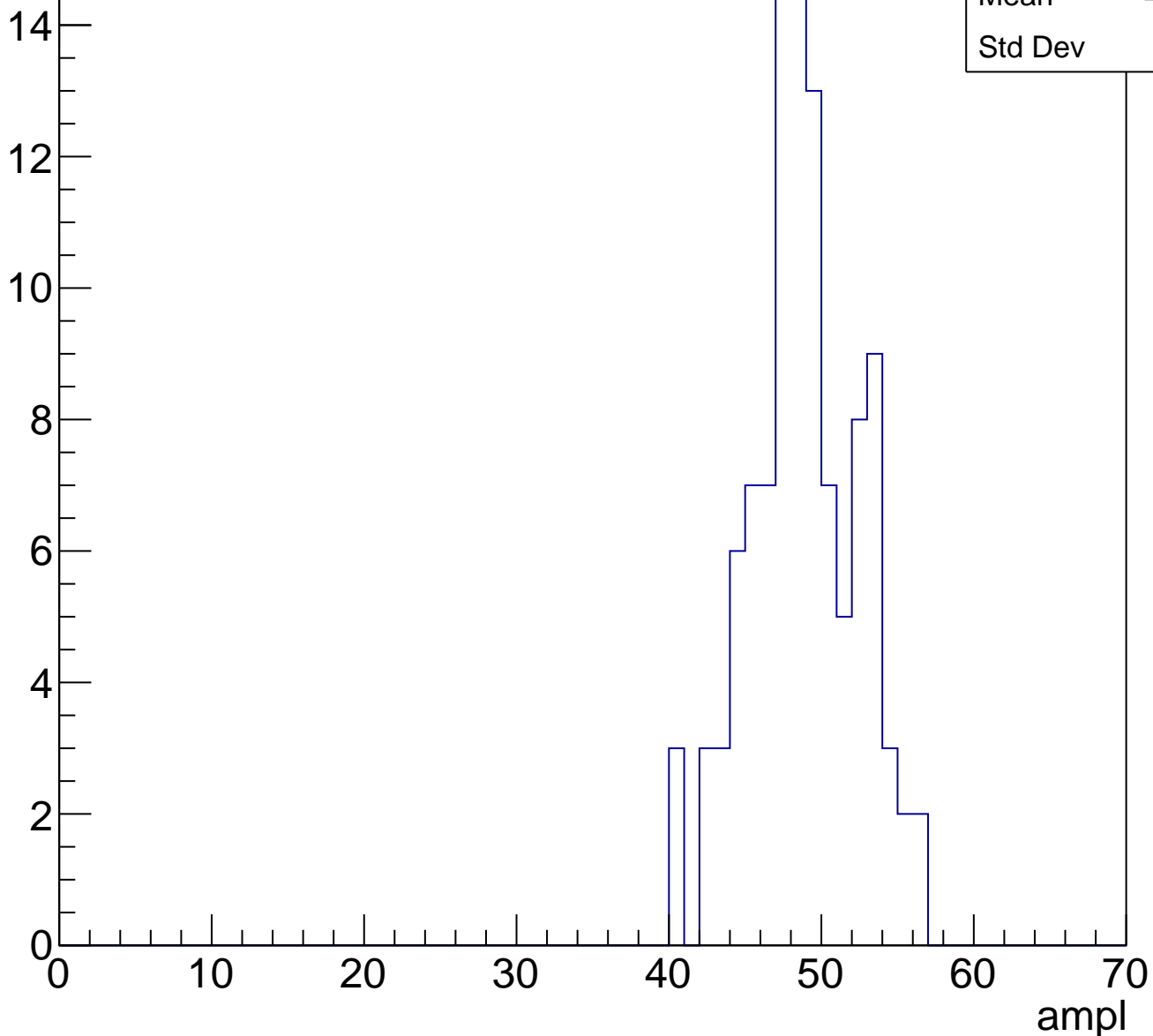


B1L001S, U24-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	48.33
Std Dev	3.52

Entry



B1L001S, U24-ch78, adc4

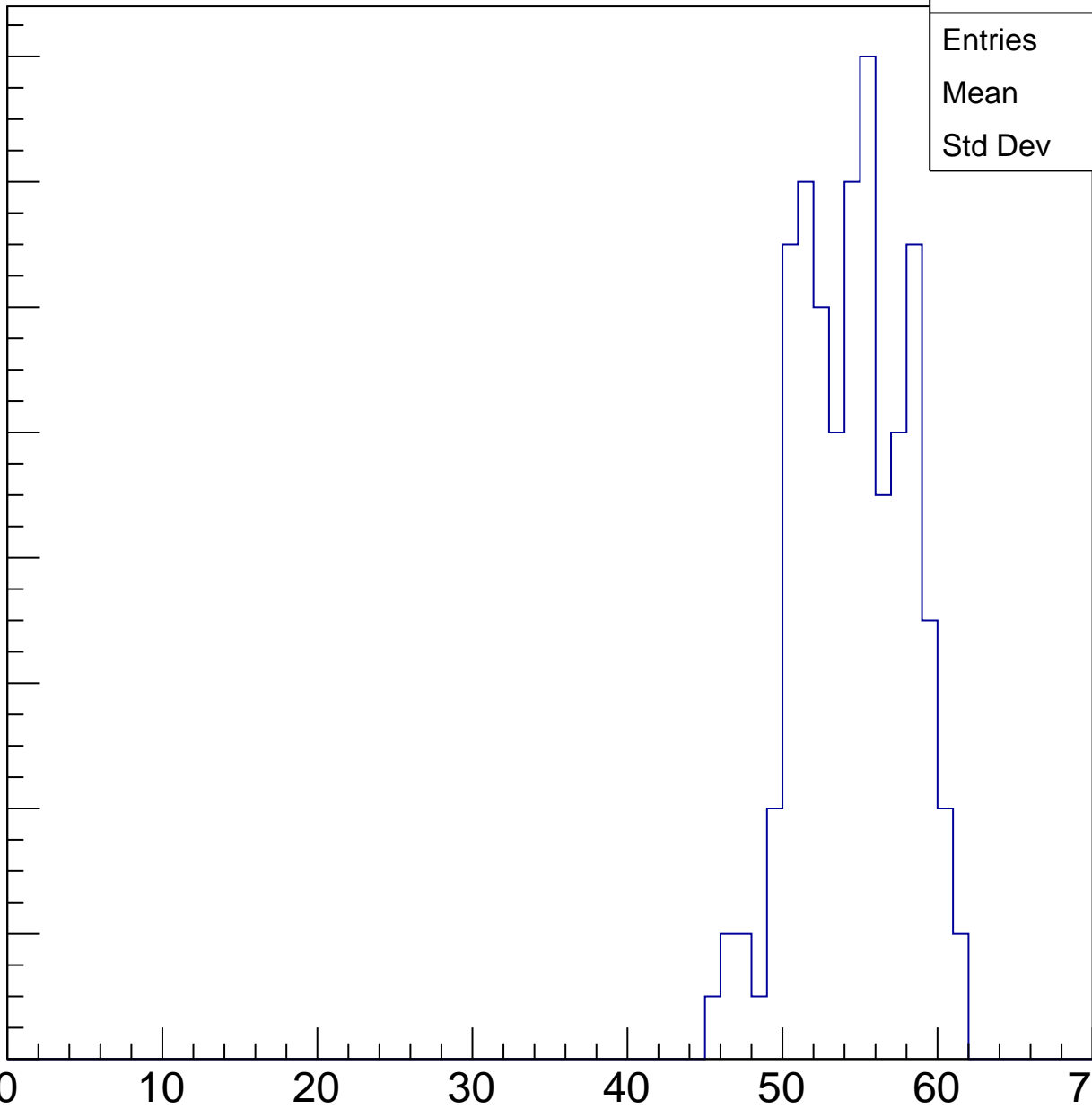
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	53.97
Std Dev	3.458

ampl



B1L001S, U24-ch78, adc5

calib_packv5_042523_0143.root, FC#2, port C2

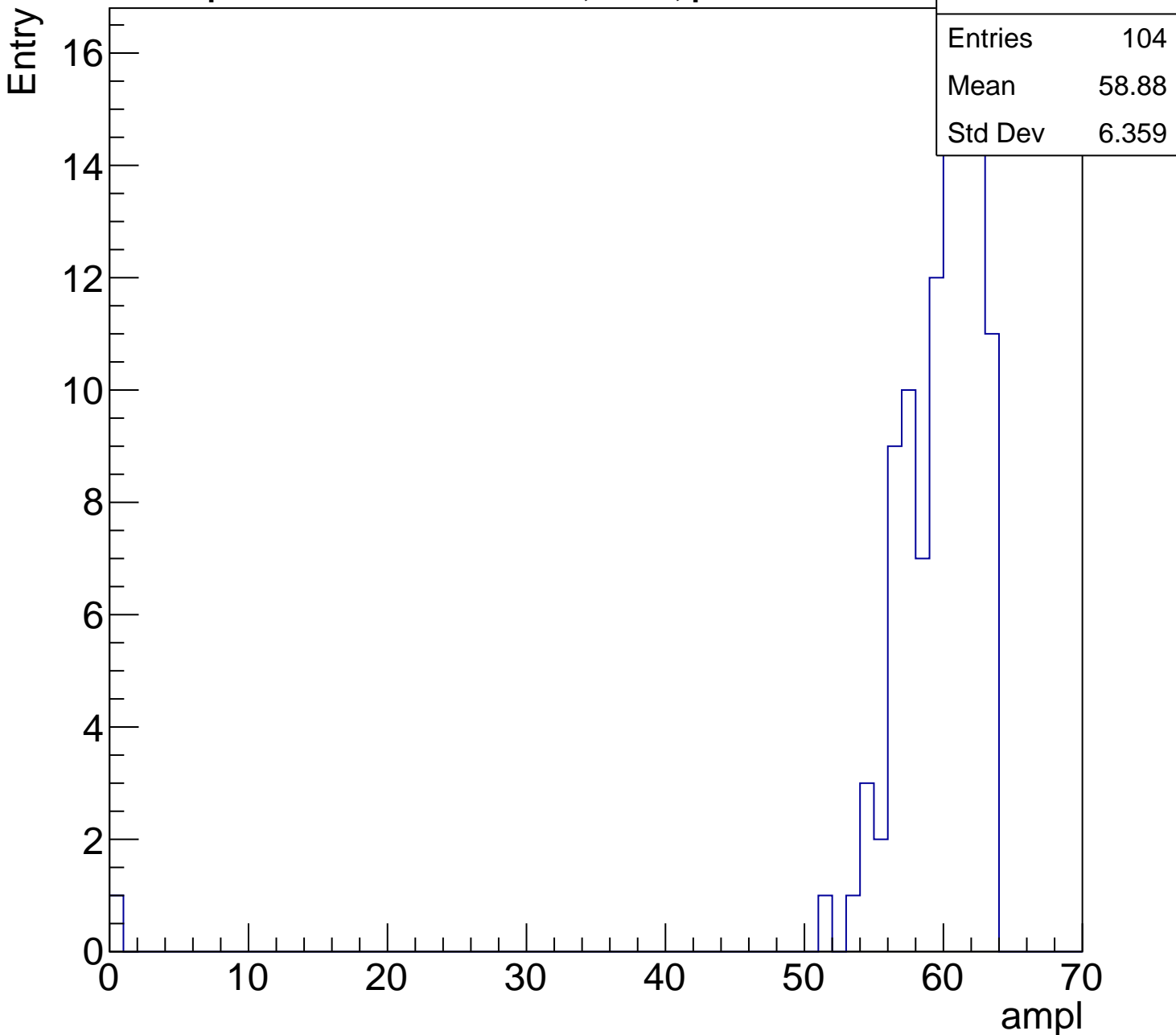
Entries	104
Mean	58.88
Std Dev	6.359

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

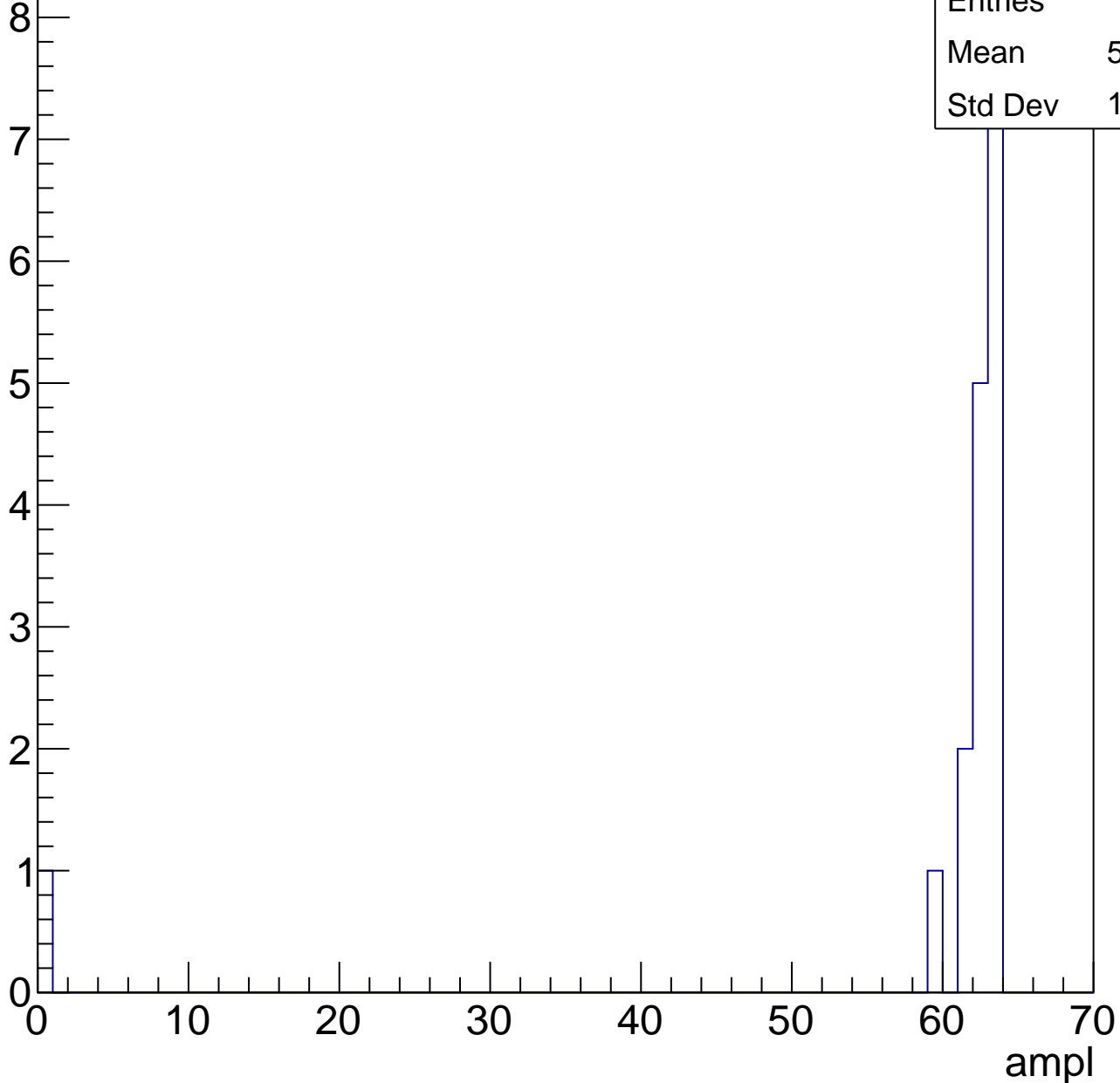


B1L001S, U24-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.53
Std Dev	14.67



B1L001S, U24-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



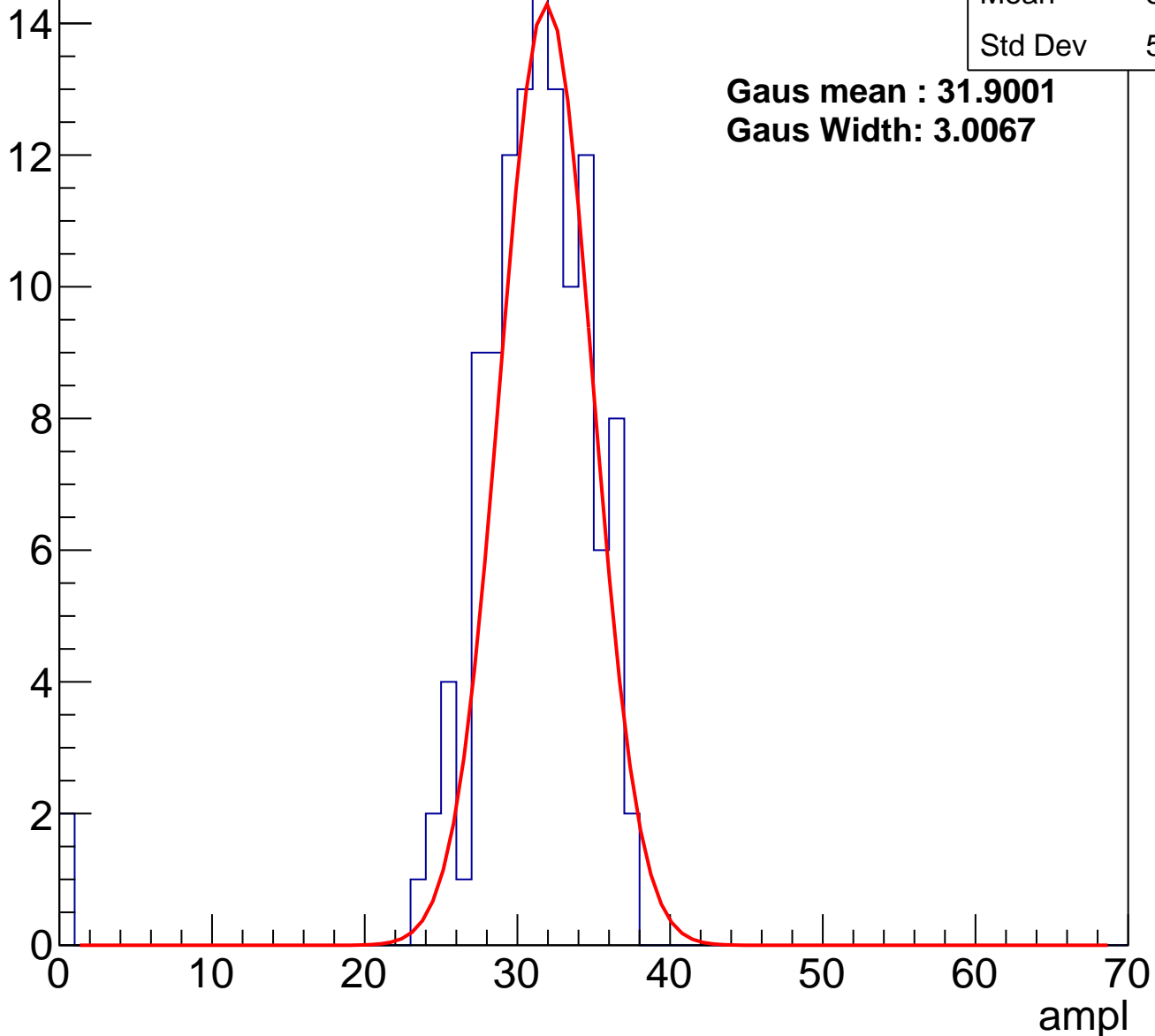
B1L001S, U24-ch79, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	30.43
Std Dev	5.045

Gaus mean : 31.9001
Gaus Width: 3.0067

Entry



B1L001S, U24-ch79, adc1

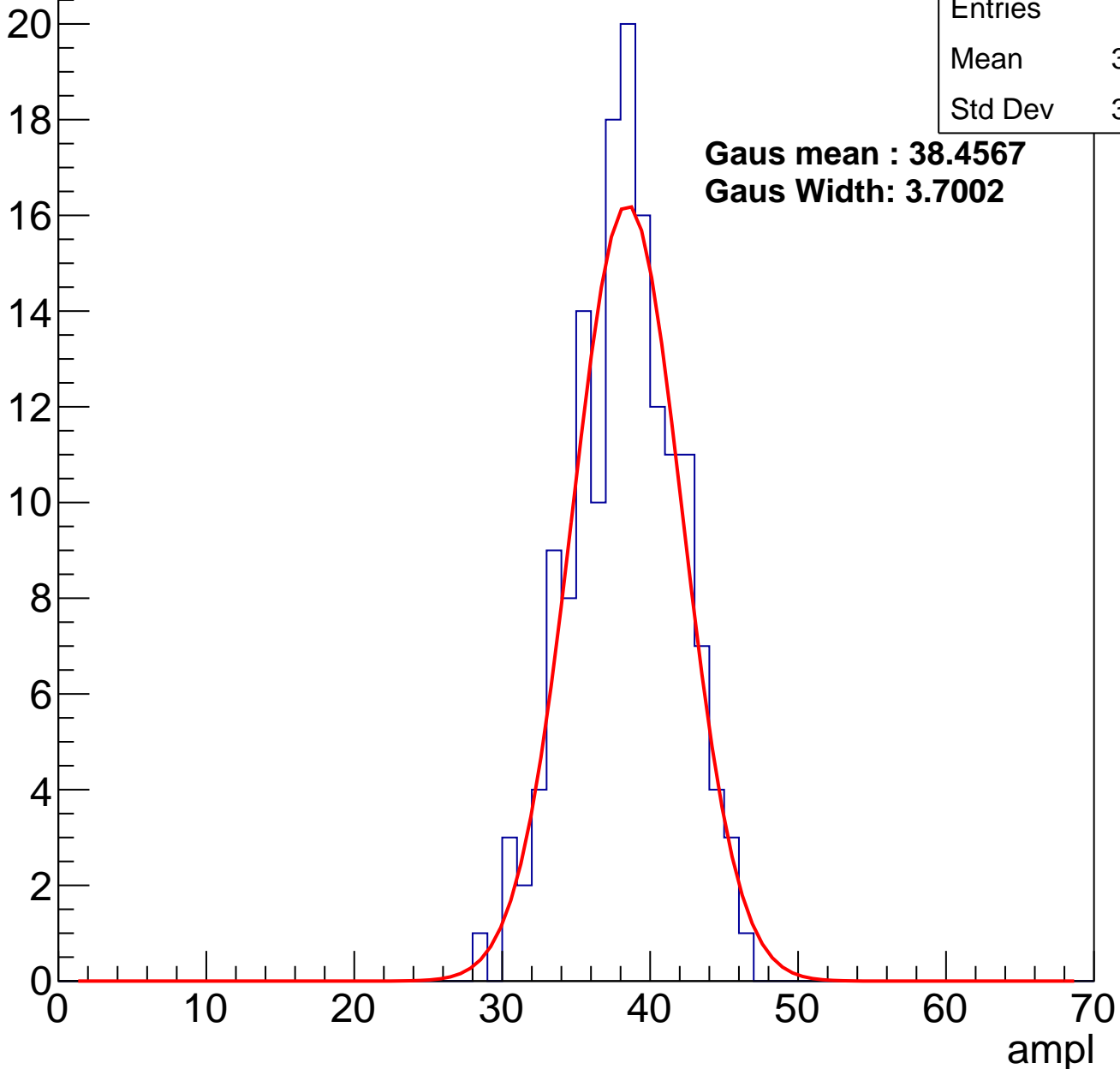
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.84
Std Dev	3.546

Gaus mean : 38.4567

Gaus Width: 3.7002

Entry



B1L001S, U24-ch79, adc2

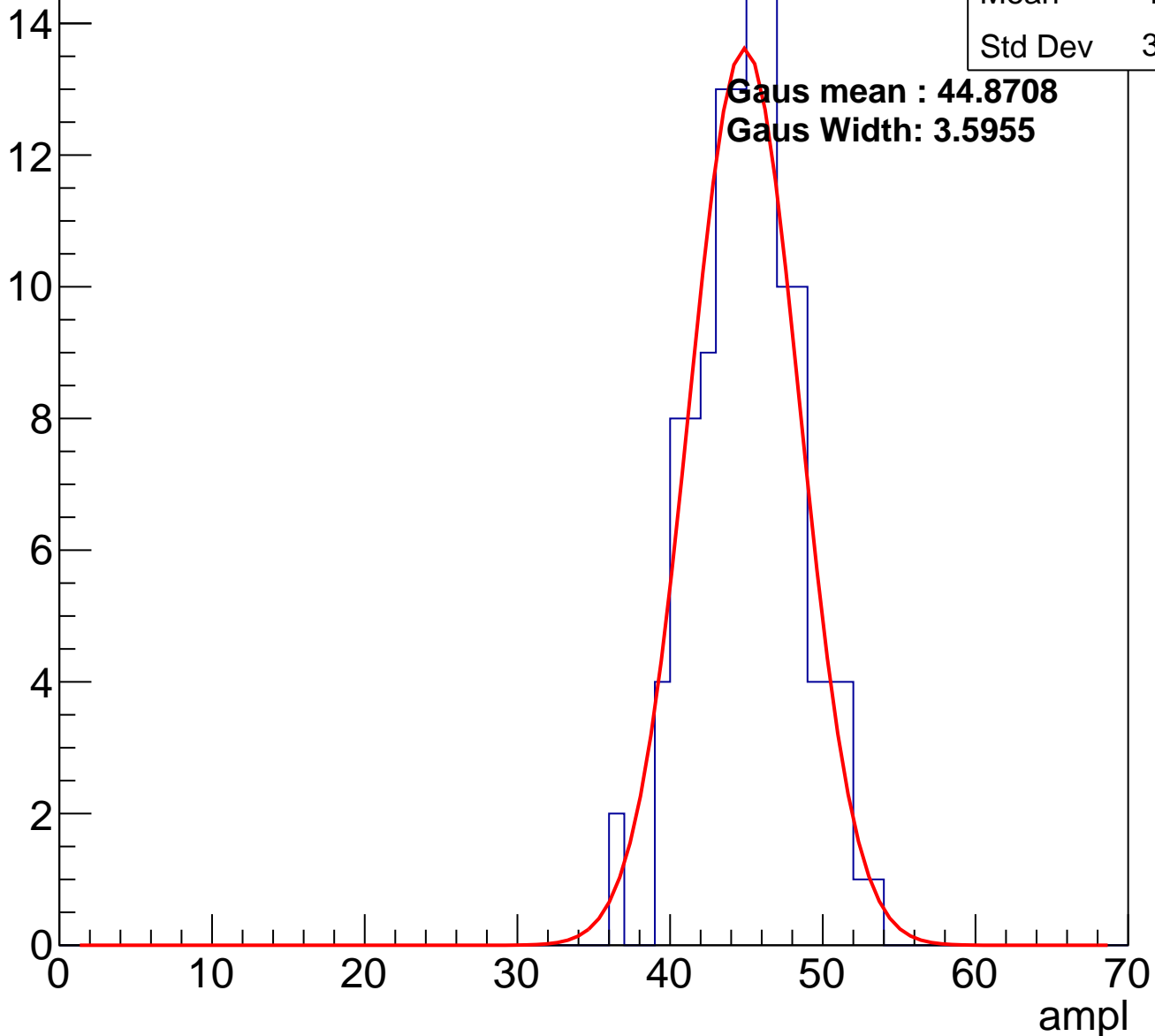
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	44.67
Std Dev	3.319

Gaus mean : 44.8708

Gaus Width: 3.5955

Entry

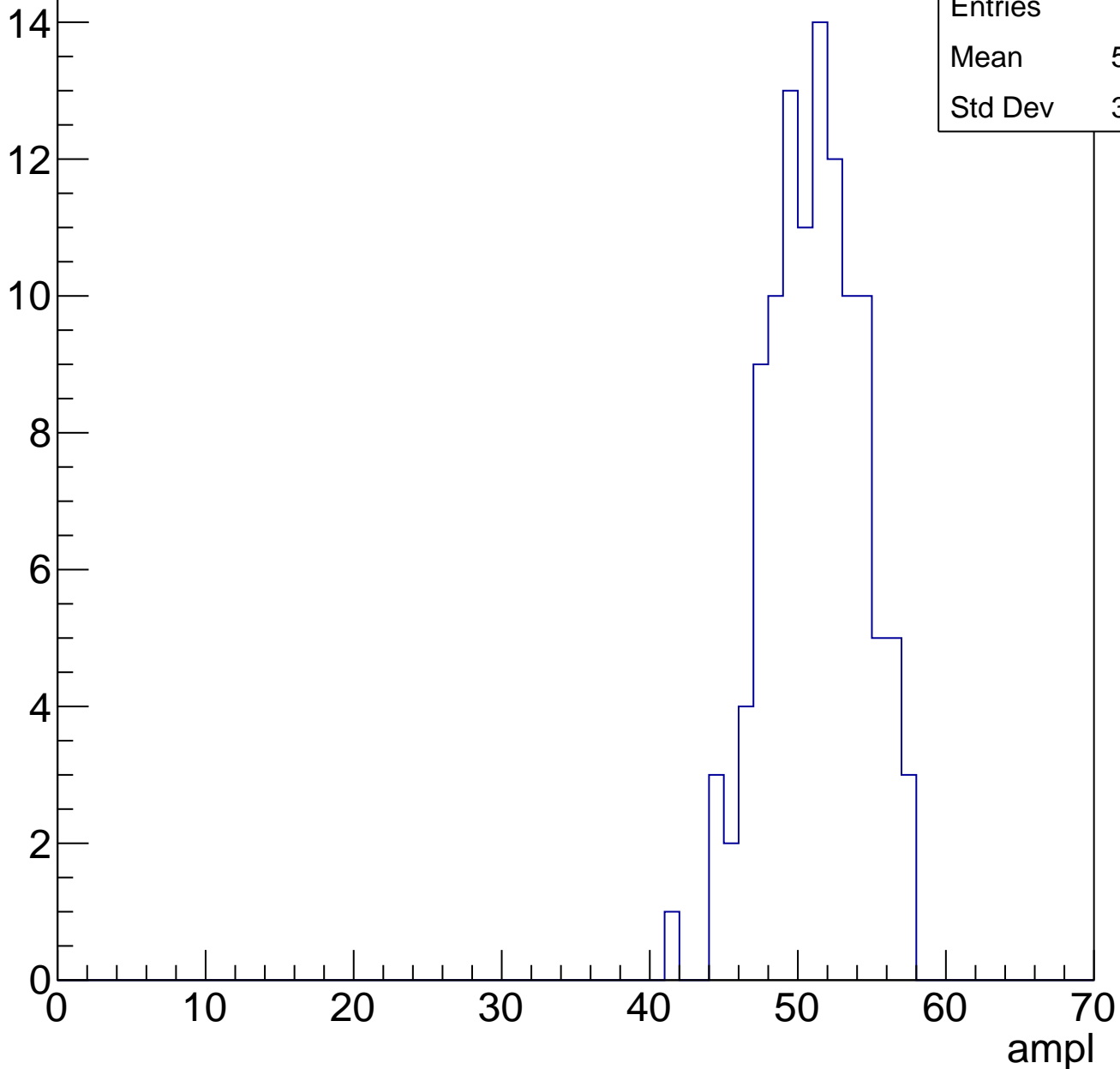


B1L001S, U24-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	50.63
Std Dev	3.213

Entry

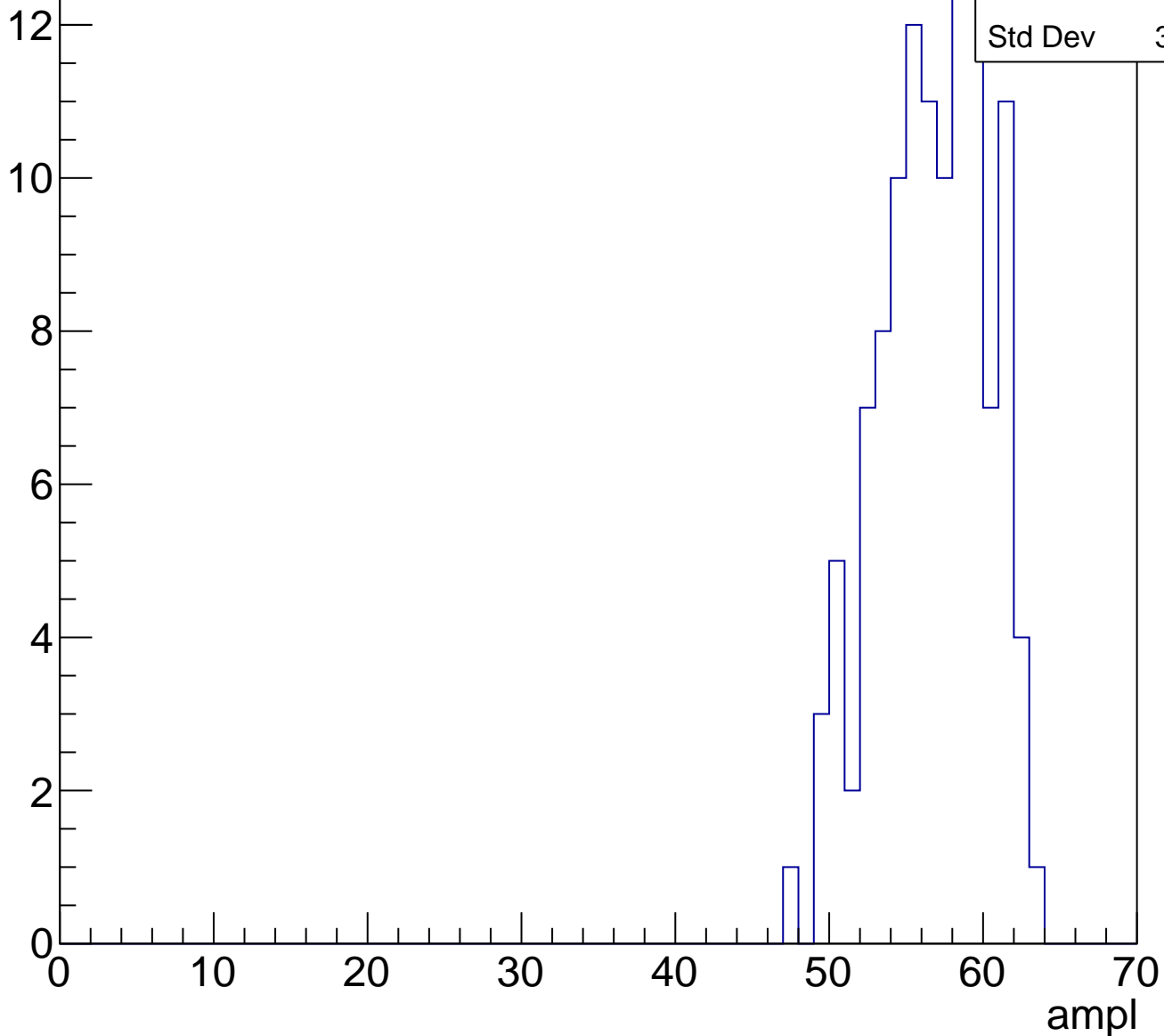


B1L001S, U24-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	56.3
Std Dev	3.497

Entry



B1L001S, U24-ch79, adc5

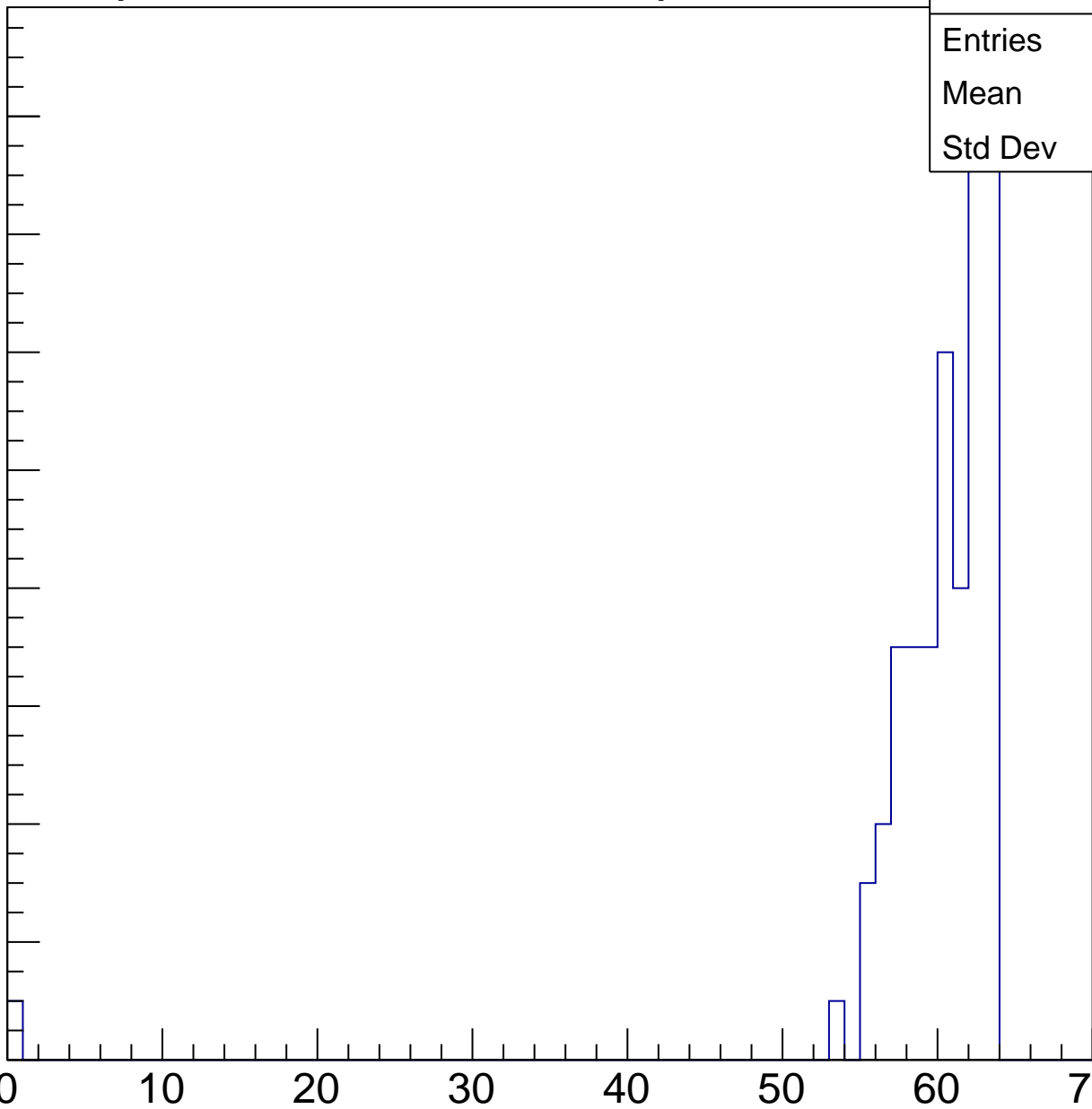
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.41
Std Dev	7.013

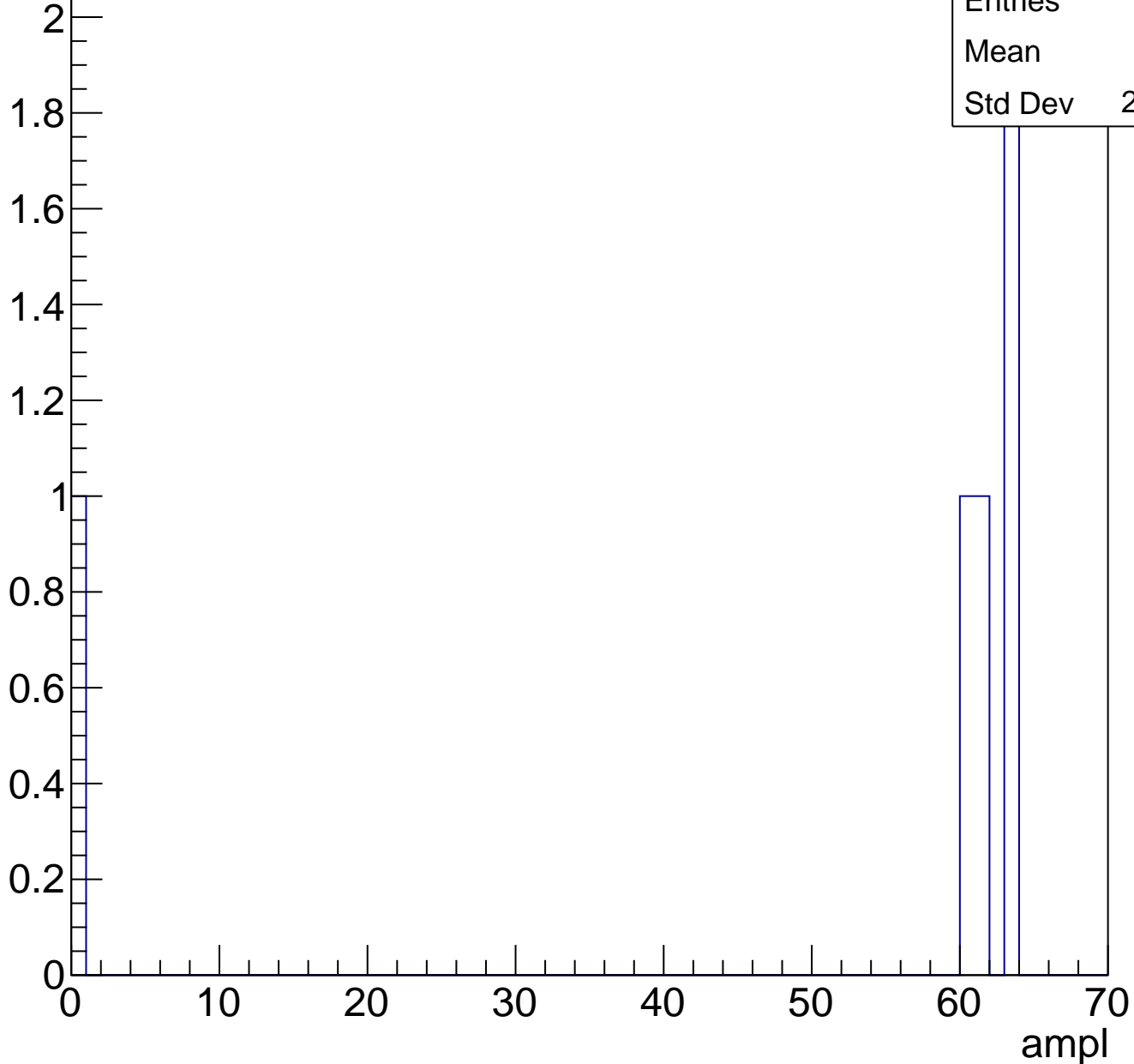
ampl



B1L001S, U24-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U24-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	28
Std Dev	3.255

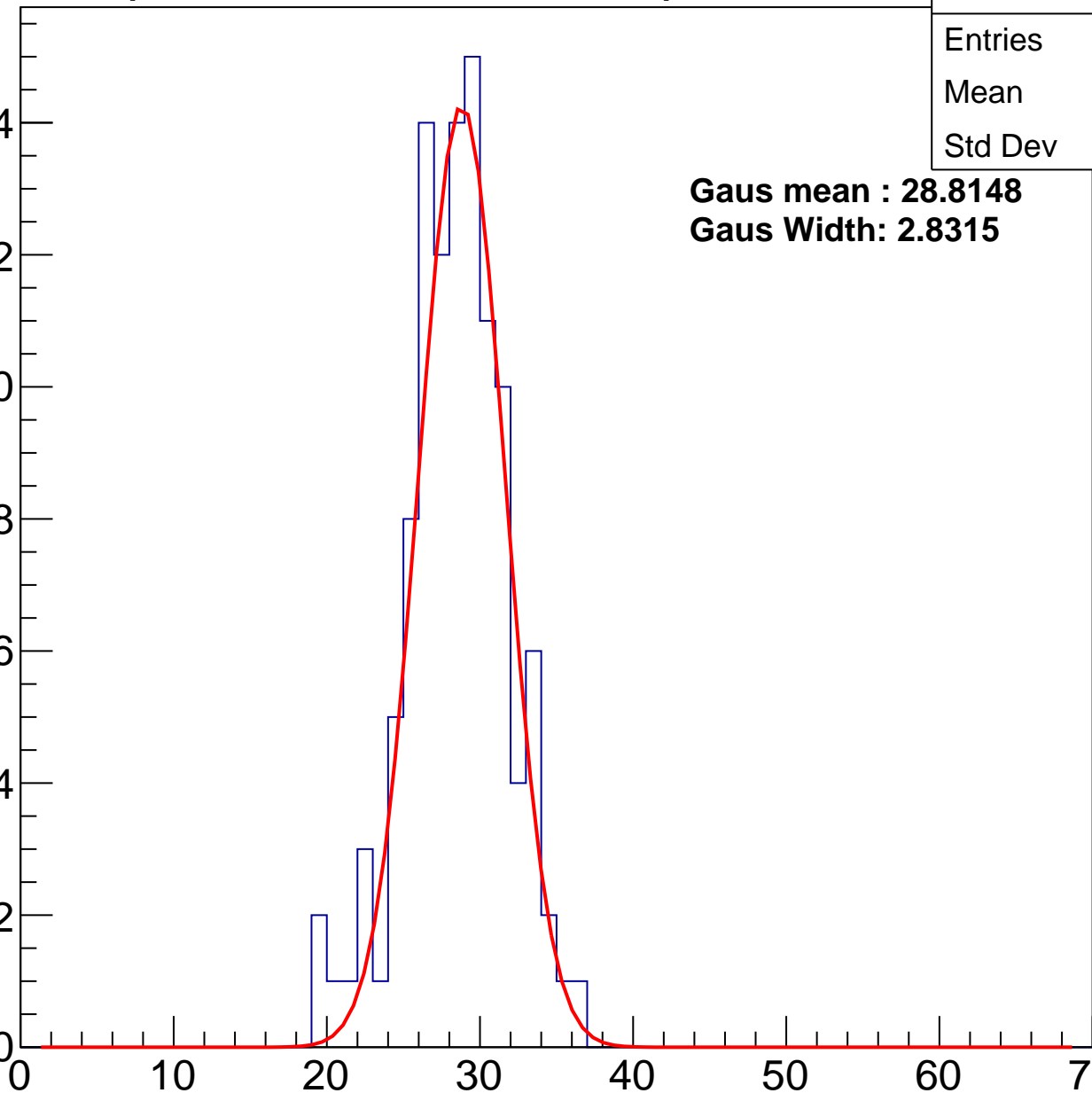
Gaus mean : 28.8148

Gaus Width: 2.8315

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch80, adc1

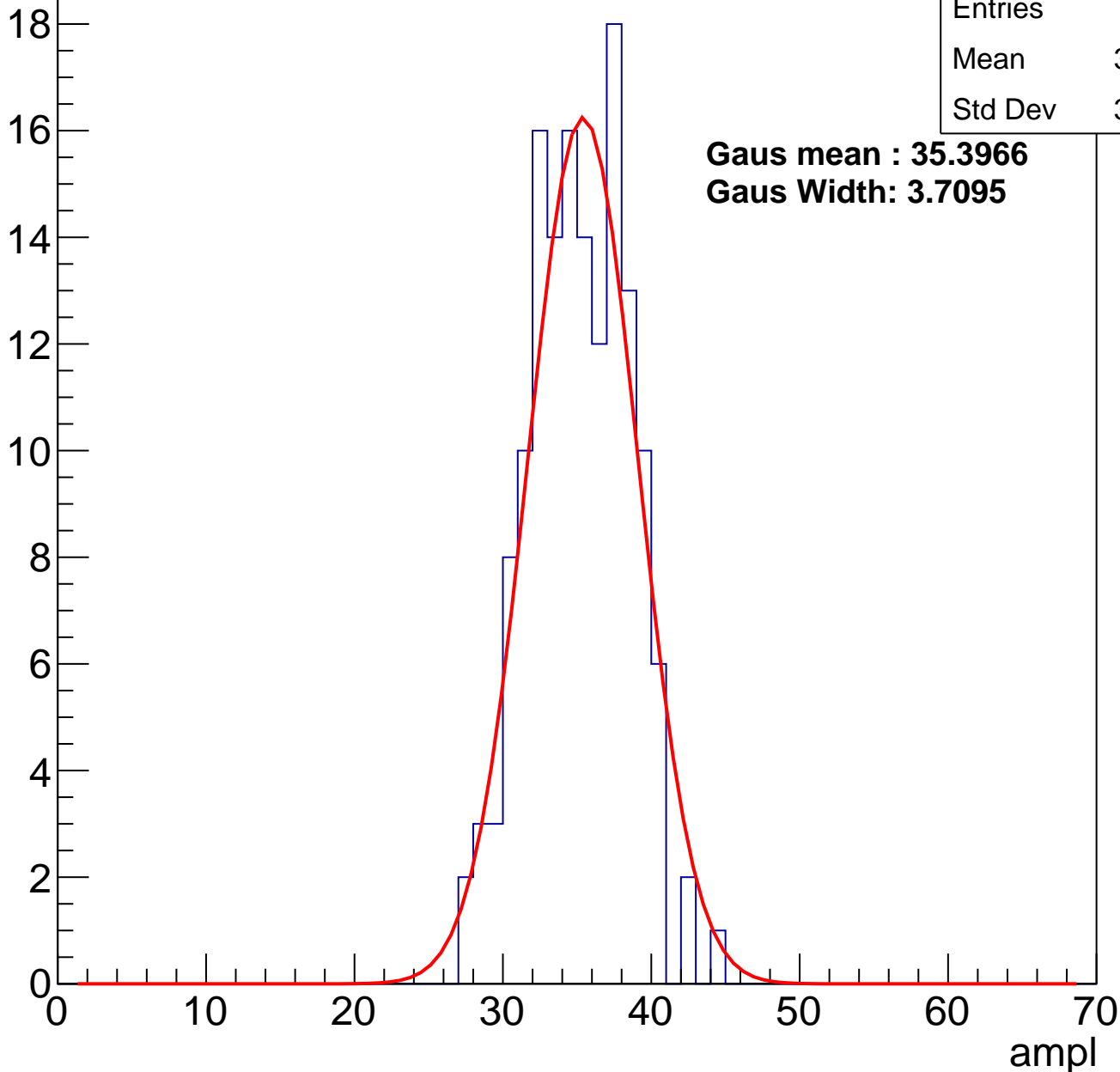
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	34.68
Std Dev	3.323

Gaus mean : 35.3966

Gaus Width: 3.7095

Entry



B1L001S, U24-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	41.31
Std Dev	3.291

Gaus mean : 42.1247

Gaus Width: 3.0207

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

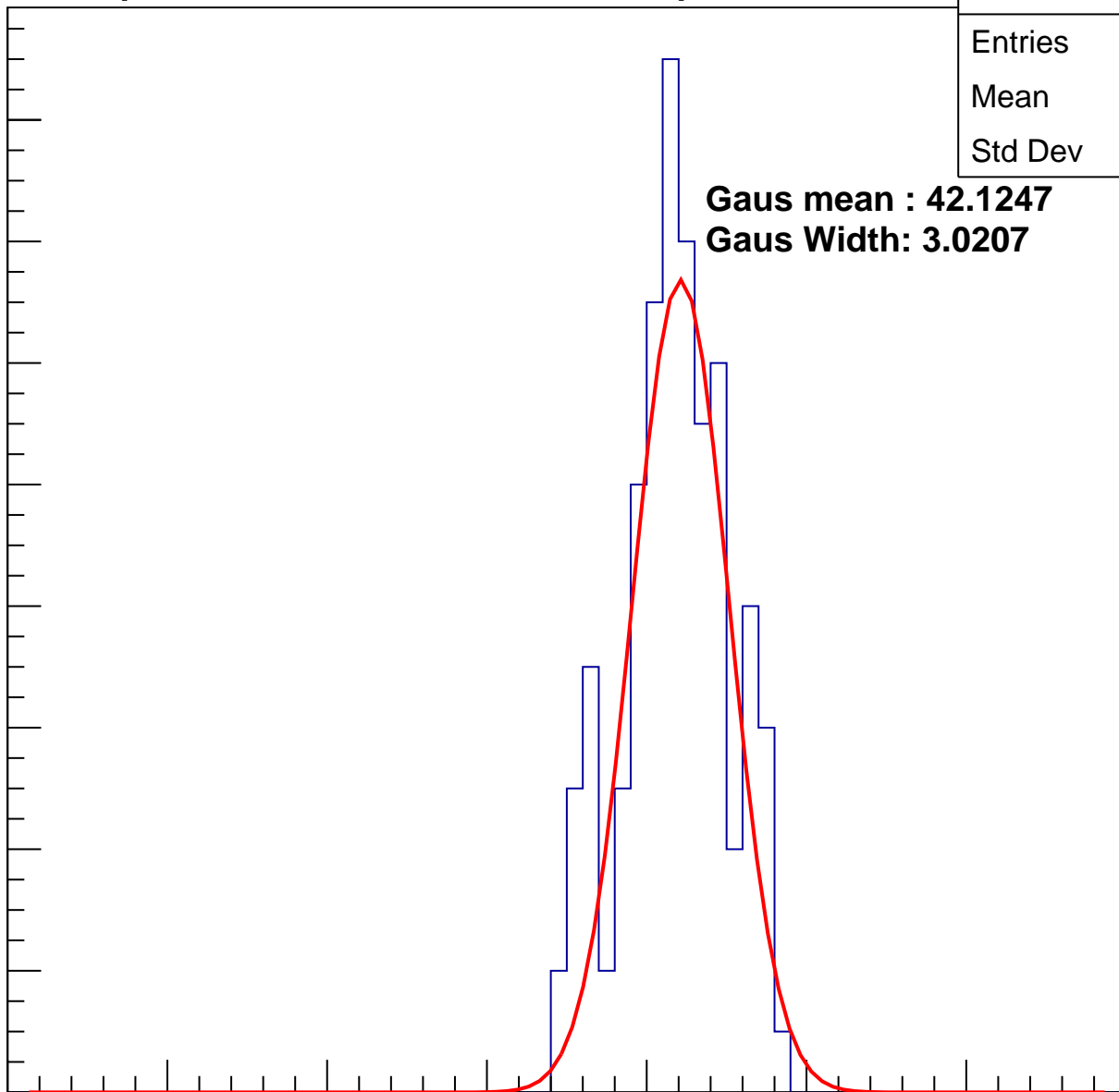
40

50

60

70

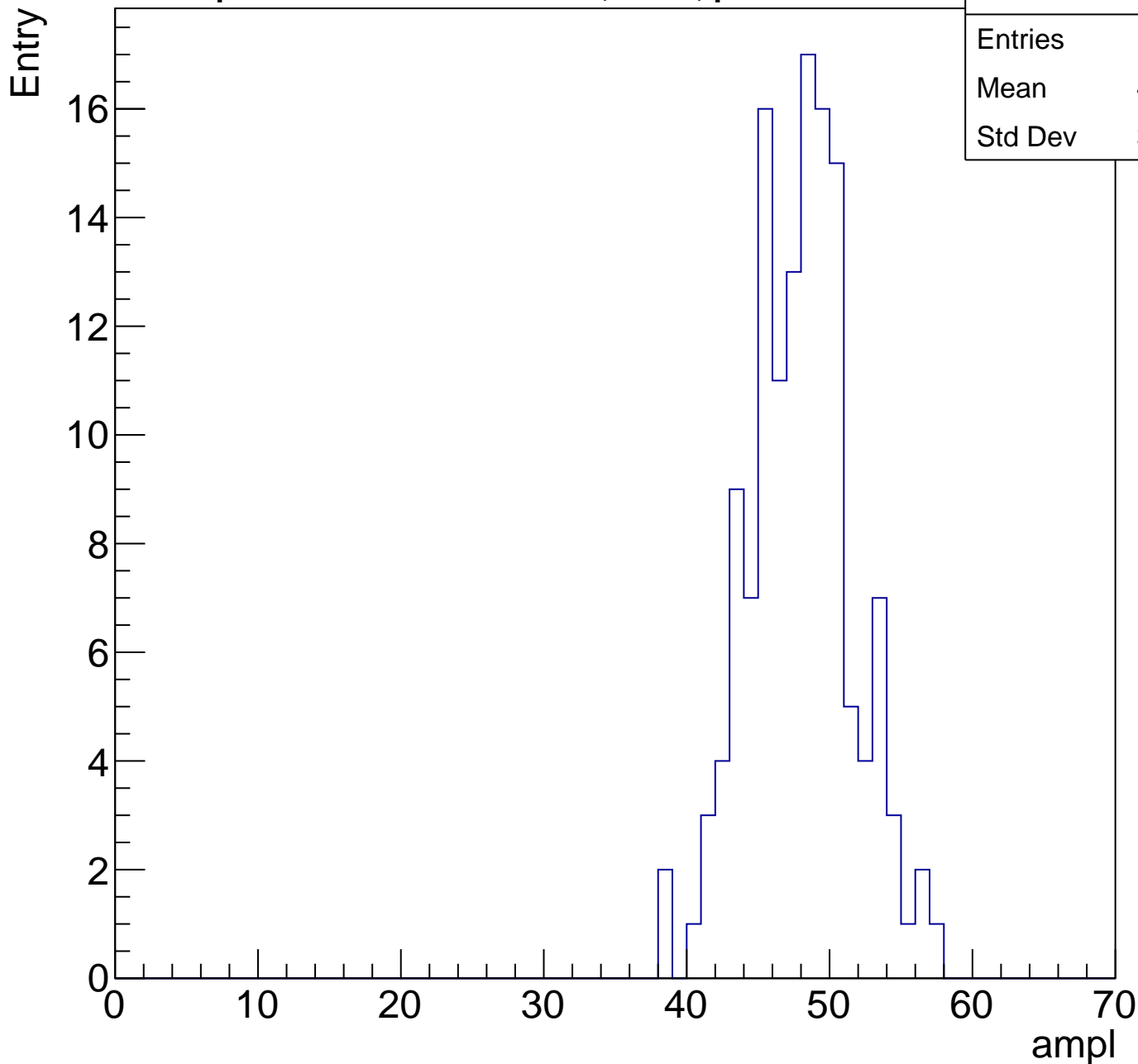
ampl



B1L001S, U24-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

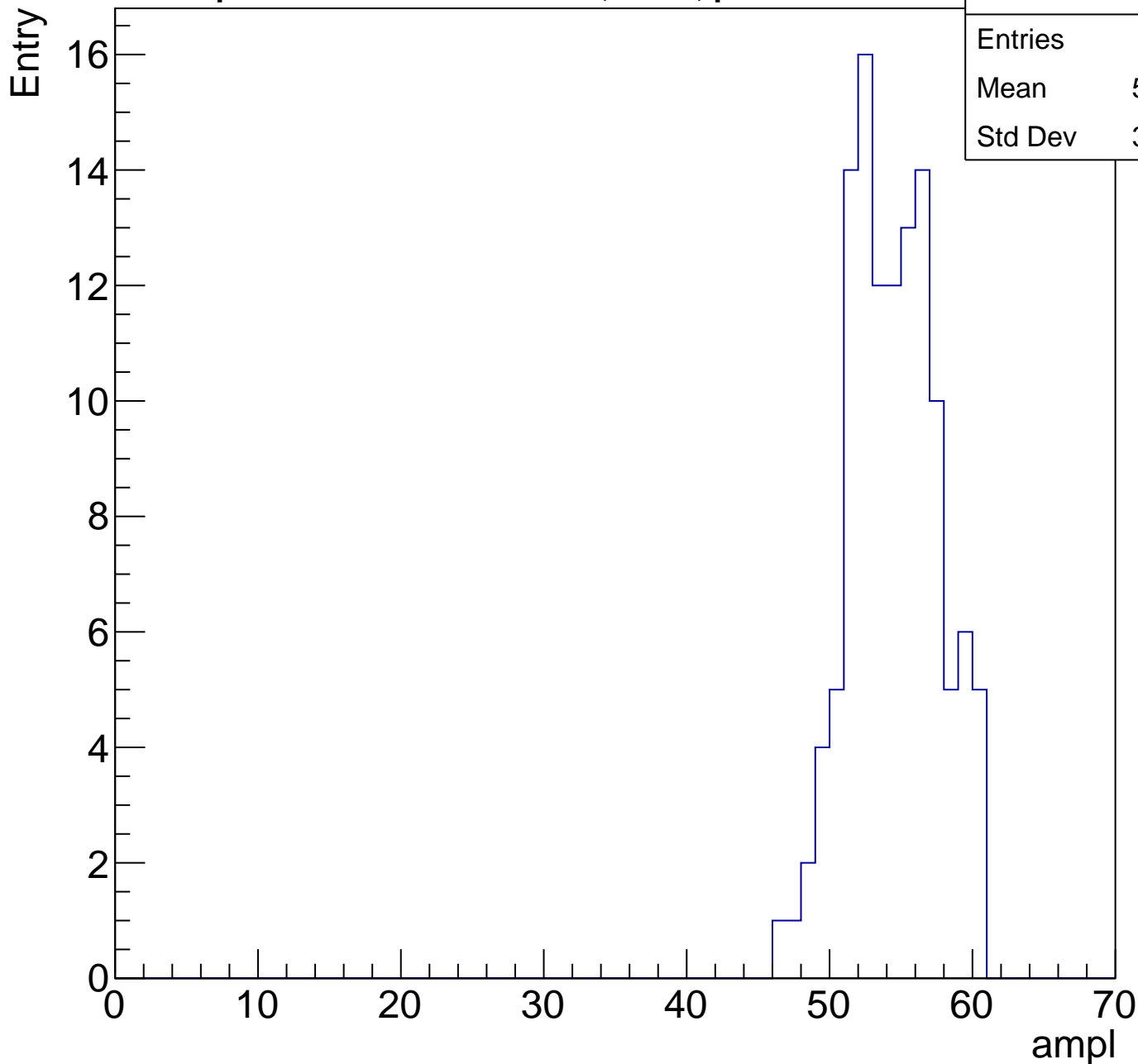
Entries	137
Mean	47.51
Std Dev	3.611



B1L001S, U24-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	53.98
Std Dev	3.077



B1L001S, U24-ch80, adc5

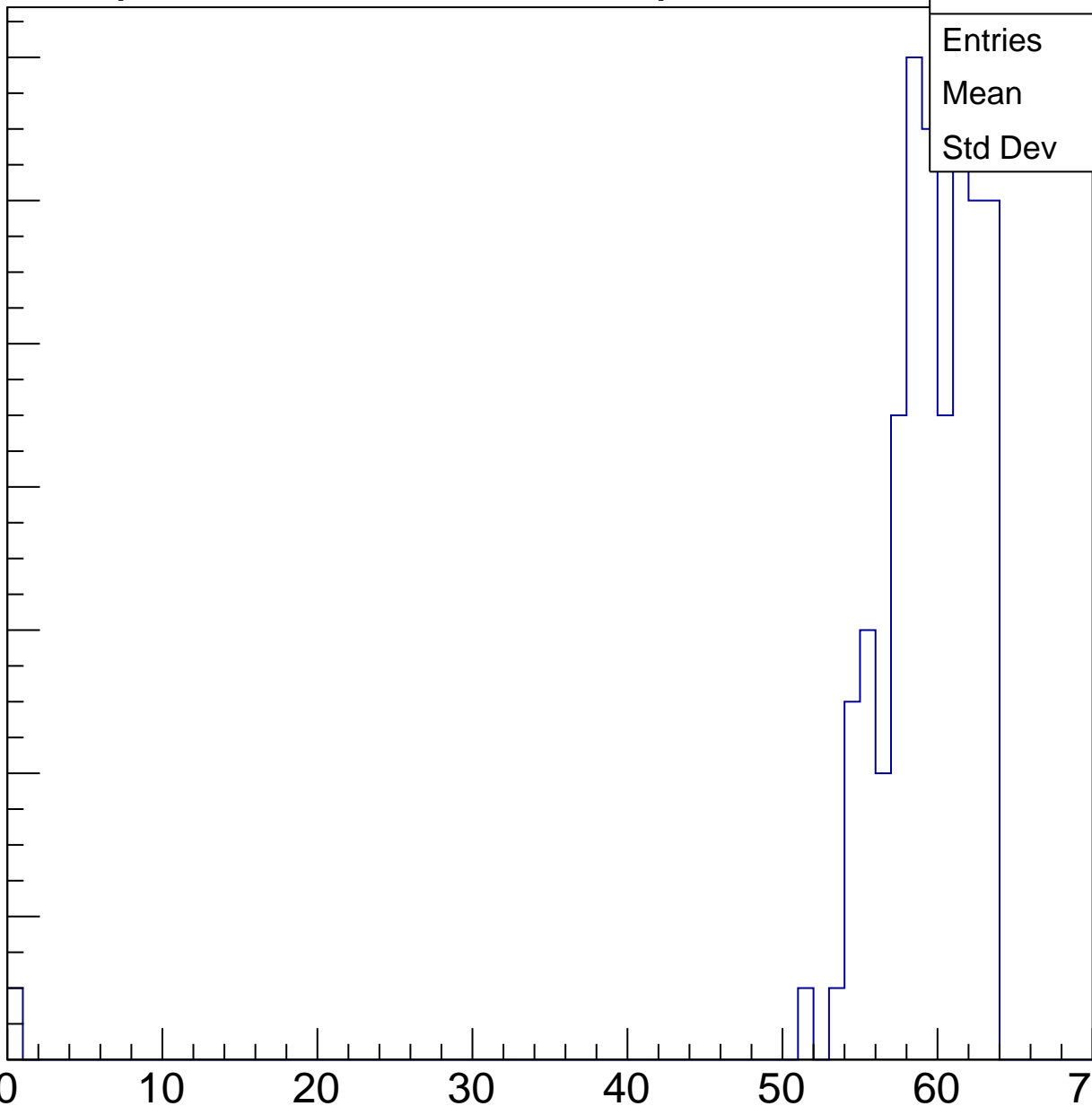
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	58.55
Std Dev	6.474

ampl



B1L001S, U24-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	31
Mean	59.45
Std Dev	10.93

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

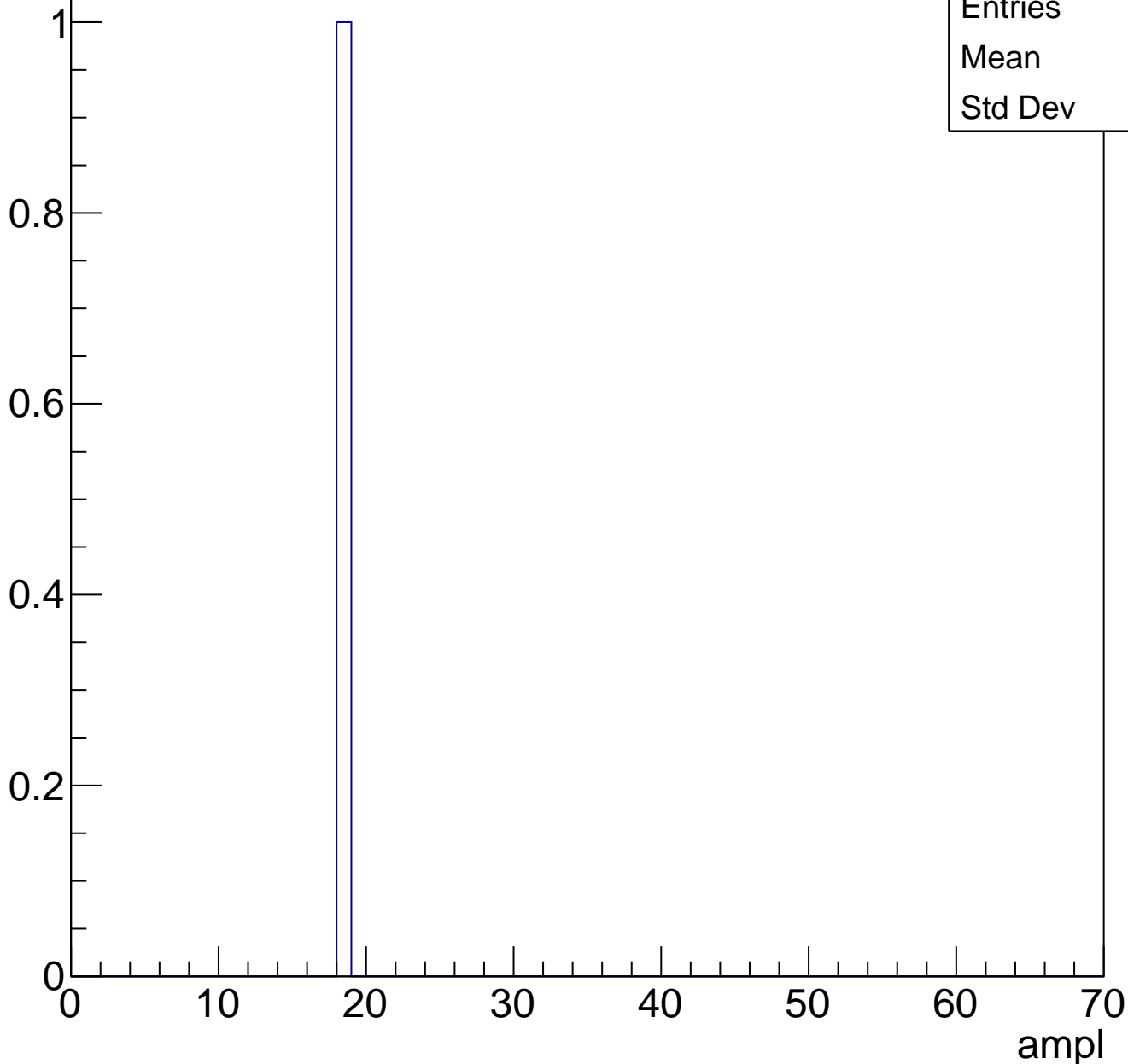
7

8

B1L001S, U24-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch81, adc0

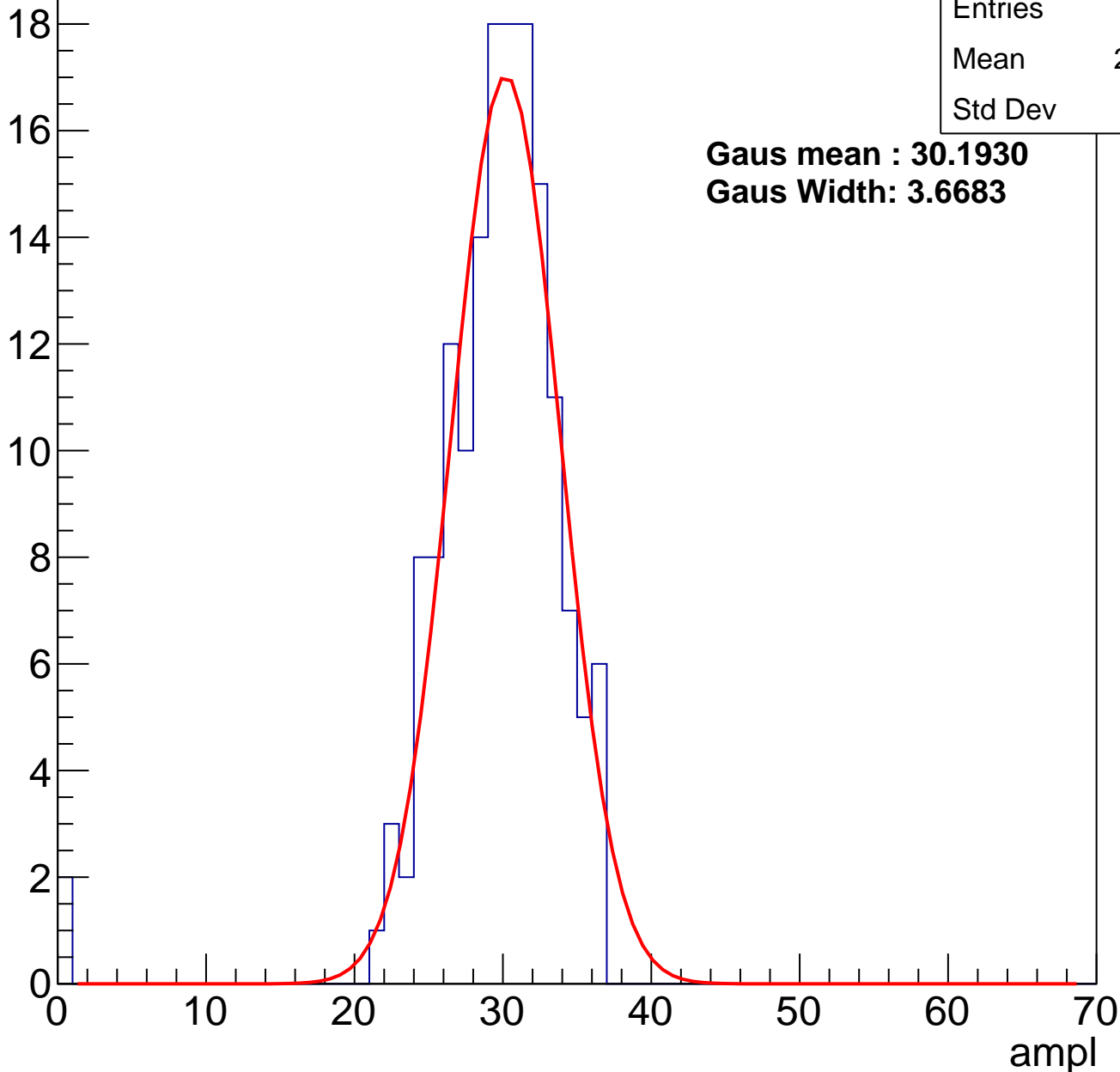
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	29.06
Std Dev	4.71

Gaus mean : 30.1930

Gaus Width: 3.6683

Entry



B1L001S, U24-ch81, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.89
Std Dev	3.257

Gaus mean : 37.3289

Gaus Width: 3.0813

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

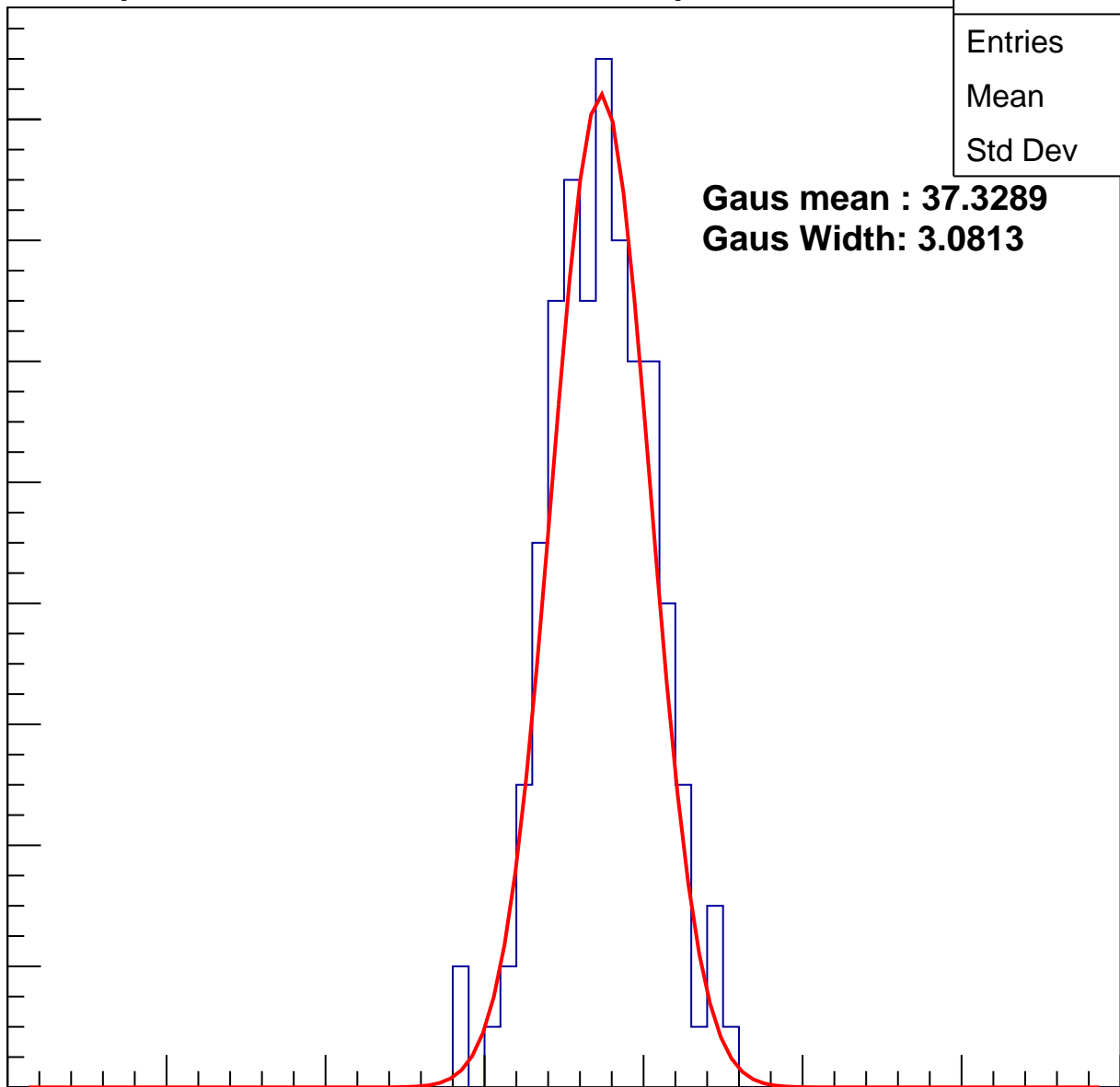
40

50

60

70

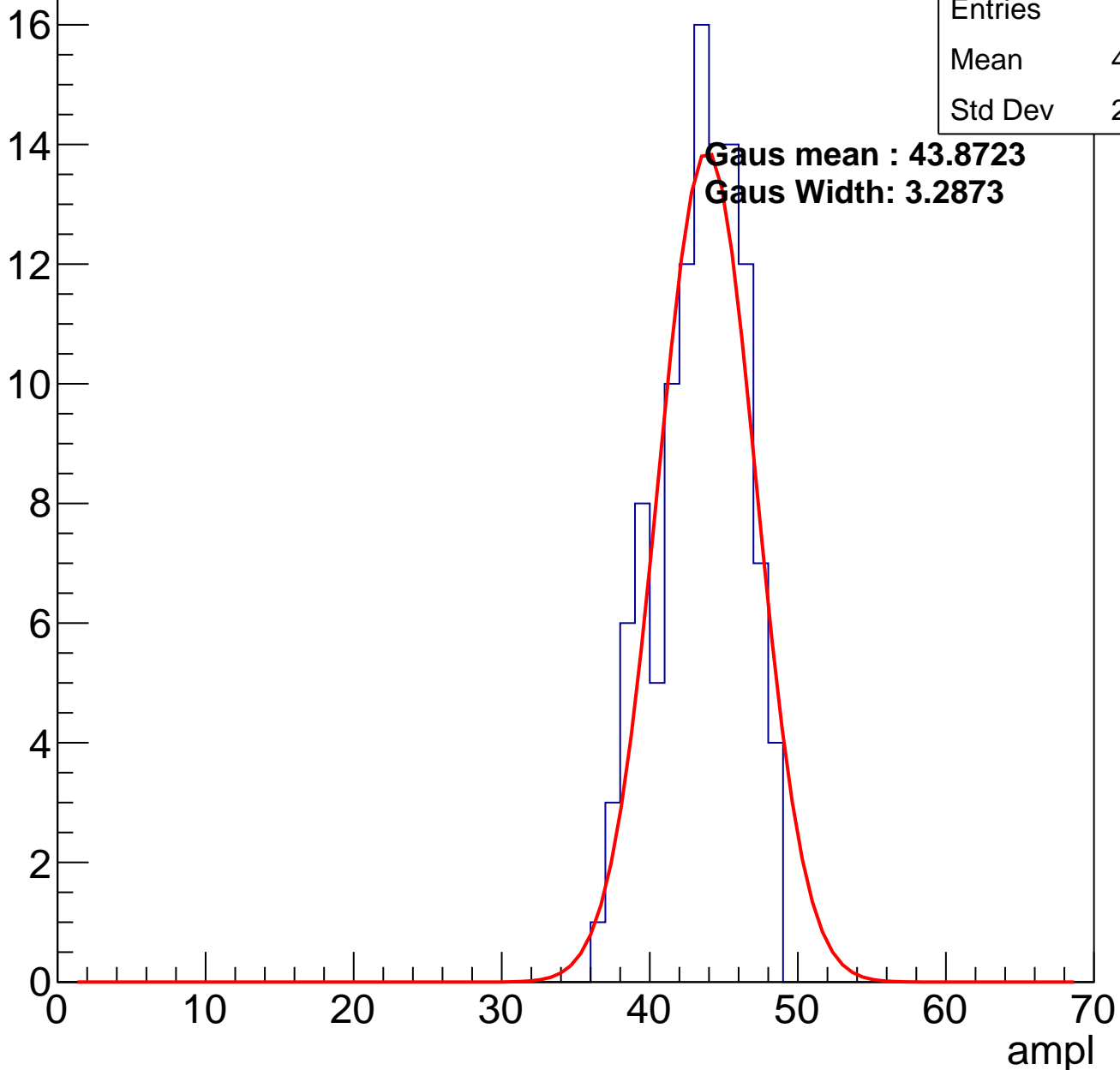
ampl



B1L001S, U24-ch81, adc2

calib_packv5_042523_0143.root, FC#2, port C2

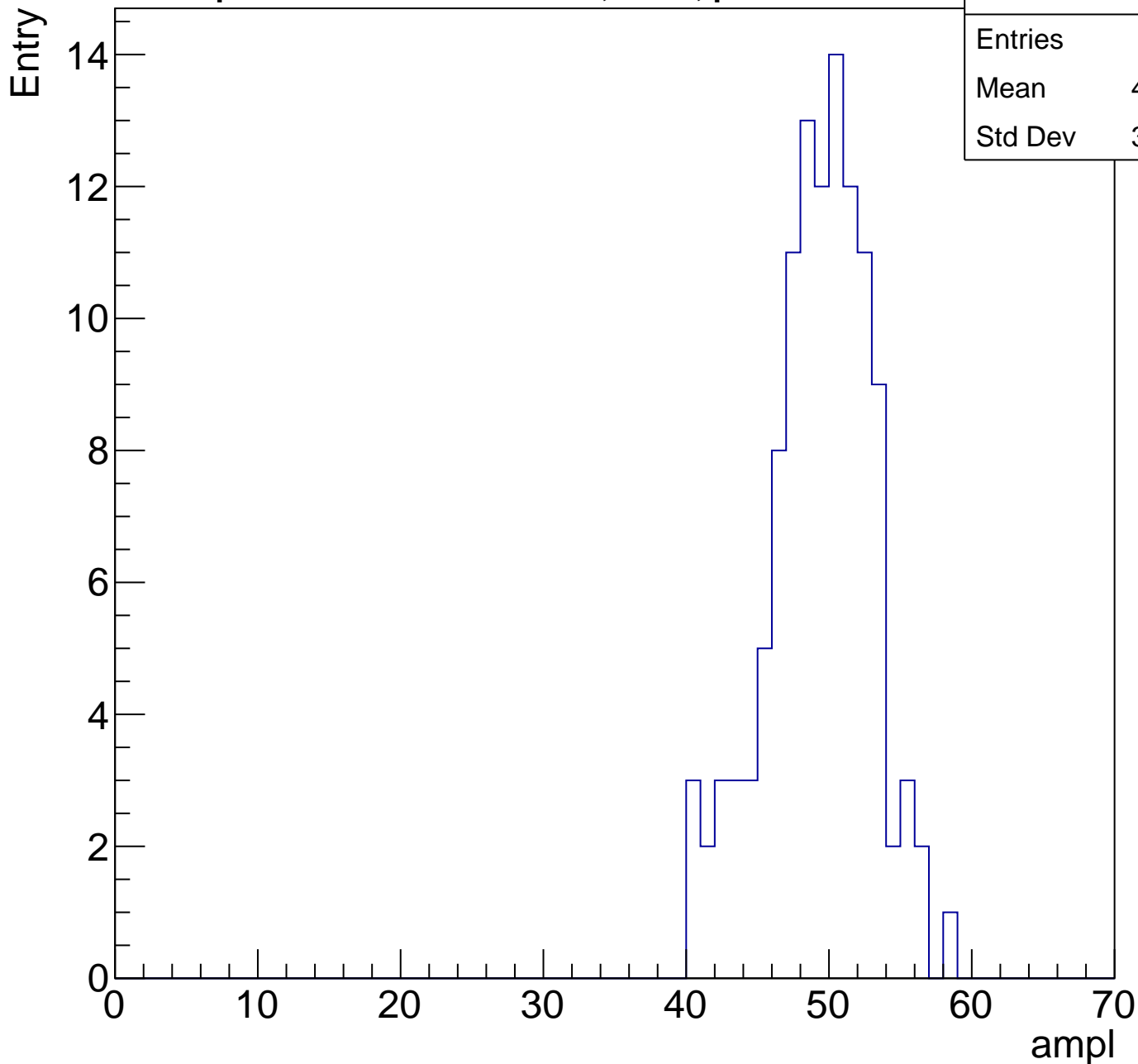
Entry



B1L001S, U24-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	48.85
Std Dev	3.639



B1L001S, U24-ch81, adc4

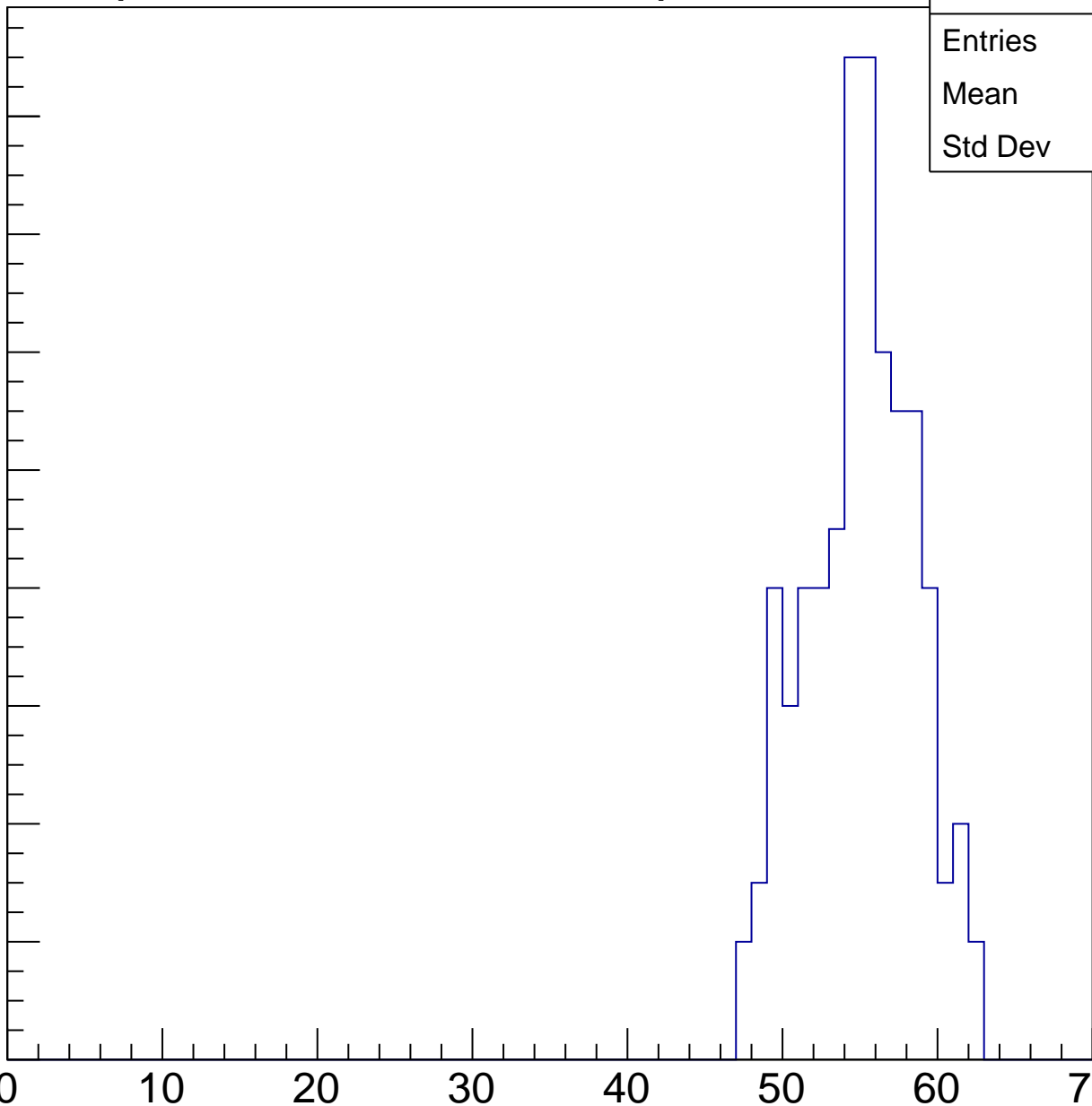
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	54.58
Std Dev	3.459

ampl



B1L001S, U24-ch81, adc5

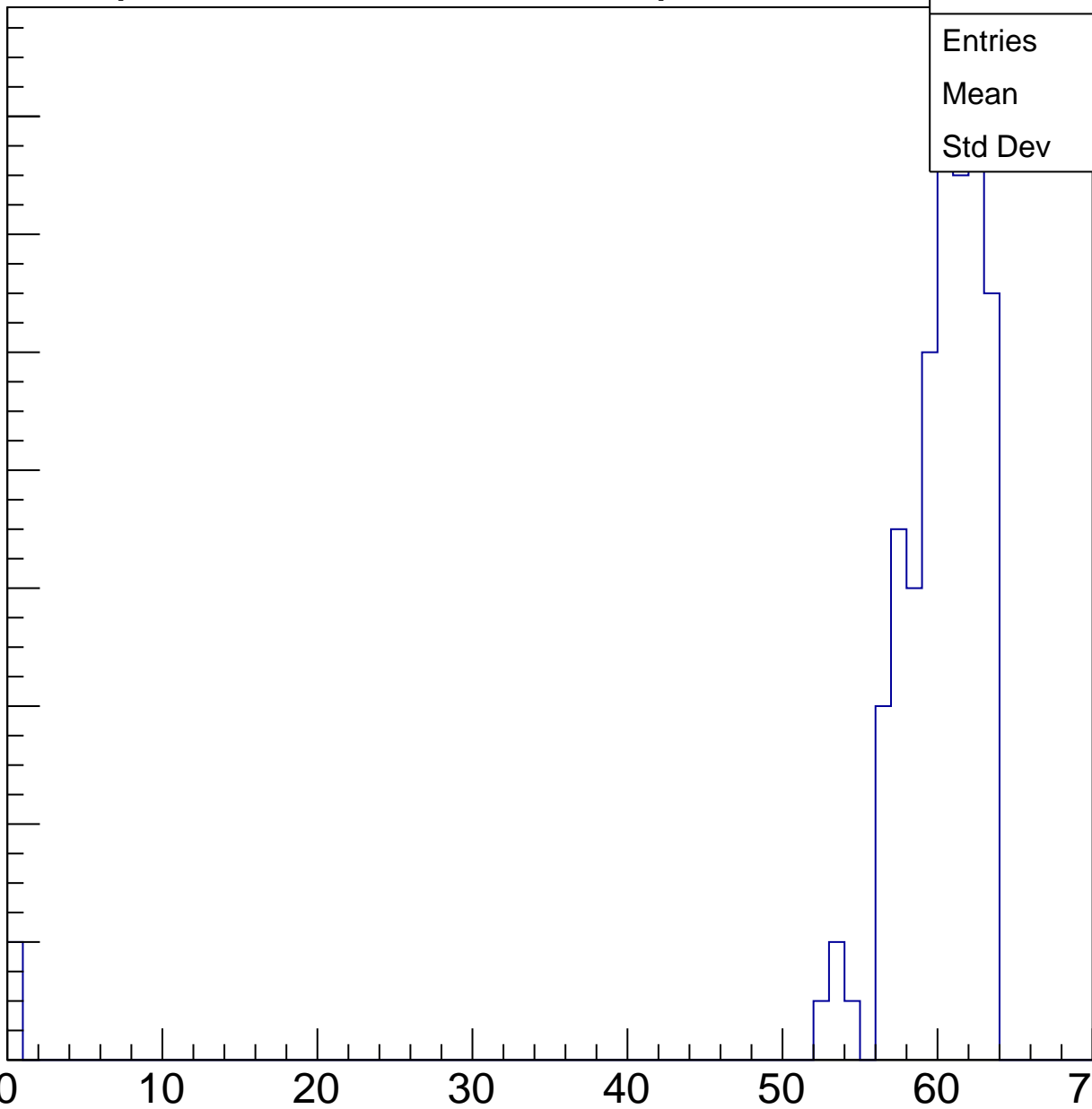
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.62
Std Dev	8.643

ampl

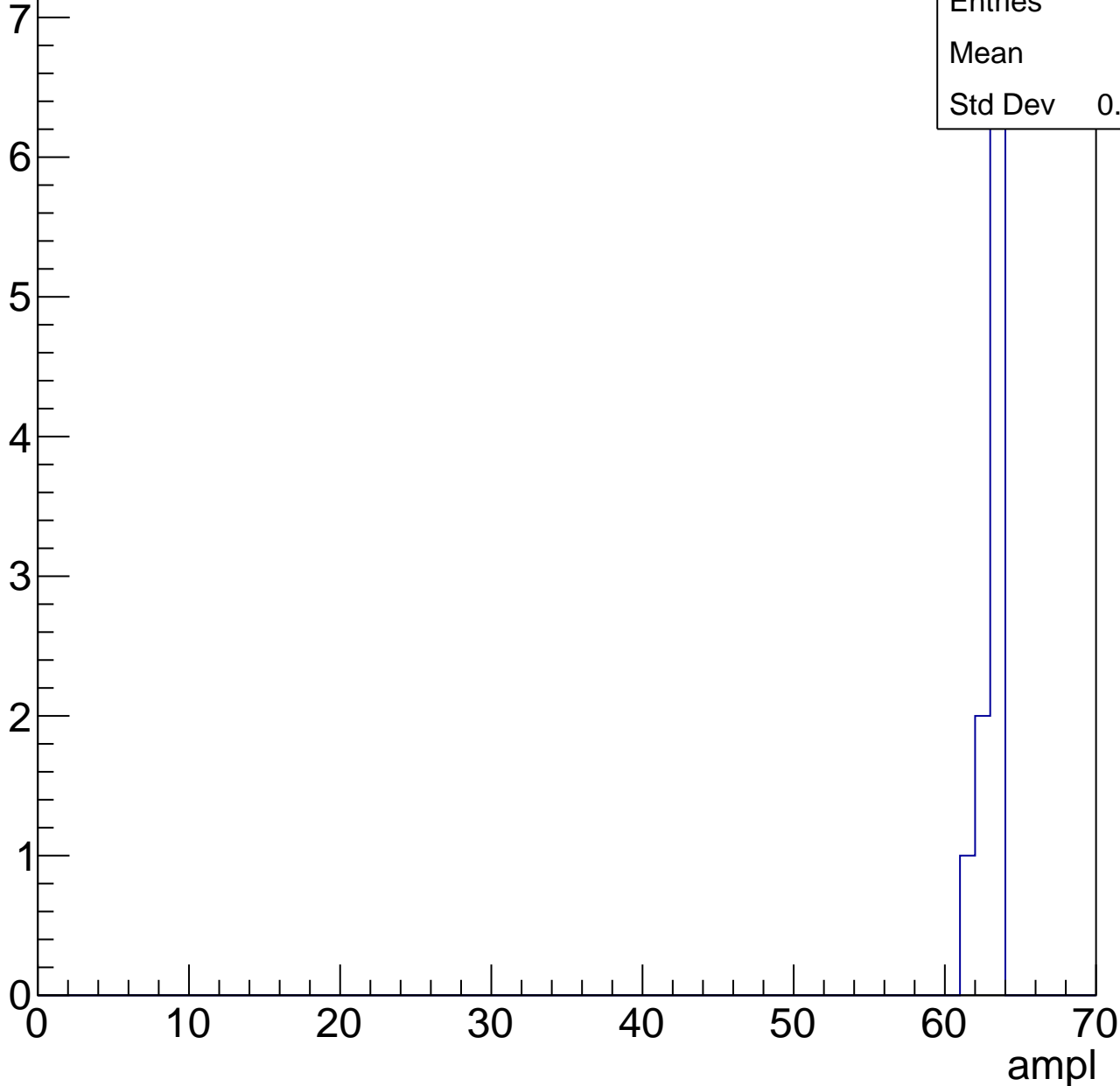


B1L001S, U24-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62.6
Std Dev	0.6633



B1L001S, U24-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch82, adc0

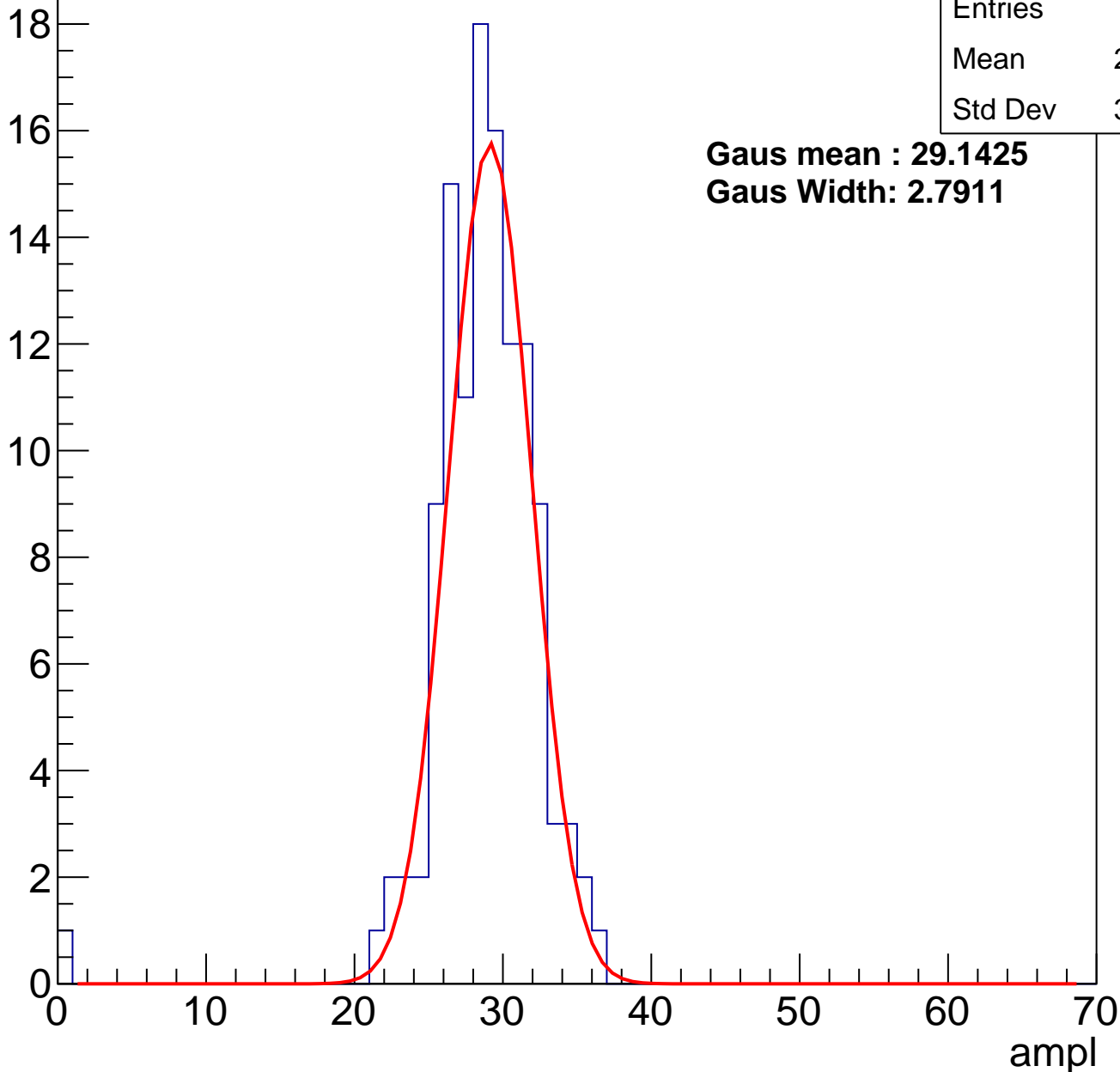
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	28.29
Std Dev	3.879

Gaus mean : 29.1425

Gaus Width: 2.7911

Entry



B1L001S, U24-ch82, adc1

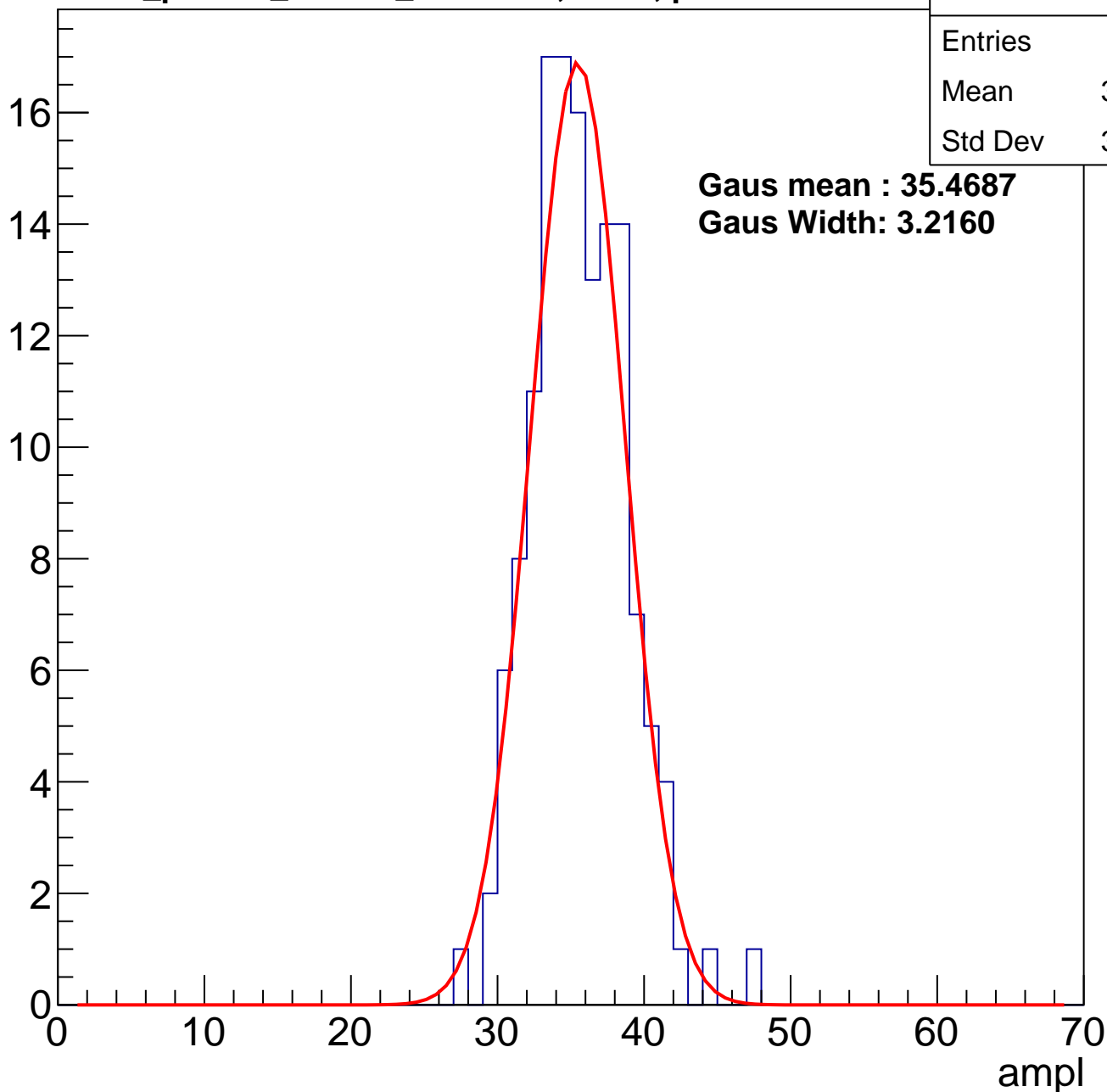
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.16
Std Dev	3.235

Gaus mean : 35.4687

Gaus Width: 3.2160

Entry



B1L001S, U24-ch82, adc2

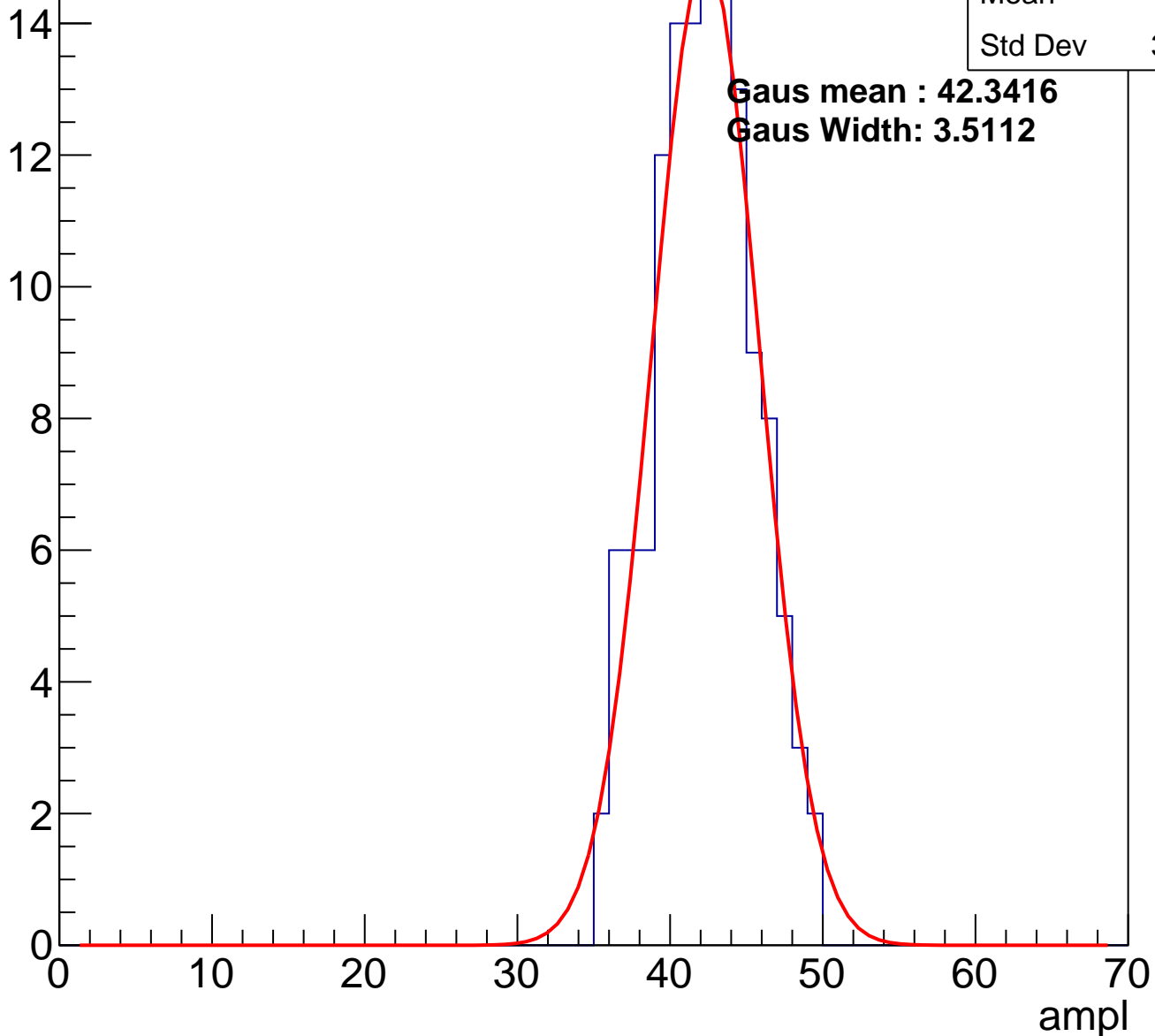
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	41.81
Std Dev	3.211

Gaus mean : 42.3416

Gaus Width: 3.5112

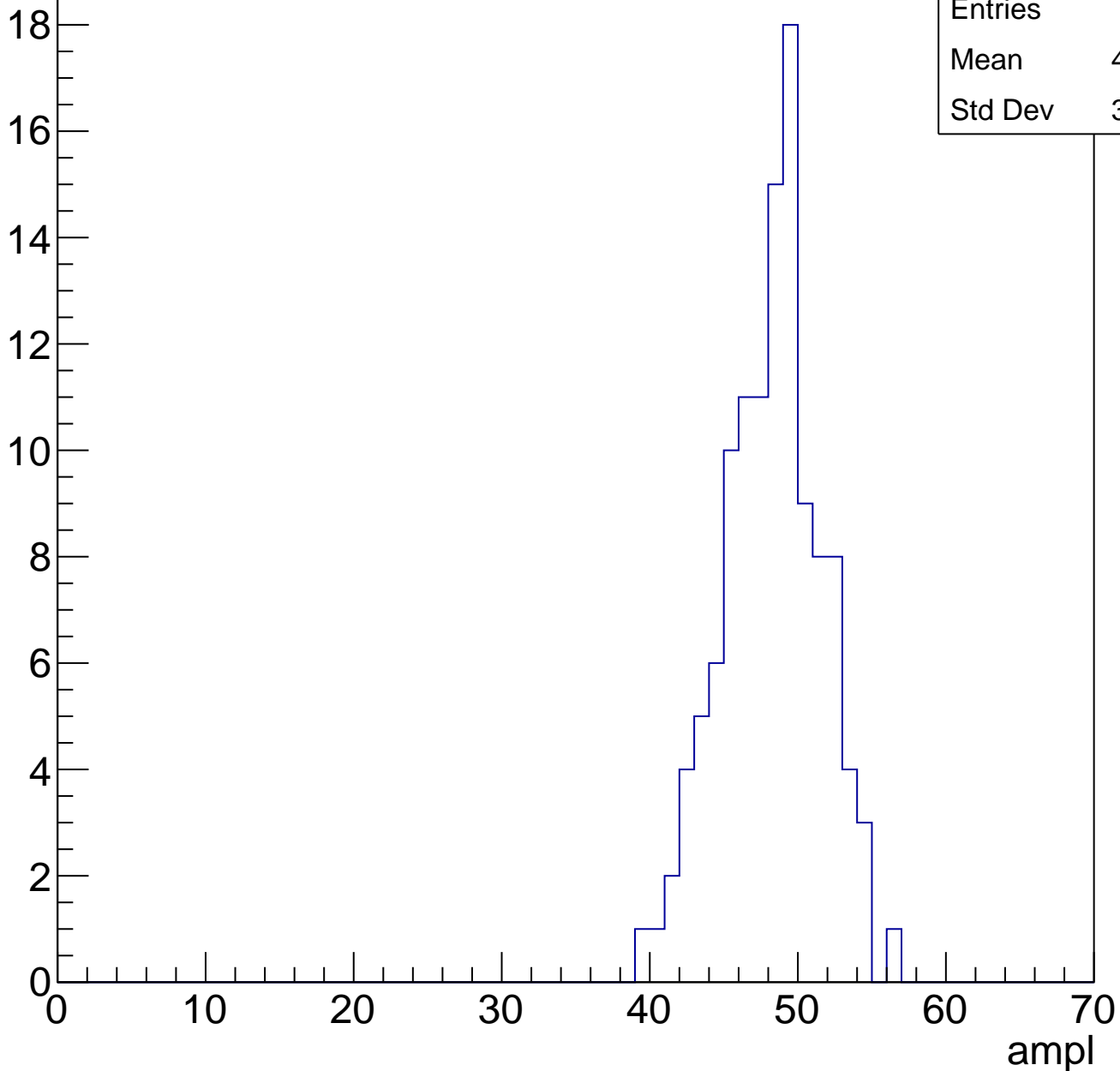
Entry



B1L001S, U24-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

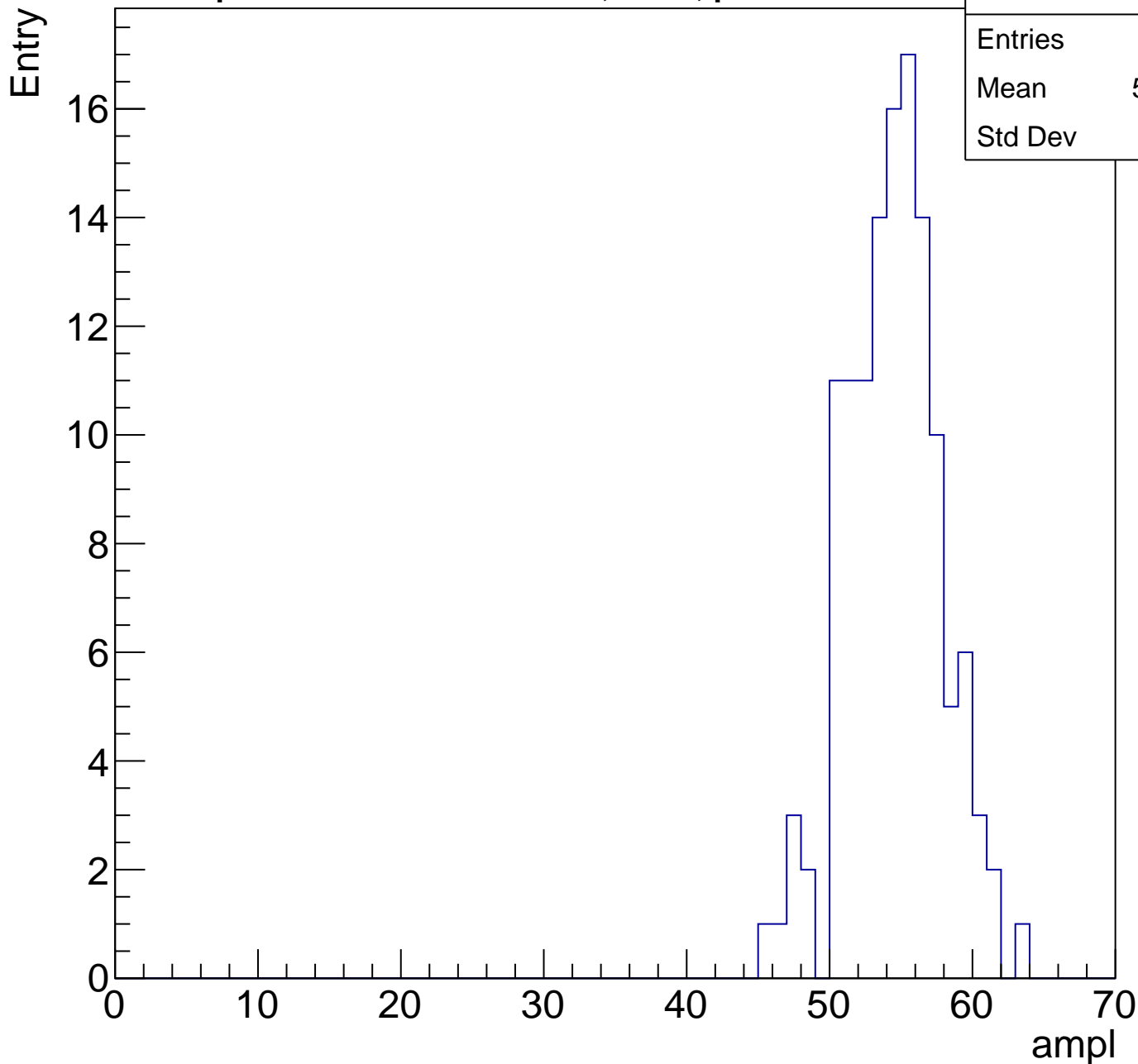
Entry



B1L001S, U24-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	54.02
Std Dev	3.28



B1L001S, U24-ch82, adc5

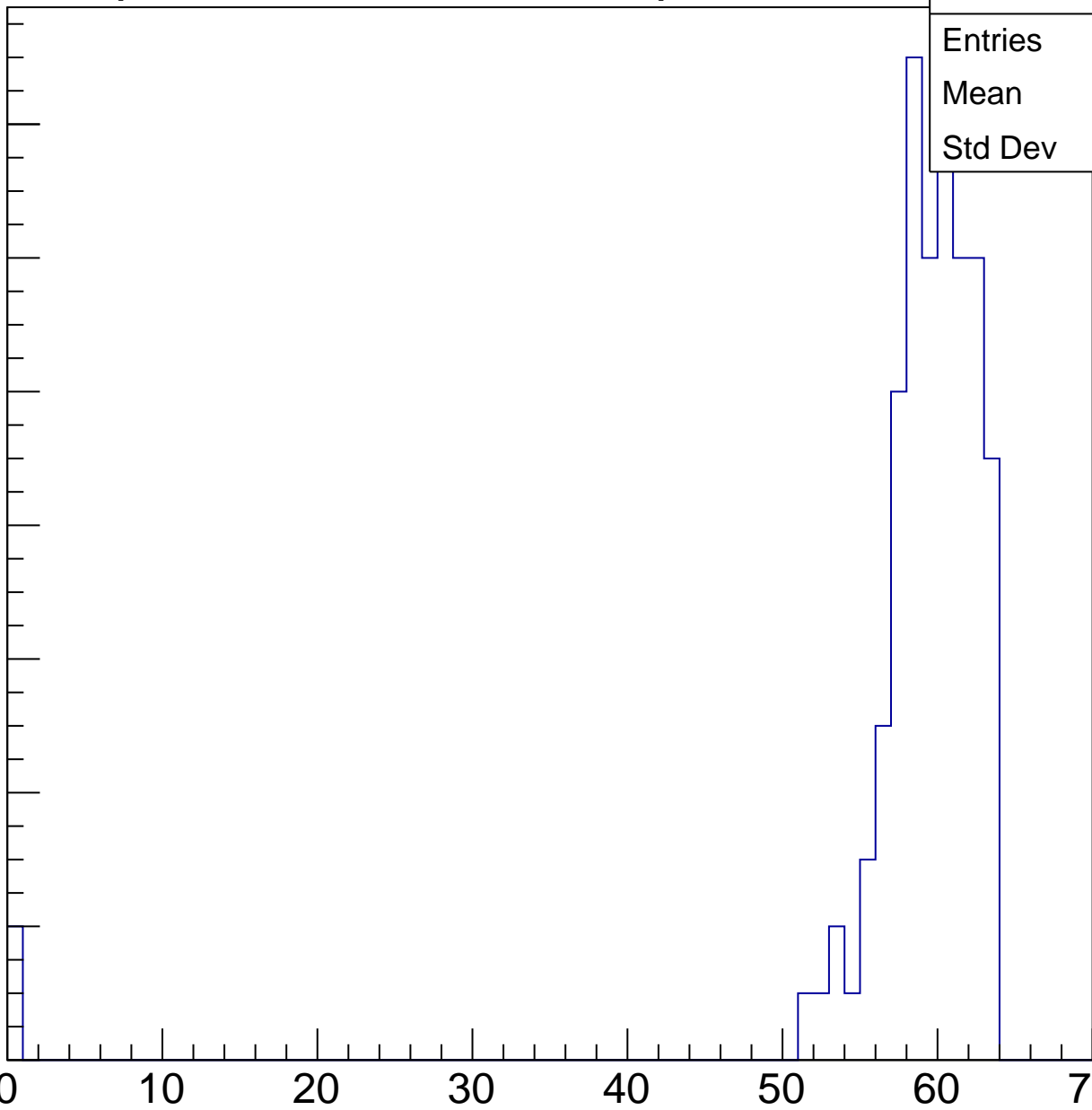
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	57.97
Std Dev	8.721

ampl

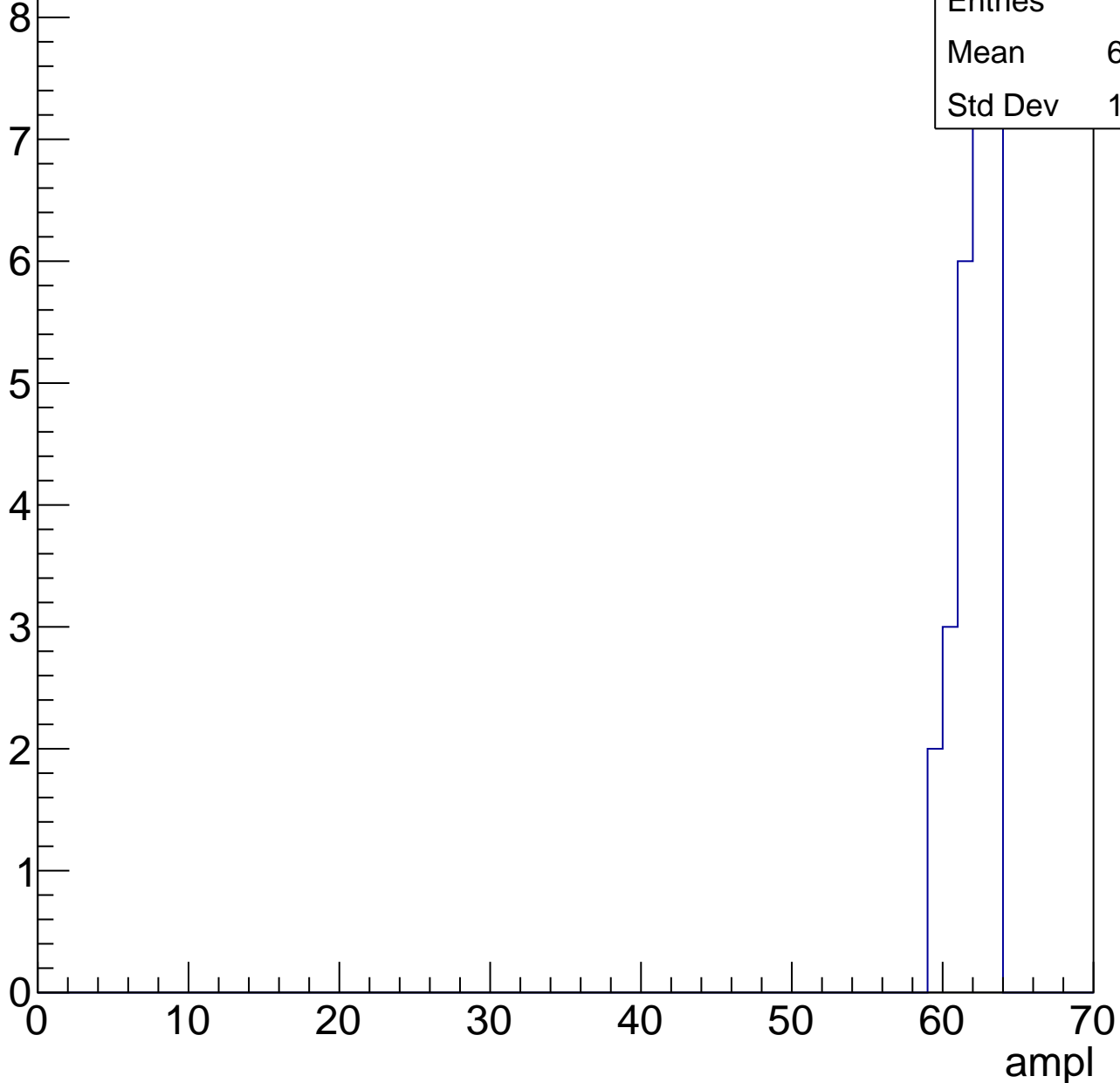


B1L001S, U24-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

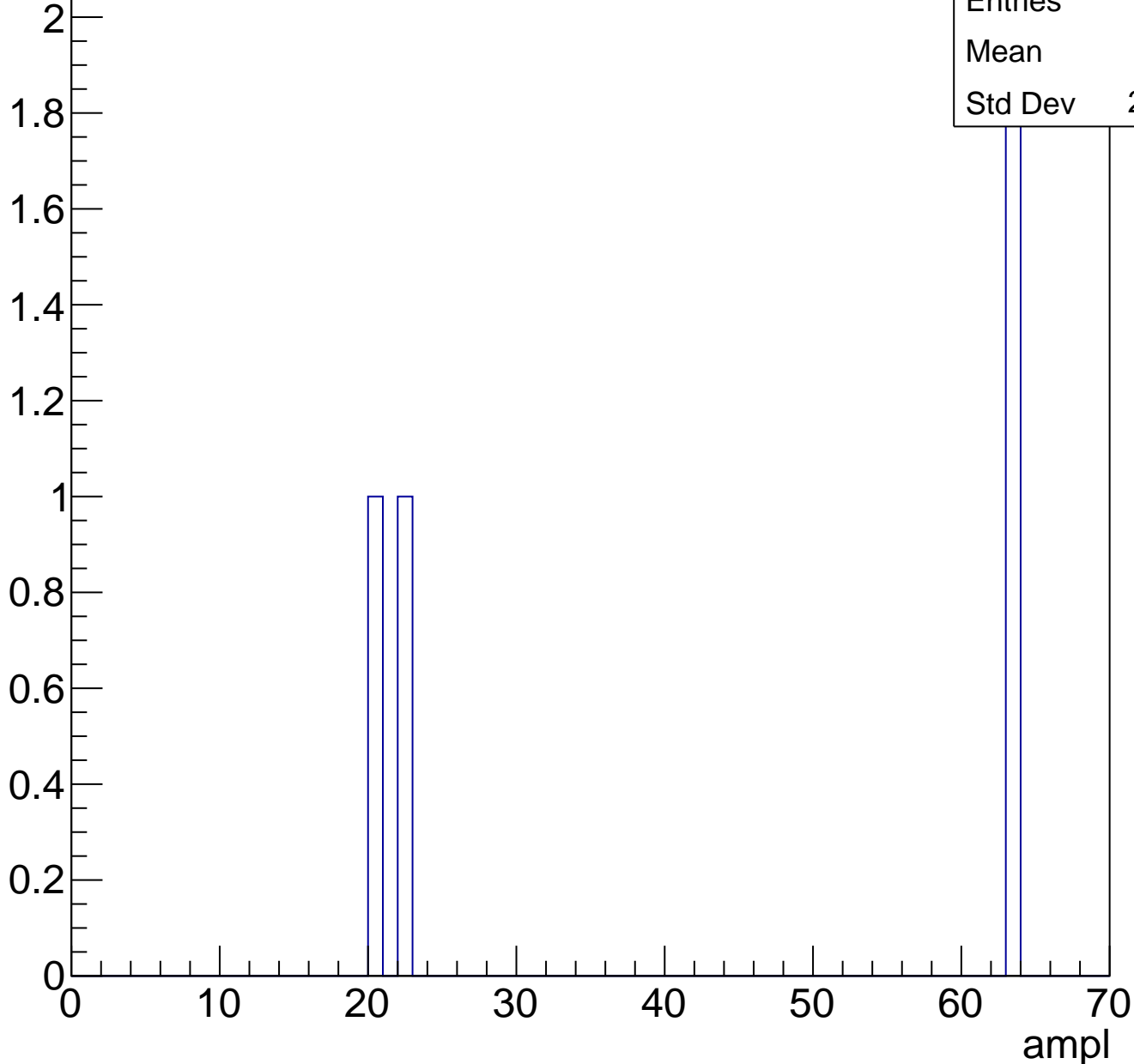
Entries	27
Mean	61.63
Std Dev	1.222



B1L001S, U24-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch83, adc0

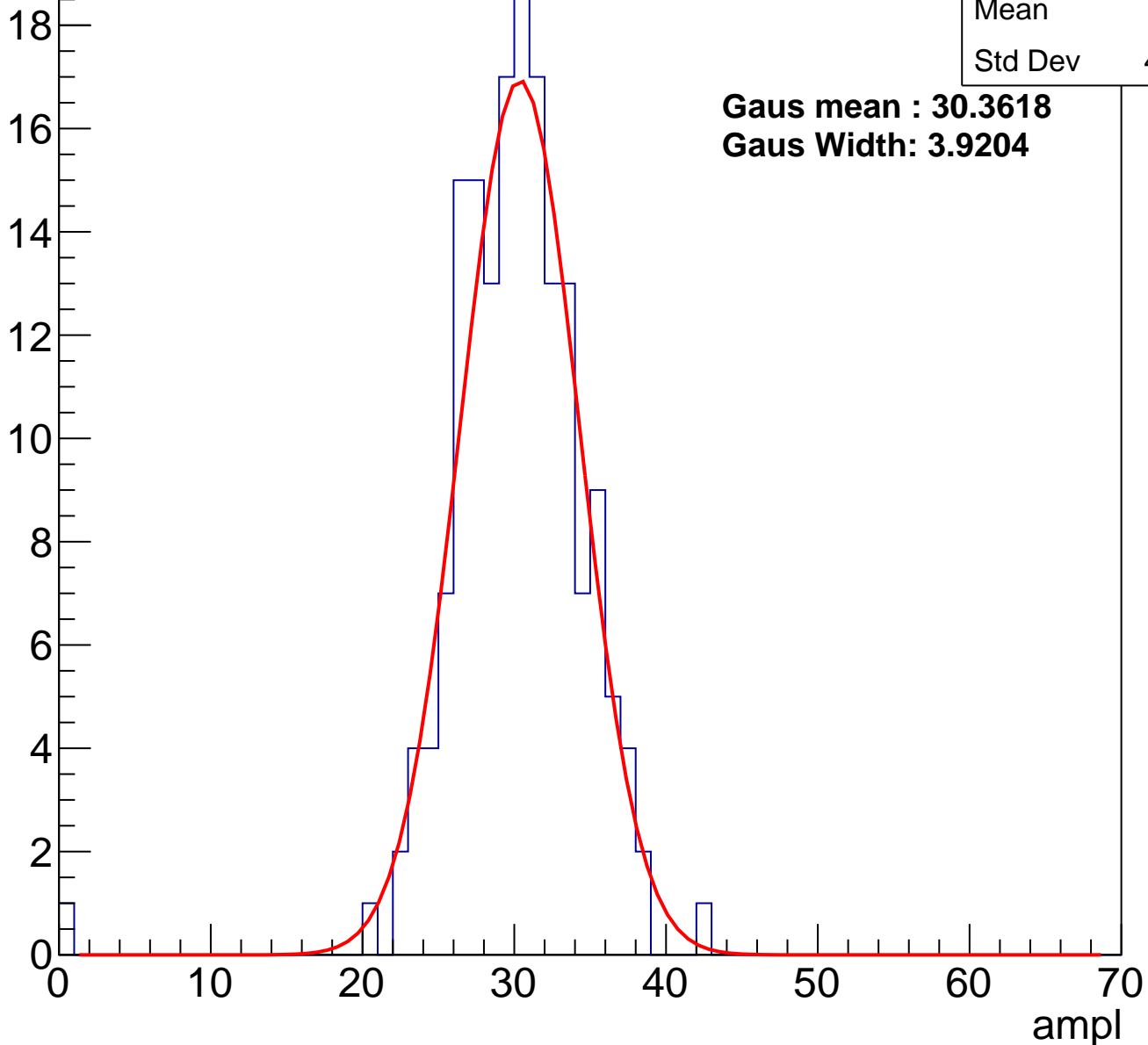
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	29.7
Std Dev	4.381

Gaus mean : 30.3618

Gaus Width: 3.9204

Entry



B1L001S, U24-ch83, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.72
Std Dev	3.07

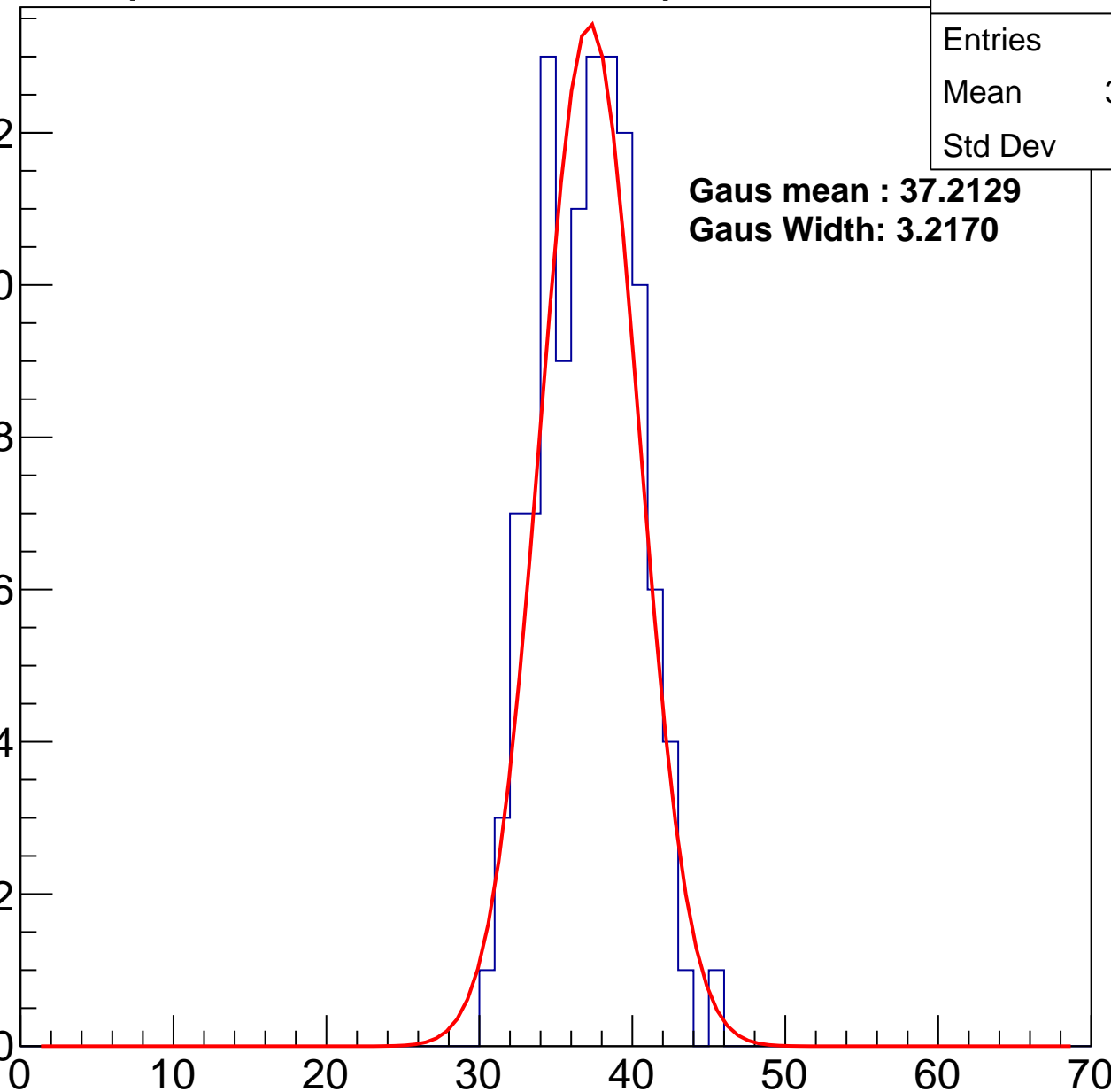
Gaus mean : 37.2129

Gaus Width: 3.2170

Entry

12
10
8
6
4
2
0

ampl



B1L001S, U24-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.02
Std Dev	3.588

Entry

16

14

12

10

8

6

4

2

0

Gaus mean : 43.1672

Gaus Width: 3.4334

0

10

20

30

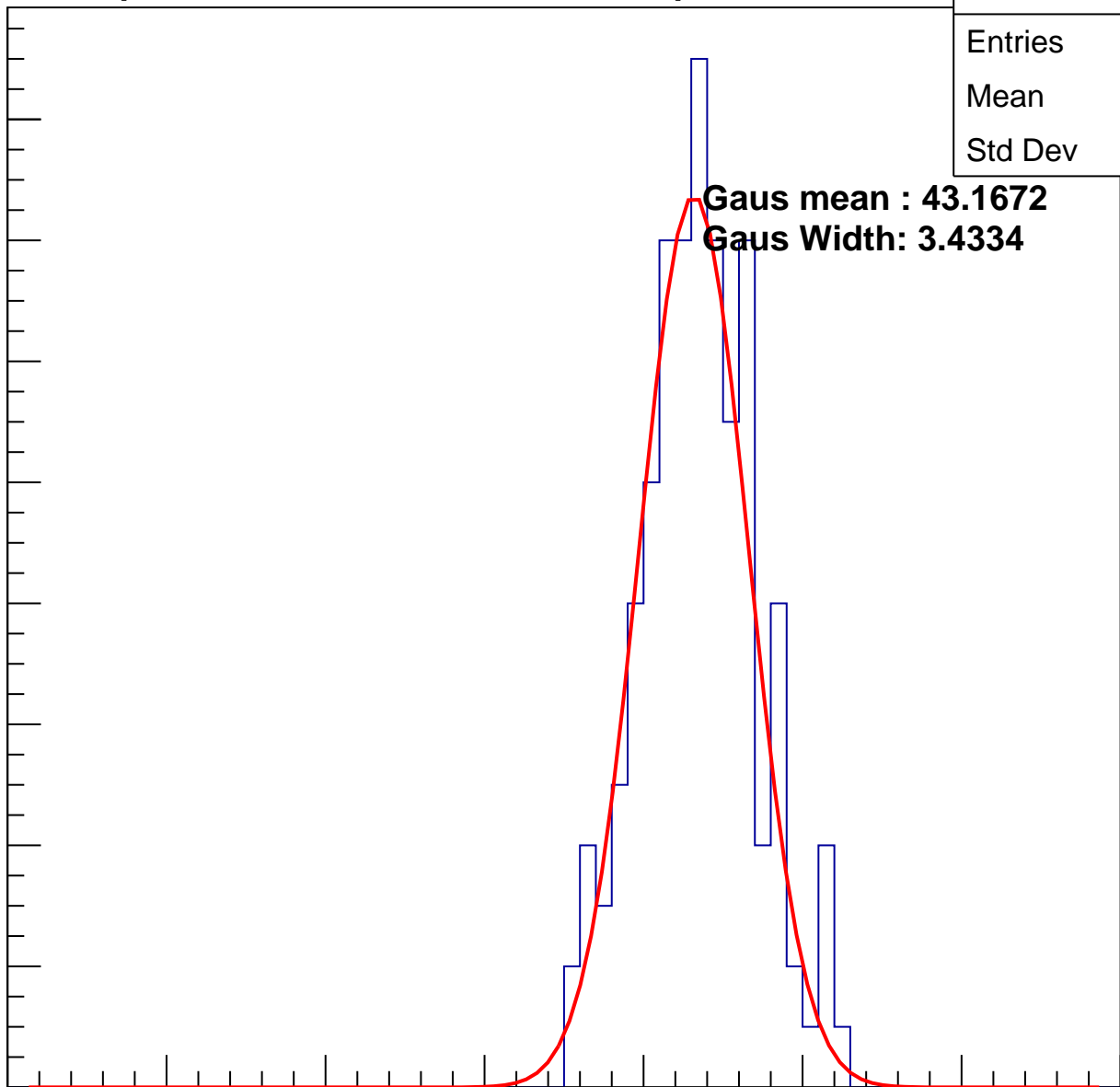
40

50

60

70

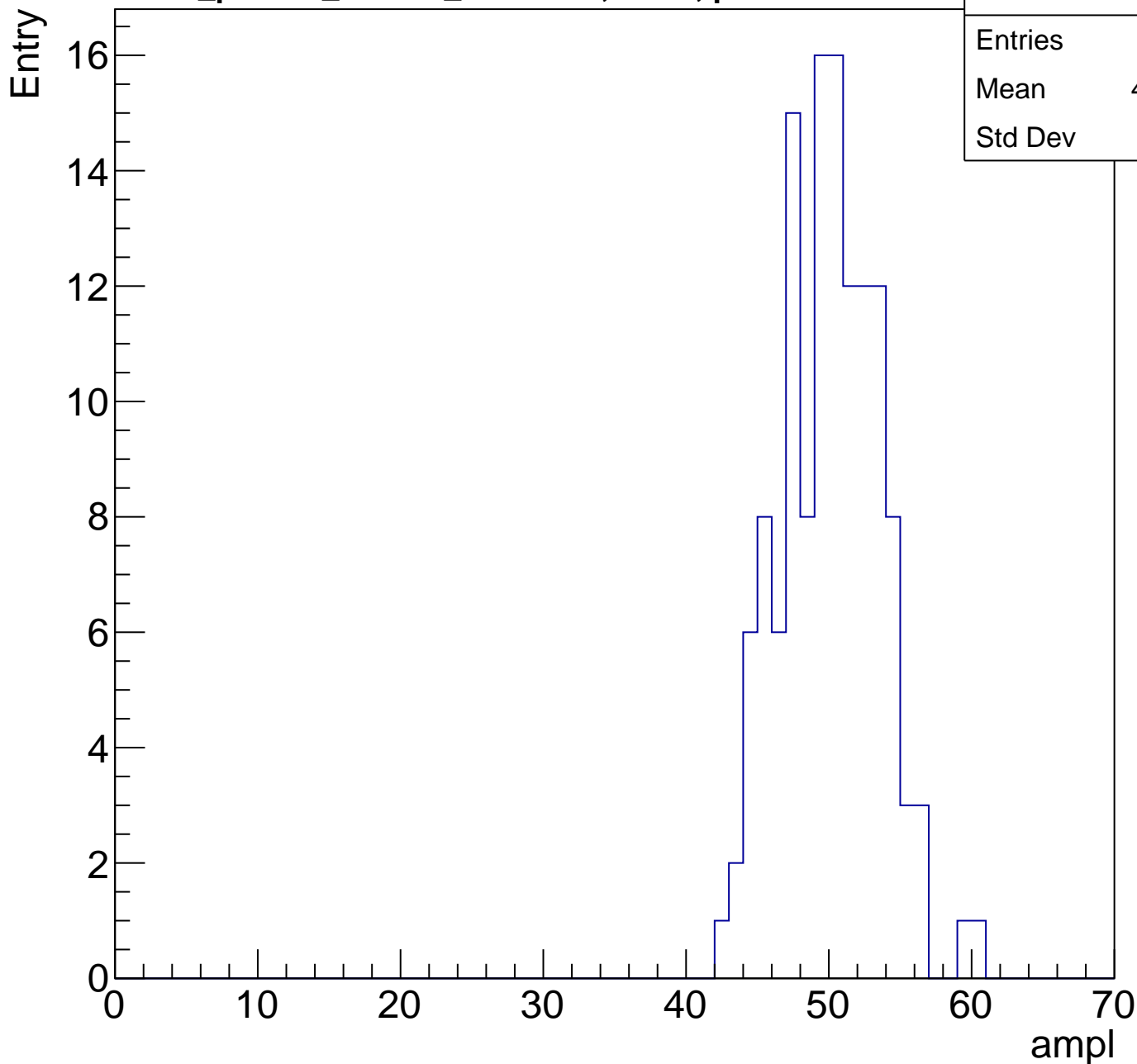
ampl



B1L001S, U24-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	49.67
Std Dev	3.4



B1L001S, U24-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	56.12
Std Dev	3.477

Entry

16

14

12

10

8

6

4

2

0

0

10

20

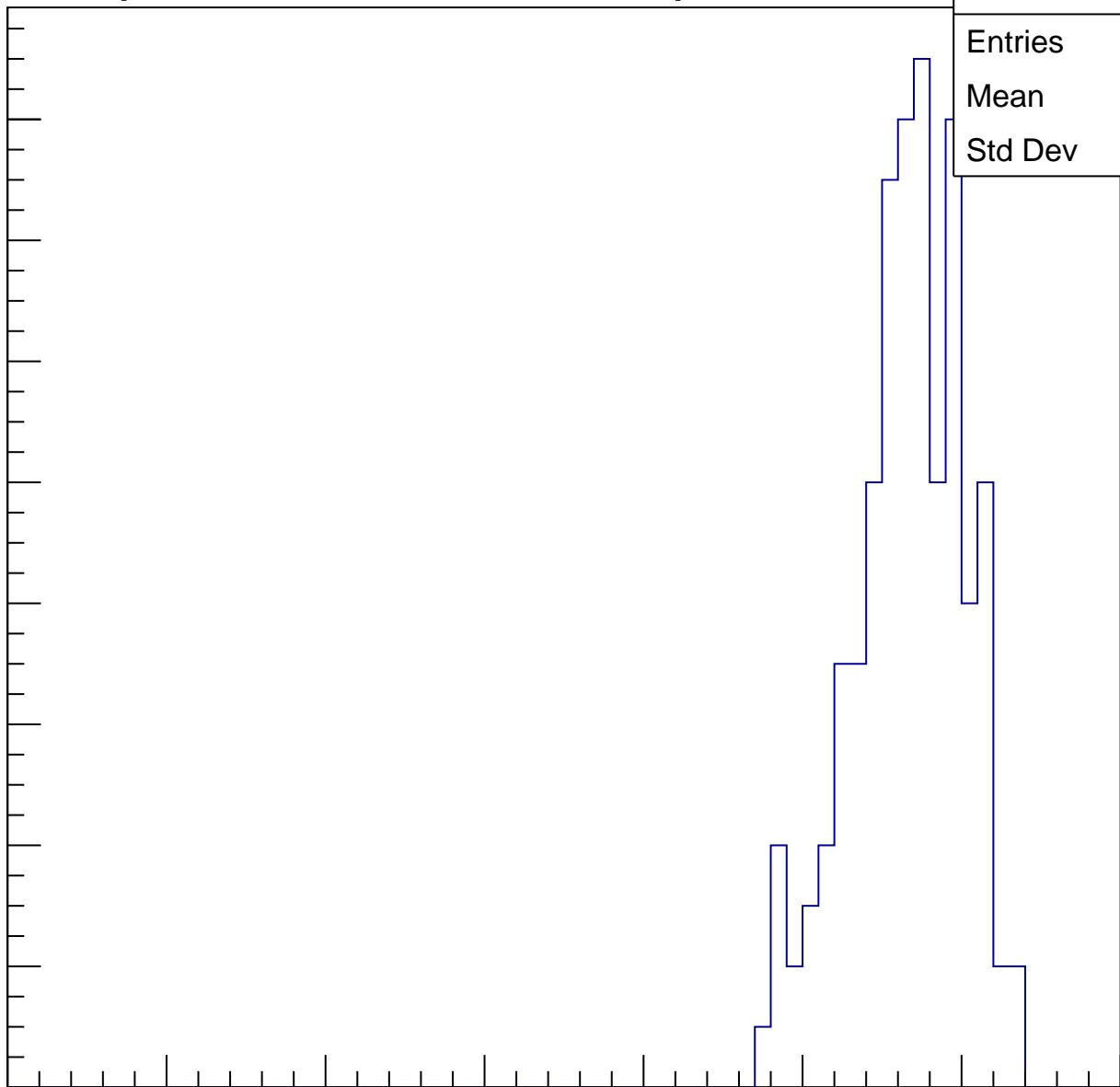
30

40

50

60

ampl



B1L001S, U24-ch83, adc5

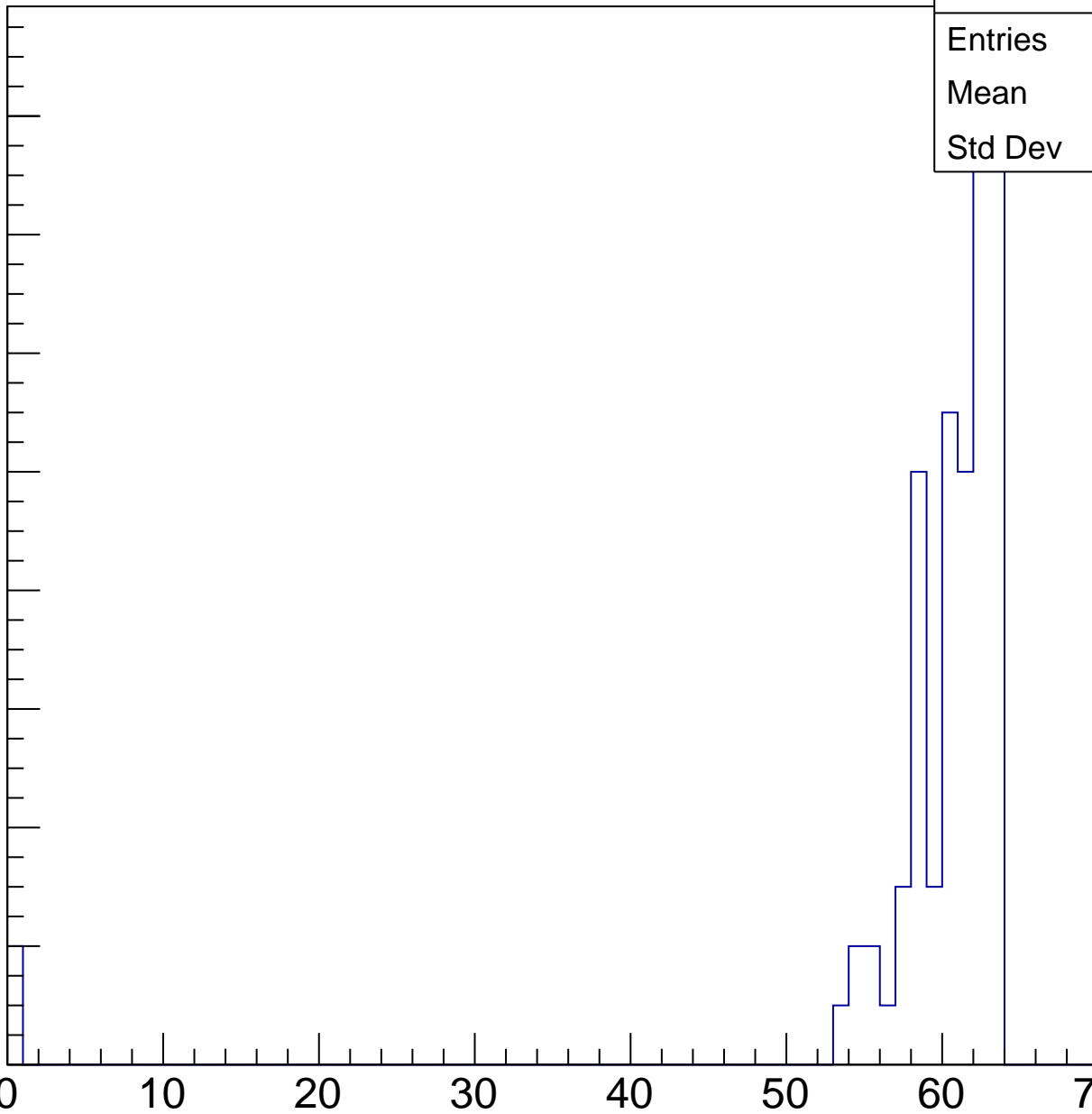
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.81
Std Dev	9.85

ampl



B1L001S, U24-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

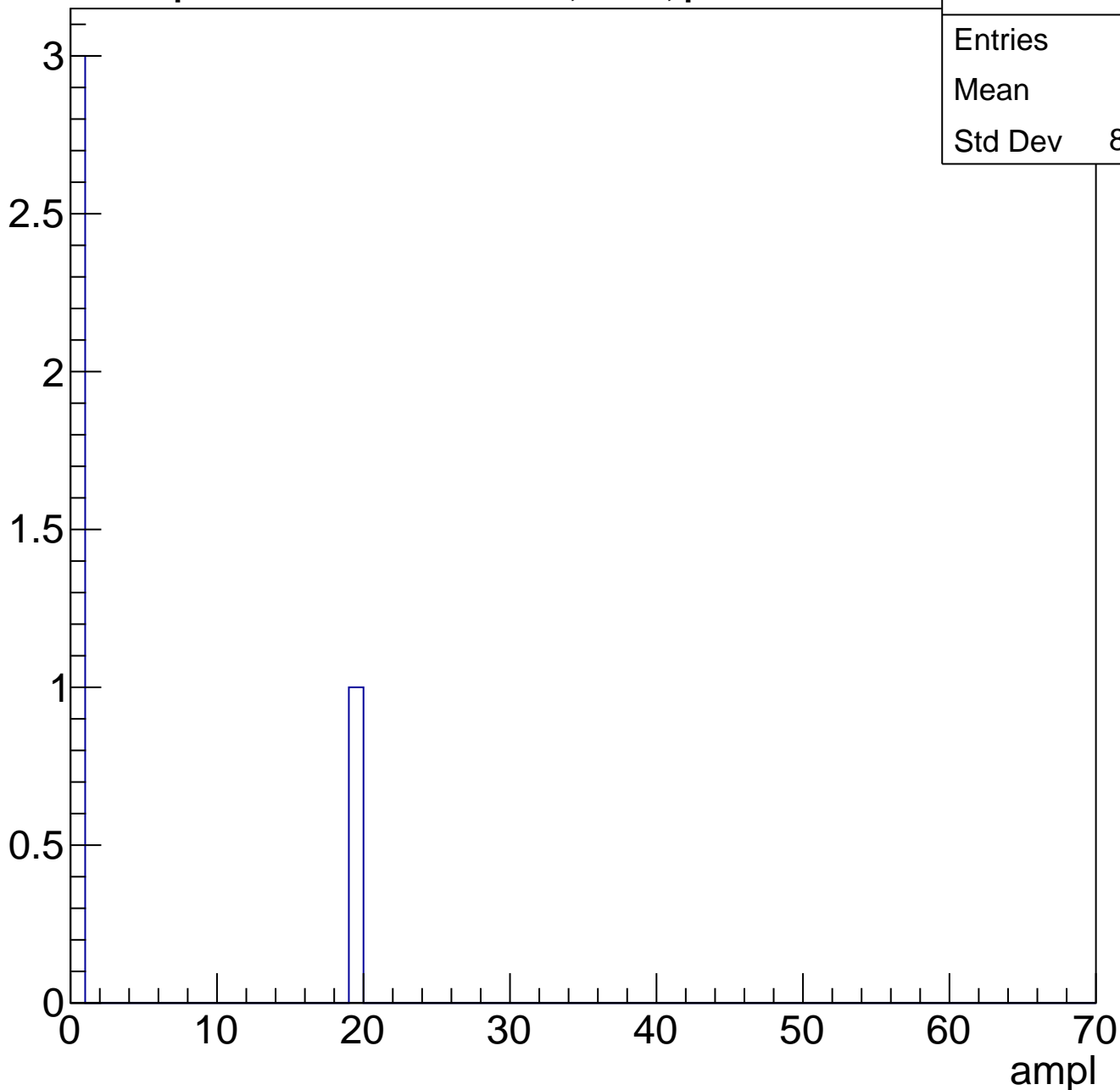
Entry



B1L001S, U24-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

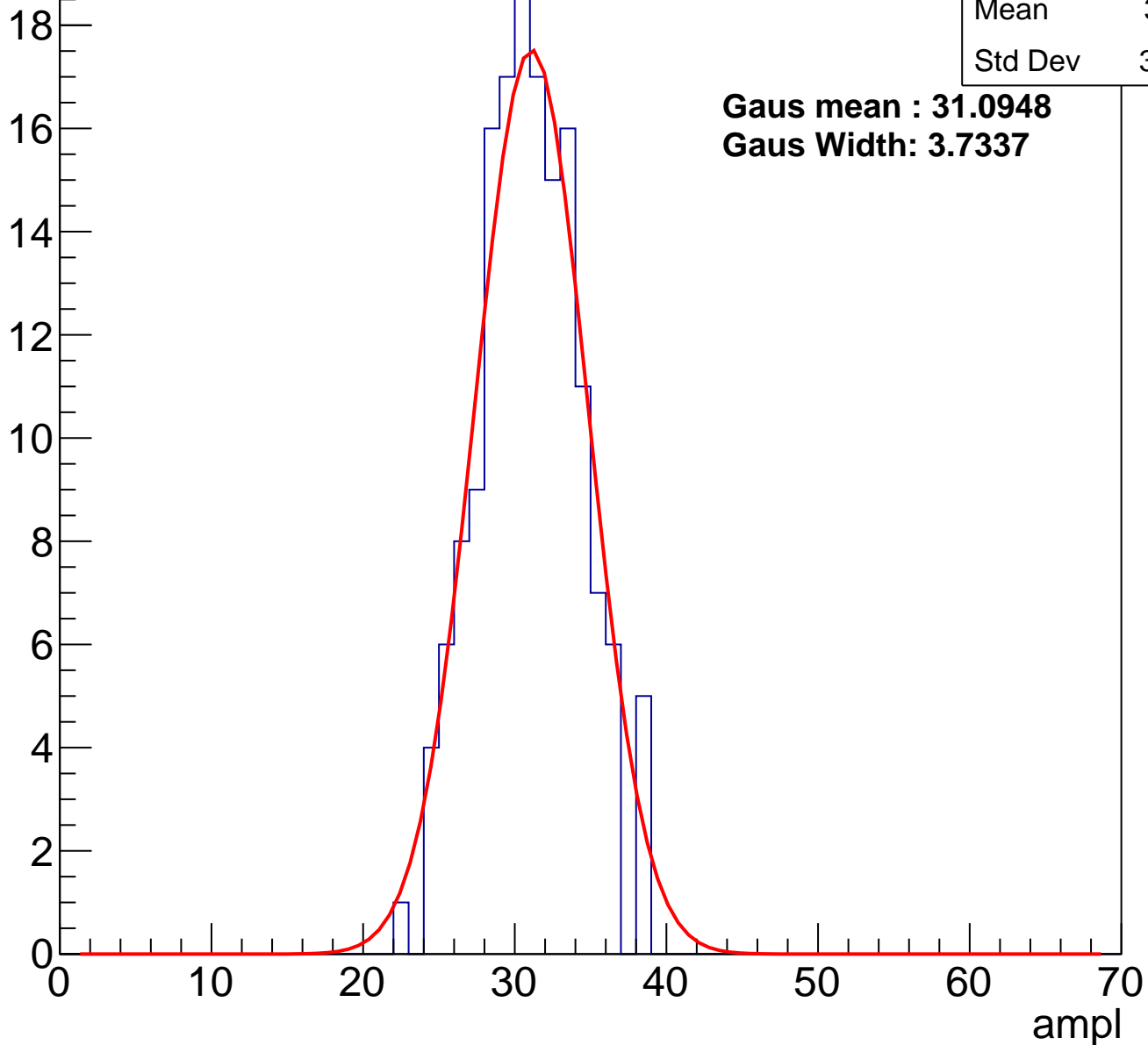


Entries	4
Mean	4.75
Std Dev	8.227

B1L001S, U24-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch84, adc1

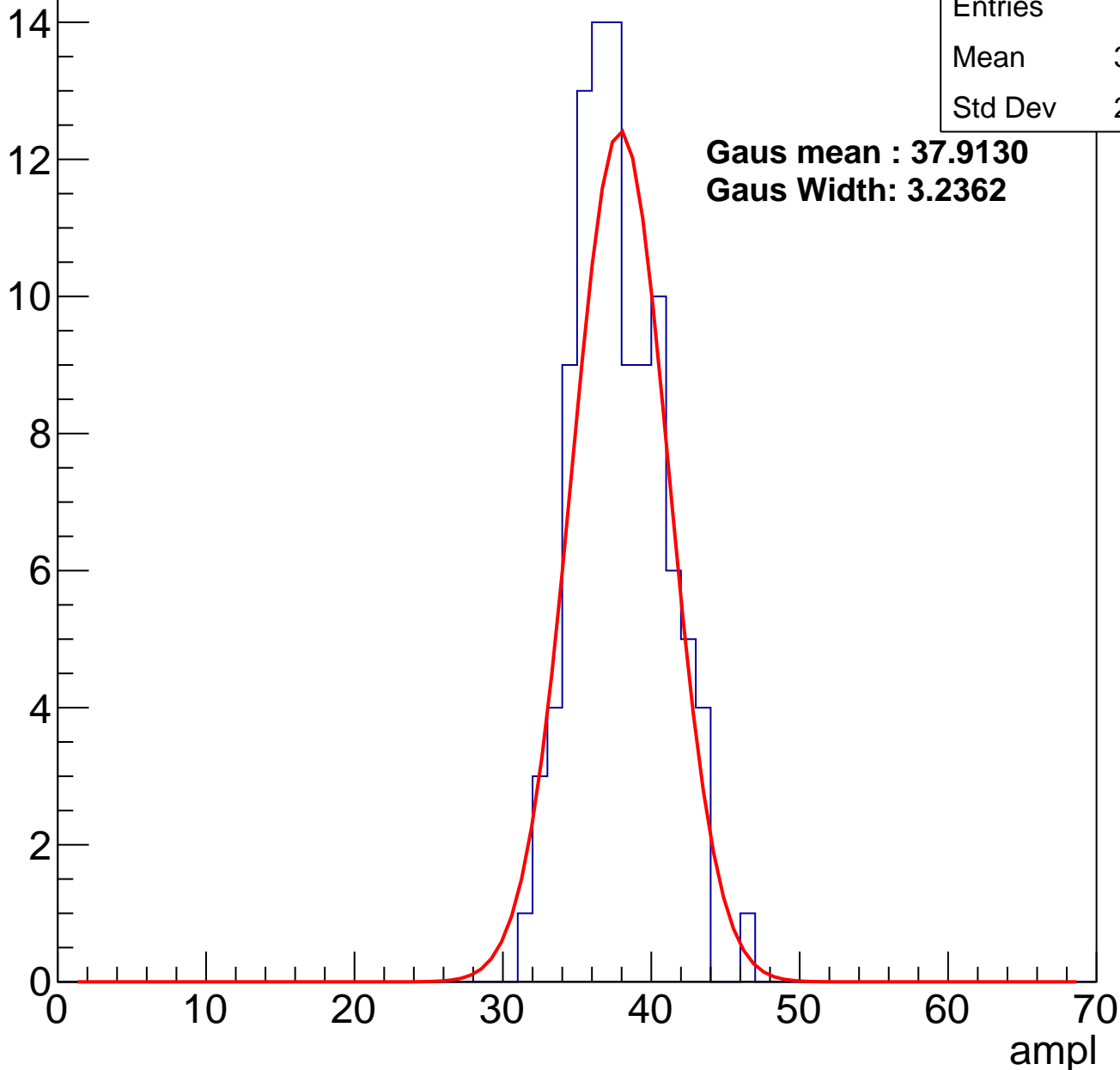
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	37.34
Std Dev	2.956

Gaus mean : 37.9130

Gaus Width: 3.2362

Entry



B1L001S, U24-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

134

Mean

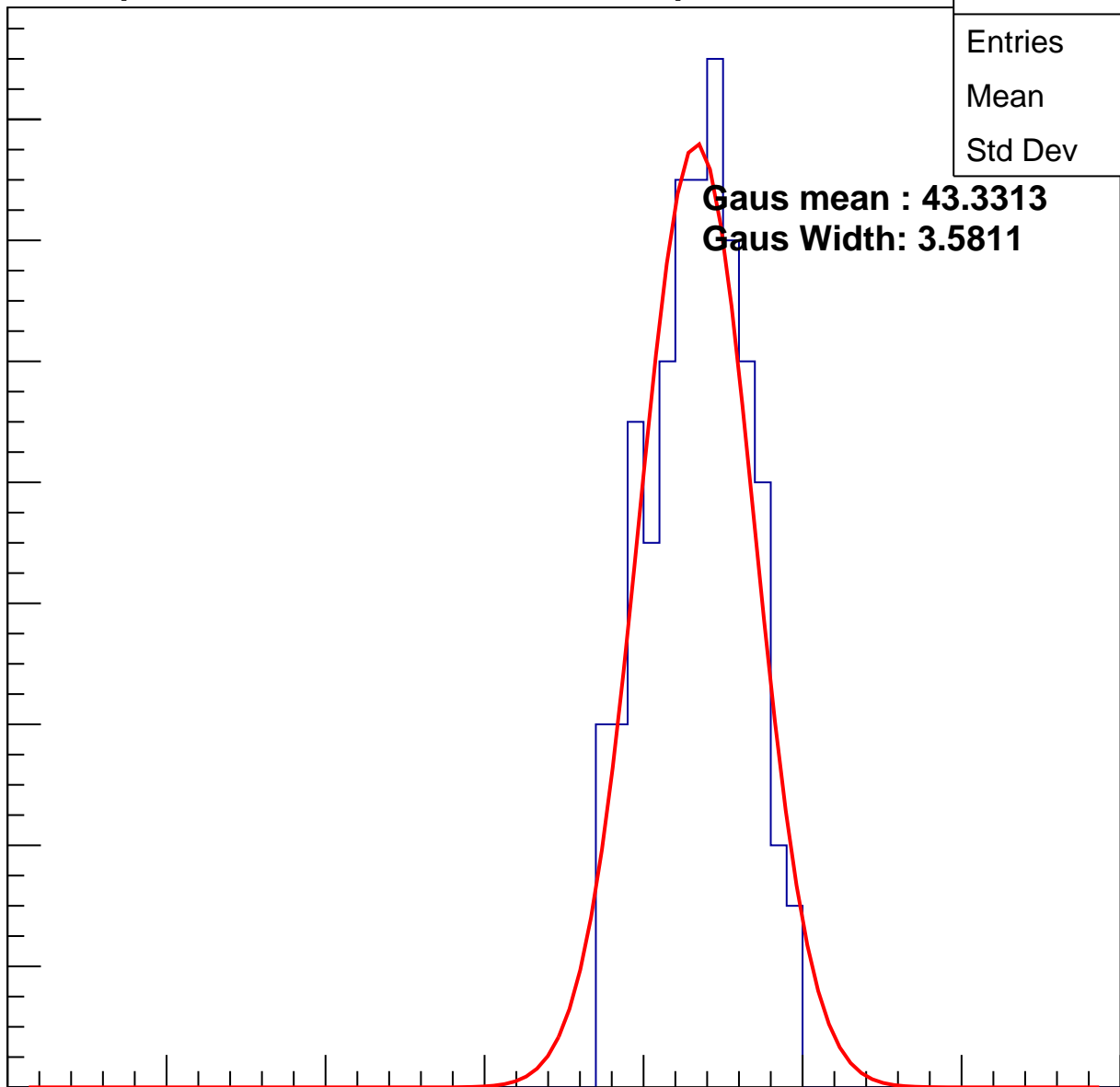
42.87

Std Dev

3.033

Gaus mean : 43.3313

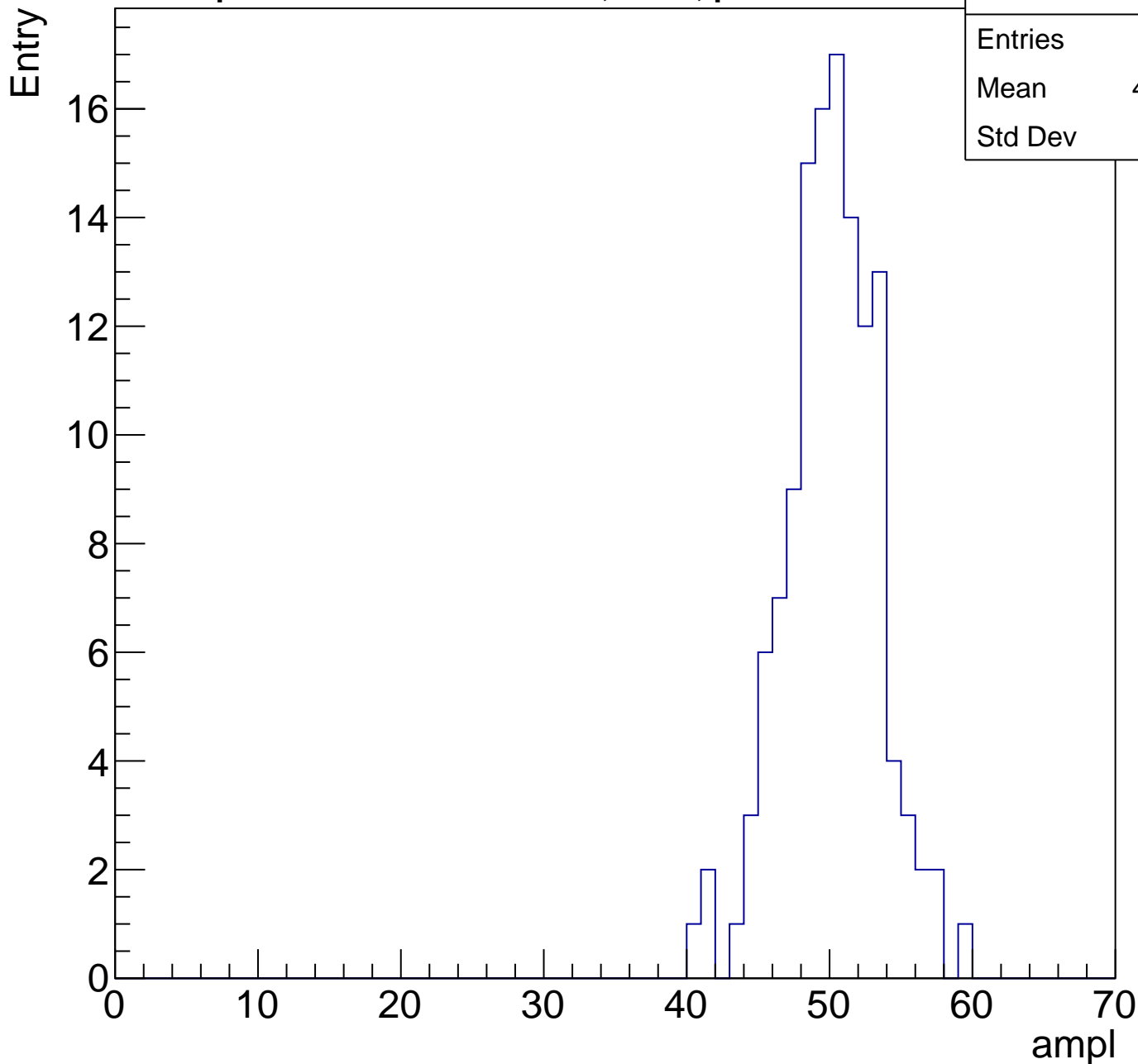
Gaus Width: 3.5811



B1L001S, U24-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

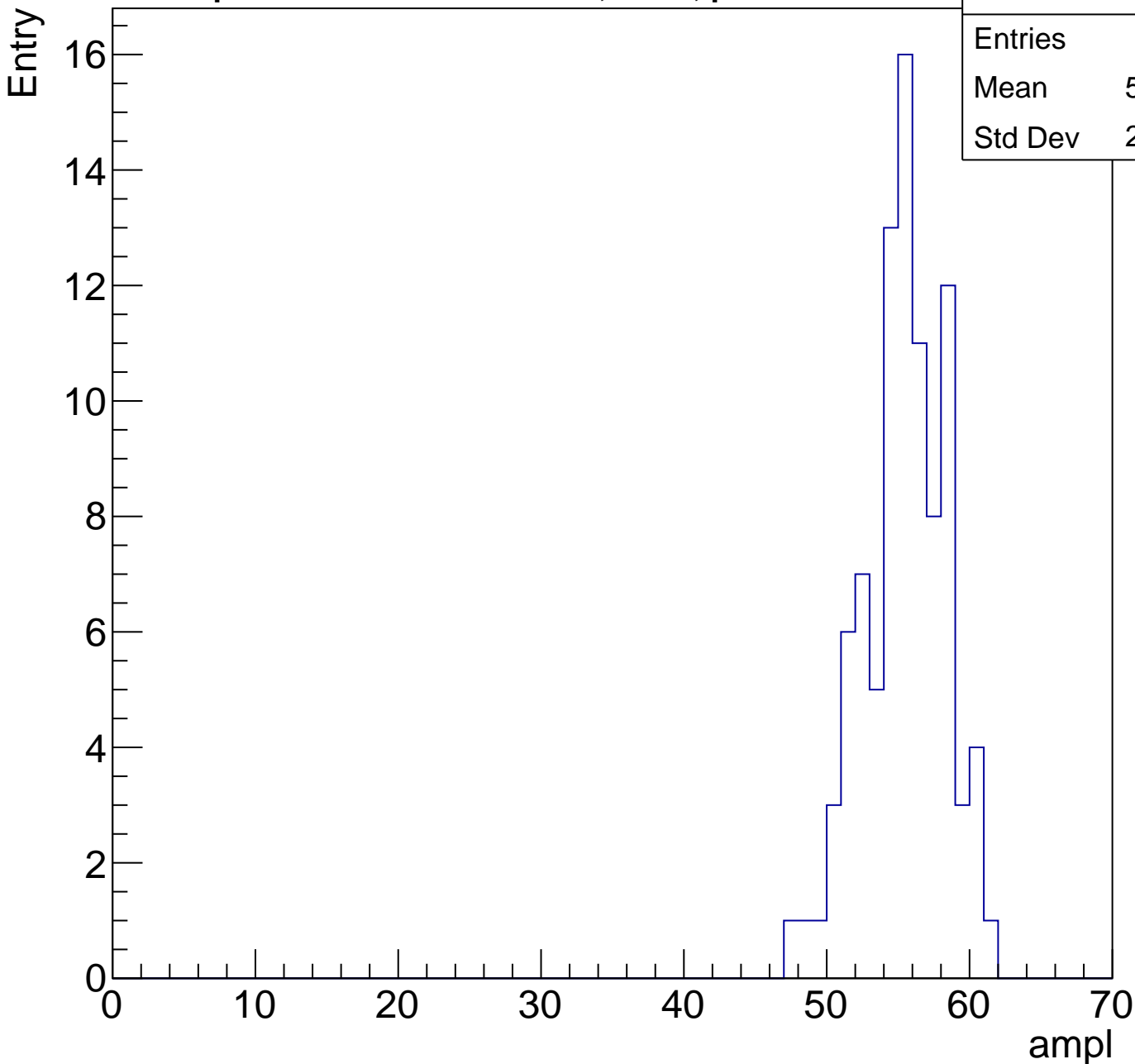
Entries	128
Mean	49.68
Std Dev	3.3



B1L001S, U24-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

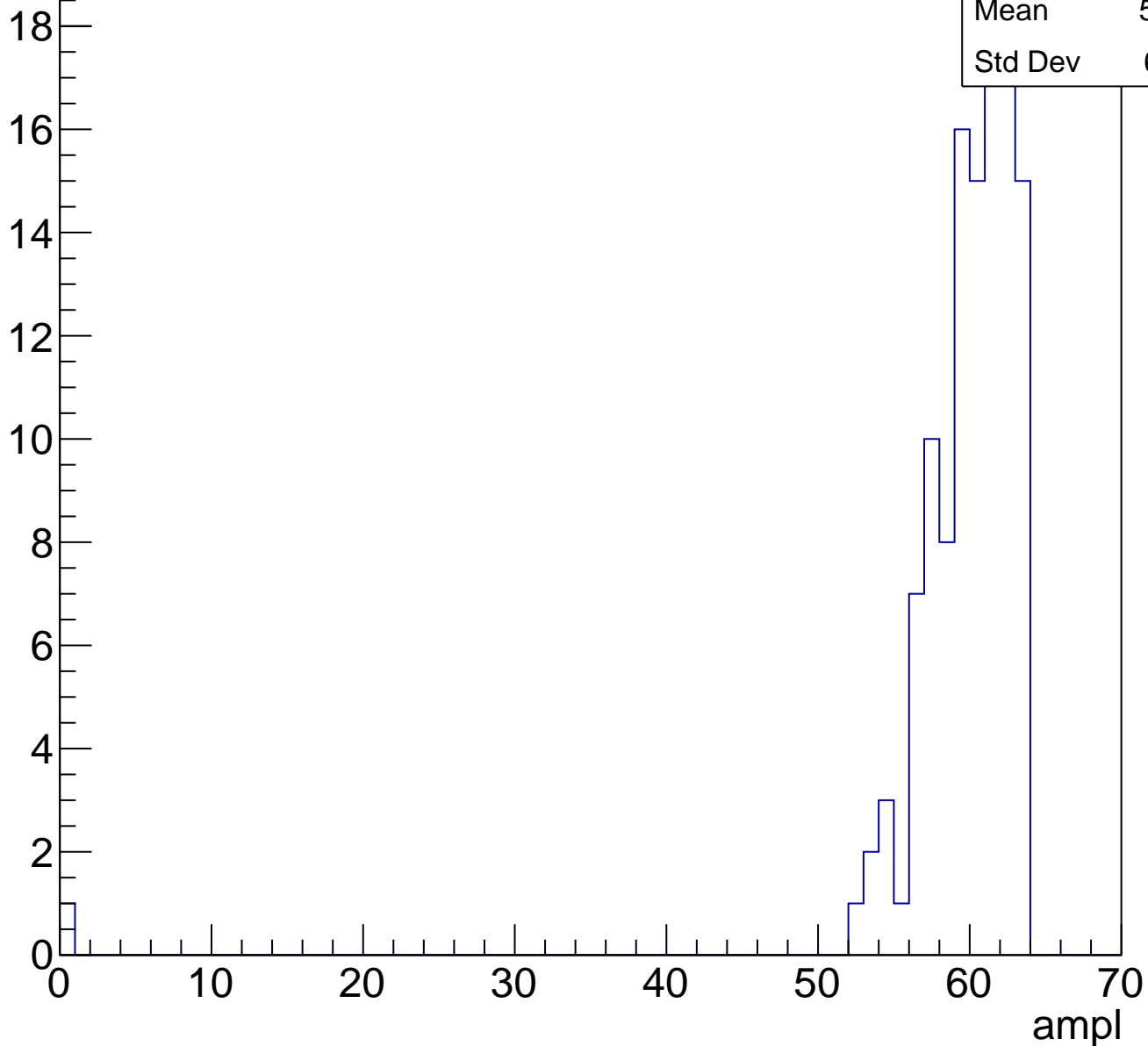
Entries	92
Mean	54.97
Std Dev	2.857



B1L001S, U24-ch84, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

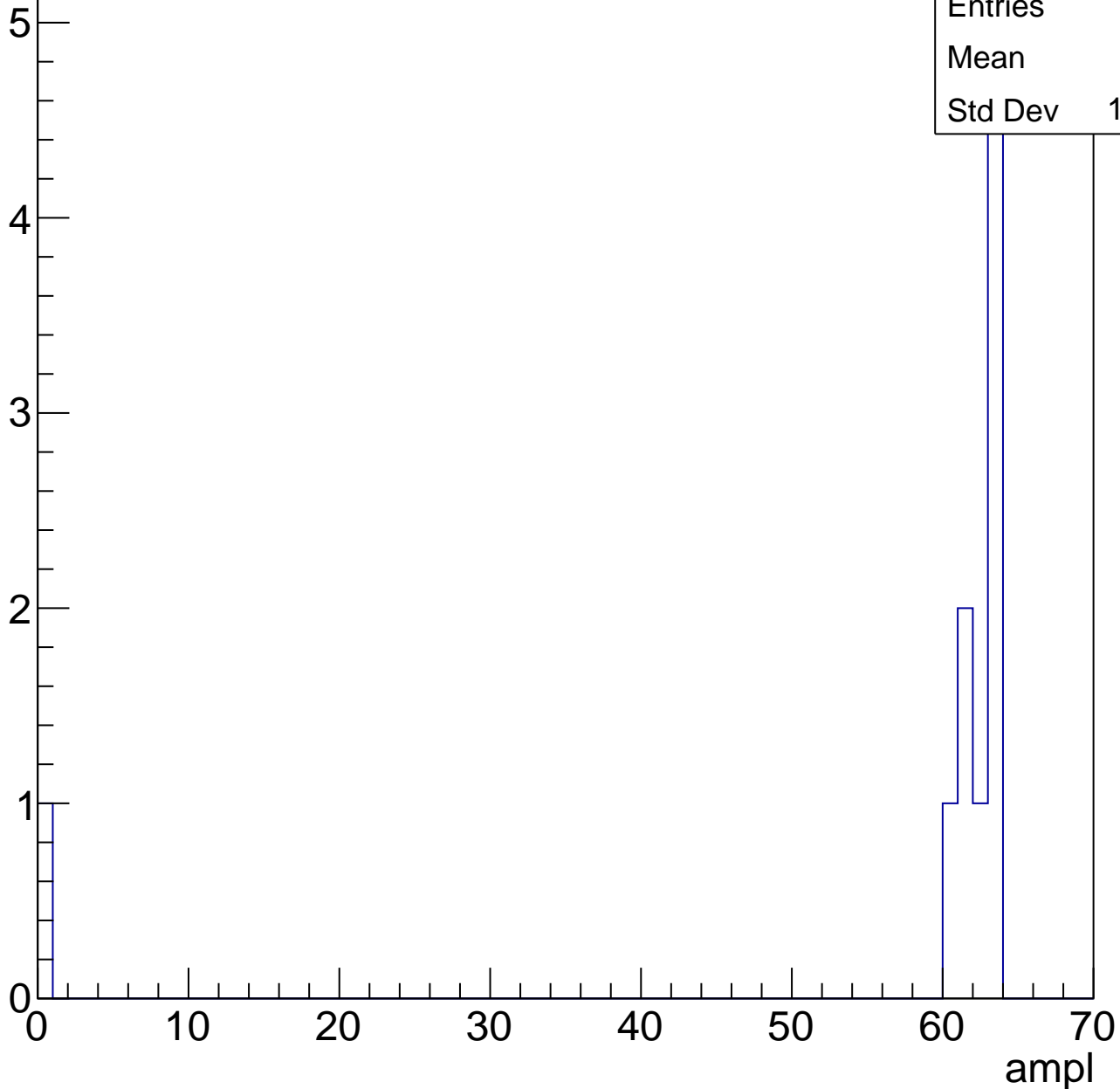


B1L001S, U24-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	55.9
Std Dev	18.66



B1L001S, U24-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U24-ch85, adc0

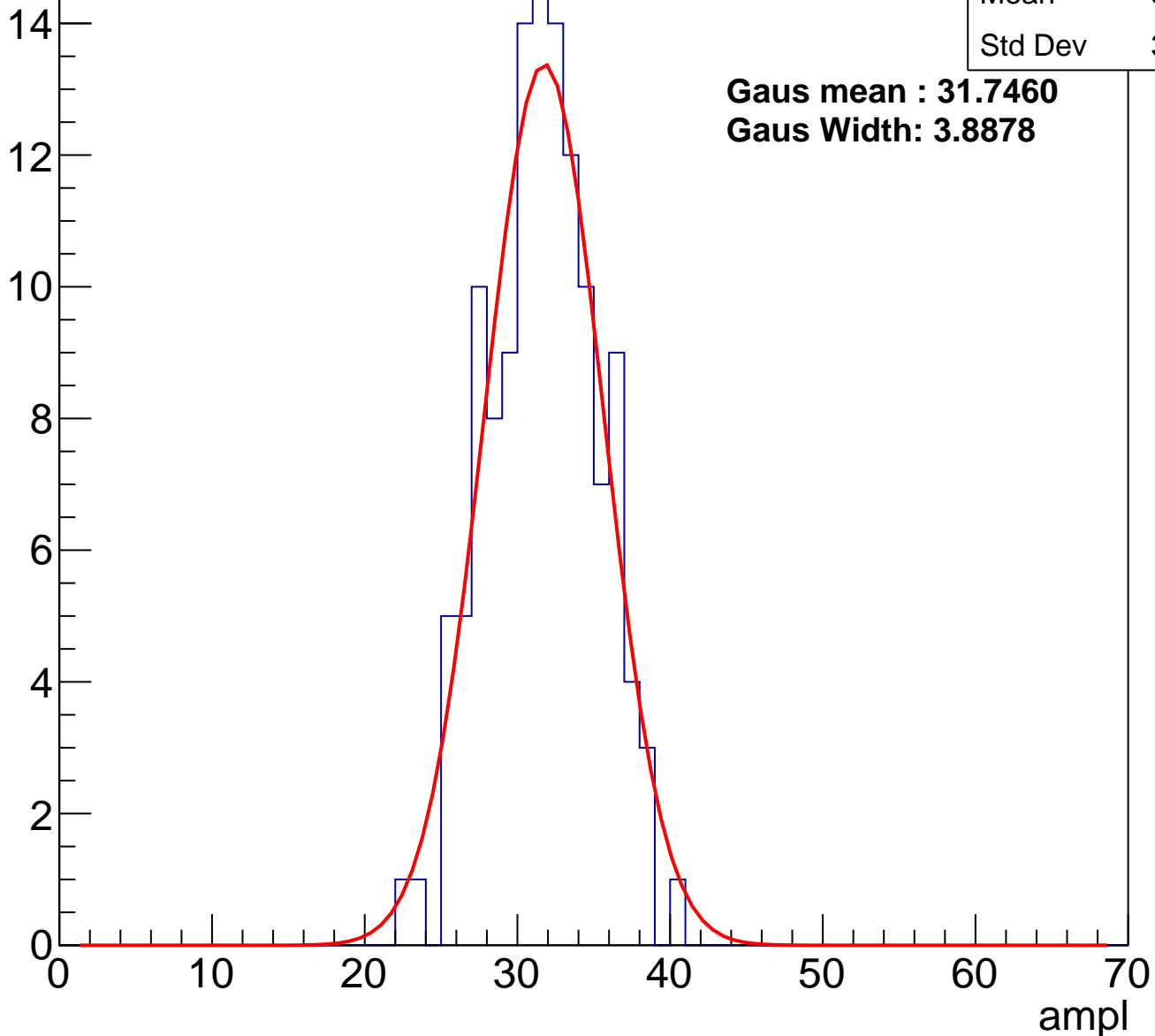
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	31.21
Std Dev	3.521

Gaus mean : 31.7460

Gaus Width: 3.8878

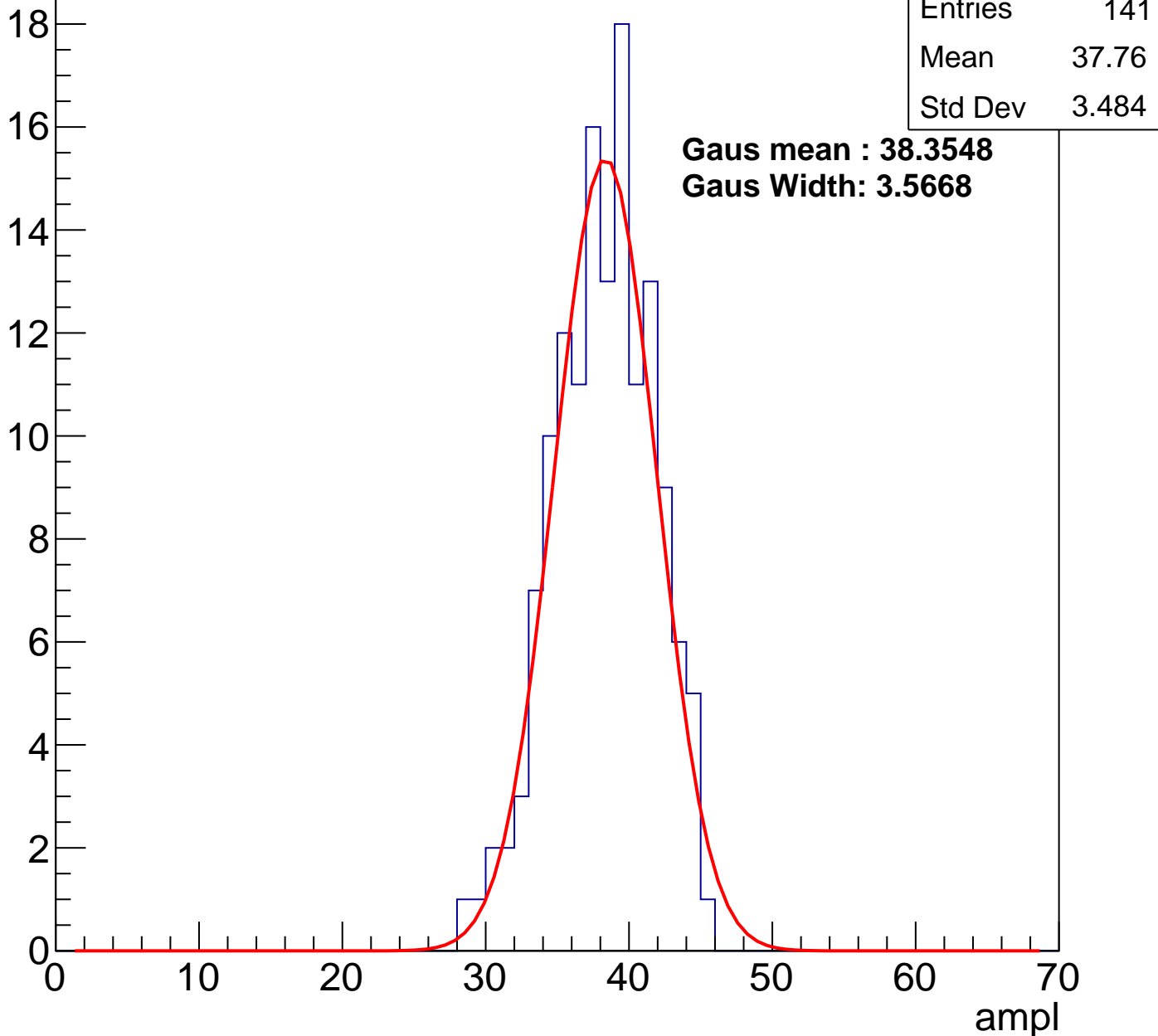
Entry



B1L001S, U24-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

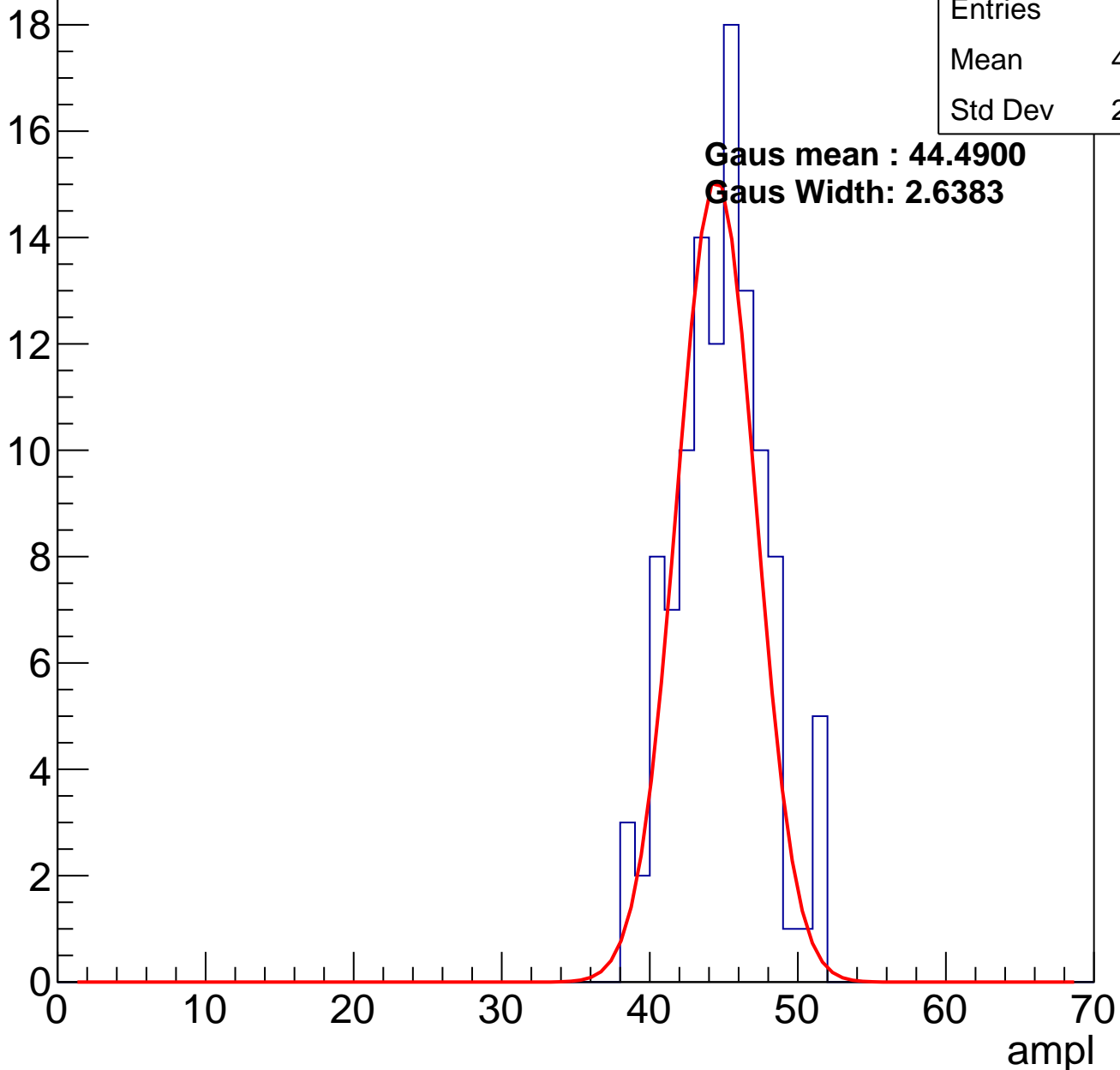
Entry



B1L001S, U24-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

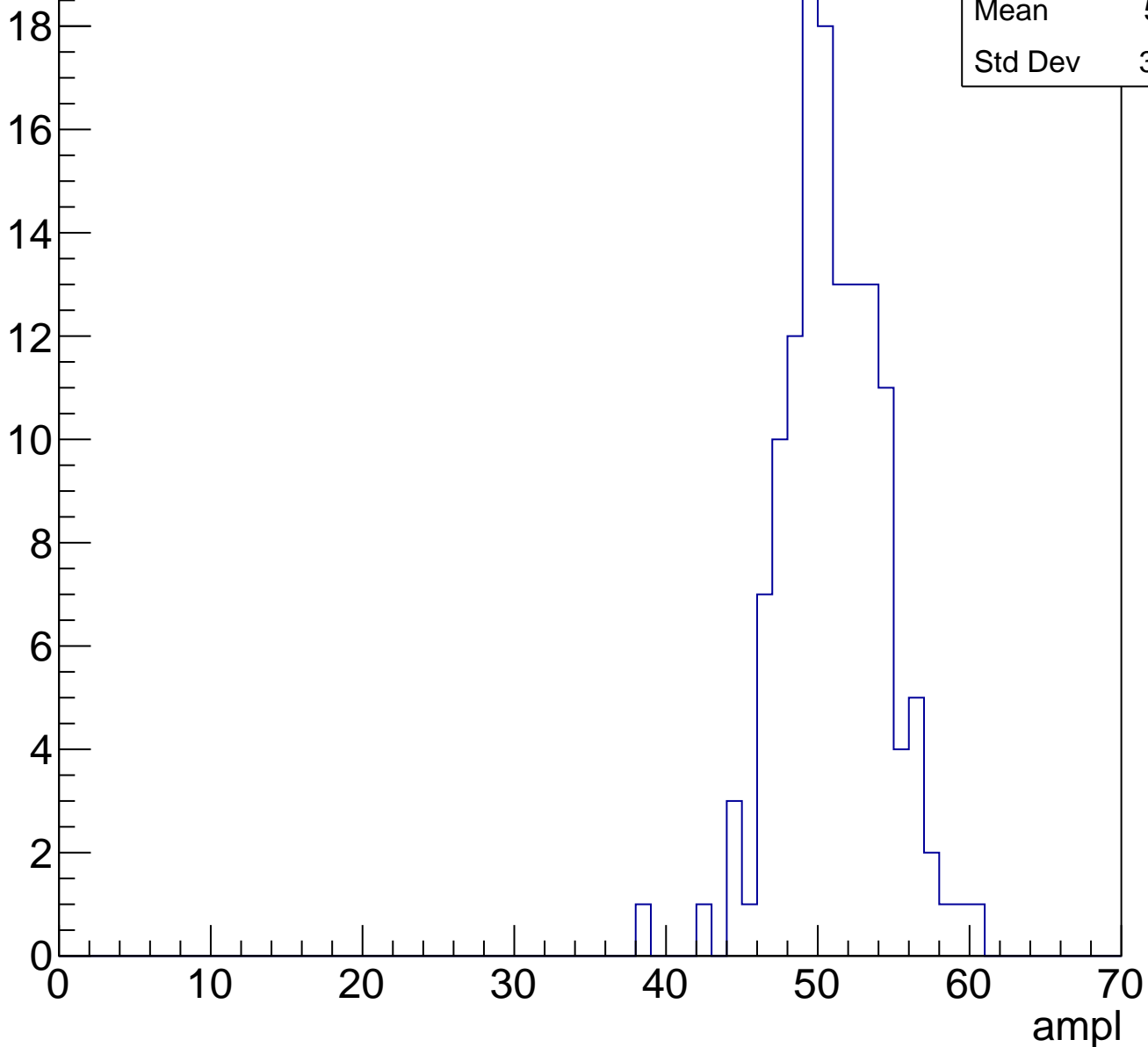


B1L001S, U24-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	50.51
Std Dev	3.382

Entry



B1L001S, U24-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

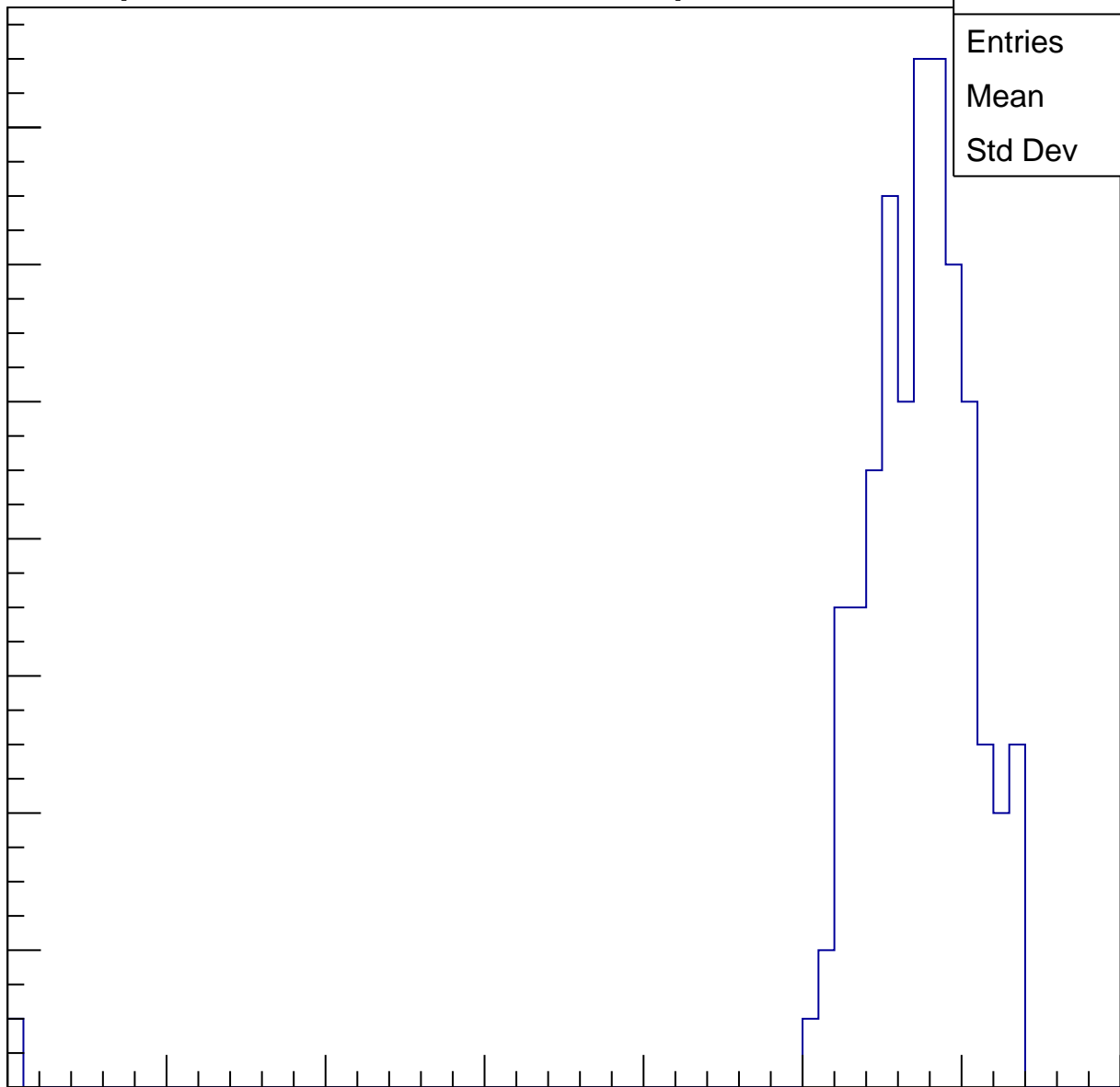
Entries	116
Mean	56.46
Std Dev	6.073

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch85, adc5

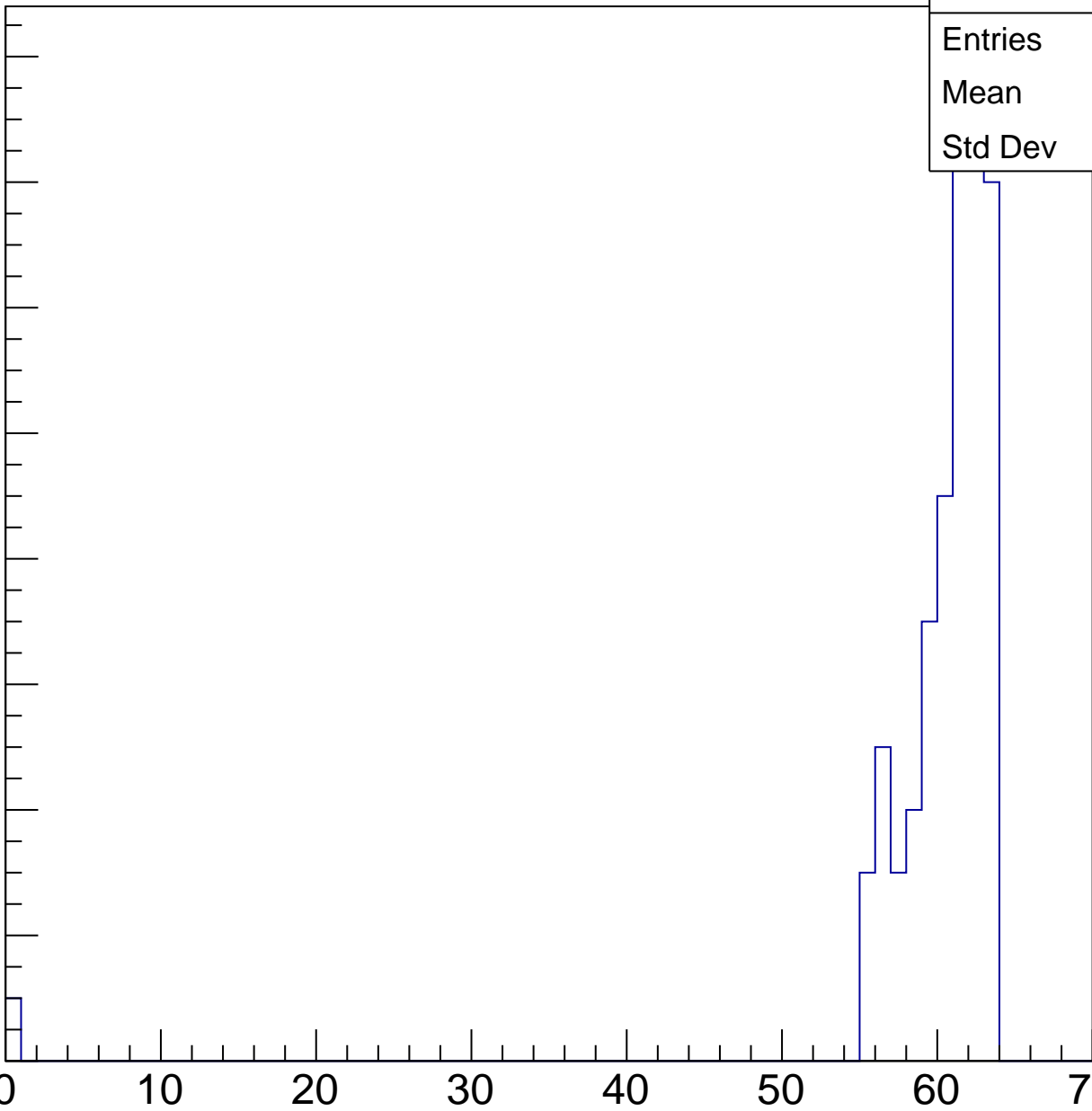
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.61
Std Dev	7.206

ampl



B1L001S, U24-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L001S, U24-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch86, adc0

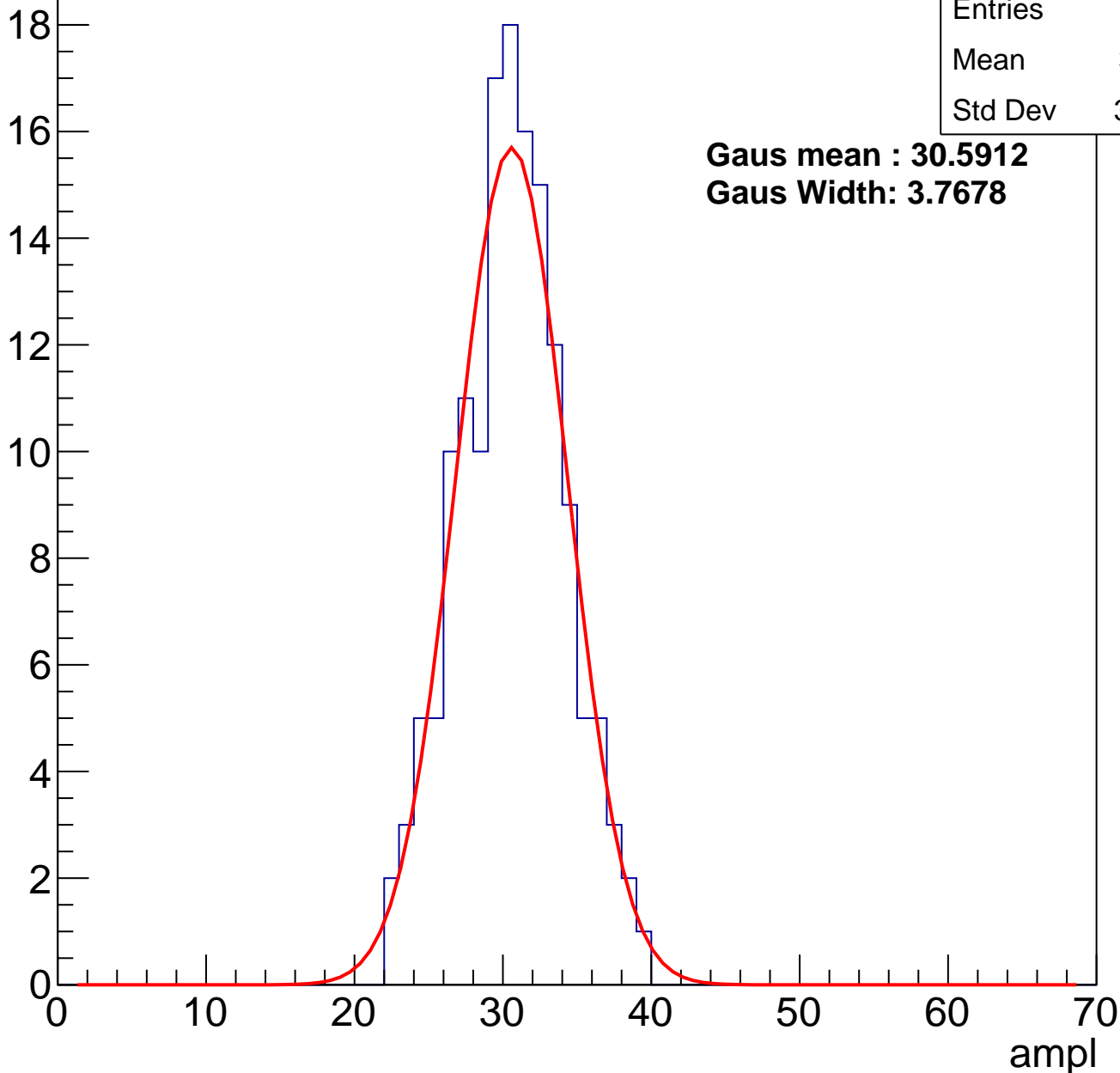
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	30.11
Std Dev	3.555

Gaus mean : 30.5912

Gaus Width: 3.7678

Entry



B1L001S, U24-ch86, adc1

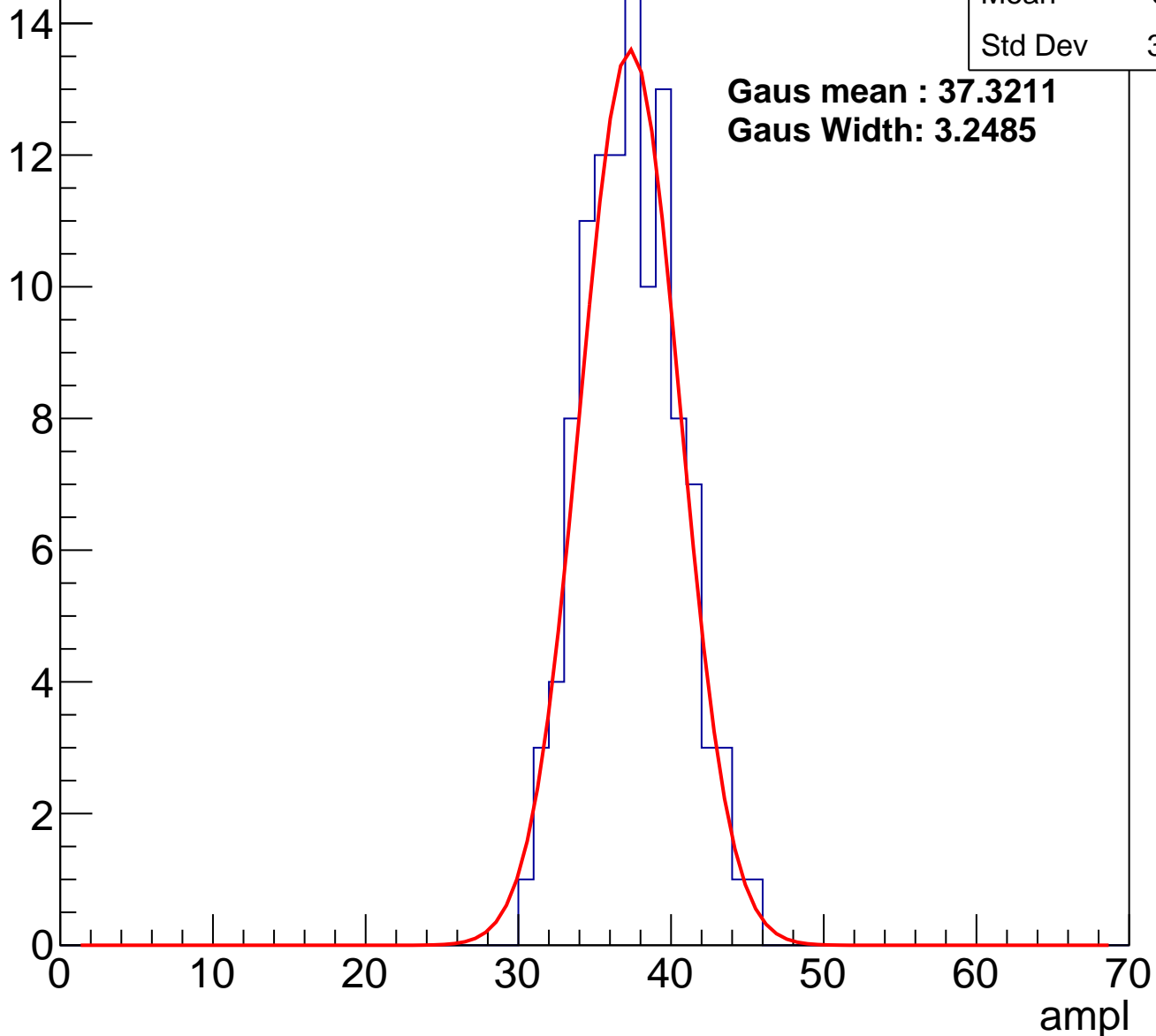
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	36.91
Std Dev	3.113

Gaus mean : 37.3211

Gaus Width: 3.2485

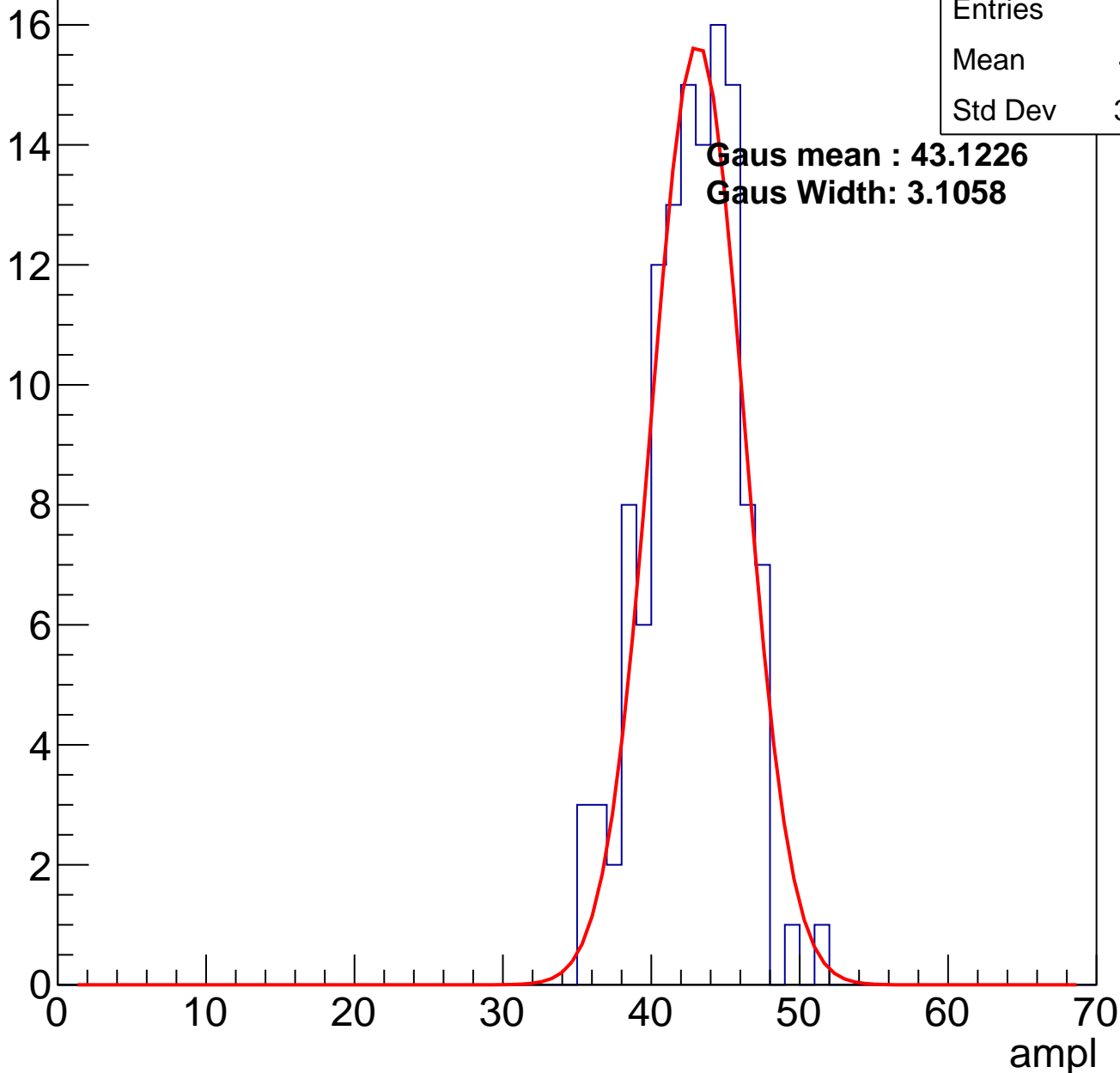
Entry



B1L001S, U24-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

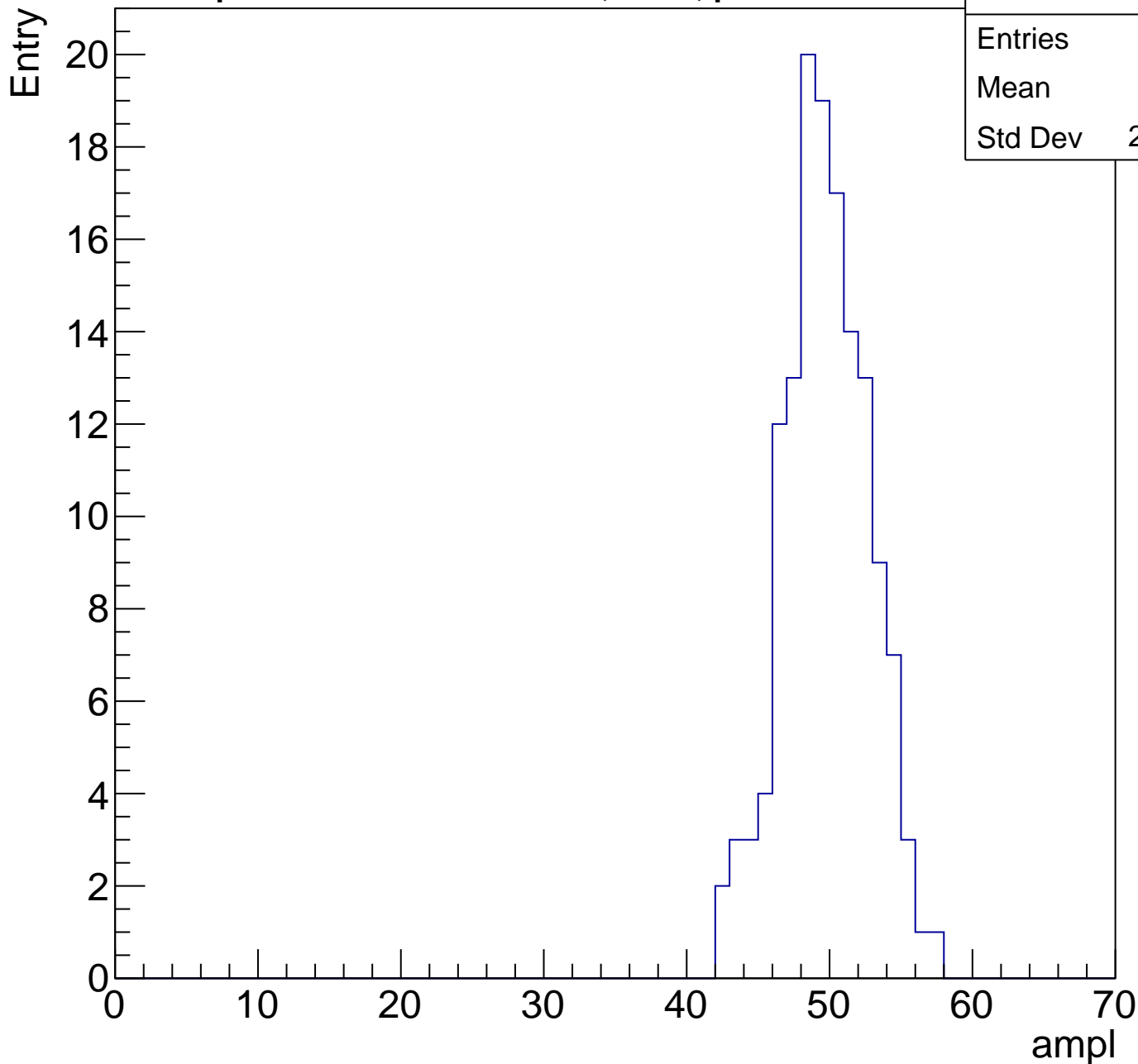
Entry



B1L001S, U24-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.3
Std Dev	2.968

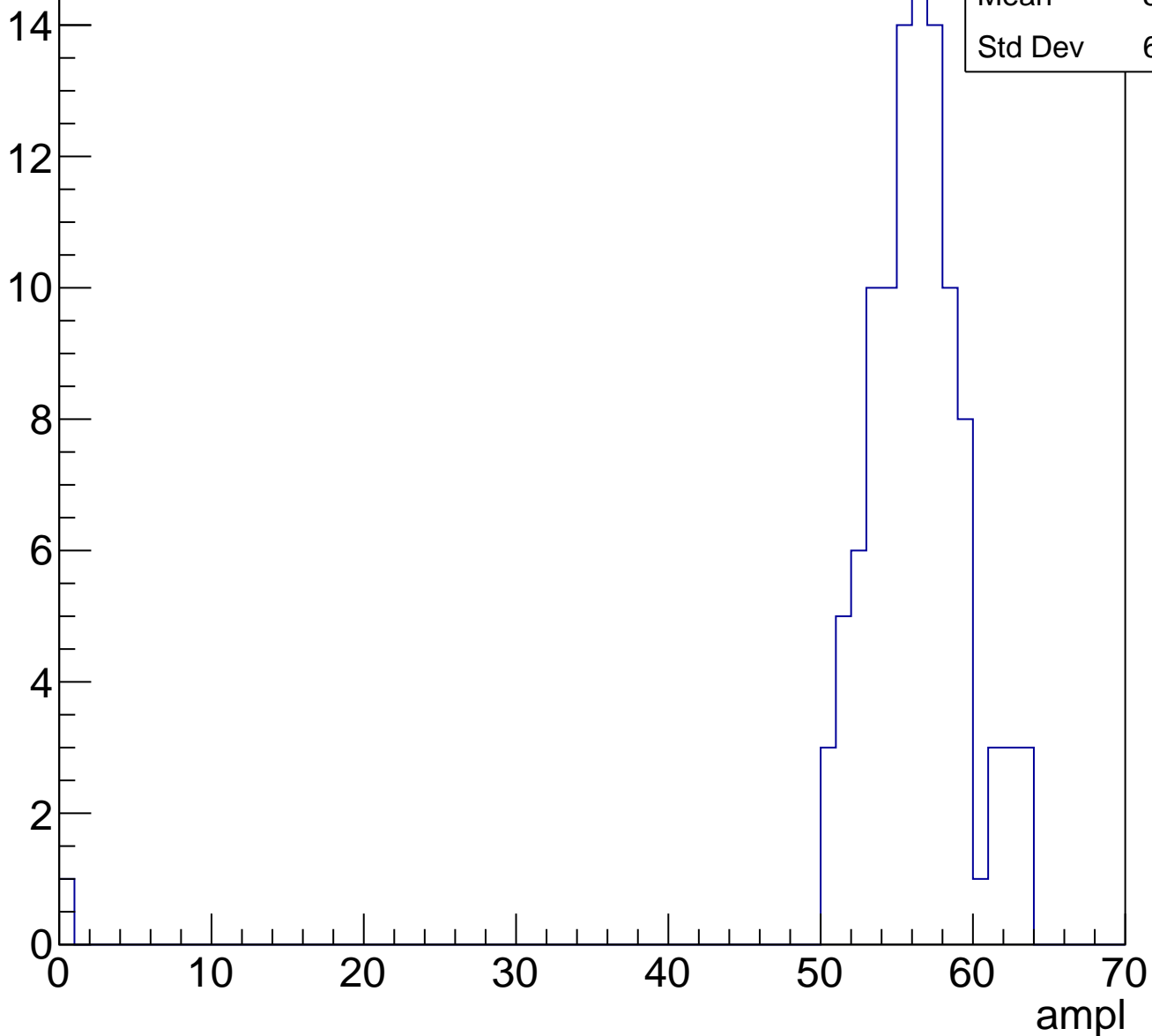


B1L001S, U24-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	55.33
Std Dev	6.169

Entry



B1L001S, U24-ch86, adc5

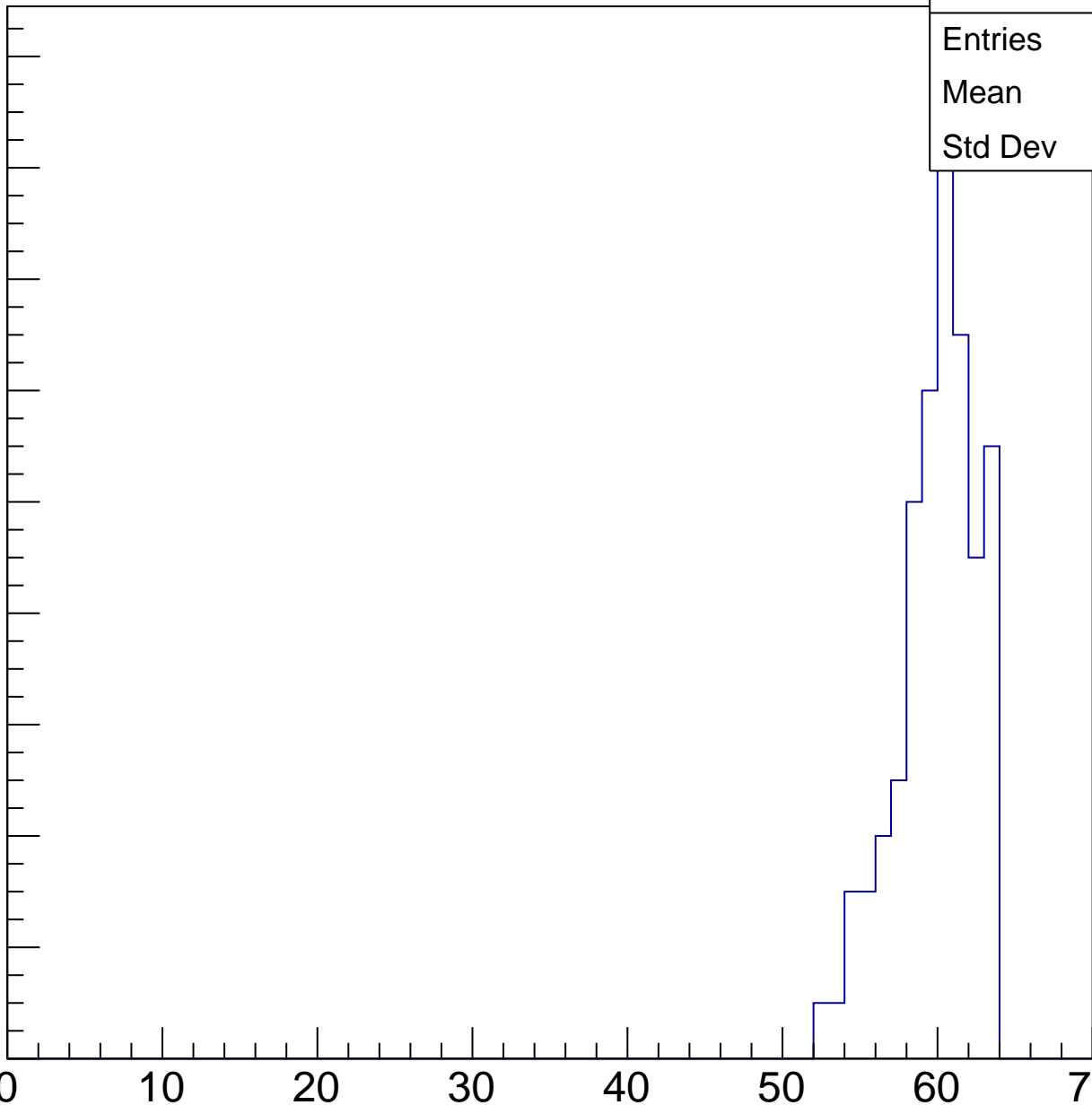
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	90
Mean	59.48
Std Dev	2.54

ampl

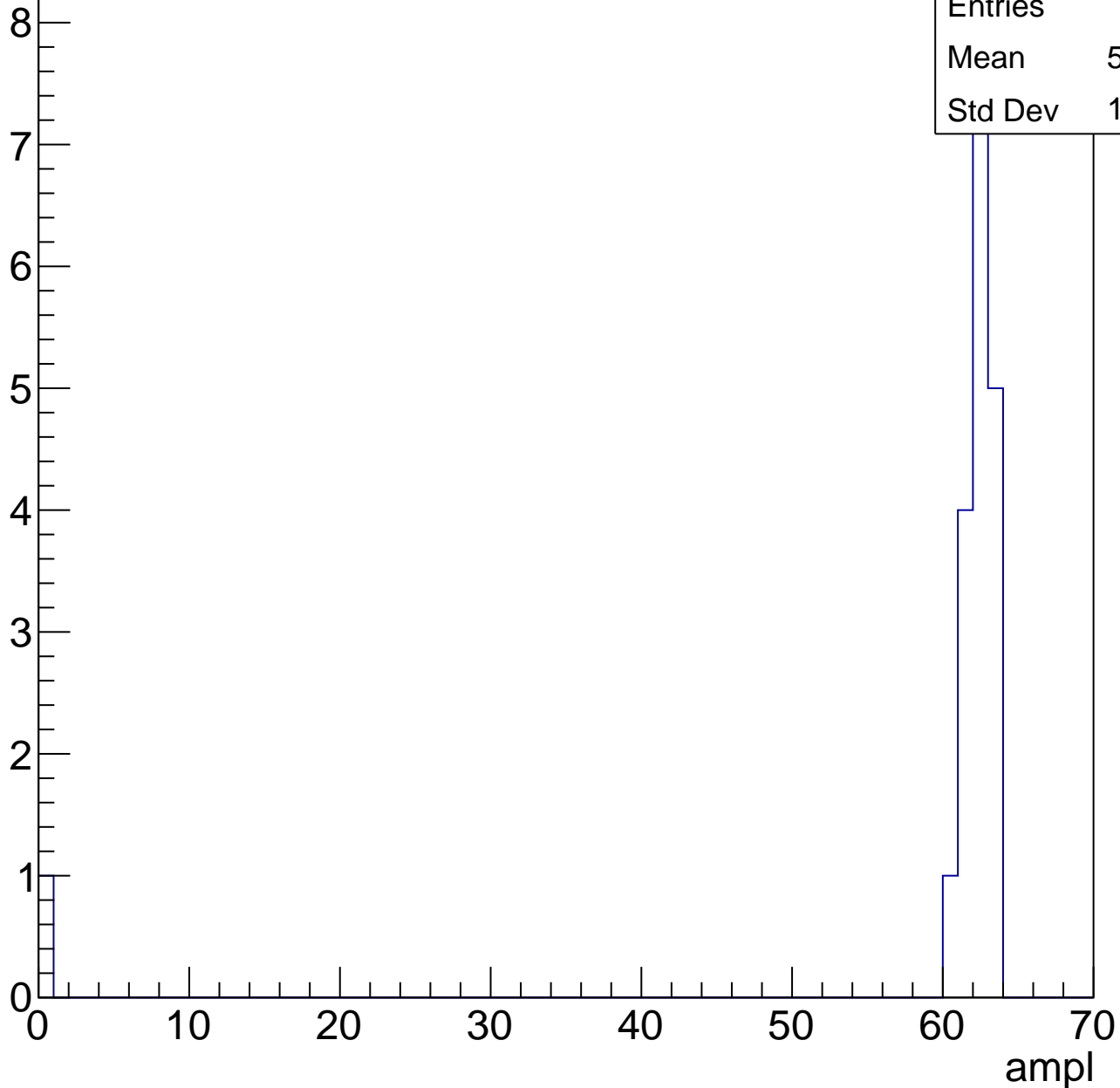


B1L001S, U24-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	58.68
Std Dev	13.86



B1L001S, U24-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.26
Std Dev	3.532

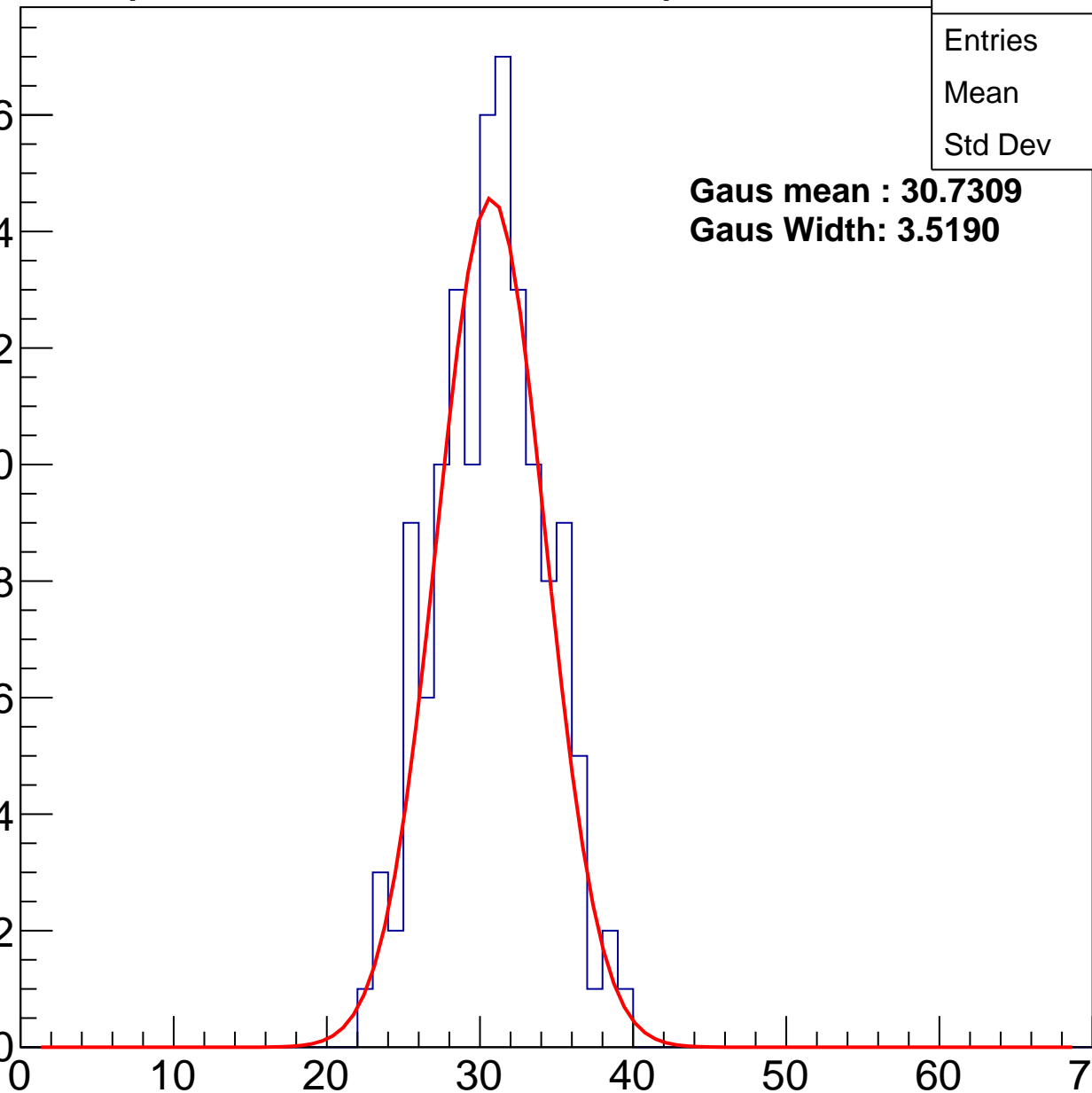
Gaus mean : 30.7309

Gaus Width: 3.5190

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch87, adc1

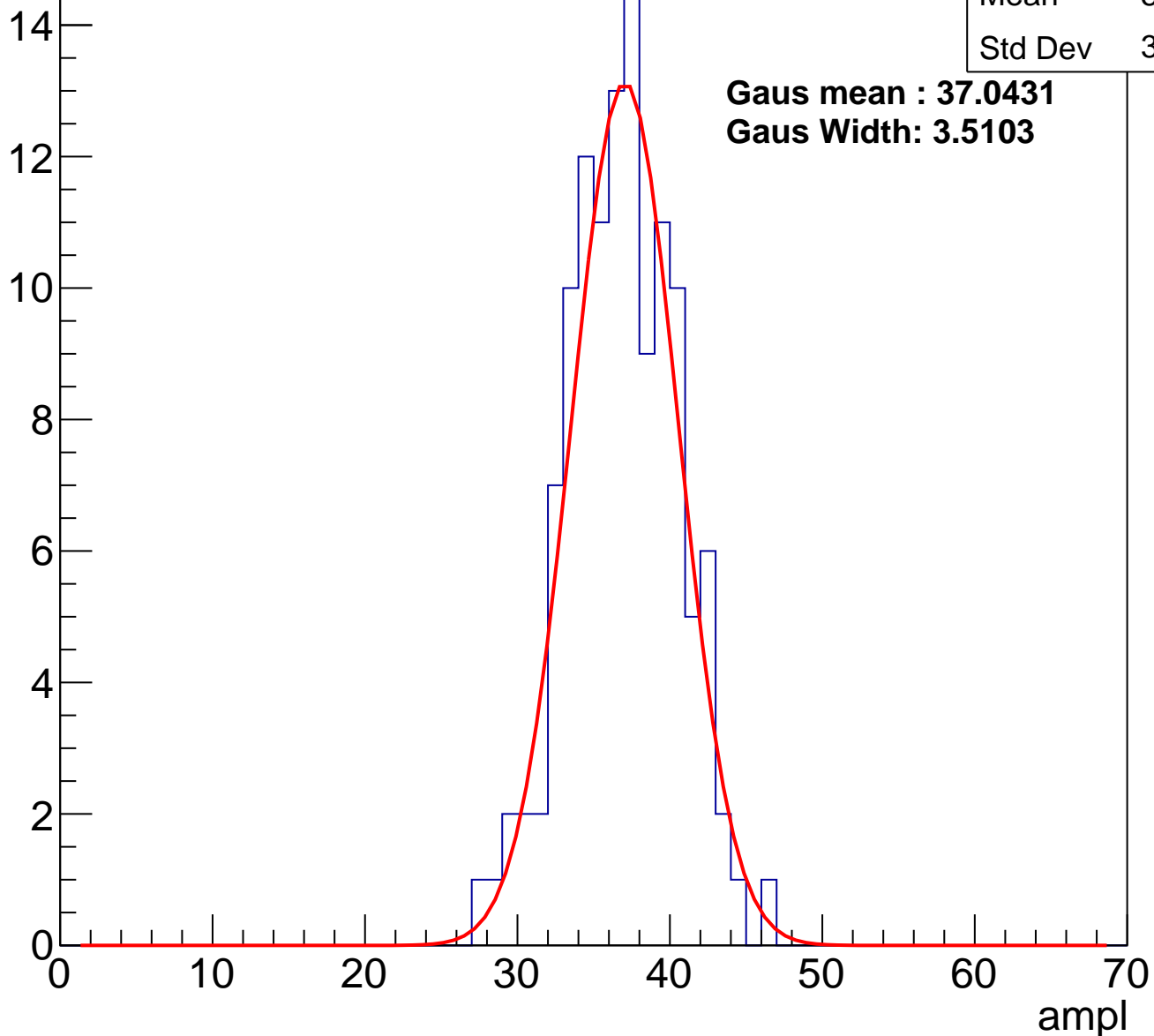
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	36.44
Std Dev	3.539

Gaus mean : 37.0431

Gaus Width: 3.5103

Entry



B1L001S, U24-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

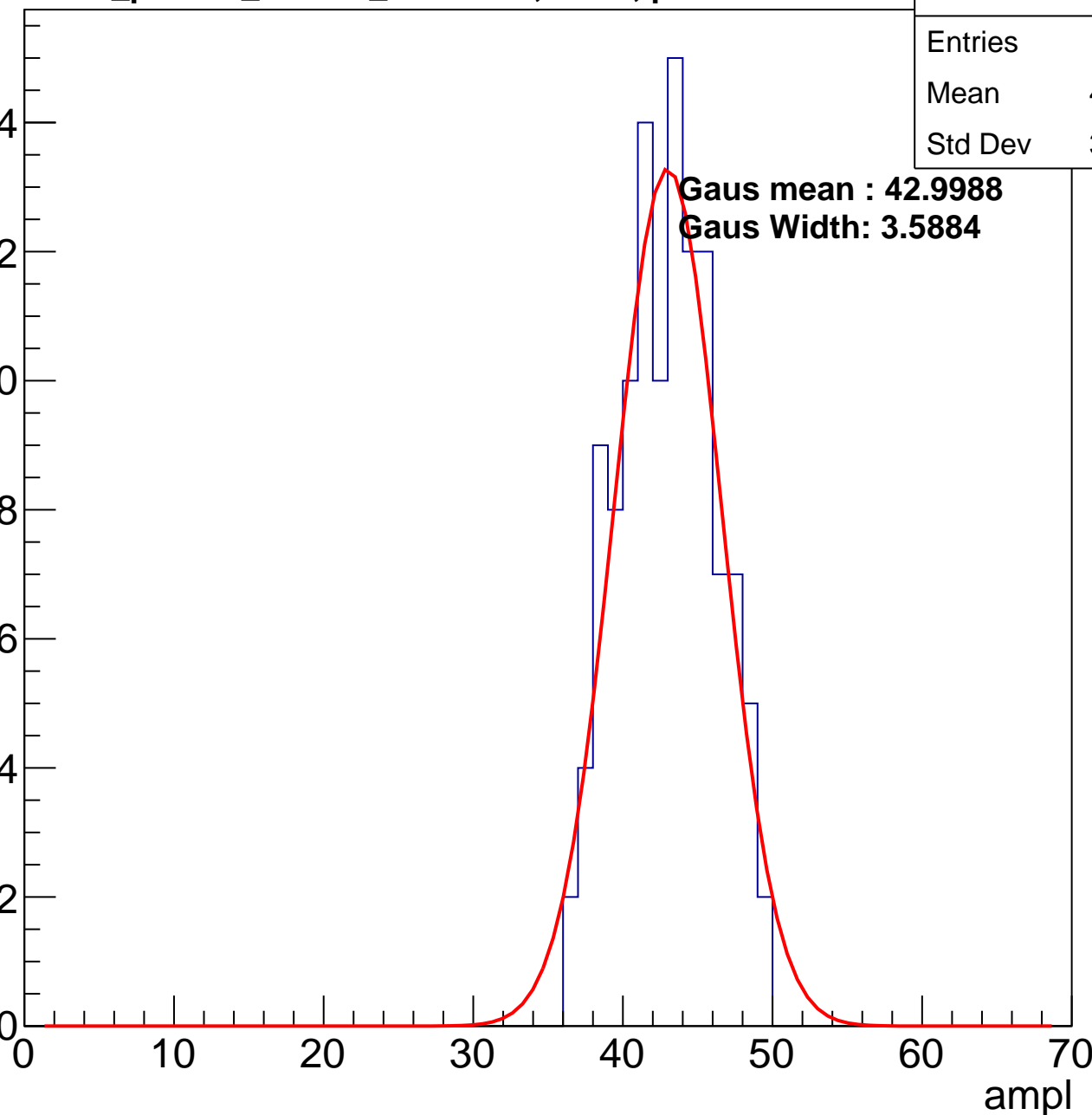
Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	42.48
Std Dev	3.137

Gaus mean : 42.9988

Gaus Width: 3.5884



ampl

B1L001S, U24-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

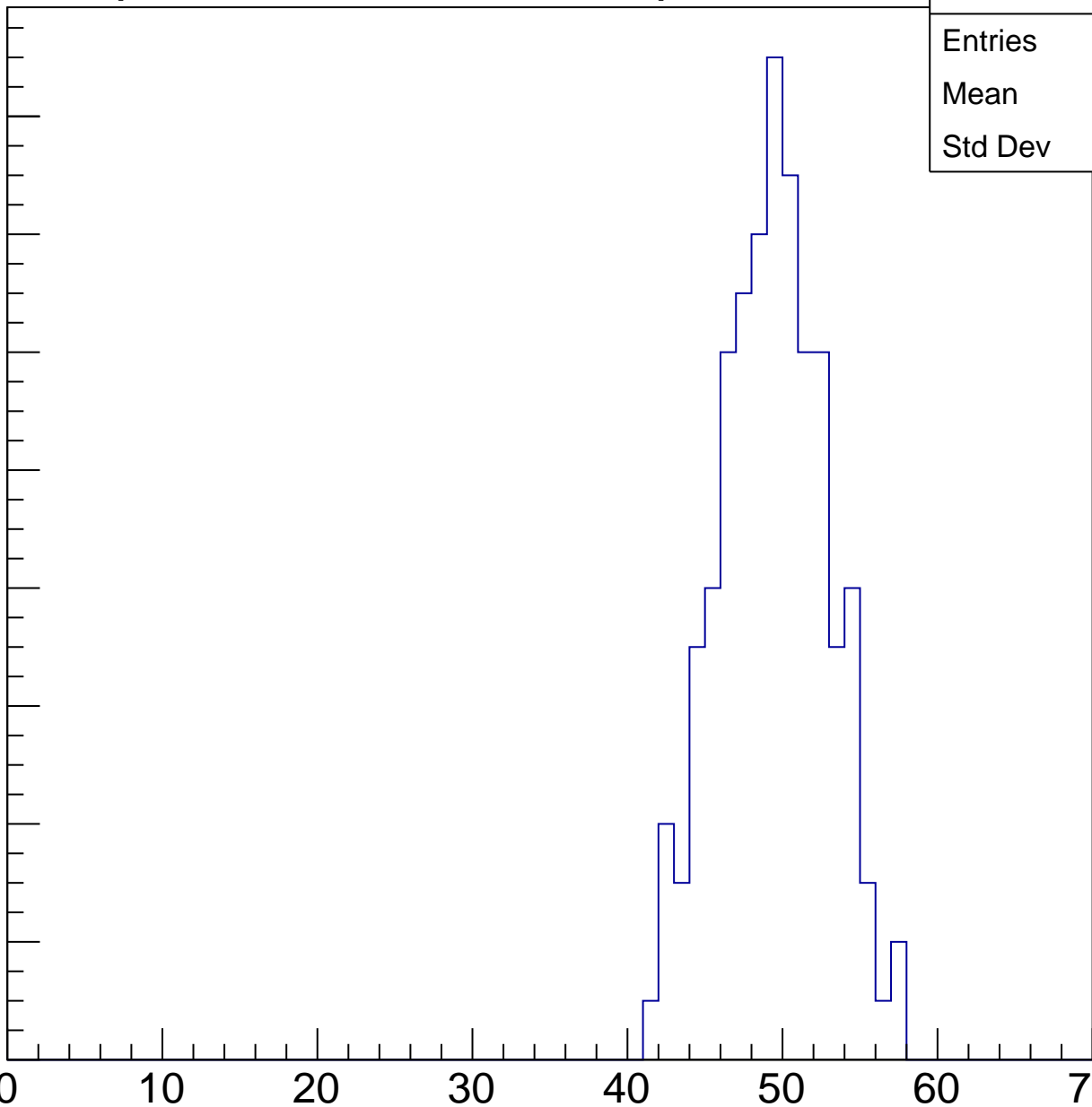
Entries	139
Mean	48.91
Std Dev	3.405

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

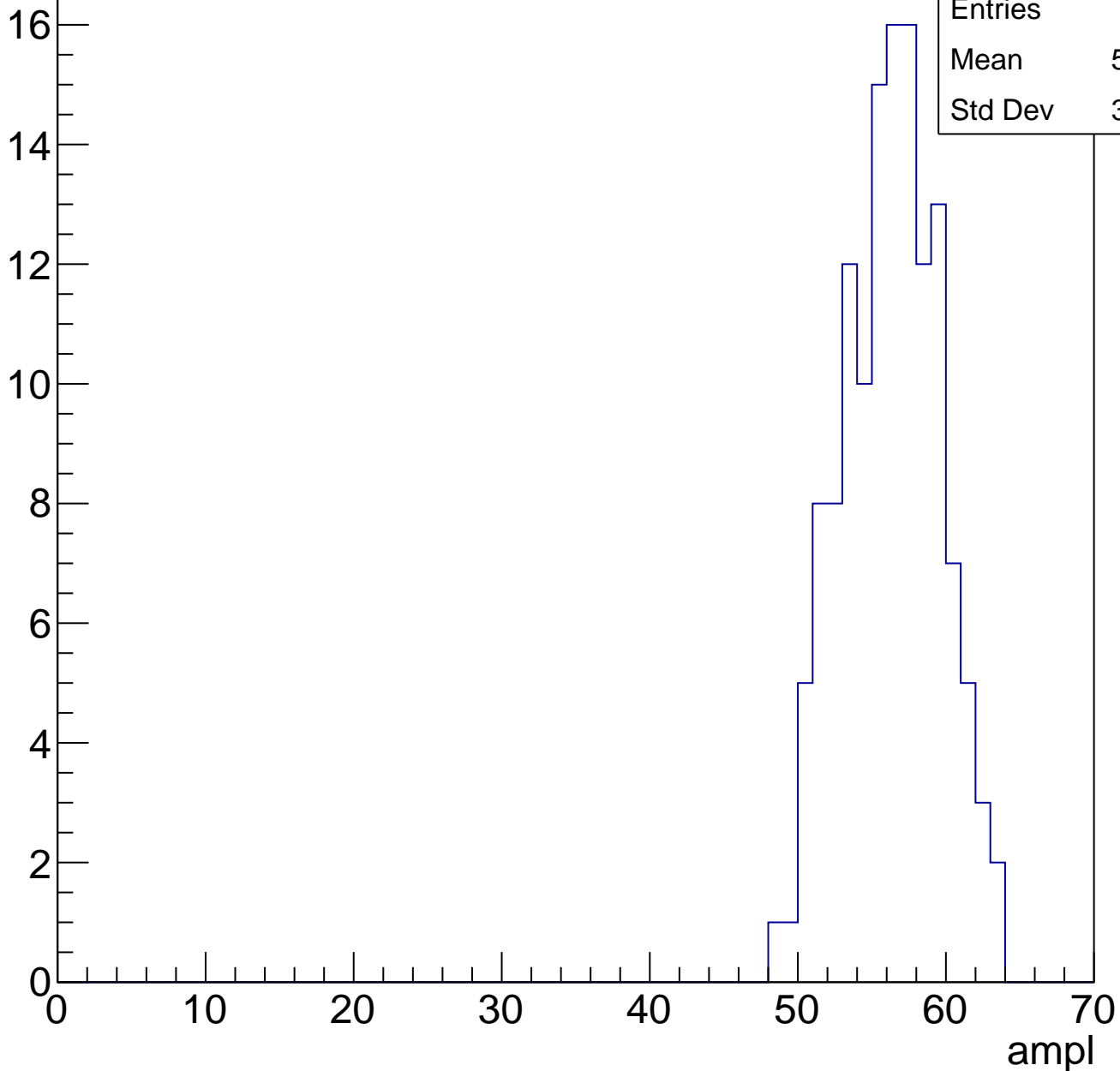
ampl



B1L001S, U24-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	134
Mean	55.82
Std Dev	3.237

B1L001S, U24-ch87, adc5

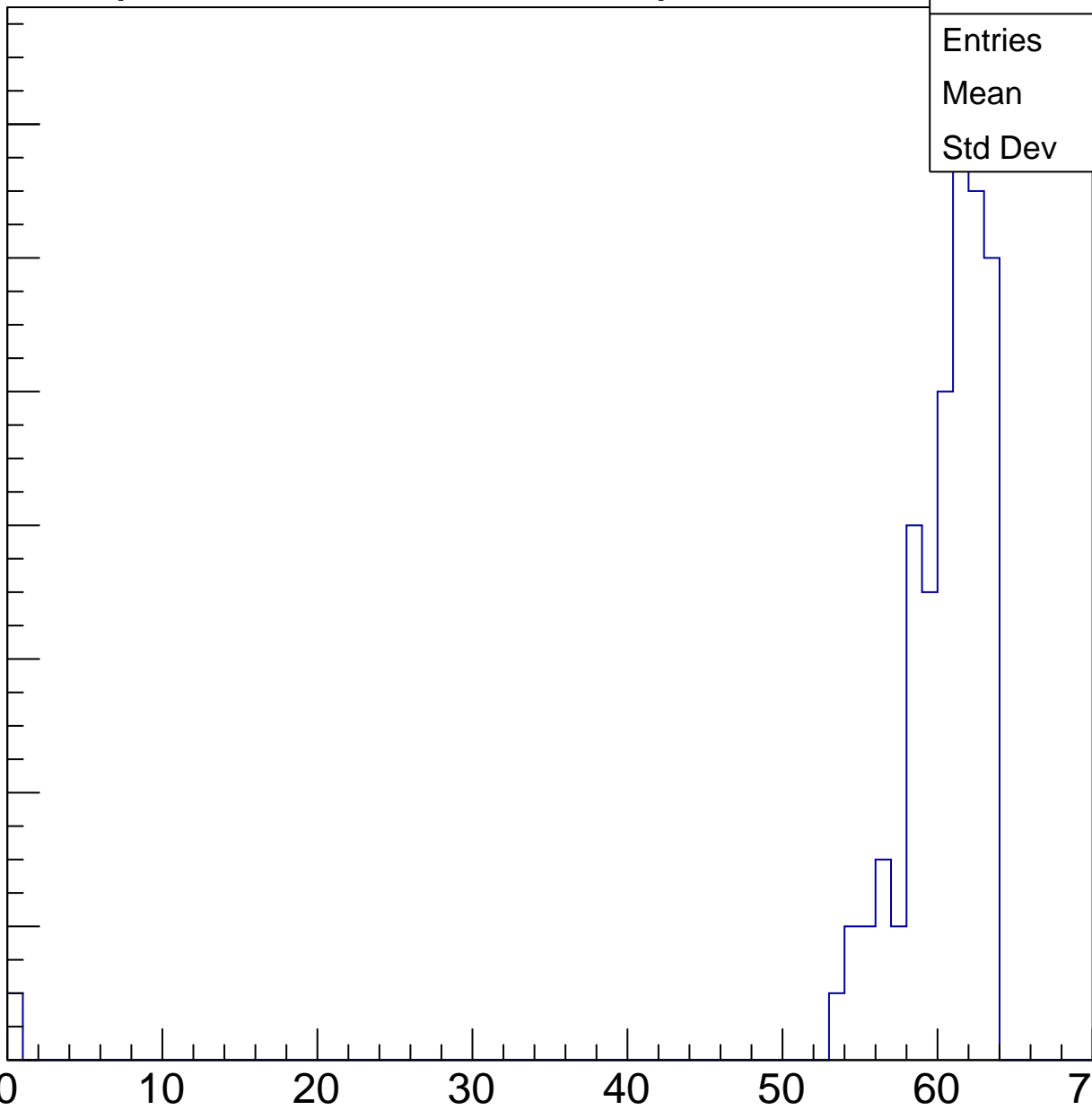
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.3
Std Dev	7.267

ampl

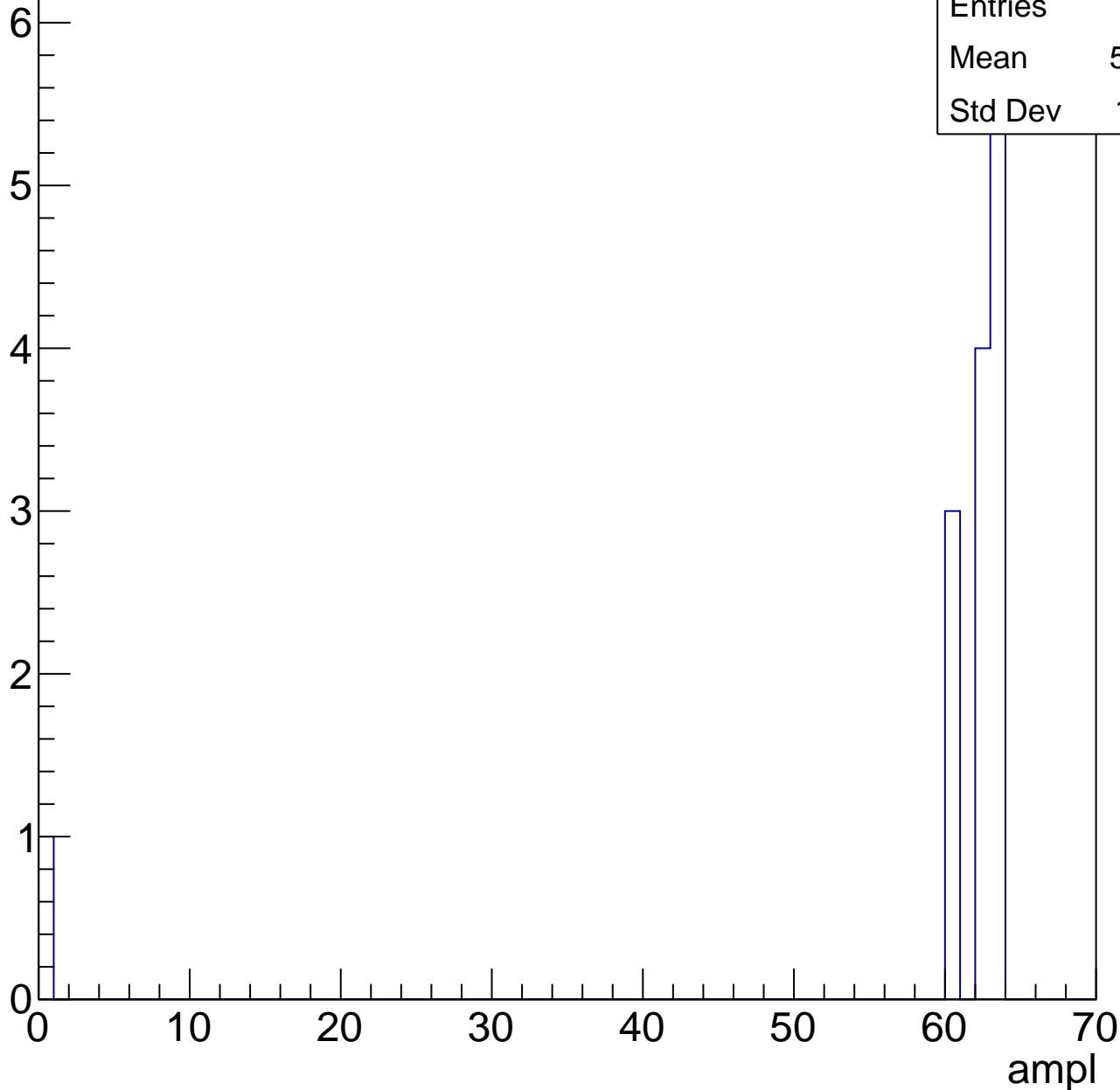


B1L001S, U24-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.57
Std Dev	16.01



B1L001S, U24-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L001S, U24-ch88, adc0

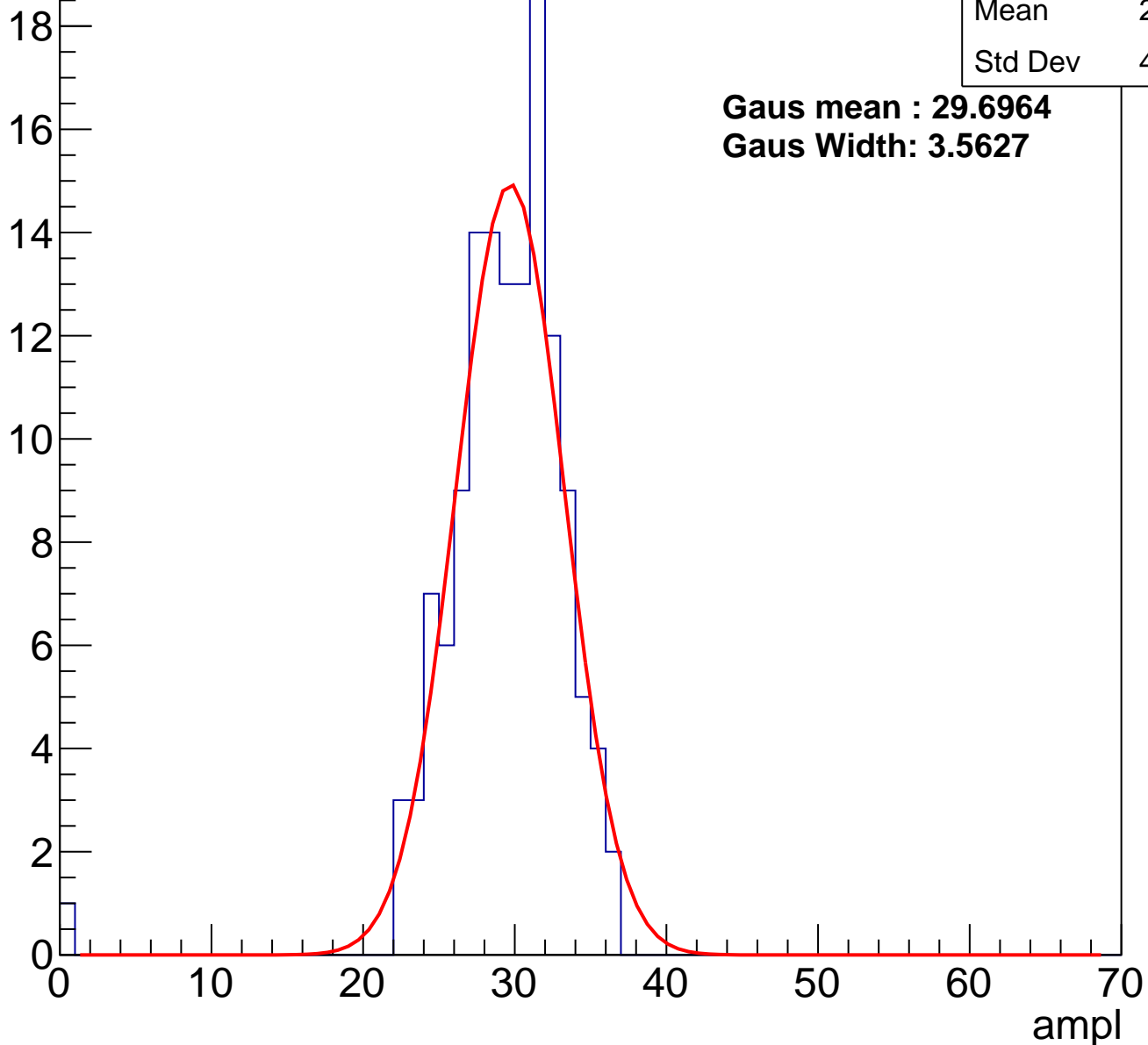
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.93
Std Dev	4.075

Gaus mean : 29.6964

Gaus Width: 3.5627

Entry



B1L001S, U24-ch88, adc1

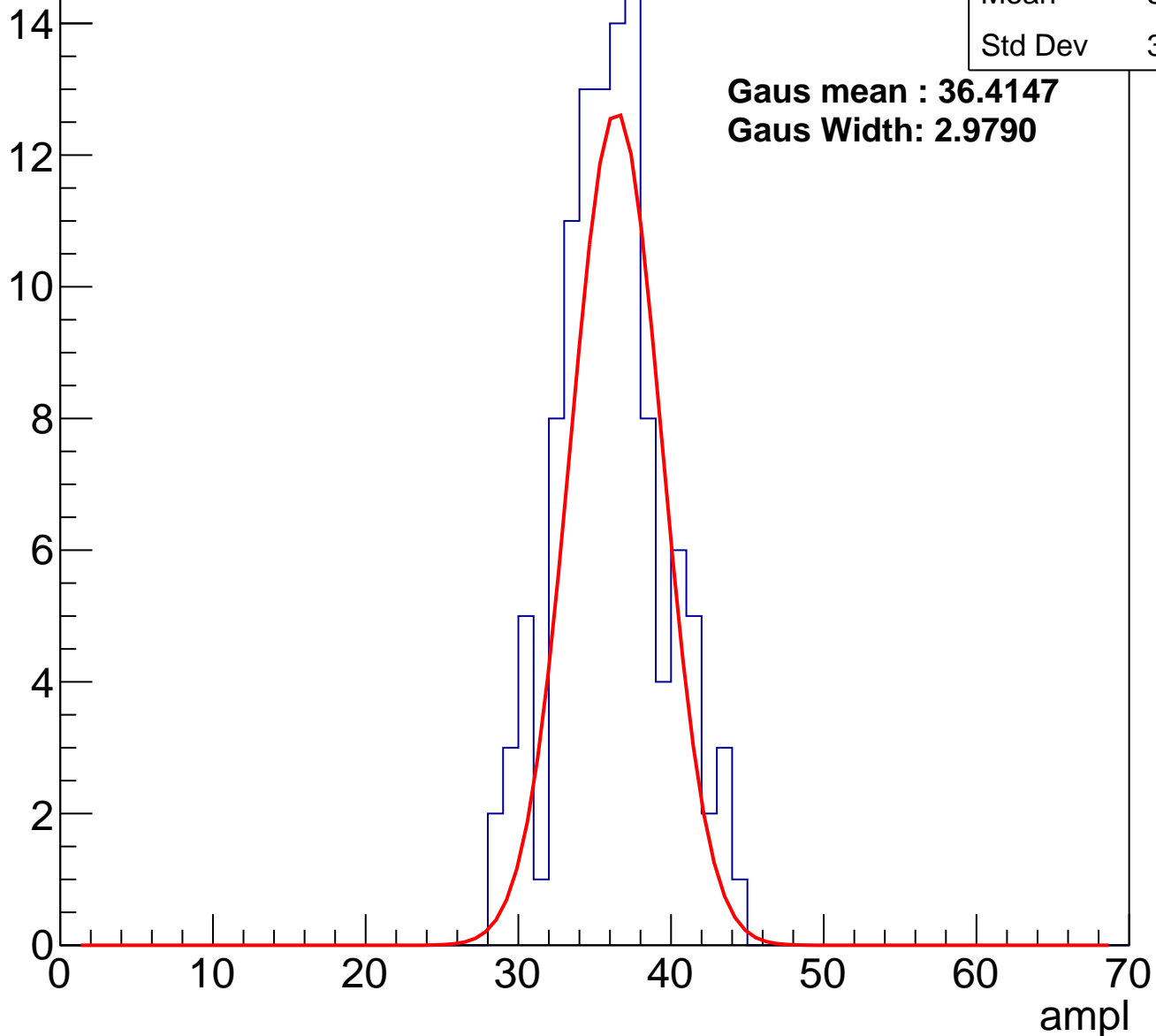
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	35.62
Std Dev	3.432

Gaus mean : 36.4147

Gaus Width: 2.9790

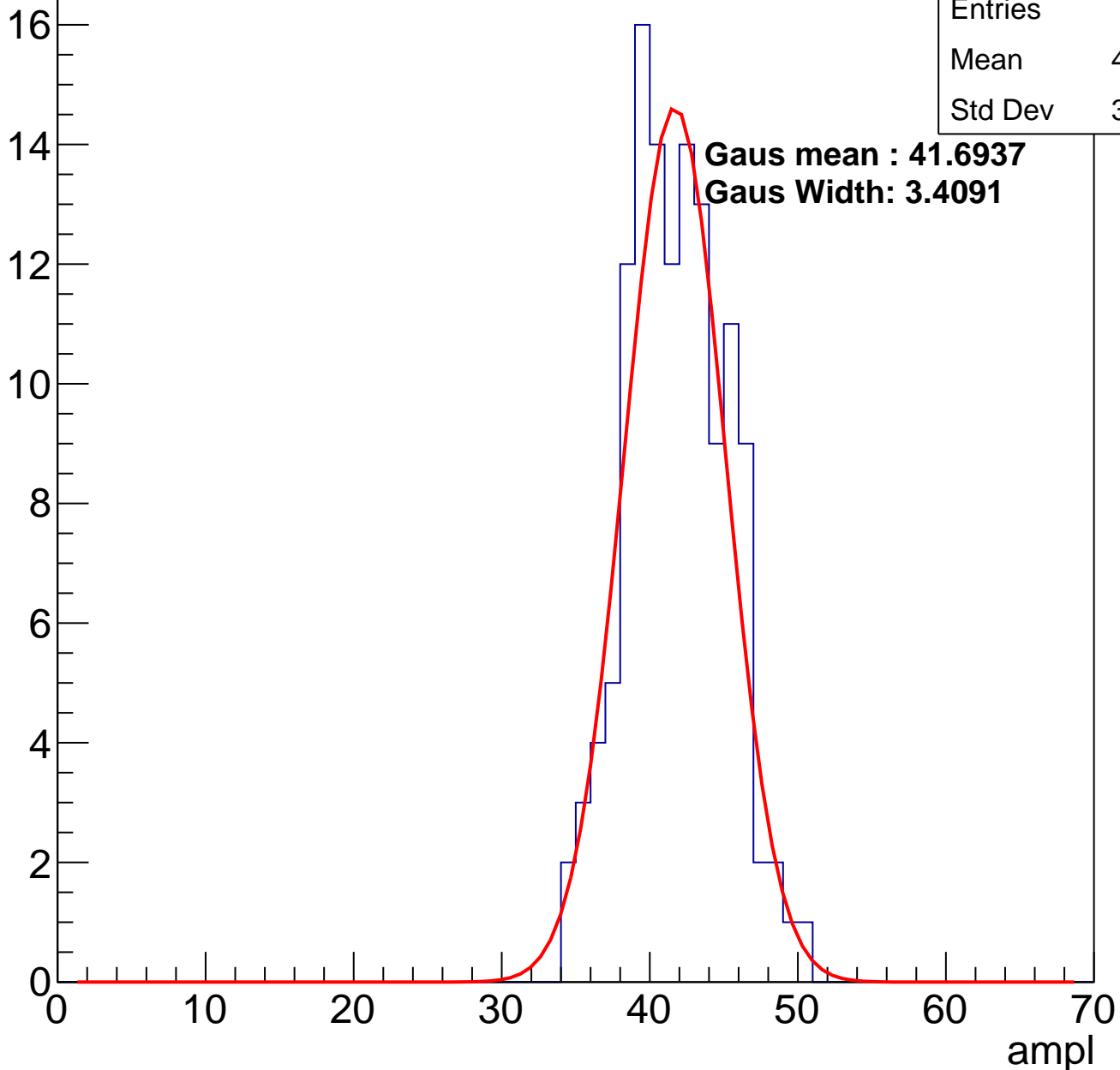
Entry



B1L001S, U24-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

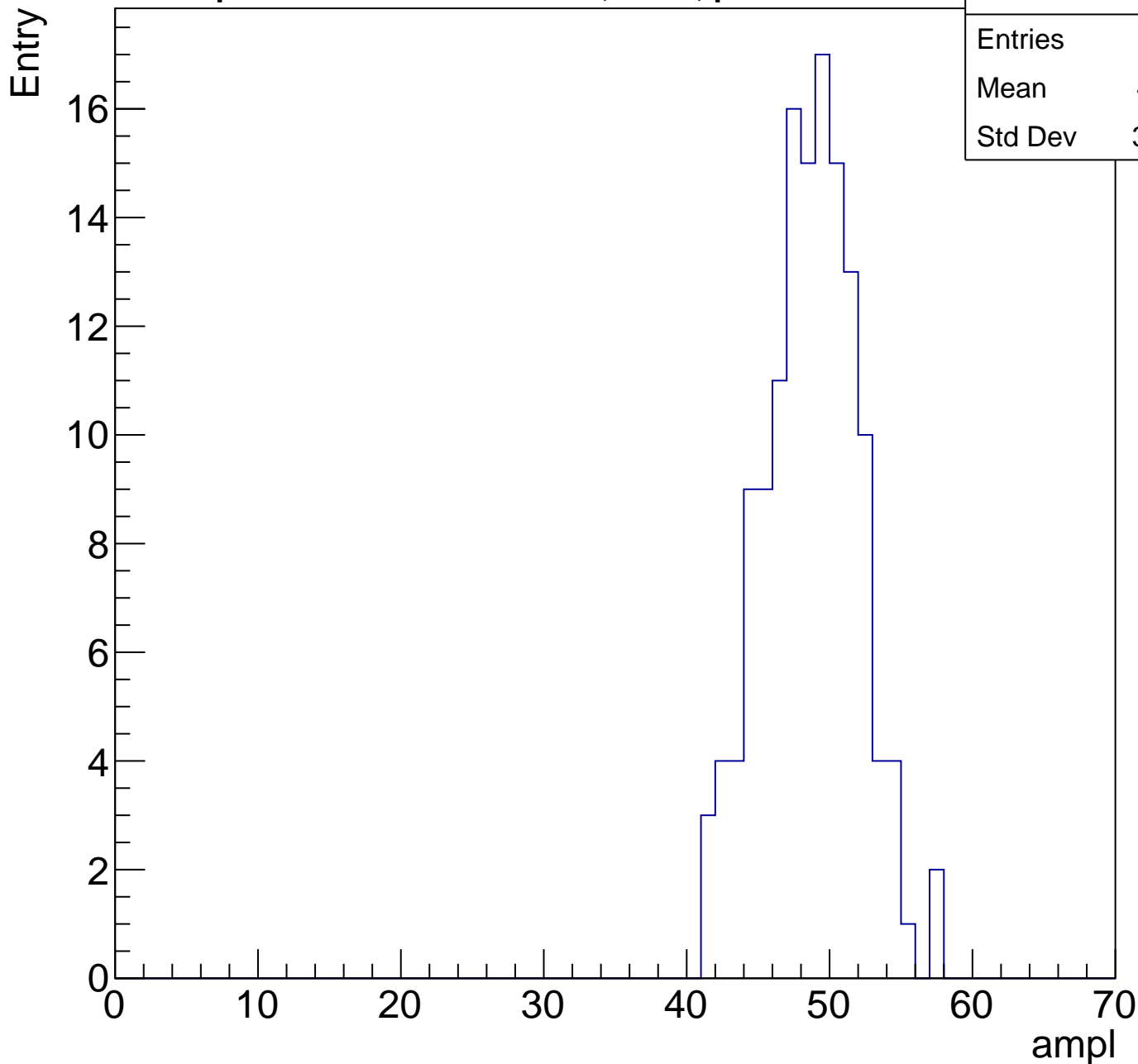
Entry



B1L001S, U24-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	48.21
Std Dev	3.286



B1L001S, U24-ch88, adc4

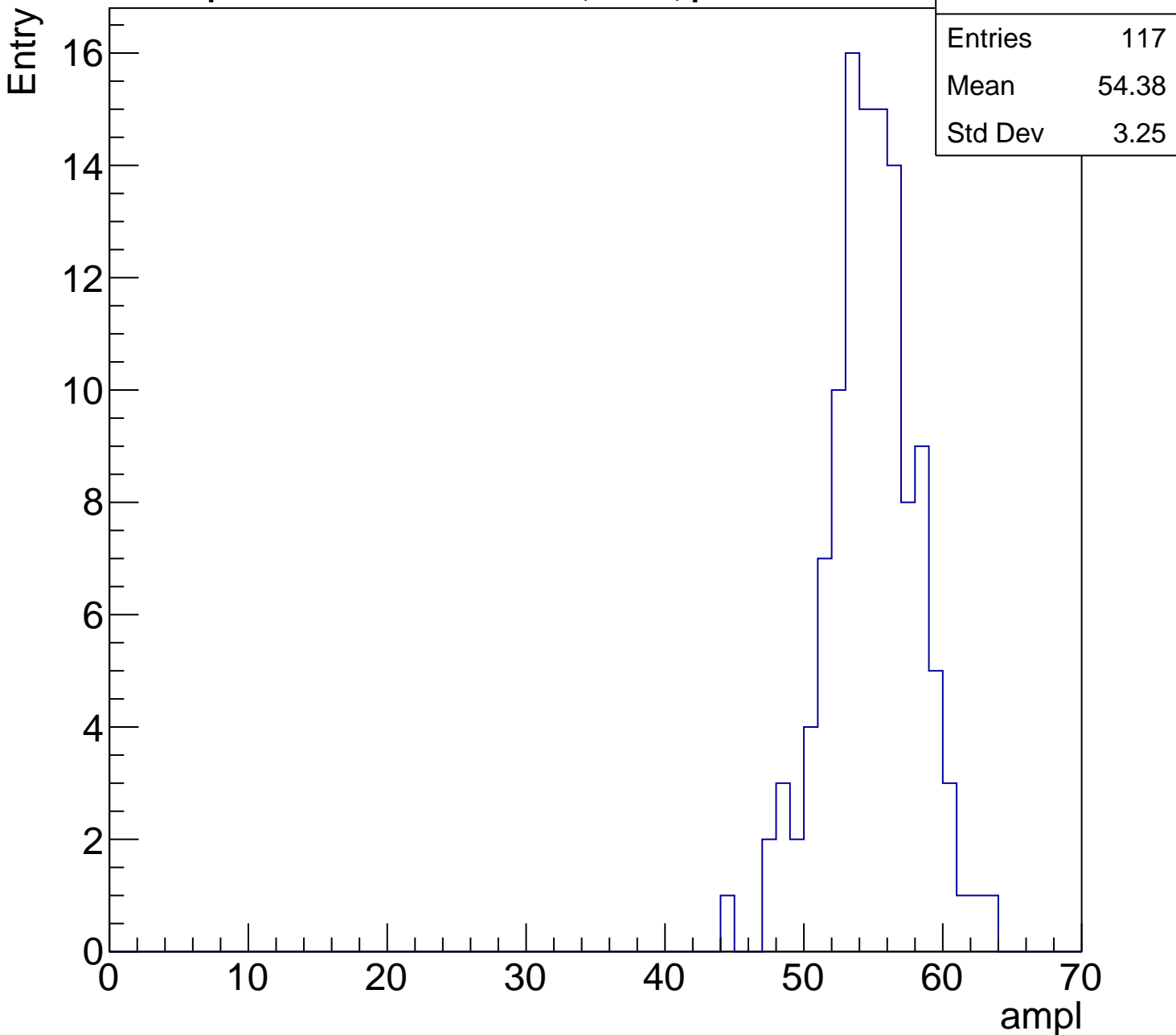
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	54.38
Std Dev	3.25

ampl



B1L001S, U24-ch88, adc5

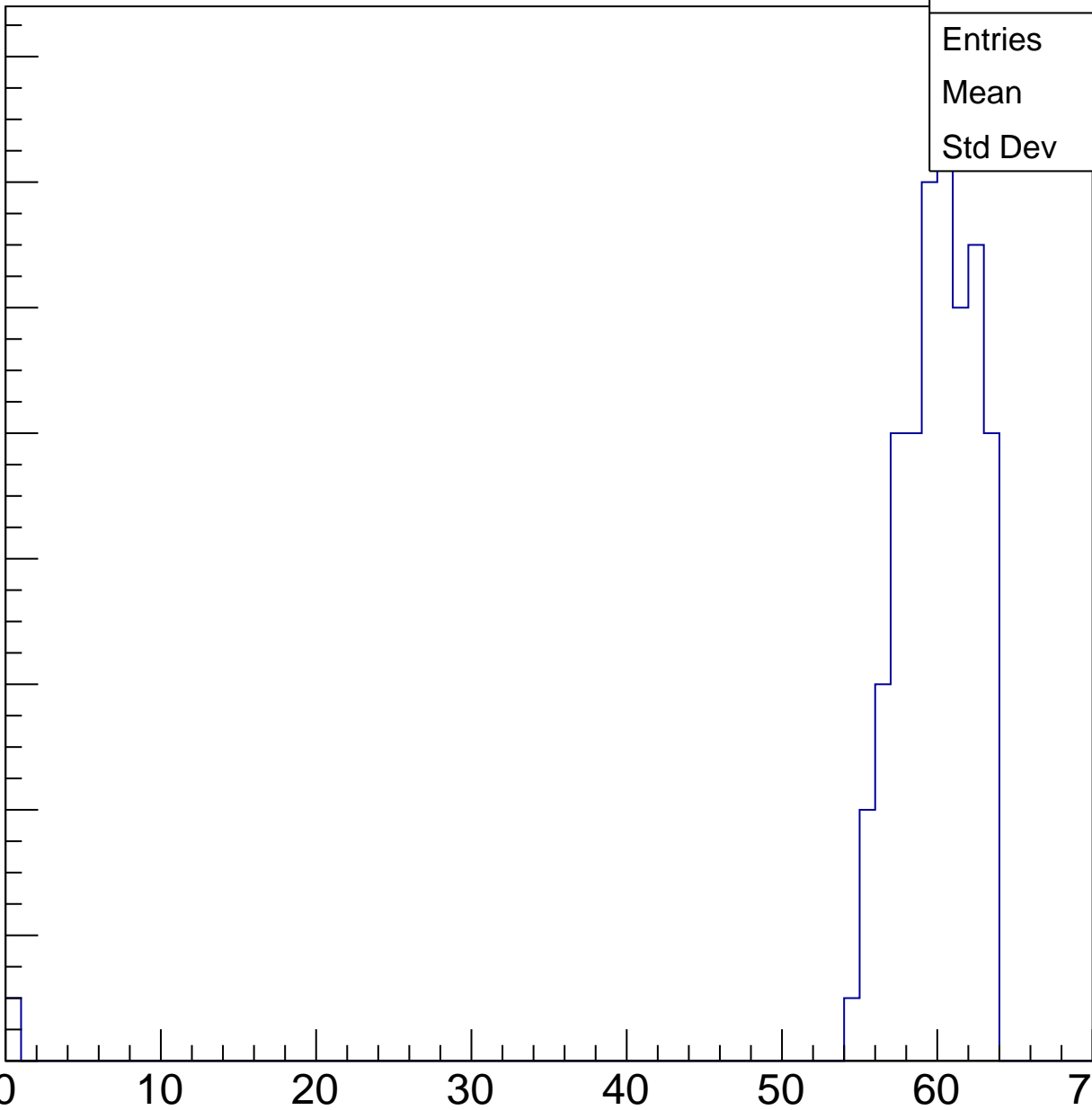
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.91
Std Dev	6.433

ampl

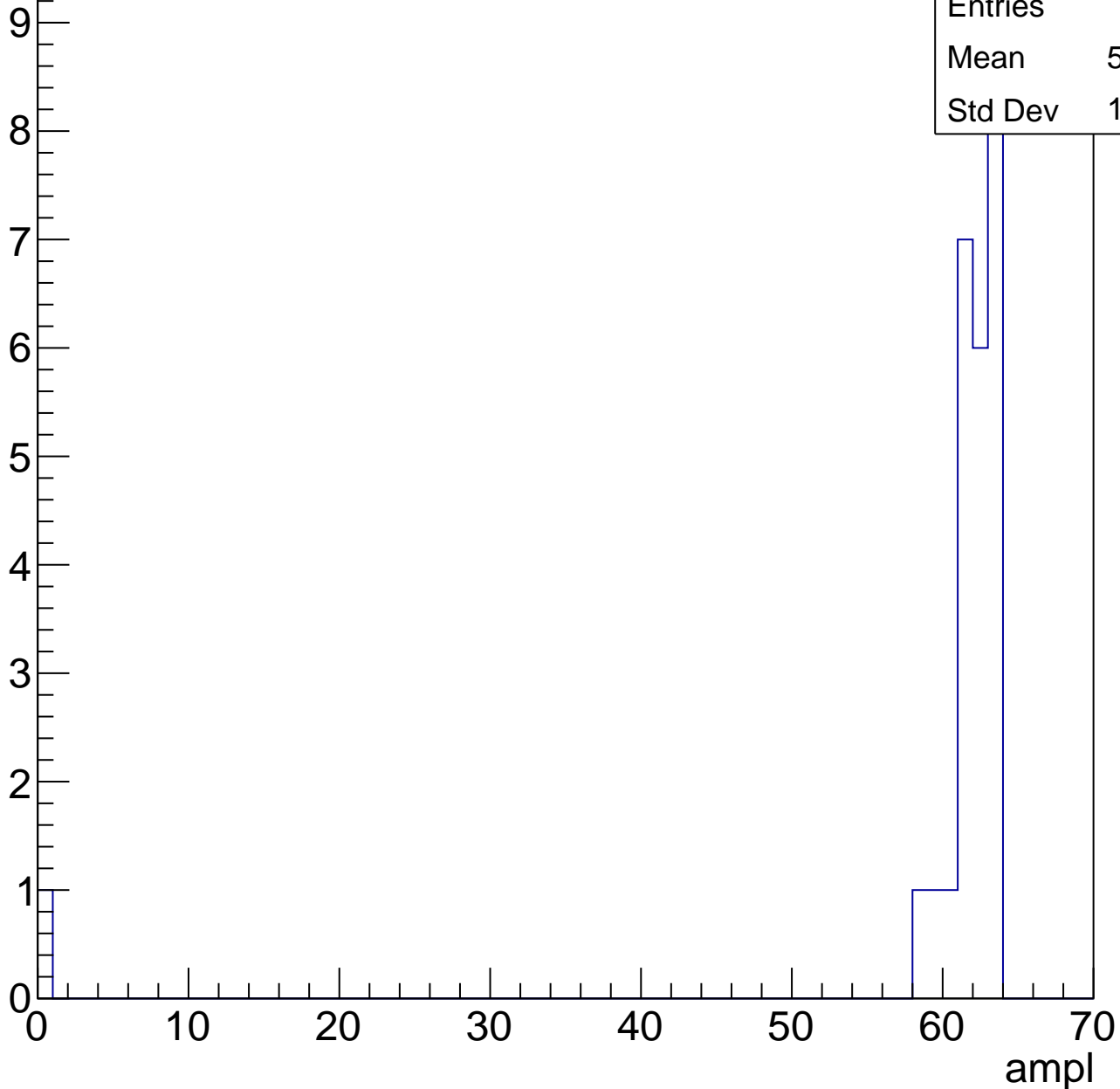


B1L001S, U24-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	59.35
Std Dev	11.94



B1L001S, U24-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch89, adc0

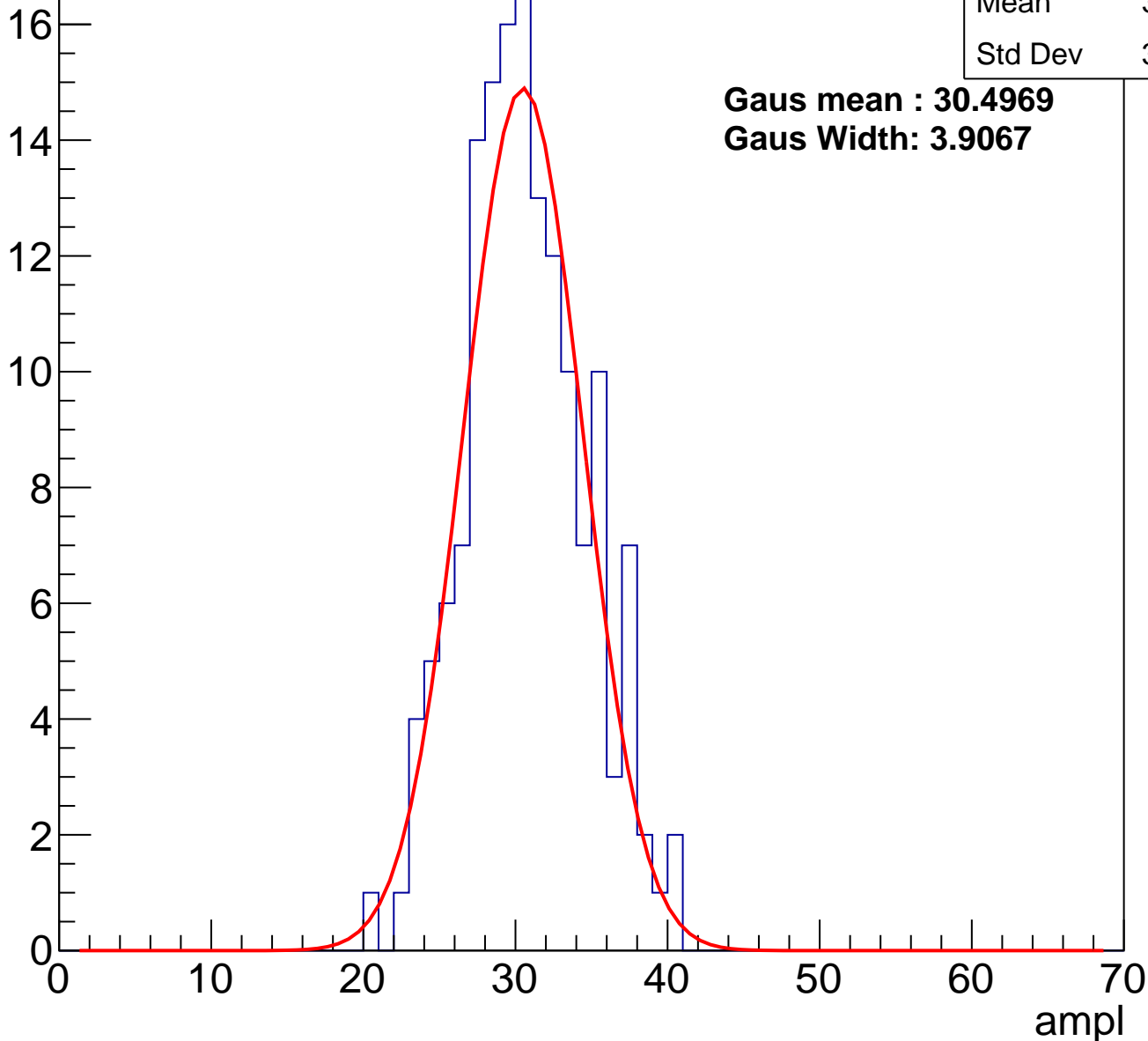
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	153
Mean	30.23
Std Dev	3.935

Gaus mean : 30.4969

Gaus Width: 3.9067



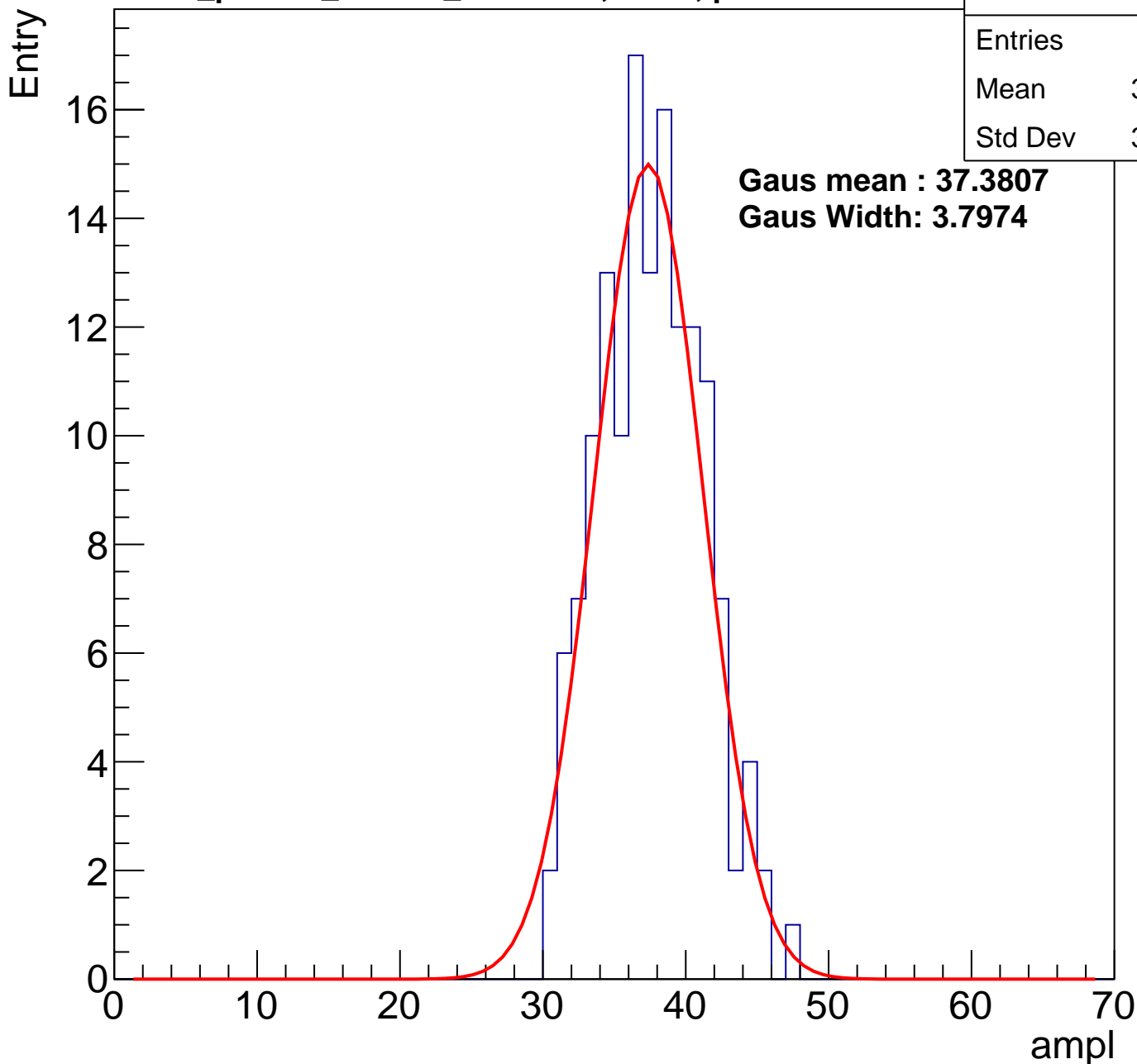
B1L001S, U24-ch89, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.14
Std Dev	3.562

Gaus mean : 37.3807

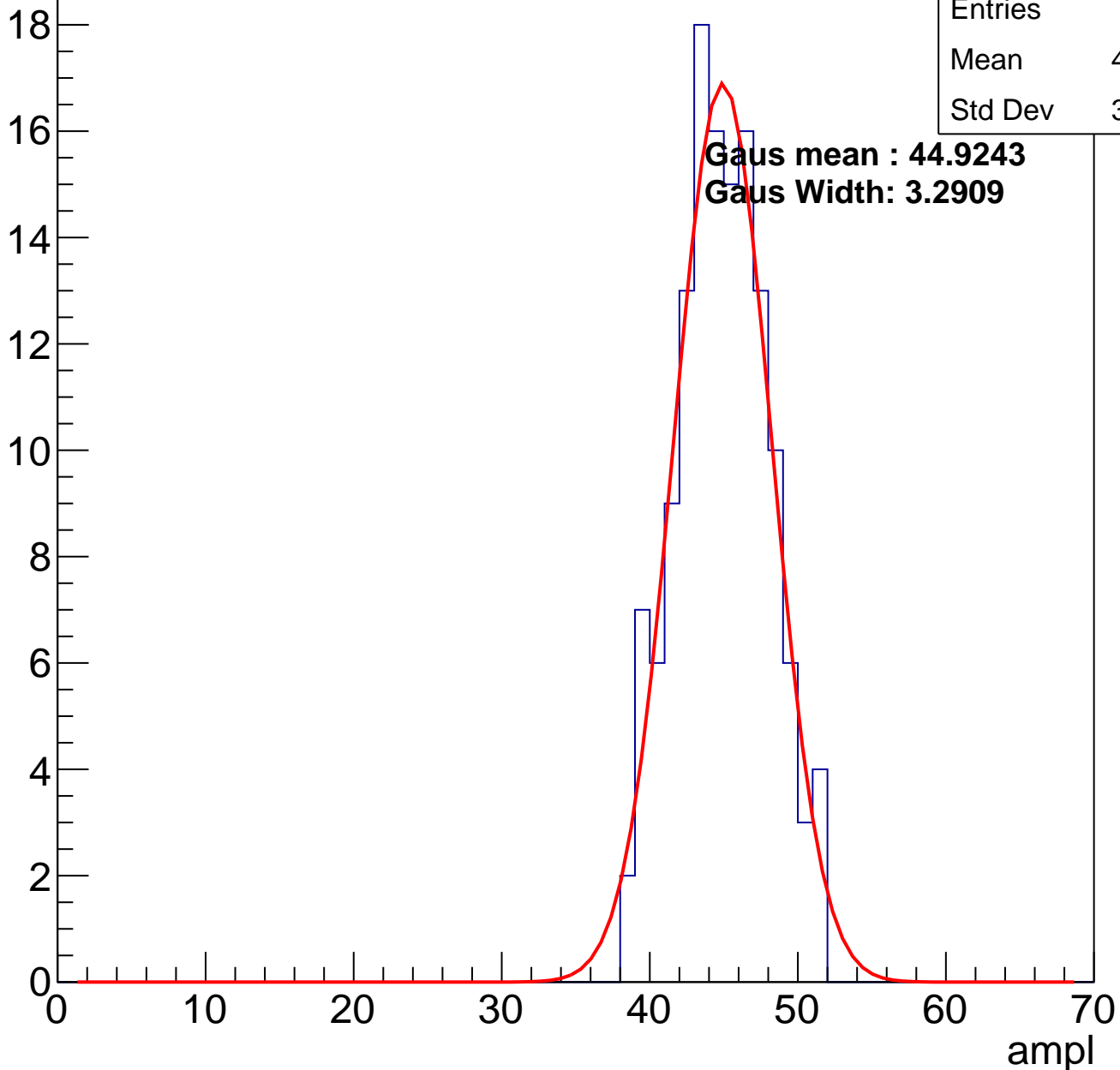
Gaus Width: 3.7974



B1L001S, U24-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

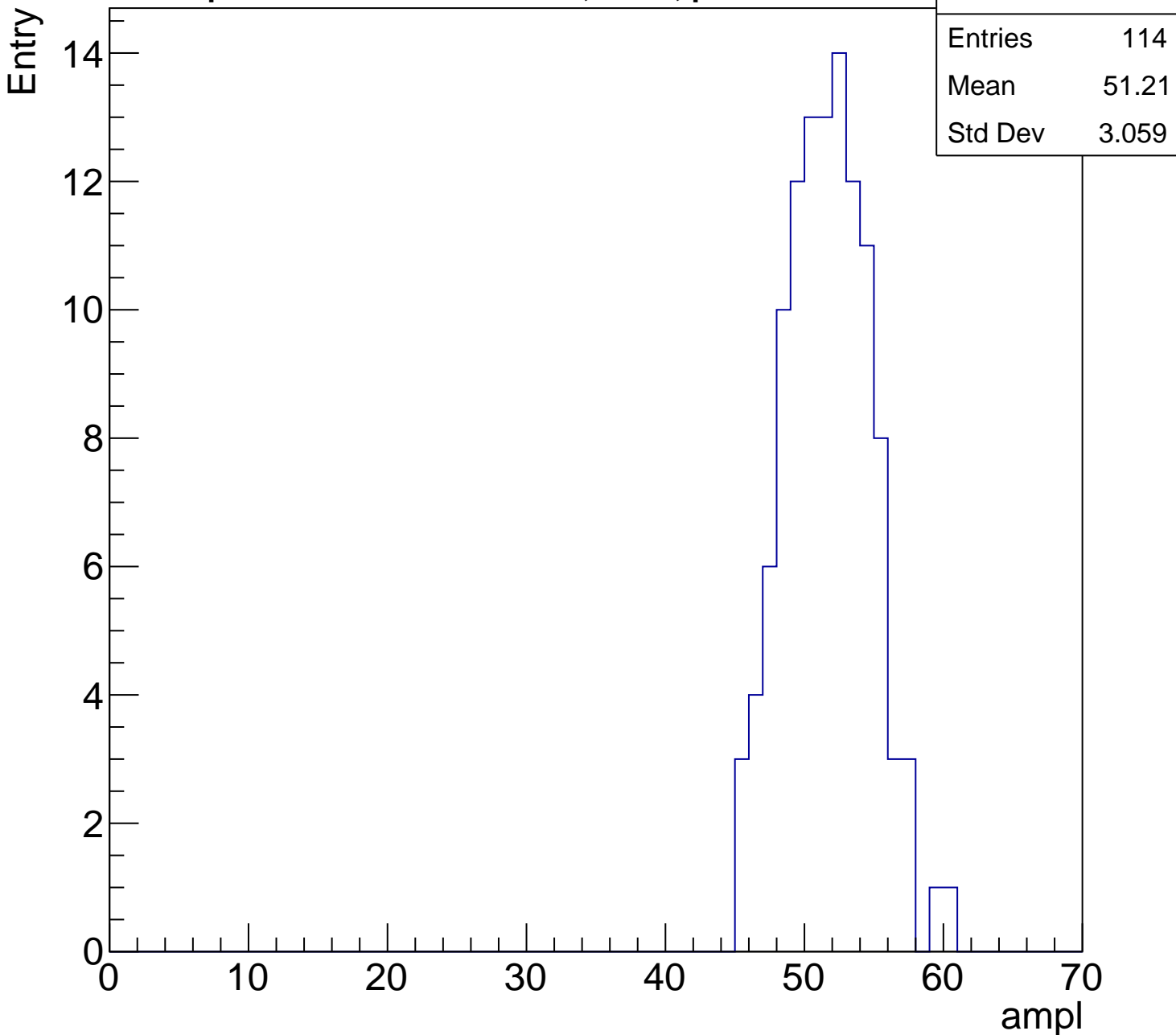
Entries	114
Mean	51.21
Std Dev	3.059

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch89, adc4

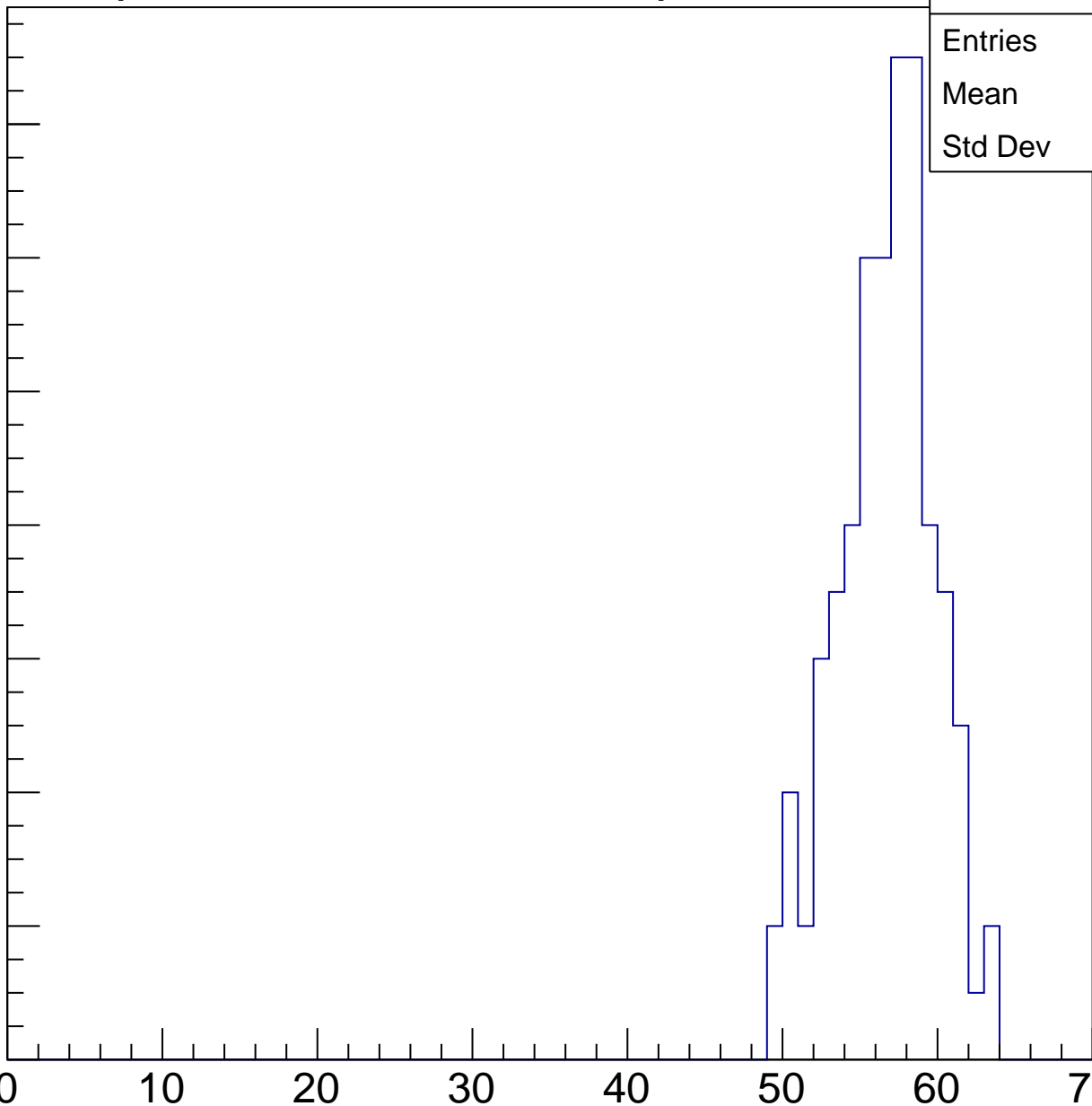
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	56.2
Std Dev	3.085

ampl



B1L001S, U24-ch89, adc5

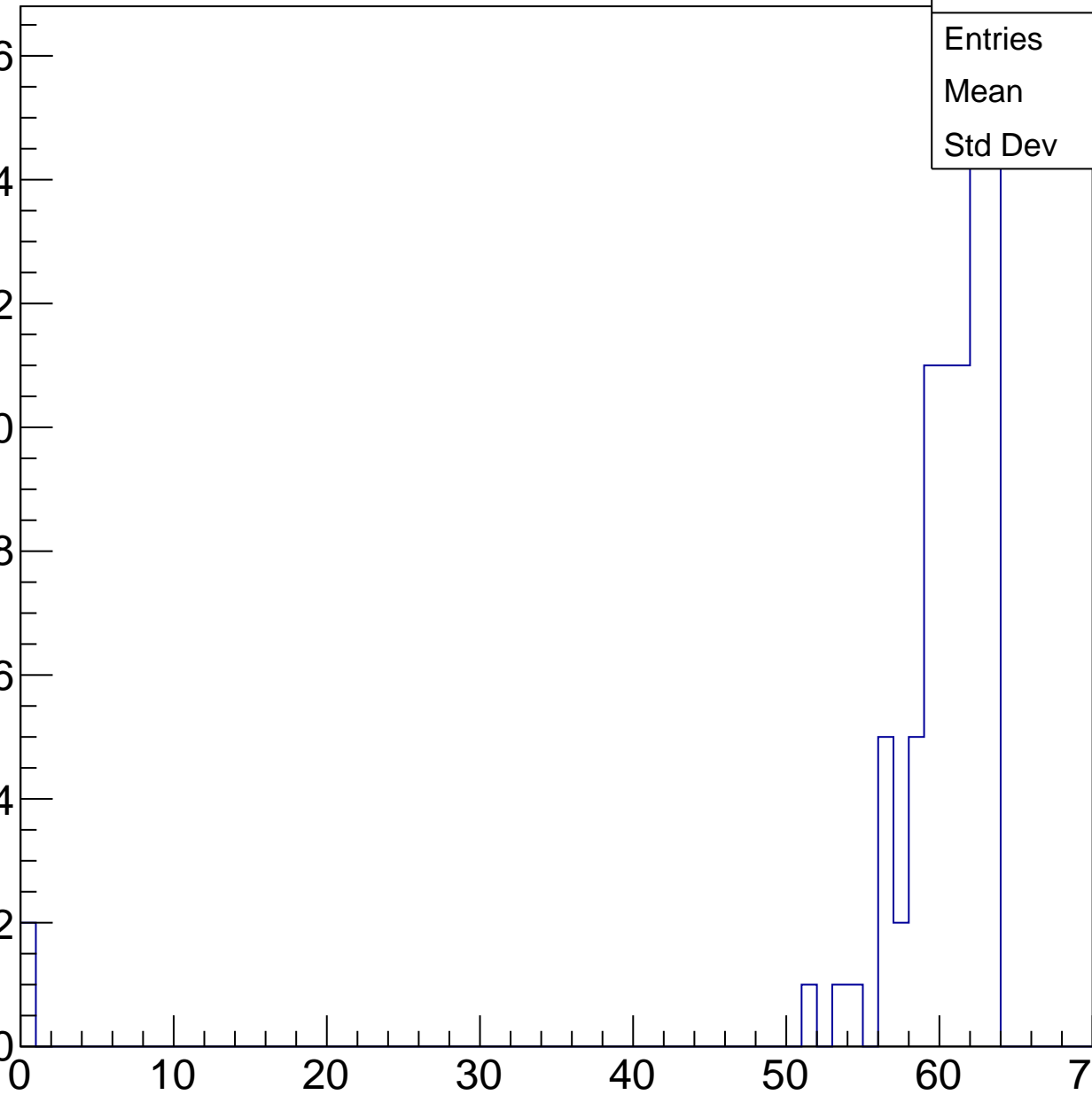
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	58.75
Std Dev	9.673

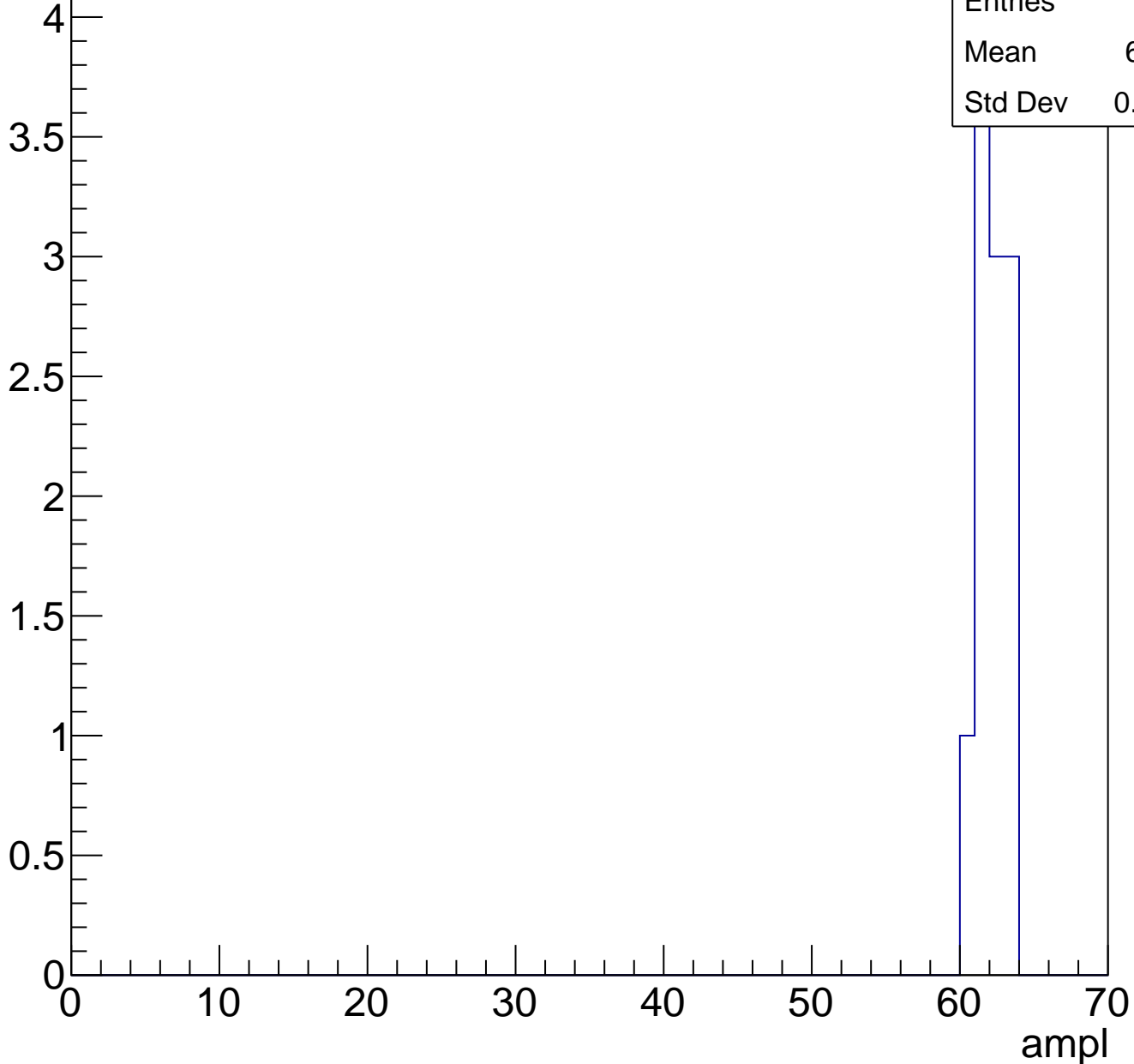
ampl



B1L001S, U24-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch90, adc0

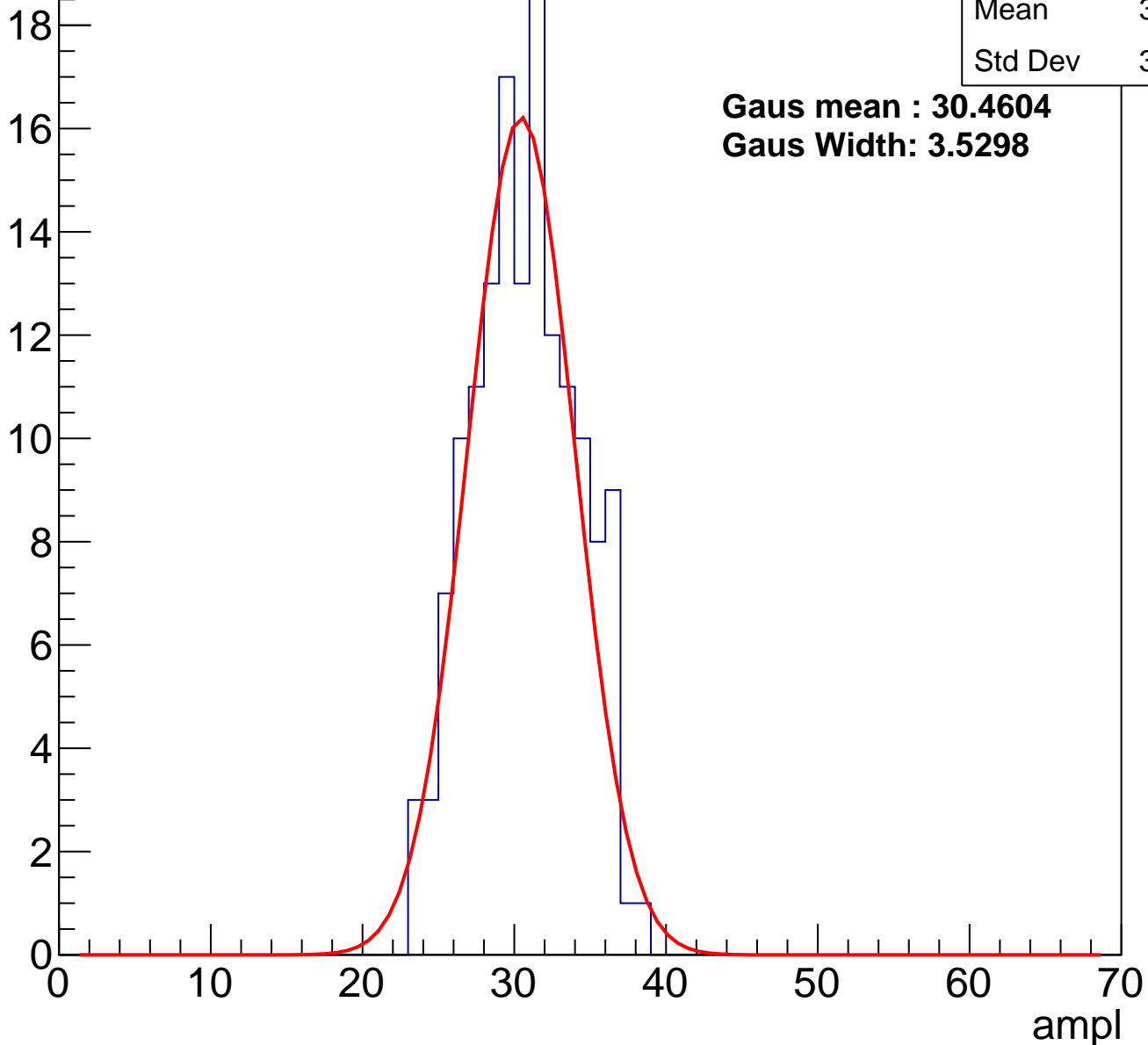
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	30.24
Std Dev	3.402

Gaus mean : 30.4604

Gaus Width: 3.5298

Entry



B1L001S, U24-ch90, adc1

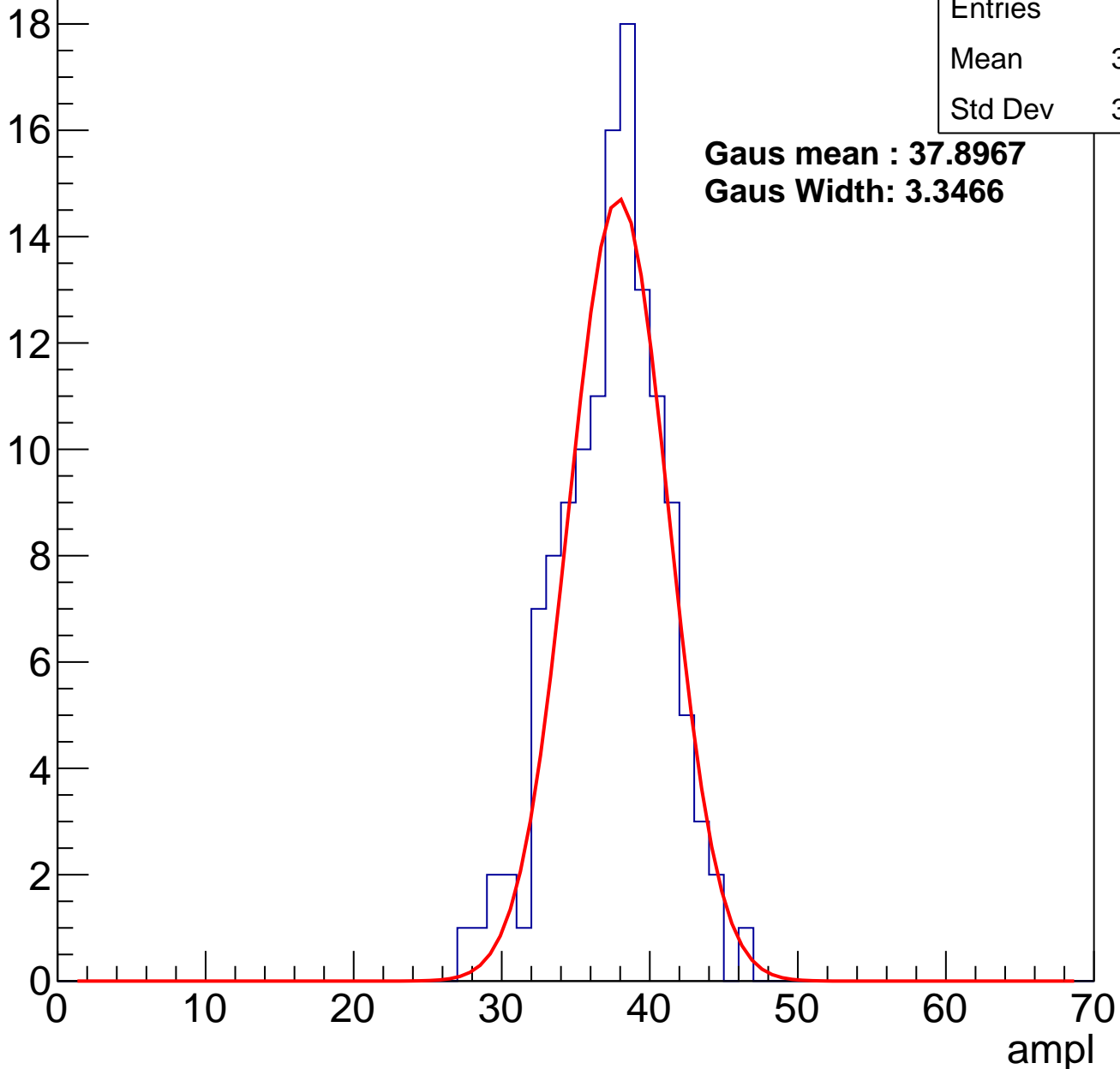
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36.99
Std Dev	3.512

Gaus mean : 37.8967

Gaus Width: 3.3466

Entry



B1L001S, U24-ch90, adc2

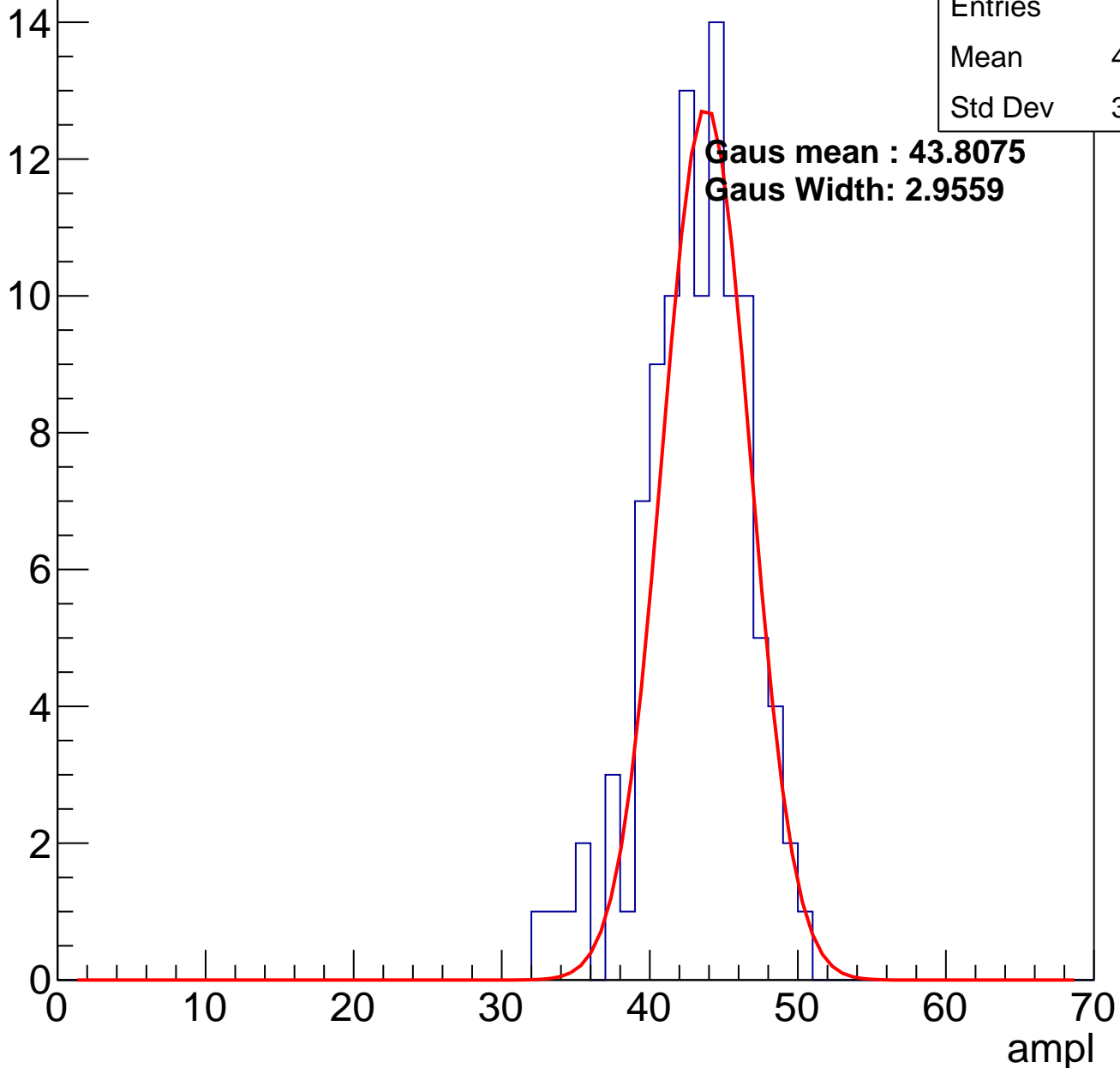
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	42.67
Std Dev	3.457

Gaus mean : 43.8075

Gaus Width: 2.9559

Entry



B1L001S, U24-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

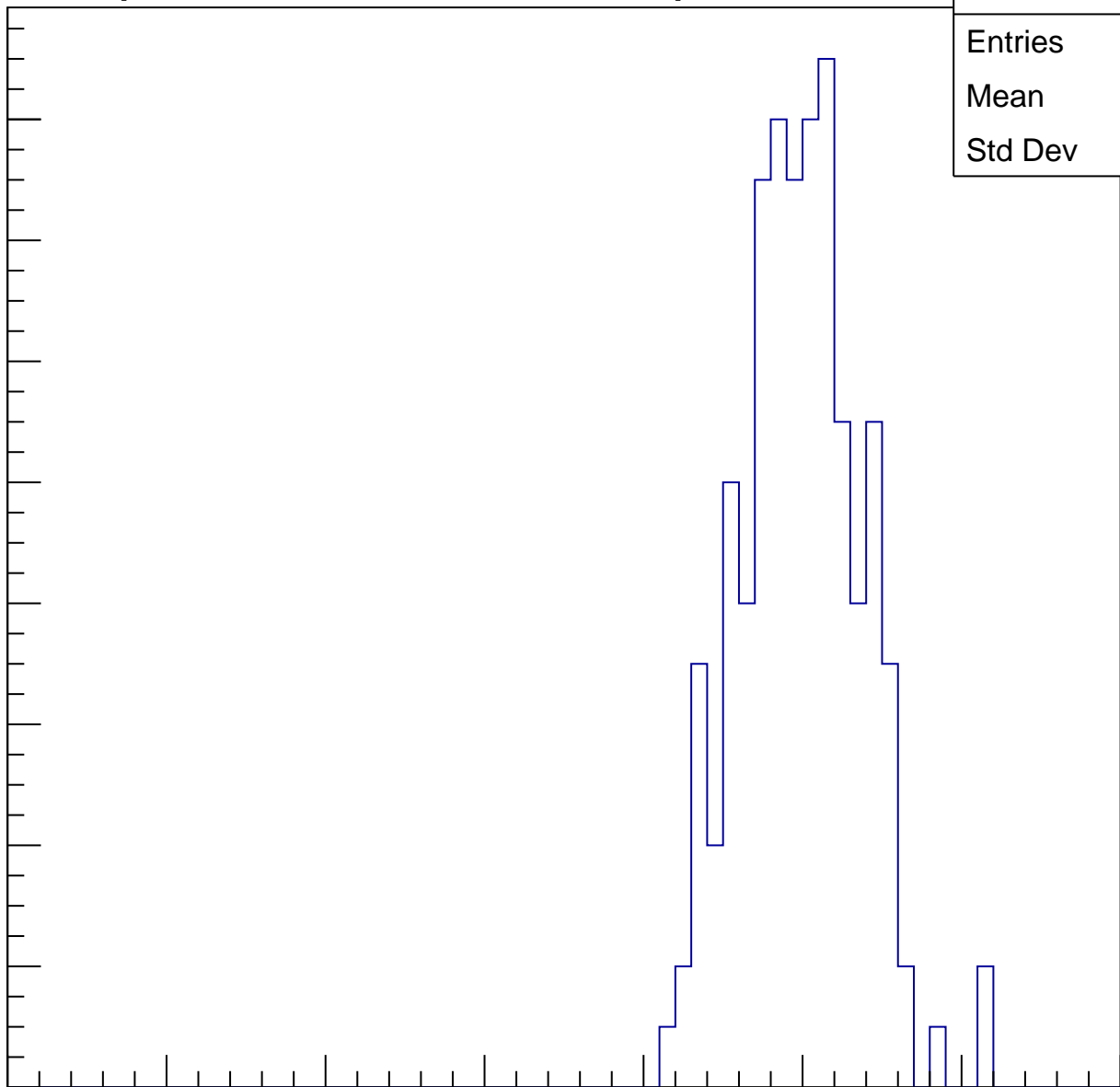
Entries	153
Mean	49.42
Std Dev	3.704

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

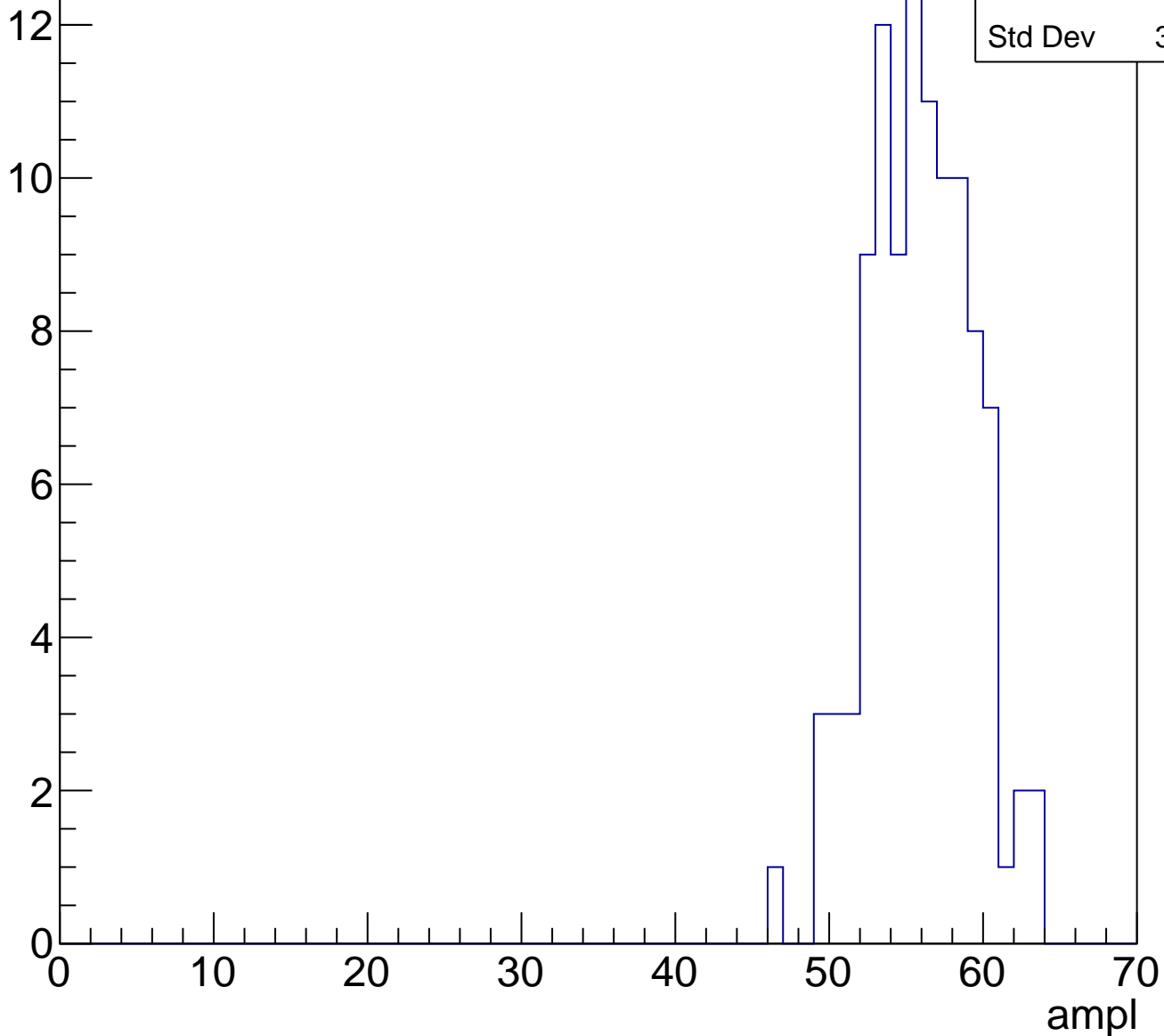


B1L001S, U24-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	55.48
Std Dev	3.299

Entry



B1L001S, U24-ch90, adc5

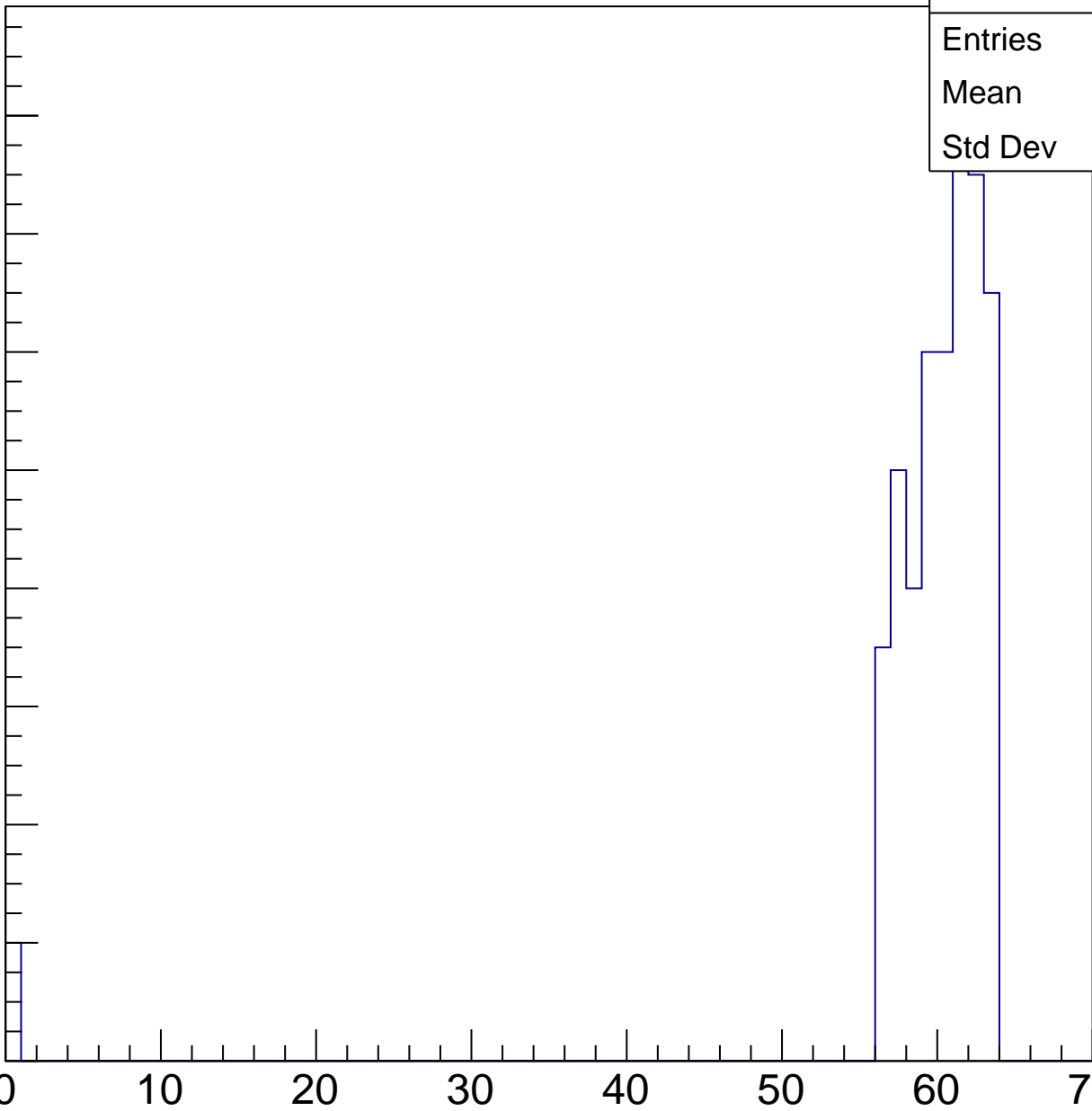
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.75
Std Dev	8.833

ampl

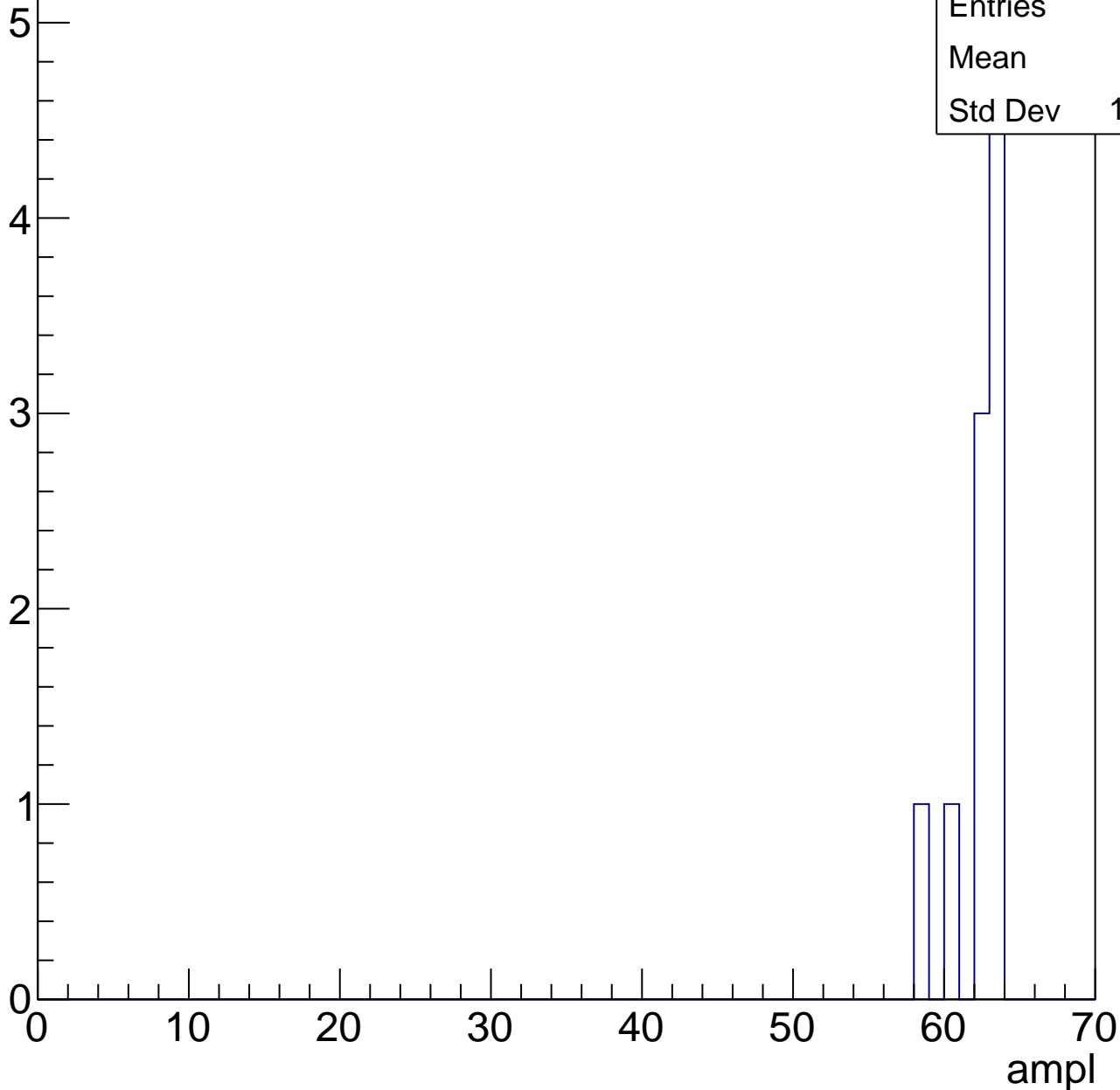


B1L001S, U24-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	61.9
Std Dev	1.578



B1L001S, U24-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U24-ch91, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	30.55
Std Dev	3.438

Gaus mean : 31.1679

Gaus Width: 3.8505

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

138

Mean

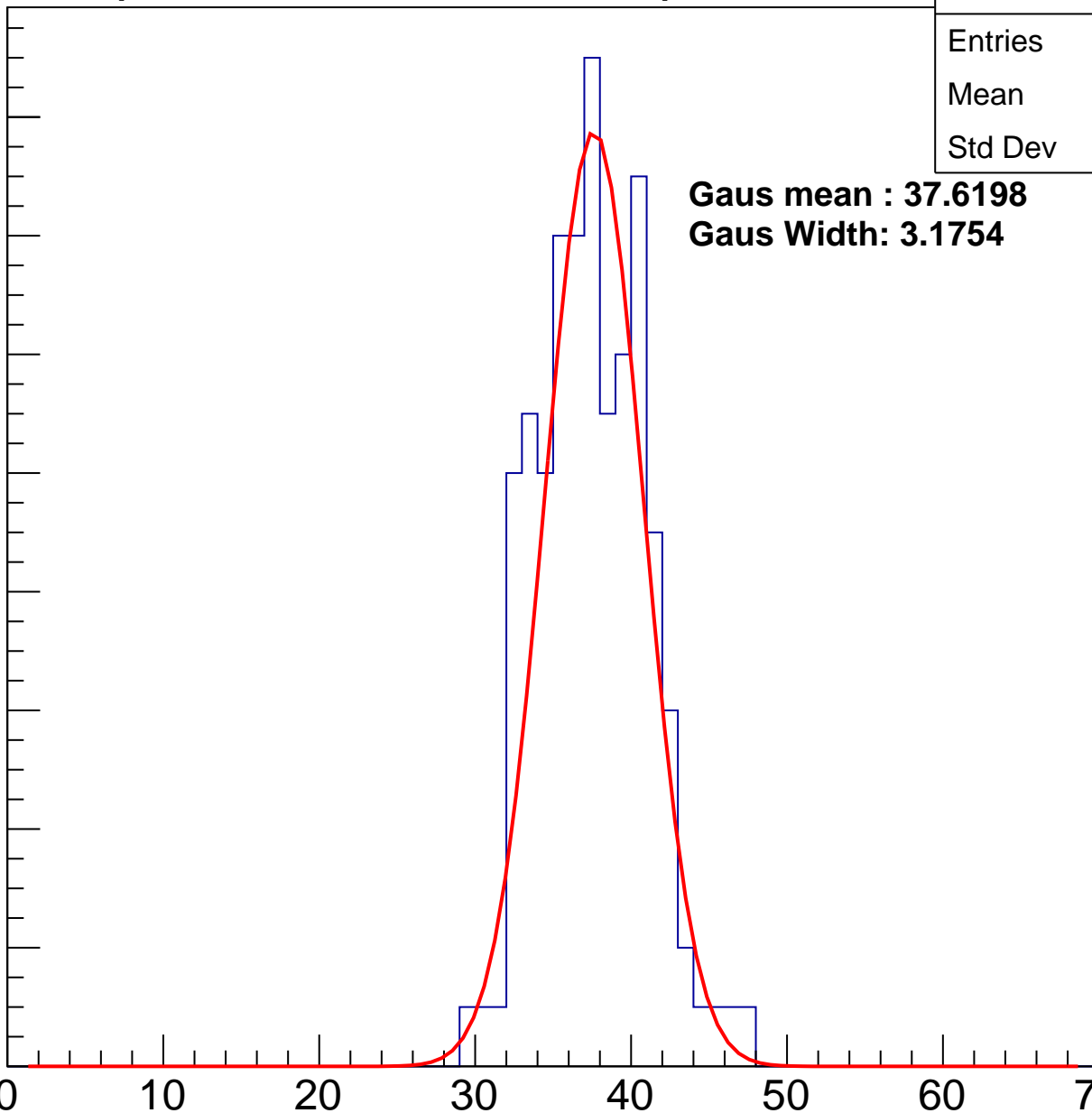
37.04

Std Dev

3.393

Gaus mean : 37.6198

Gaus Width: 3.1754



B1L001S, U24-ch91, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	43.26
Std Dev	3.162

Gaus mean : 43.8392

Gaus Width: 3.1057

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

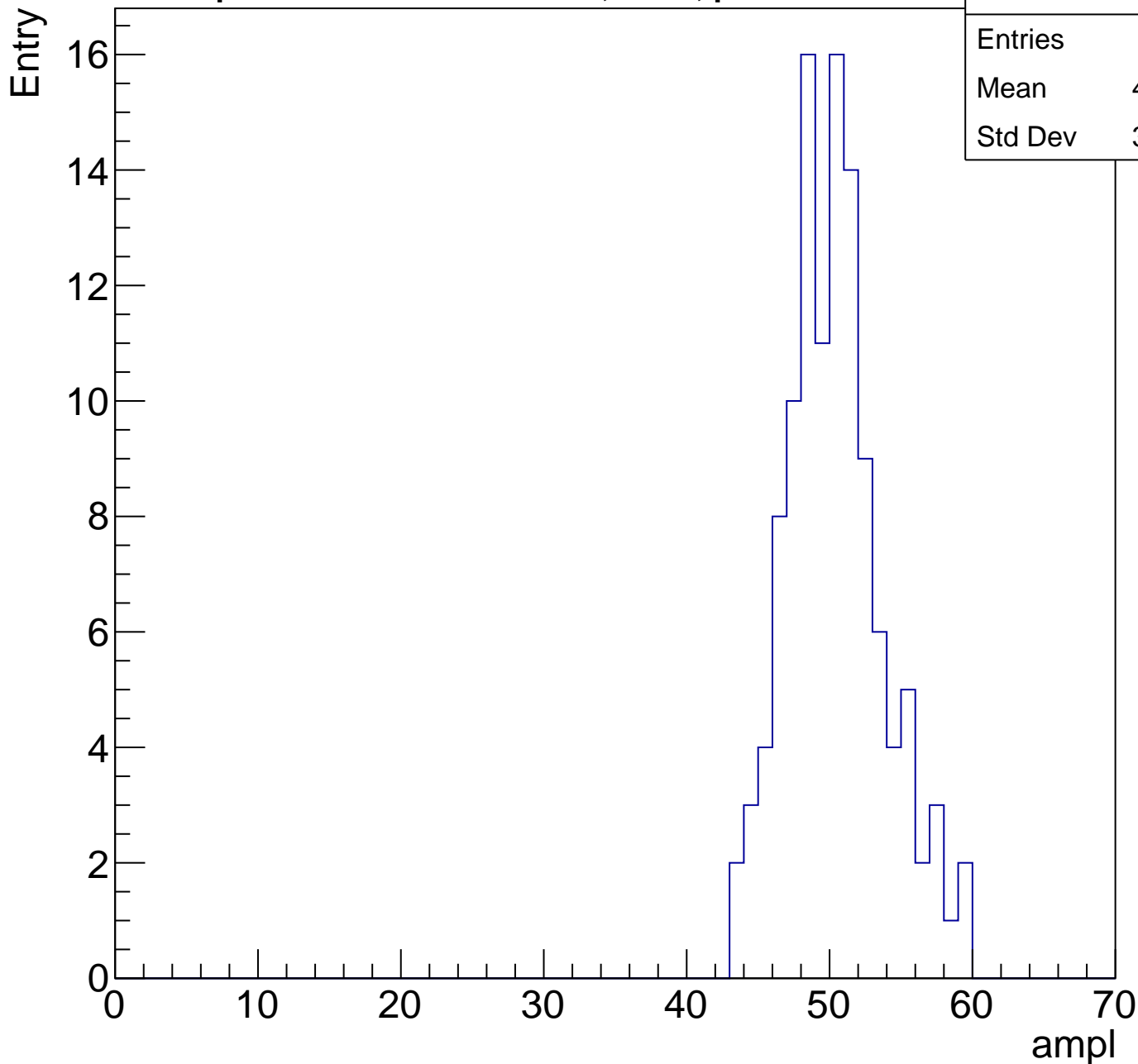
60

70

B1L001S, U24-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	49.94
Std Dev	3.394

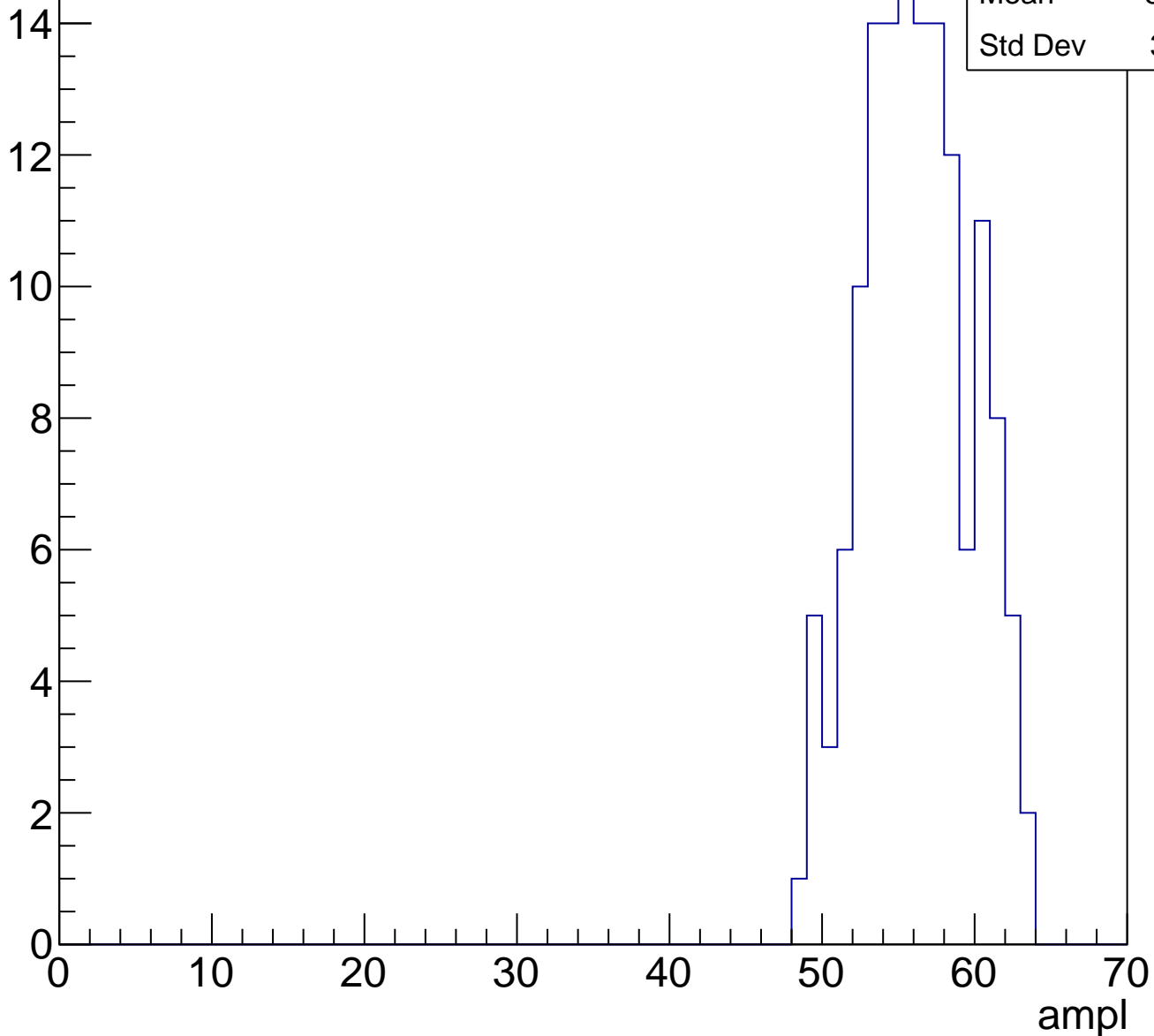


B1L001S, U24-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	55.77
Std Dev	3.471

Entry



B1L001S, U24-ch91, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

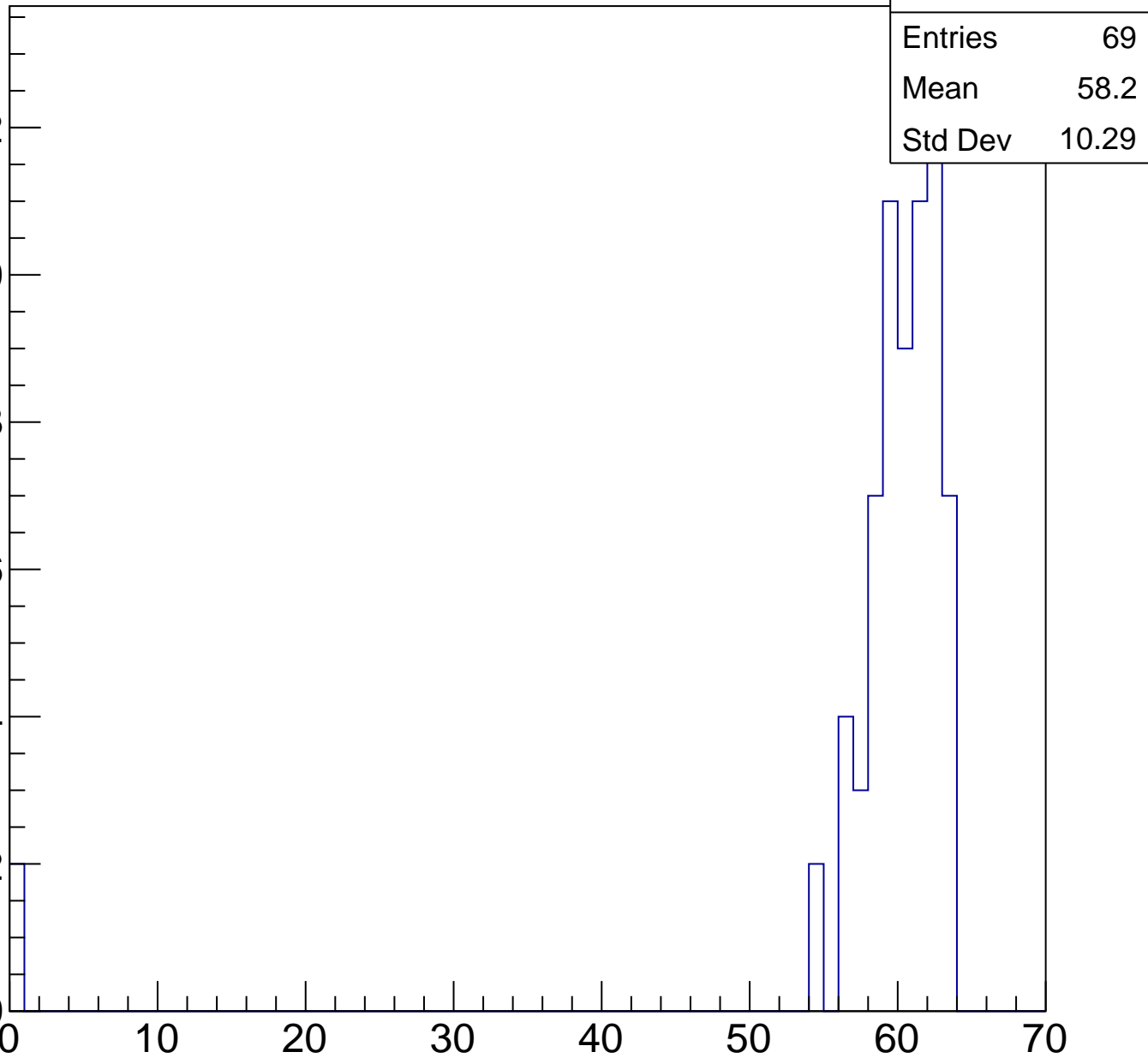
4

2

0

Entries	69
Mean	58.2
Std Dev	10.29

ampl



B1L001S, U24-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	15
Mean	62.47
Std Dev	1.087

B1L001S, U24-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L001S, U24-ch92, adc0

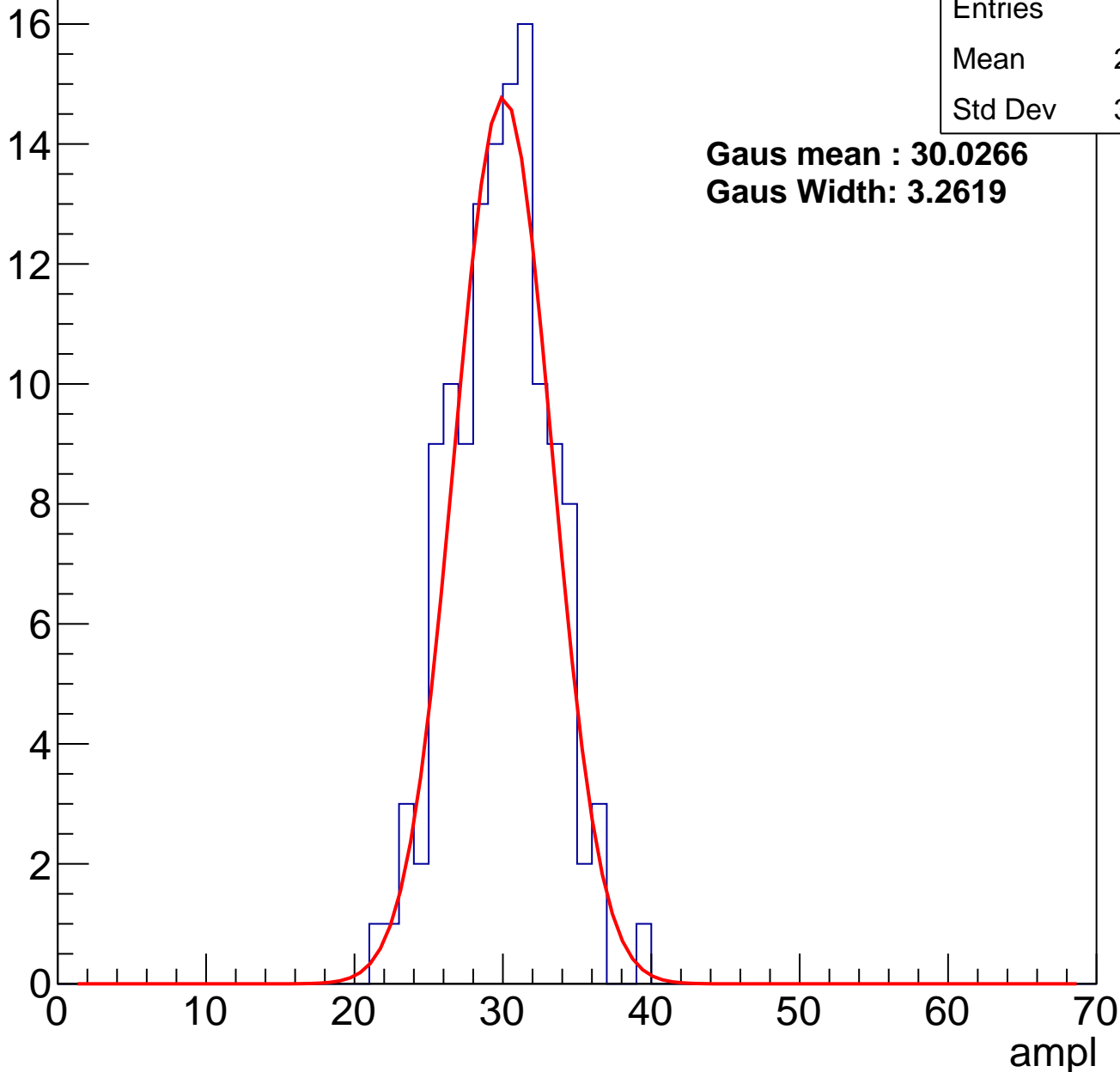
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.44
Std Dev	3.287

Gaus mean : 30.0266

Gaus Width: 3.2619

Entry



B1L001S, U24-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	36.38
Std Dev	3.424

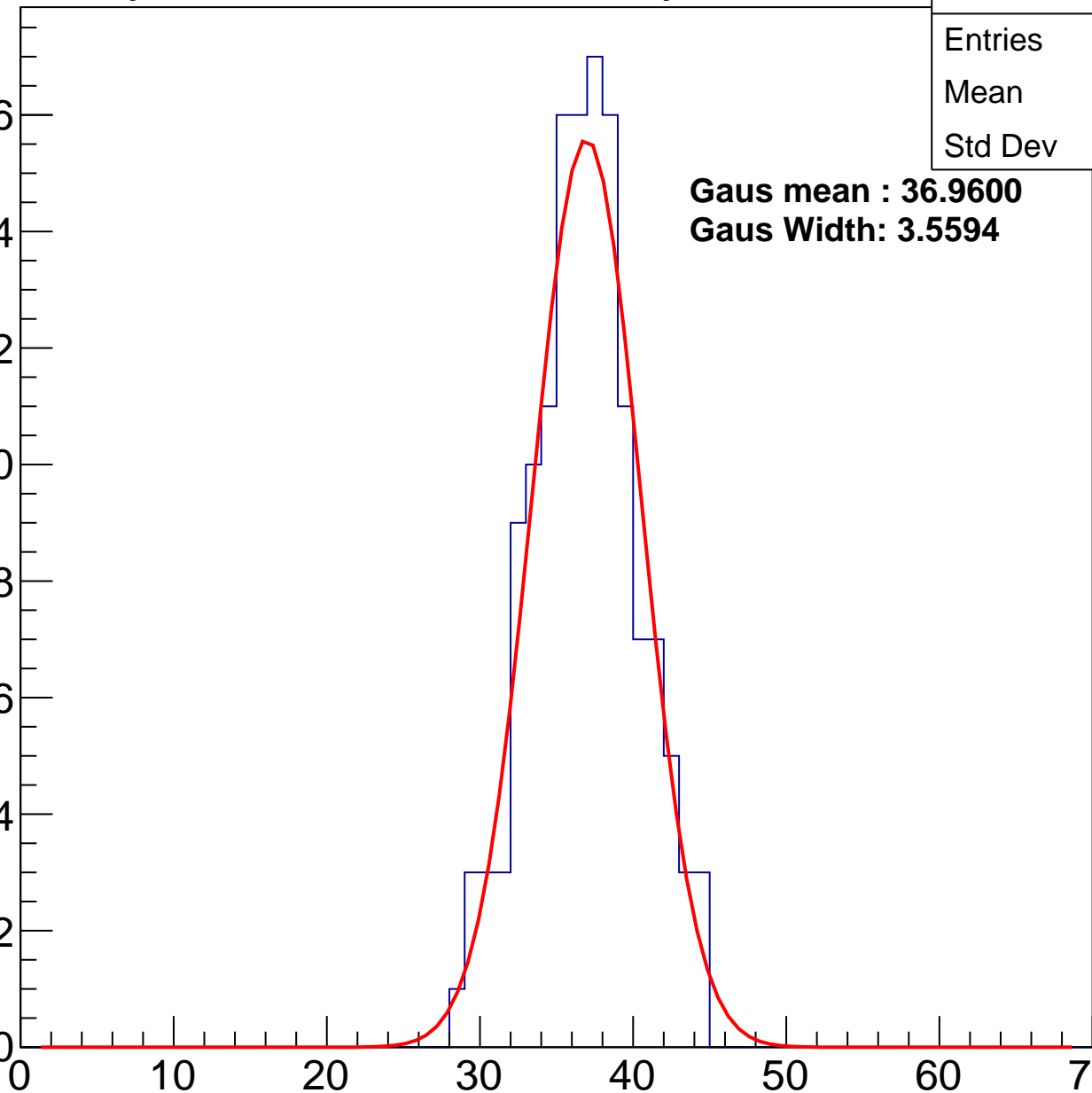
Gaus mean : 36.9600

Gaus Width: 3.5594

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

105

Mean

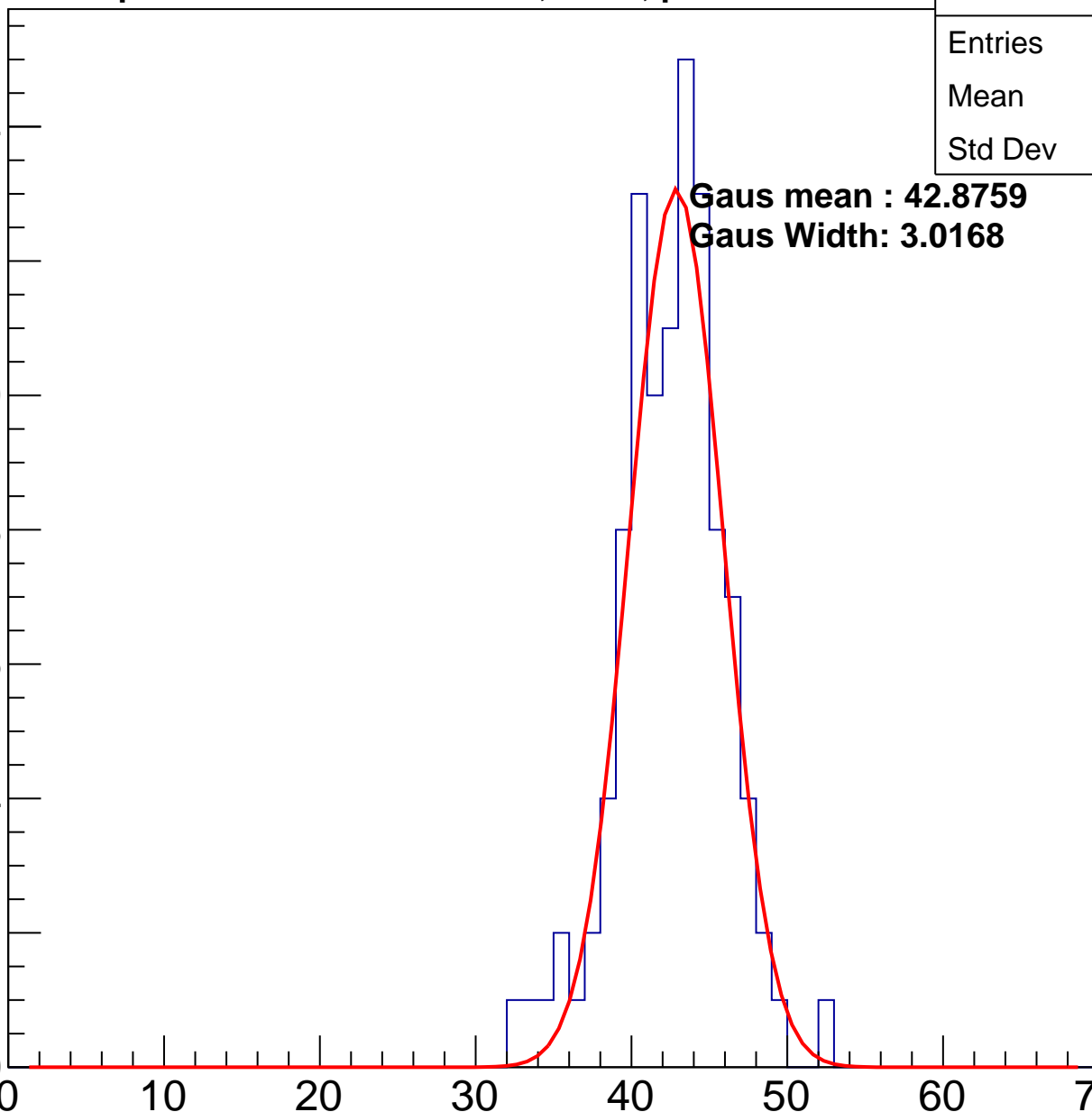
42.09

Std Dev

3.378

Gaus mean : 42.8759

Gaus Width: 3.0168

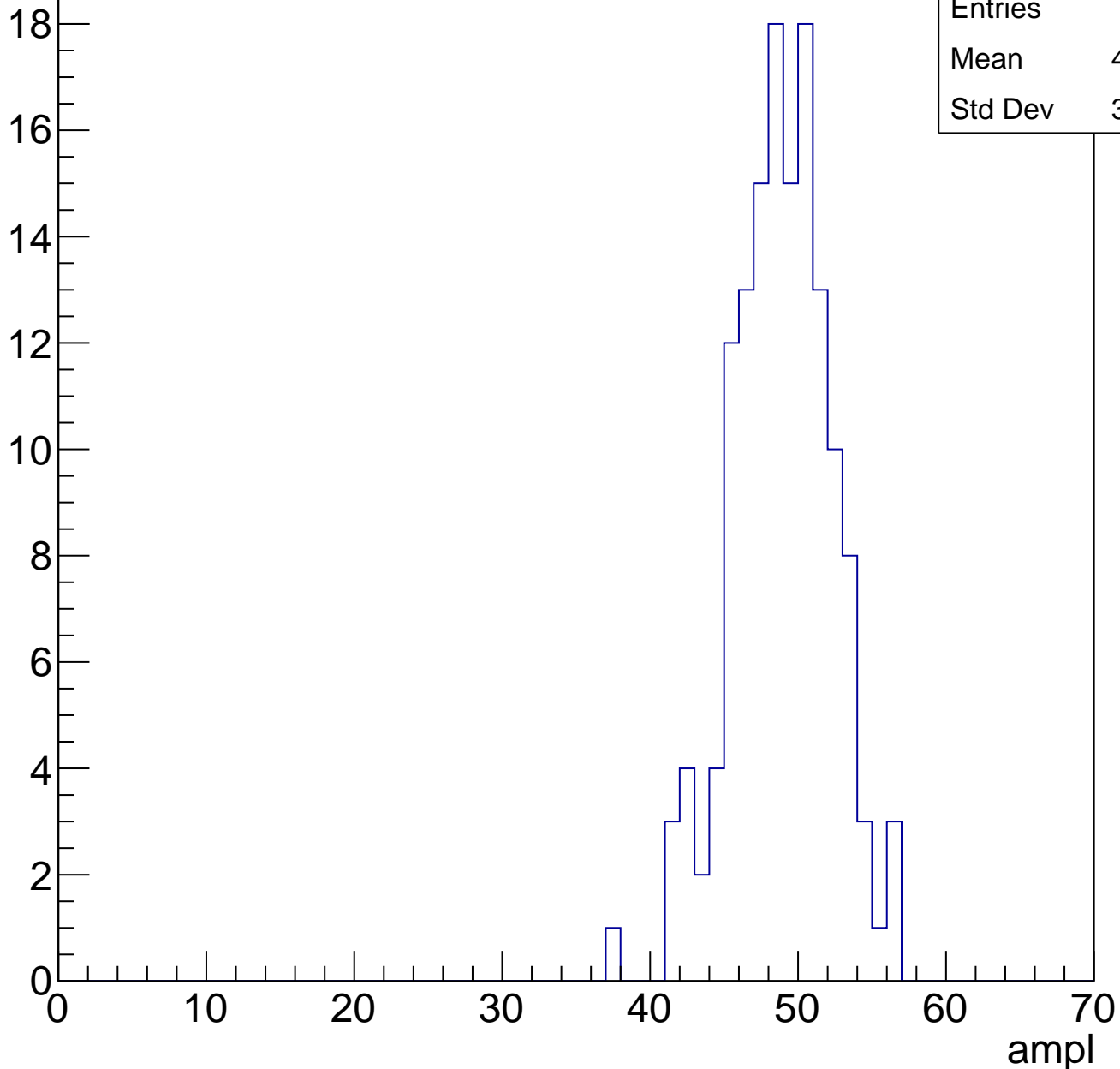


B1L001S, U24-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	48.42
Std Dev	3.333

Entry



B1L001S, U24-ch92, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.1
Std Dev	5.701

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B1L001S, U24-ch92, adc5

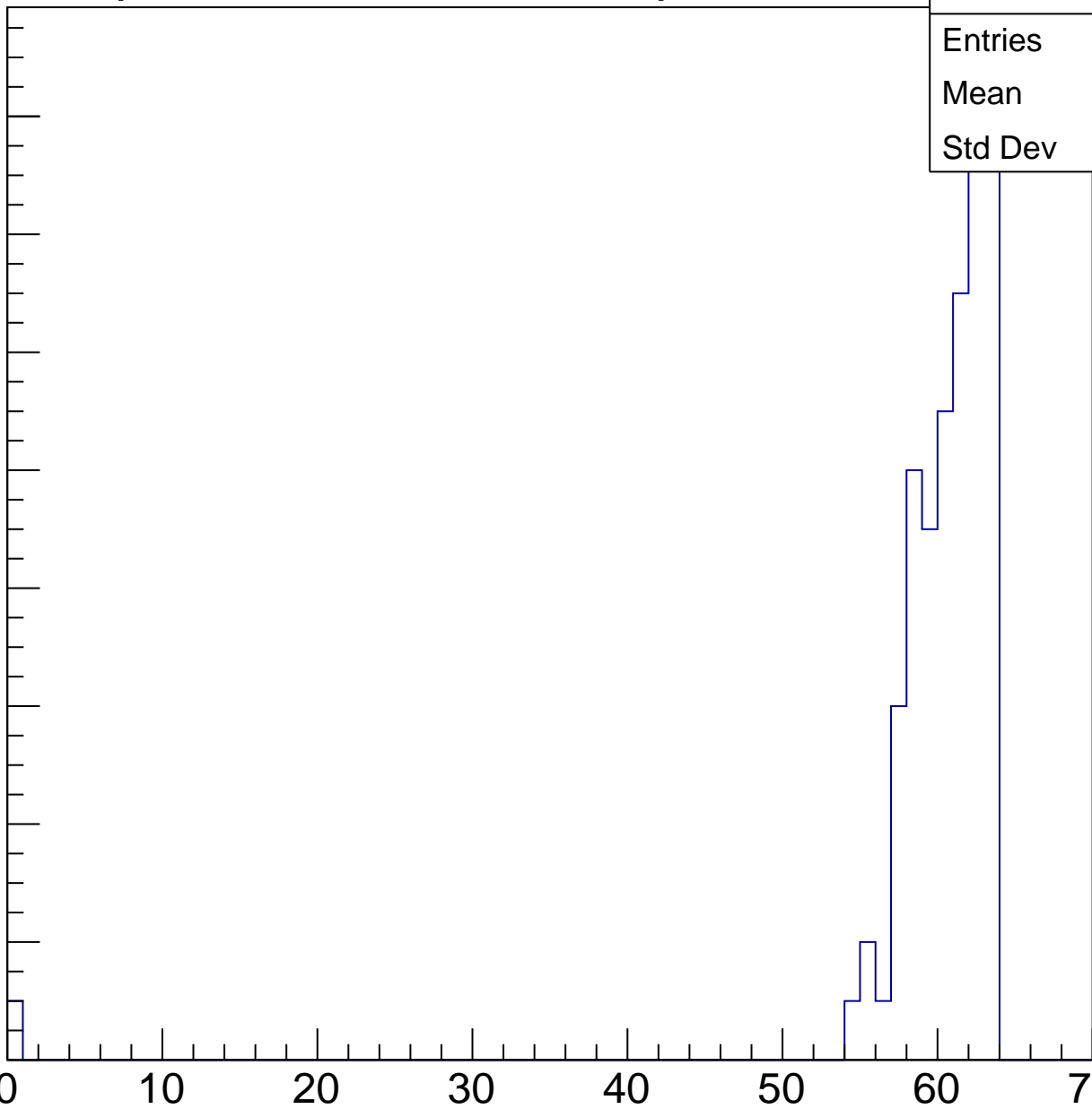
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.63
Std Dev	6.796

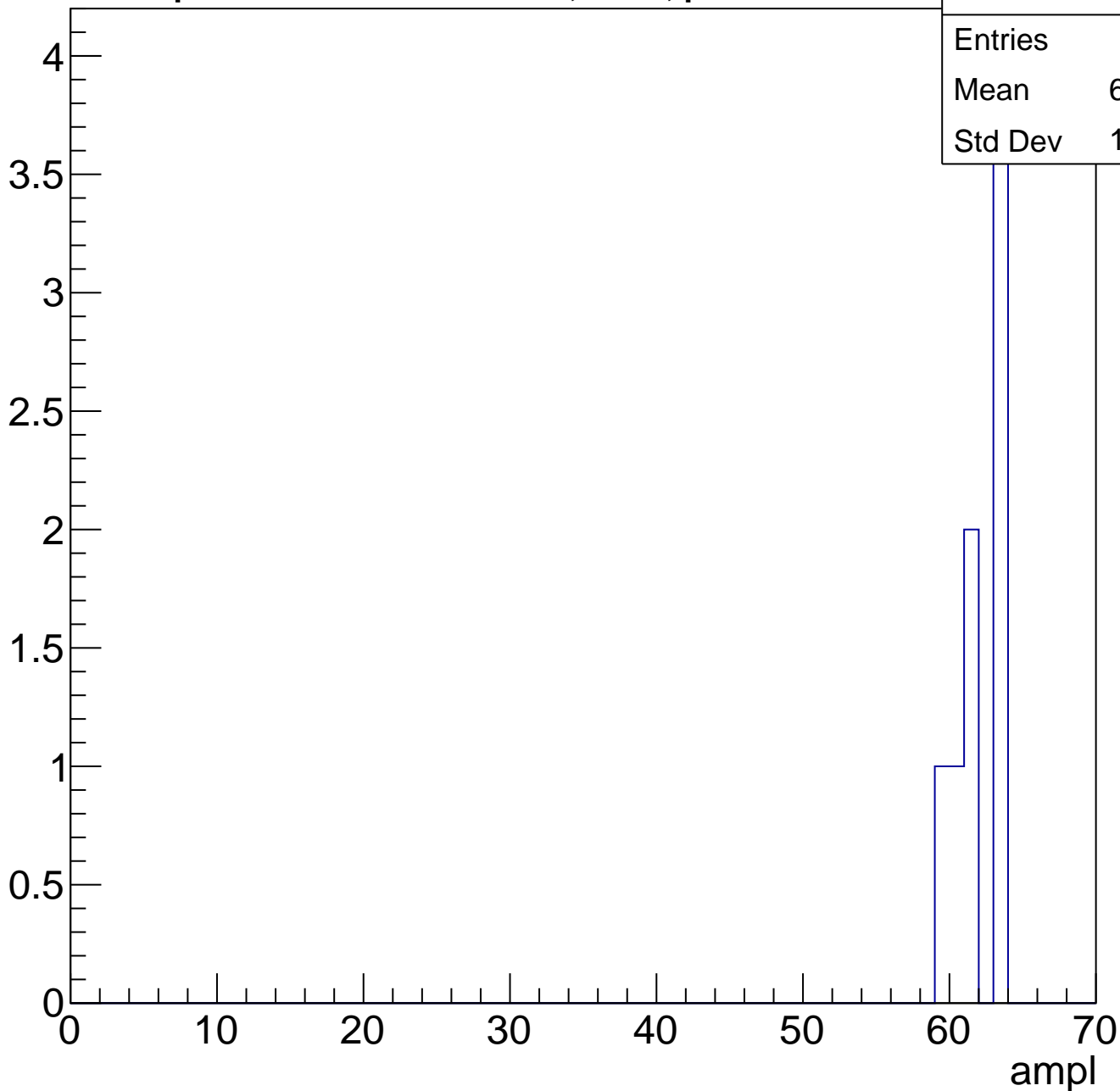
ampl



B1L001S, U24-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U24-ch93, adc0

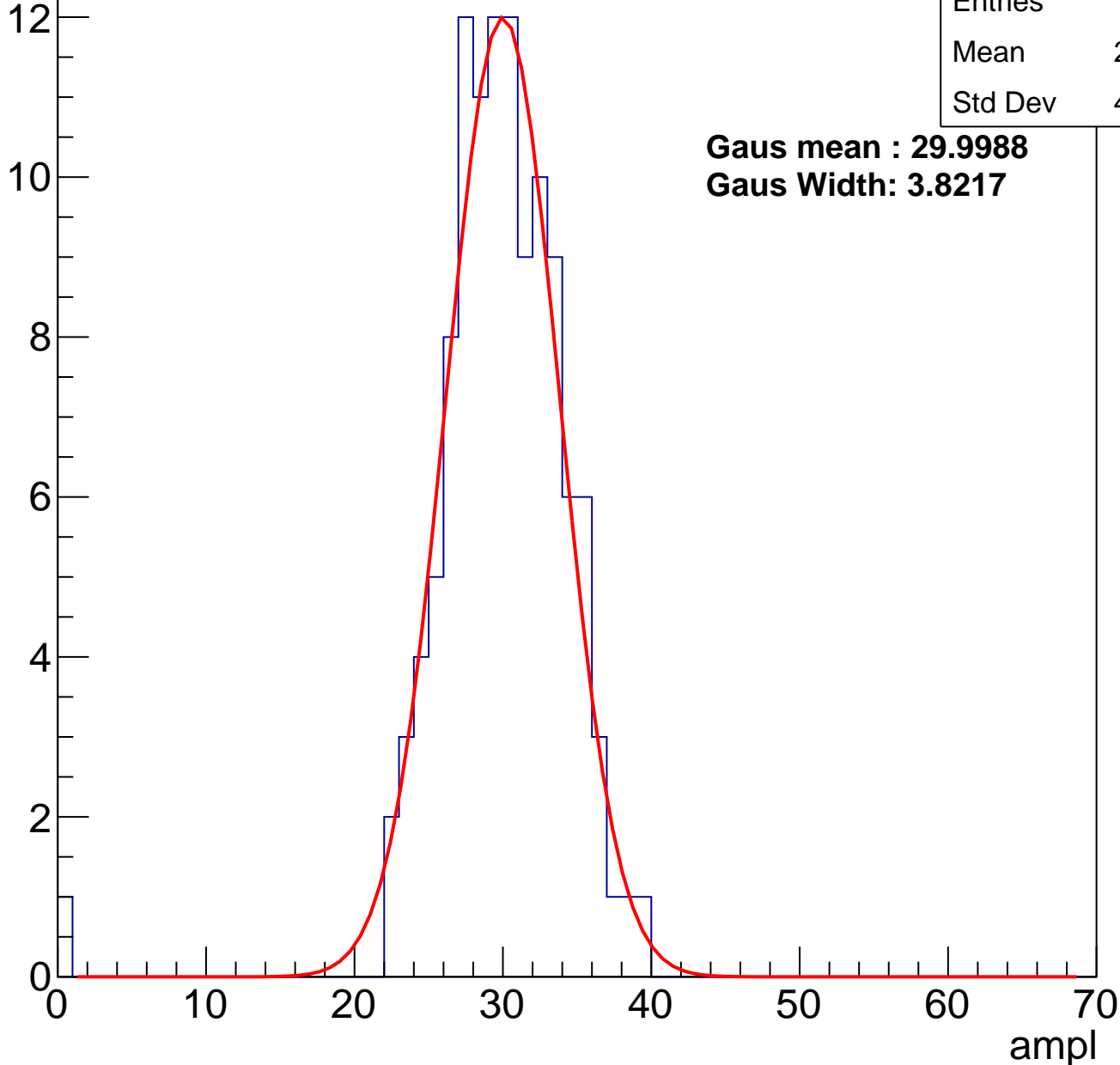
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.43
Std Dev	4.526

Gaus mean : 29.9988

Gaus Width: 3.8217

Entry



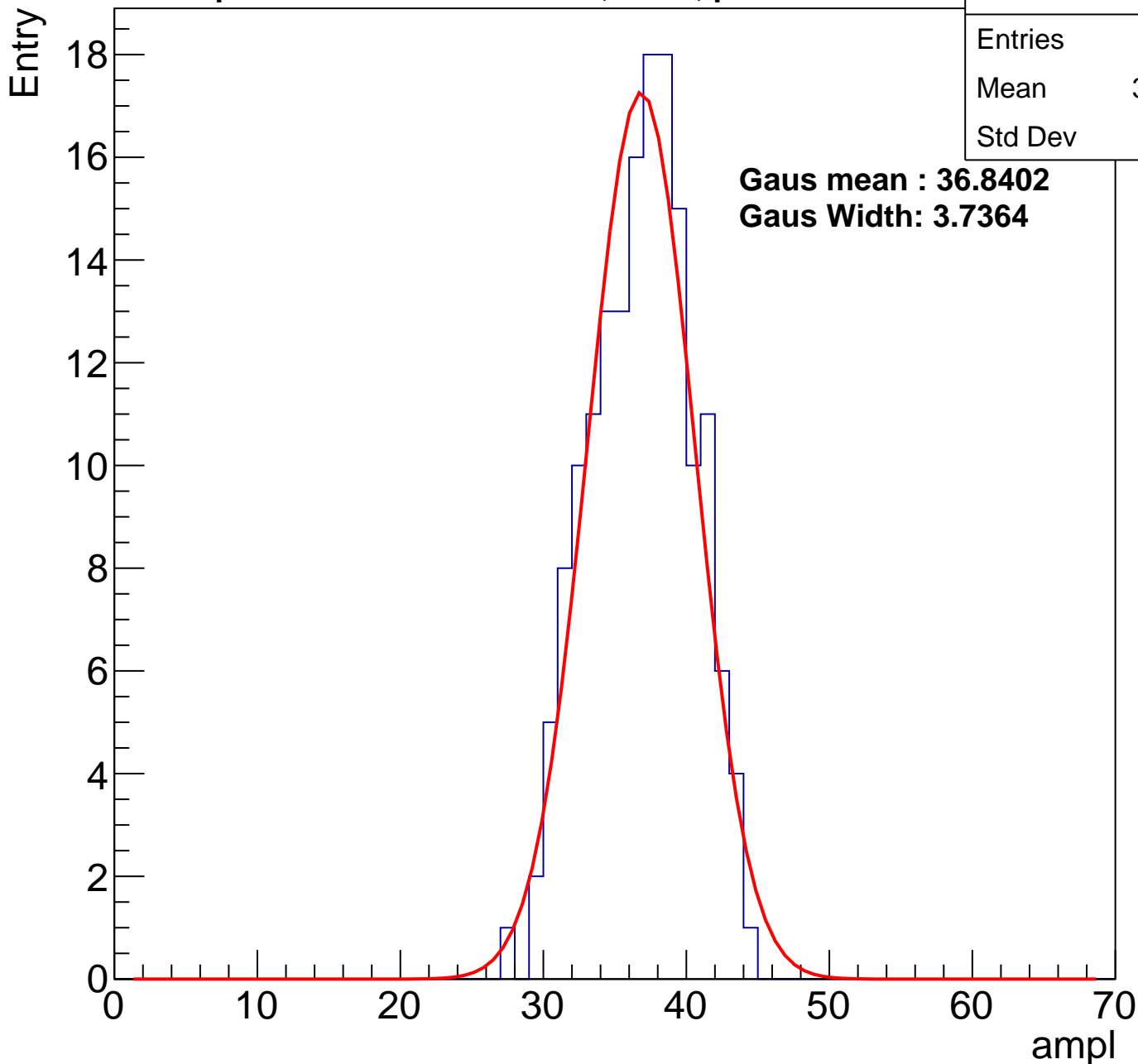
B1L001S, U24-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	36.38
Std Dev	3.51

Gaus mean : 36.8402

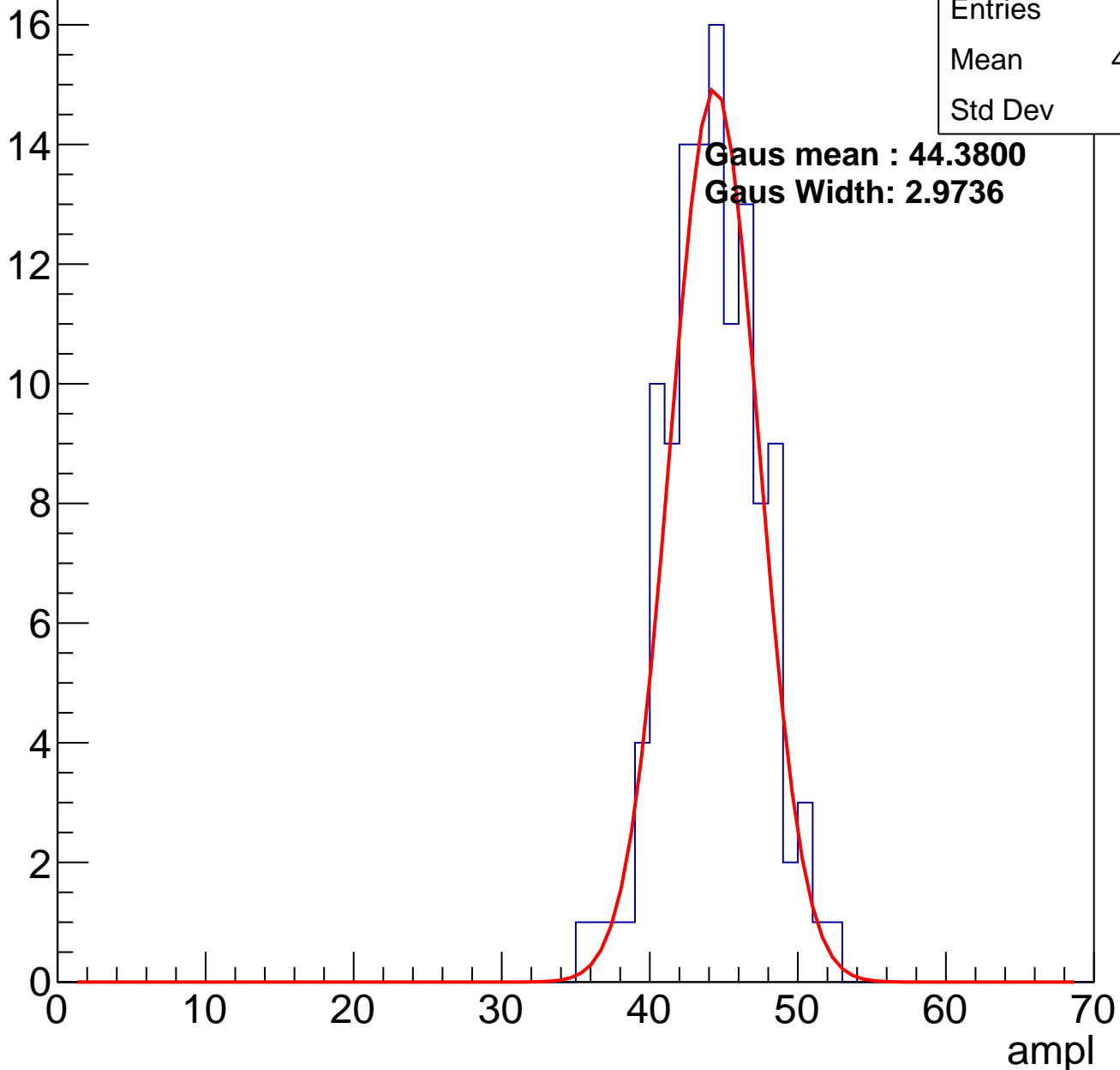
Gaus Width: 3.7364



B1L001S, U24-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

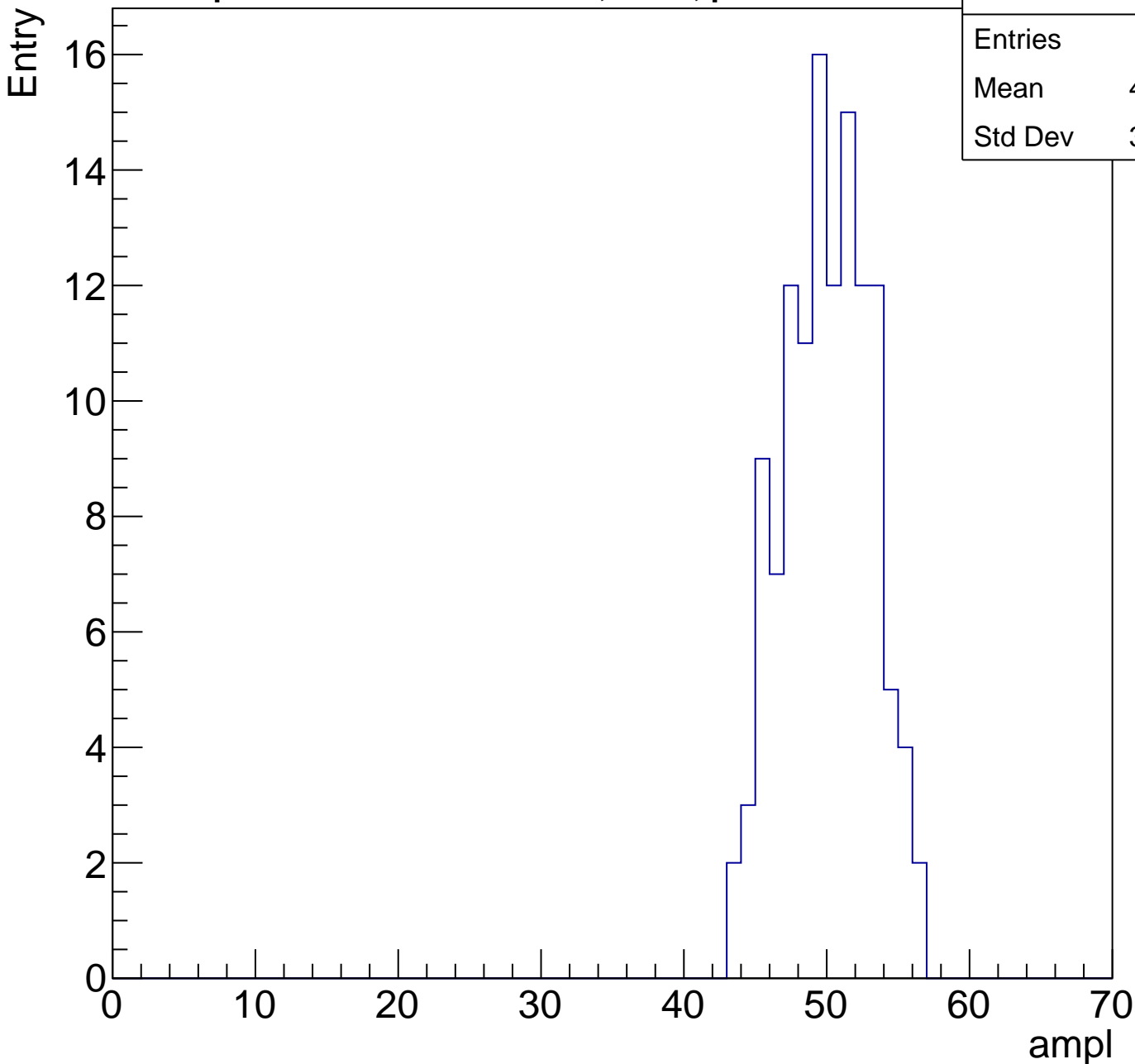


Entries	119
Mean	43.84
Std Dev	3.16

B1L001S, U24-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	49.57
Std Dev	3.019



B1L001S, U24-ch93, adc4

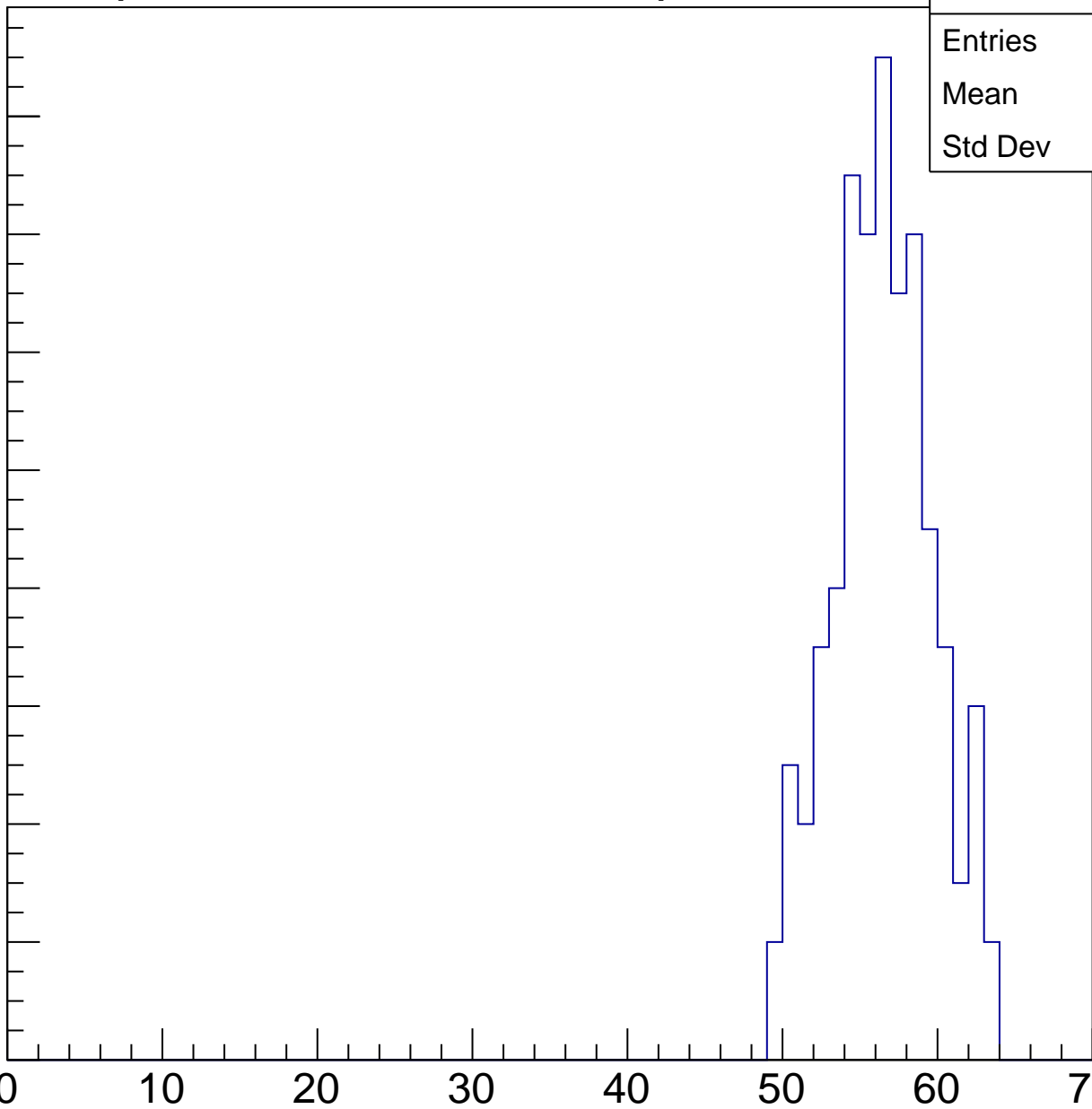
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	56.01
Std Dev	3.196

ampl



B1L001S, U24-ch93, adc5

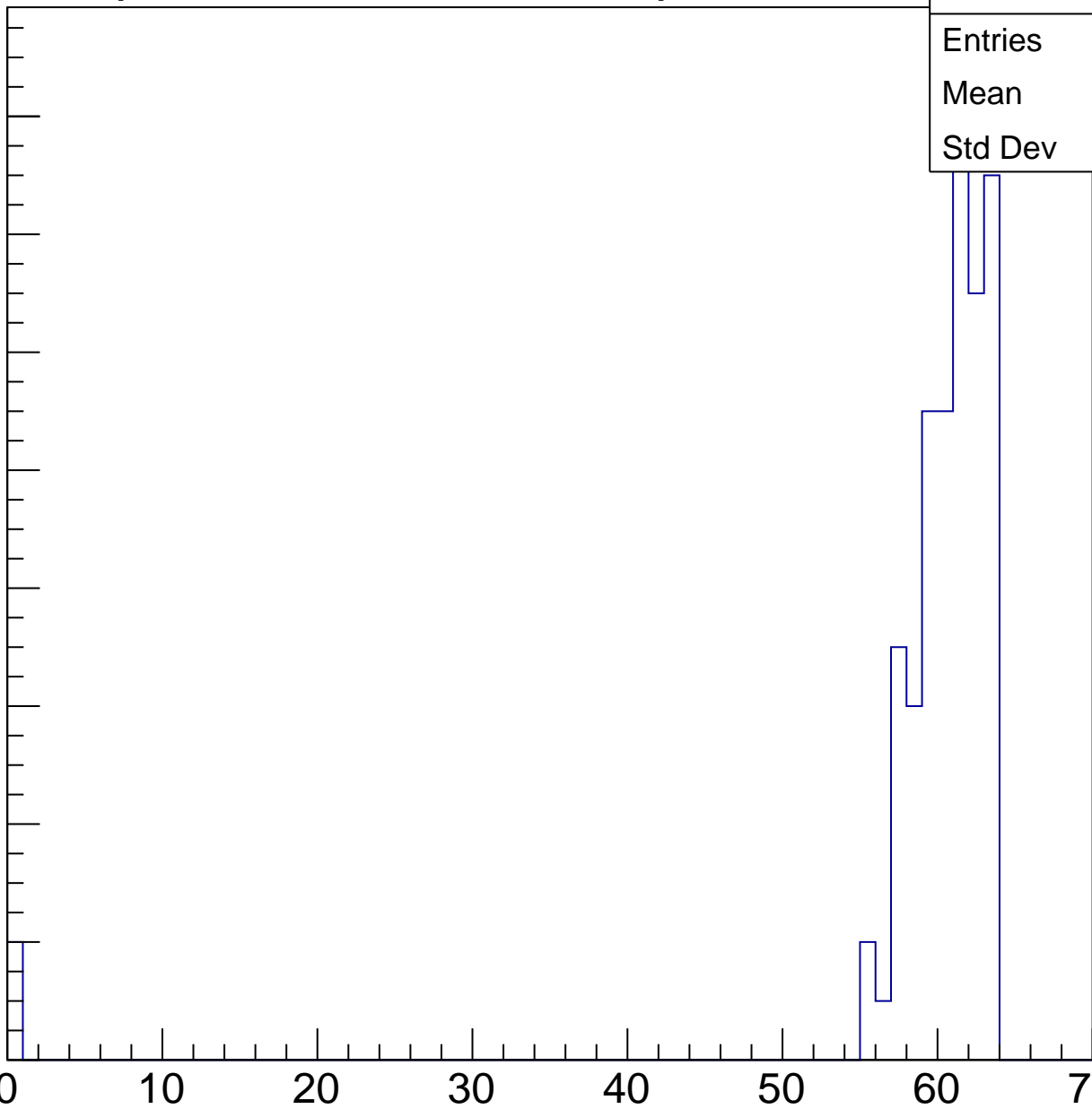
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.94
Std Dev	9.376

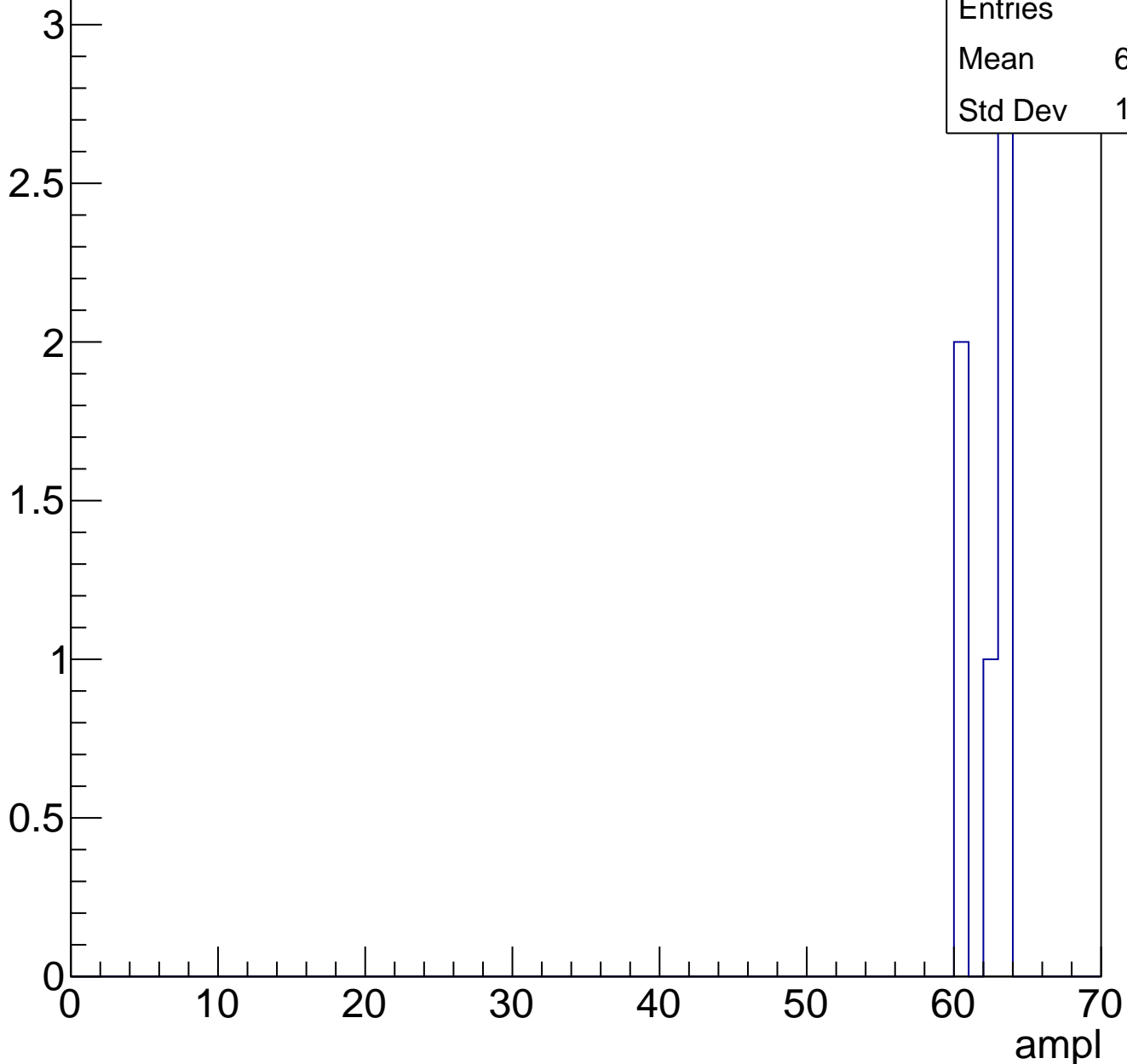
ampl



B1L001S, U24-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L001S, U24-ch94, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	28.72
Std Dev	3.987

Gaus mean : 29.4744

Gaus Width: 3.2741

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

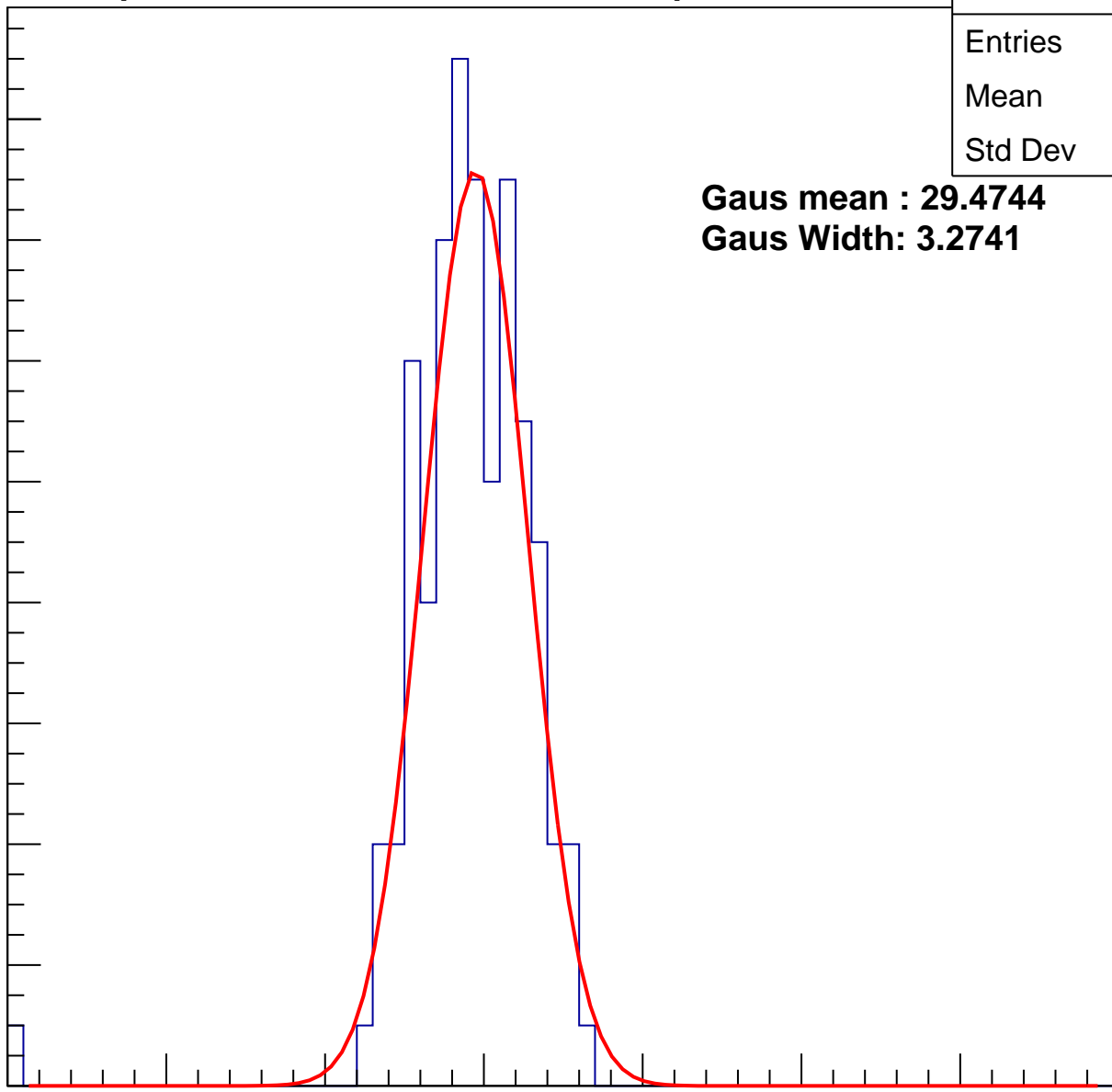
40

50

60

70

ampl



B1L001S, U24-ch94, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.26
Std Dev	3.437

Gaus mean : 35.6787

Gaus Width: 3.5029

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	41.93
Std Dev	3.081

Gaus mean : 42.3572

Gaus Width: 3.3562

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

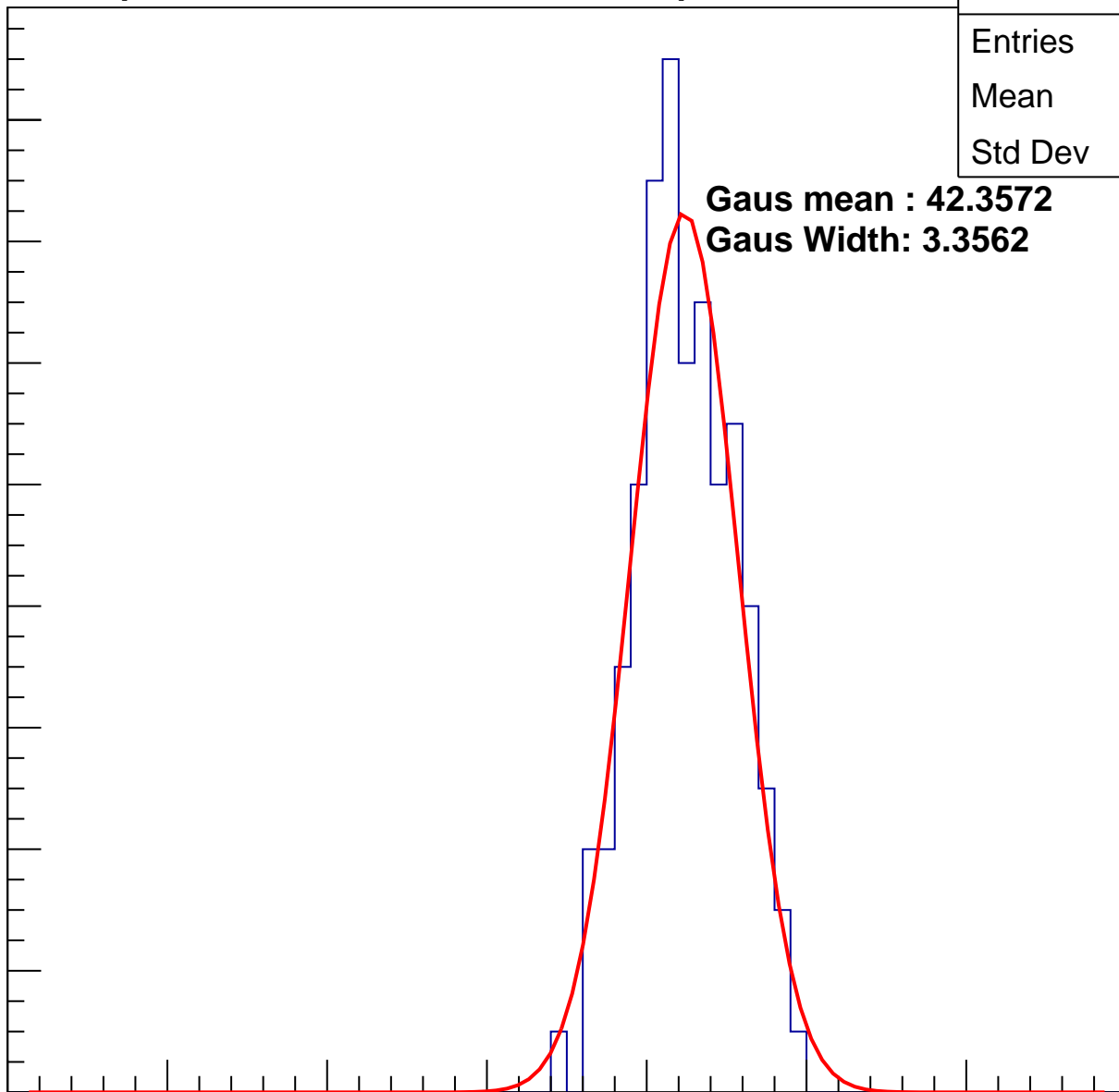
40

50

60

70

ampl



B1L001S, U24-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

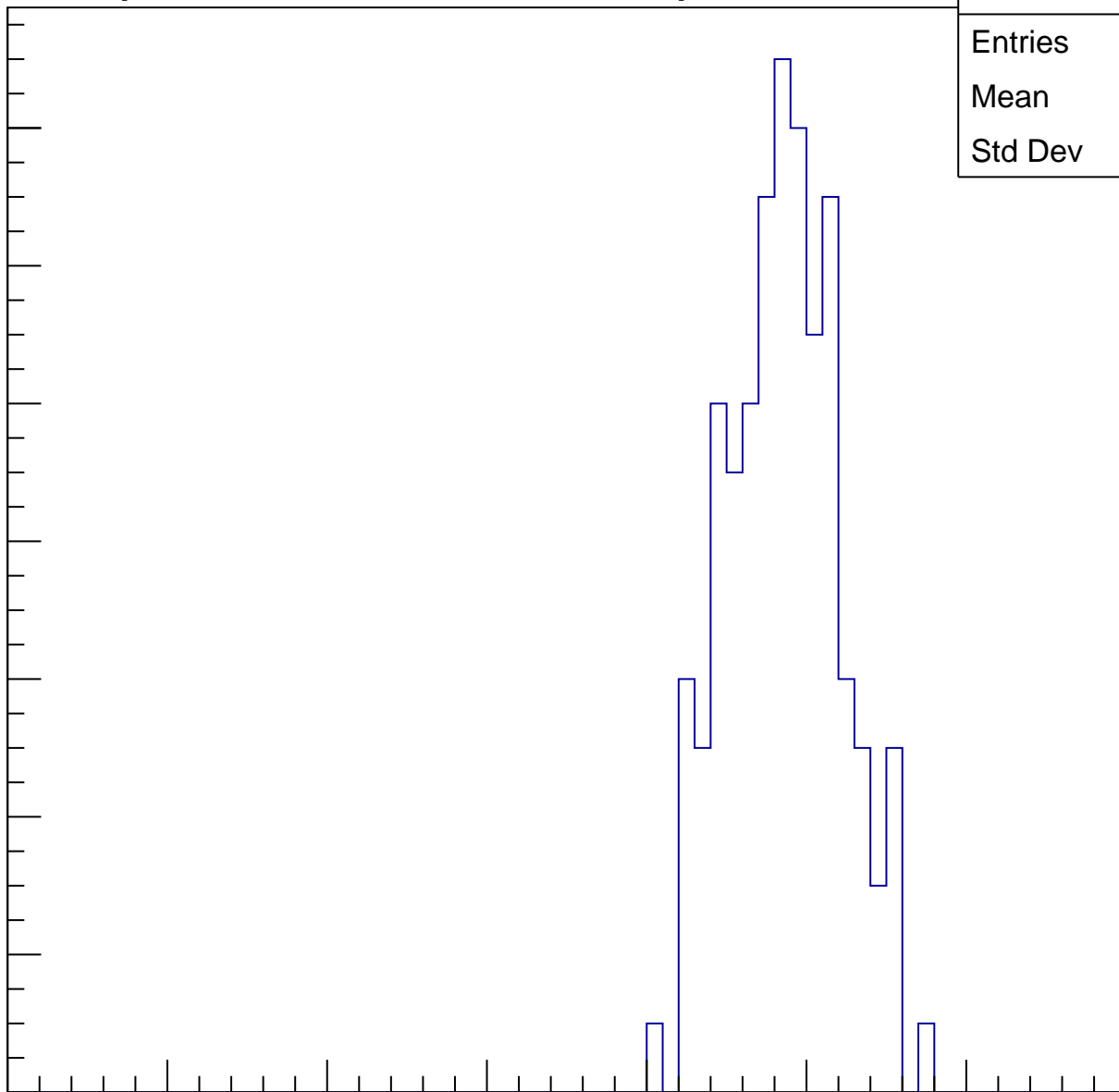
Entries	127
Mean	48.13
Std Dev	3.467

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

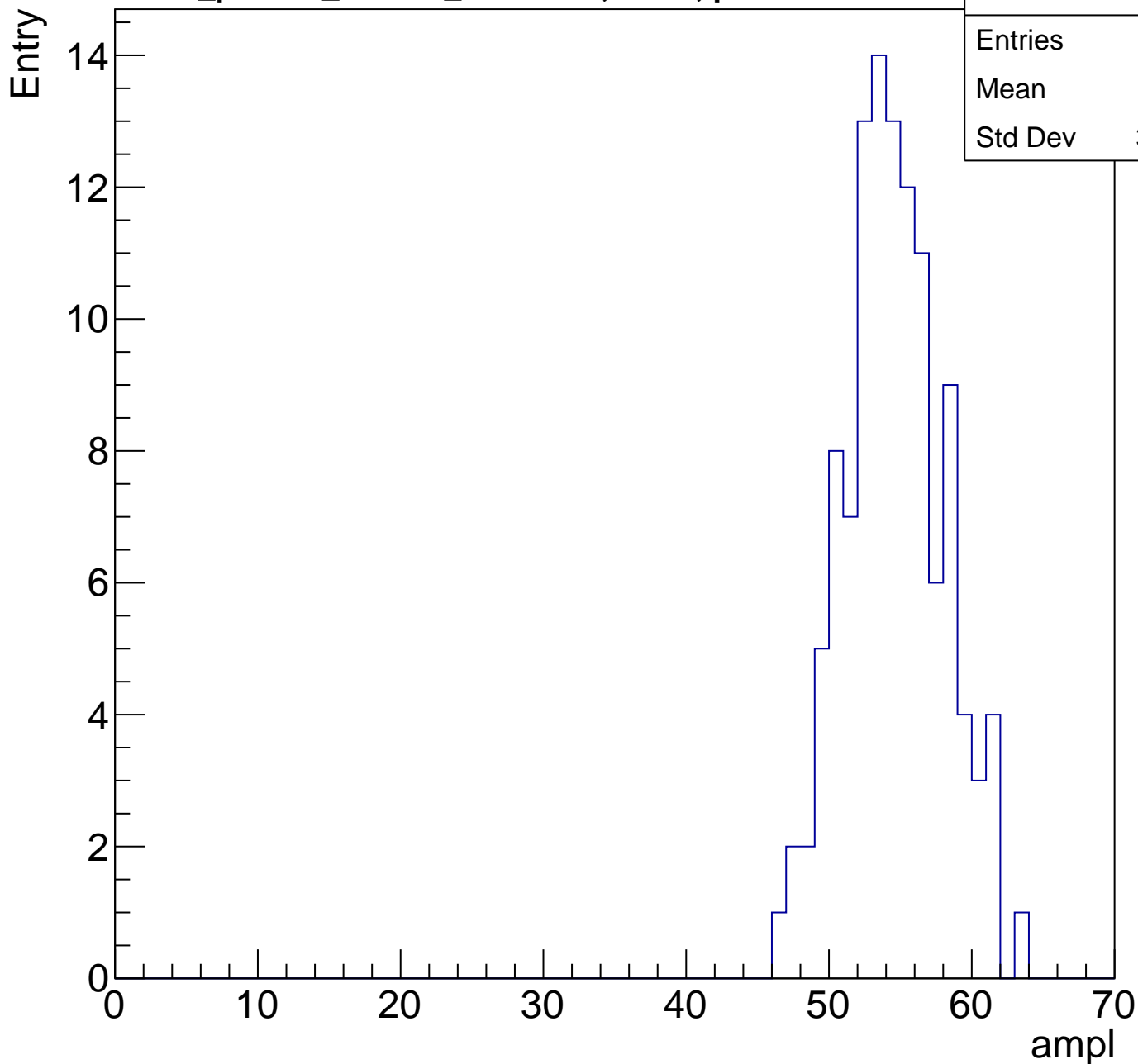
ampl



B1L001S, U24-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	54.1
Std Dev	3.431



B1L001S, U24-ch94, adc5

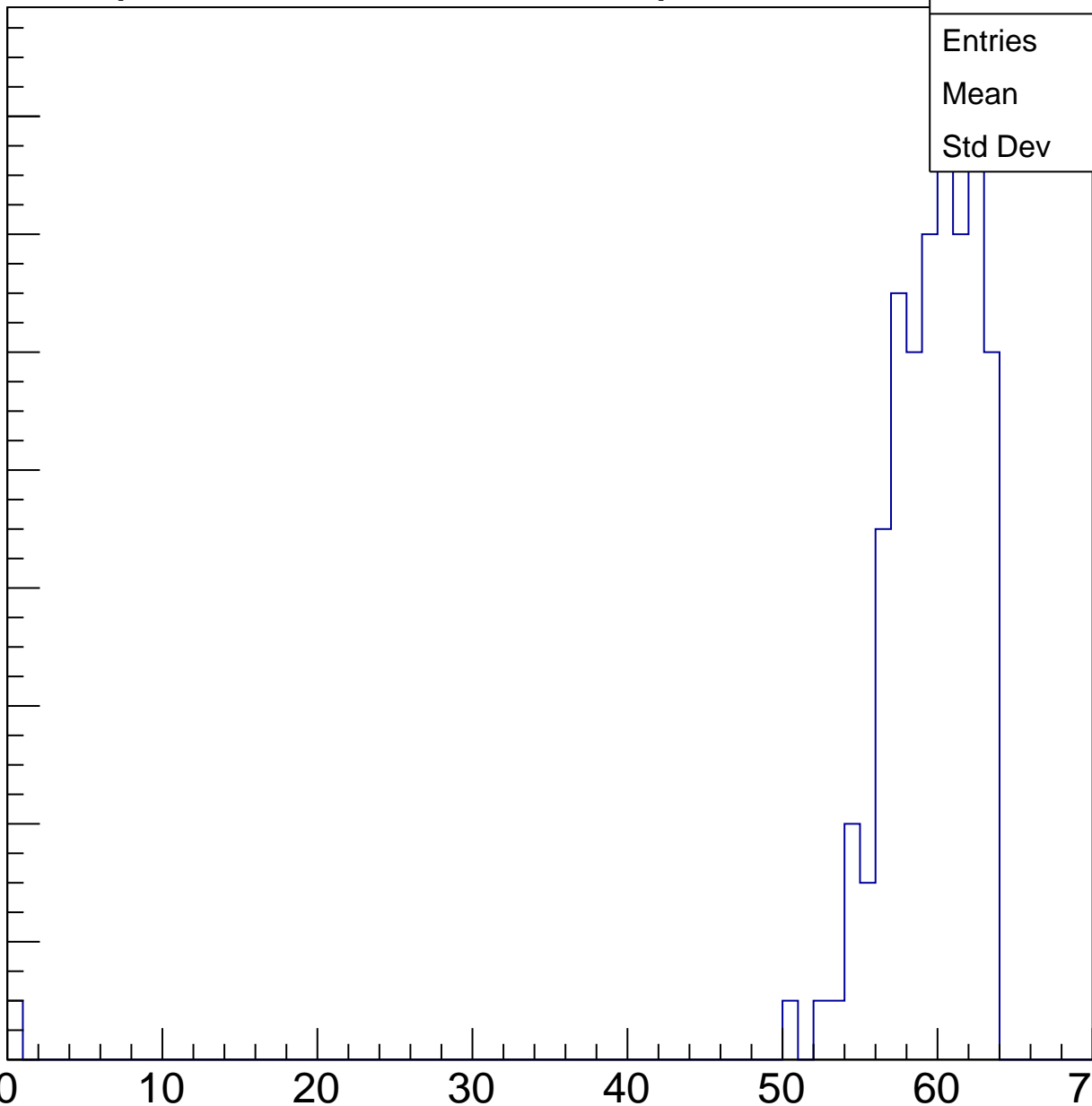
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	58.7
Std Dev	6.069

ampl

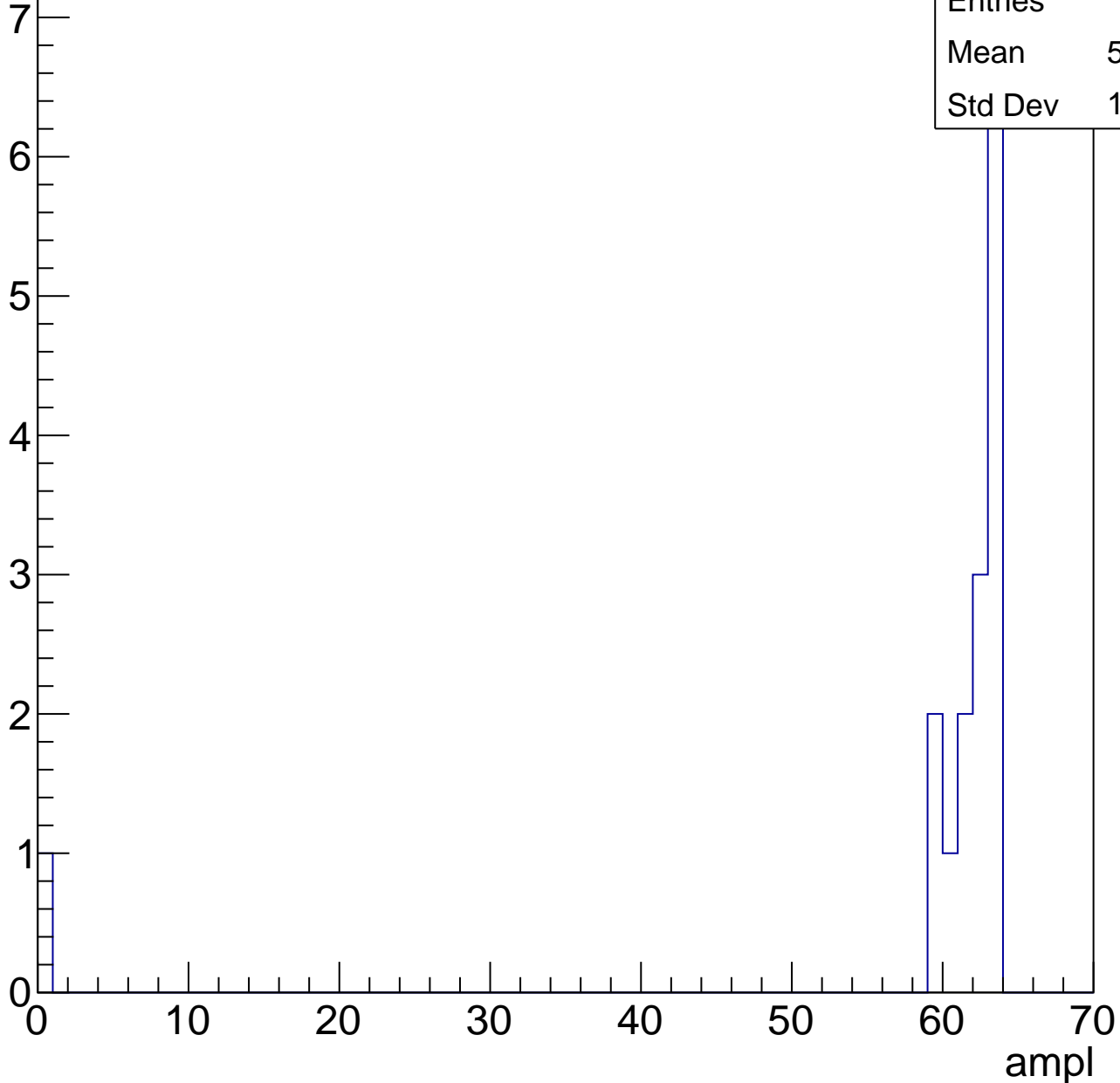


B1L001S, U24-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	57.94
Std Dev	15.02



B1L001S, U24-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

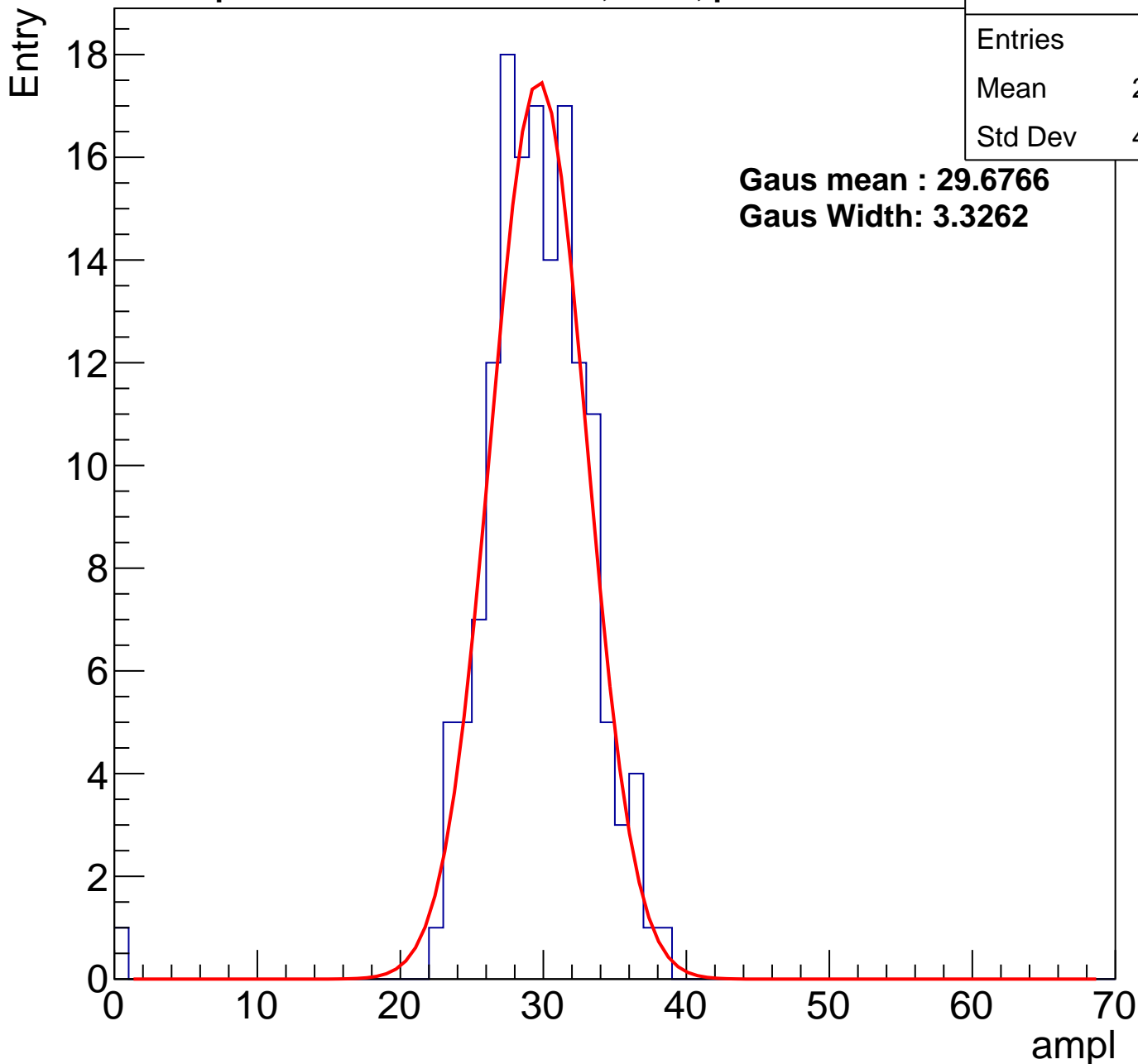
B1L001S, U24-ch95, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	29.06
Std Dev	4.035

Gaus mean : 29.6766

Gaus Width: 3.3262



B1L001S, U24-ch95, adc1

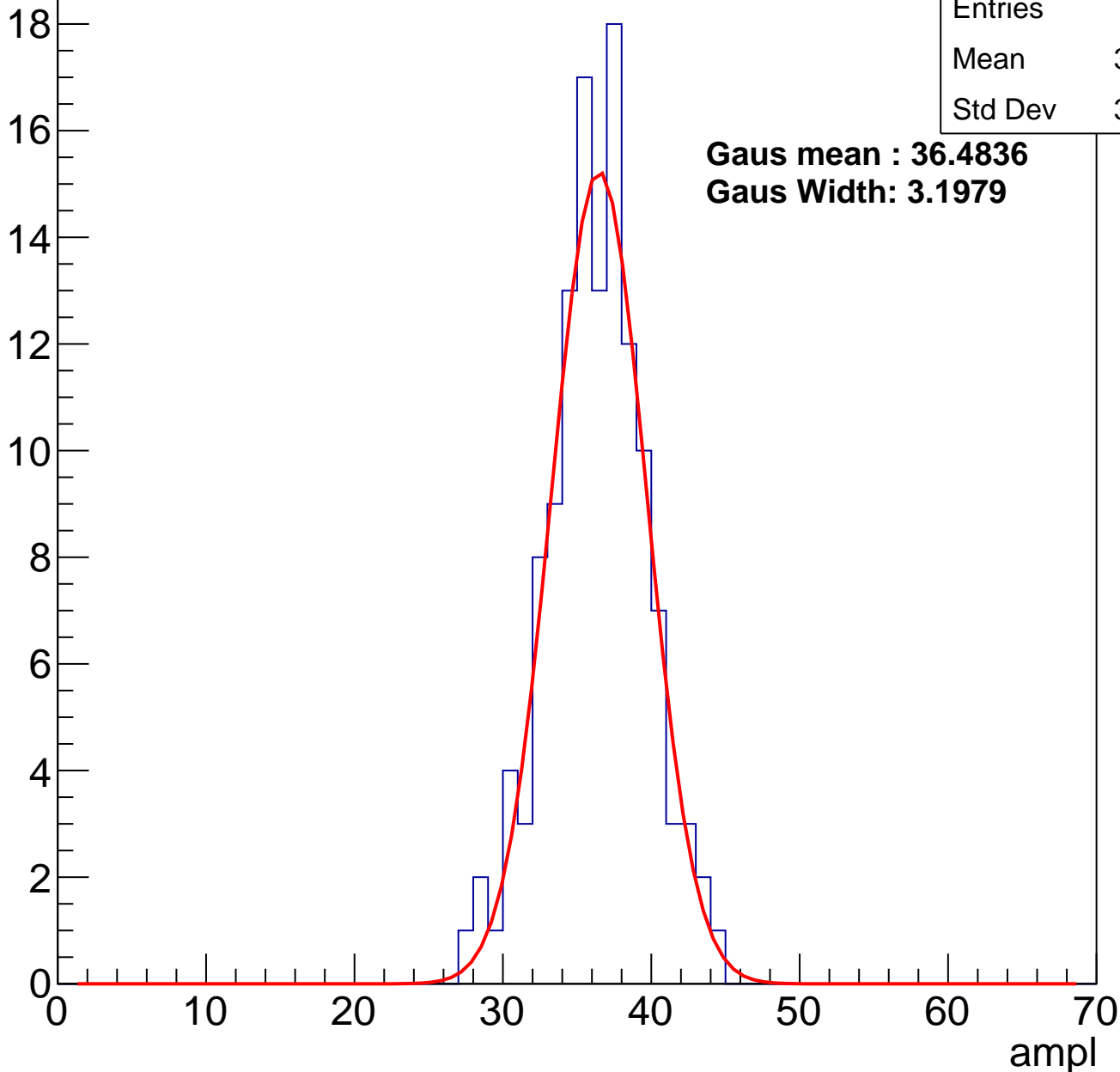
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	35.86
Std Dev	3.279

Gaus mean : 36.4836

Gaus Width: 3.1979

Entry



B1L001S, U24-ch95, adc2

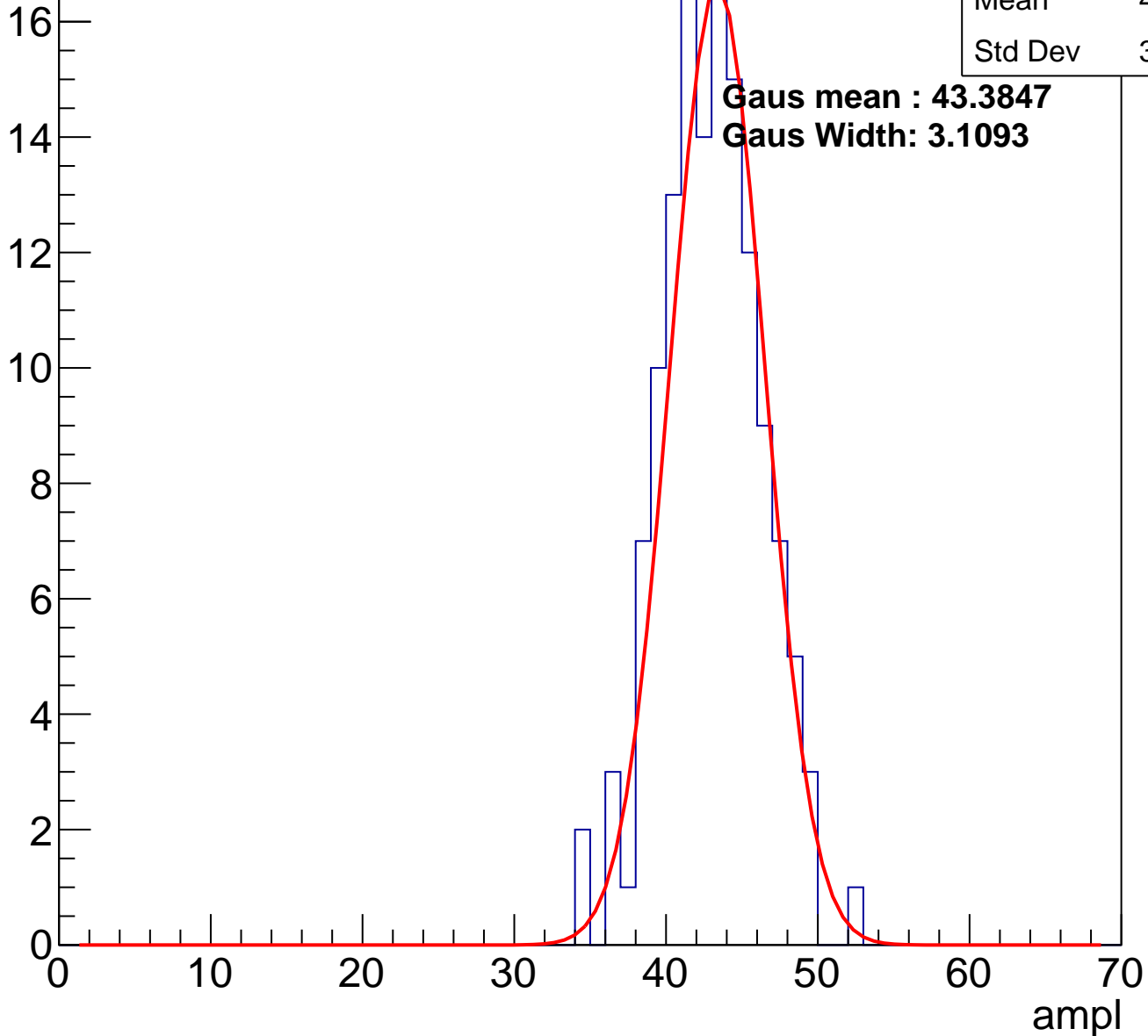
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	136
Mean	42.55
Std Dev	3.233

Gaus mean : 43.3847

Gaus Width: 3.1093

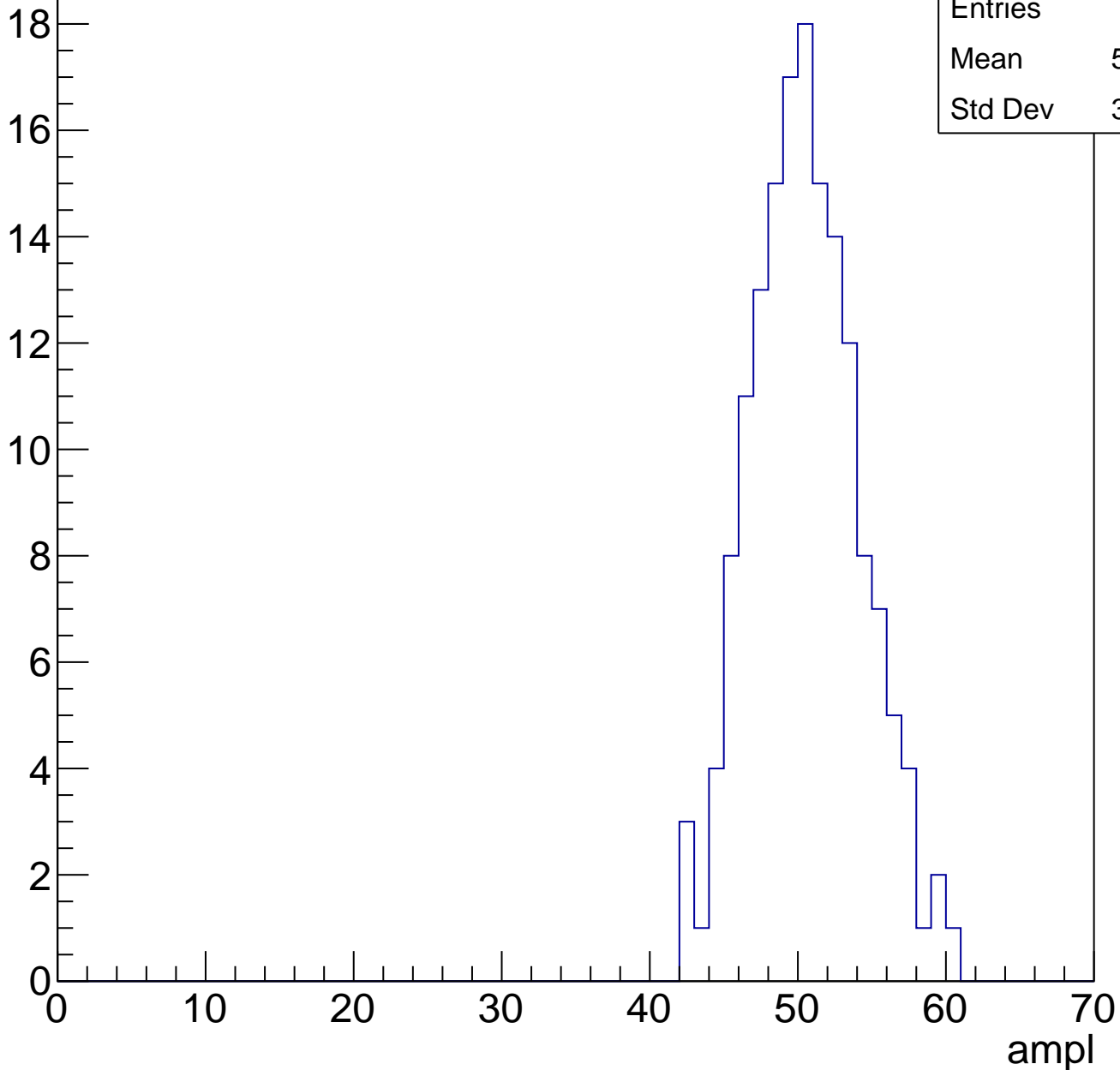


B1L001S, U24-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	50.09
Std Dev	3.667

Entry



B1L001S, U24-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	56.6
Std Dev	3.164

Entry

12

10

8

6

4

2

0

0

10

20

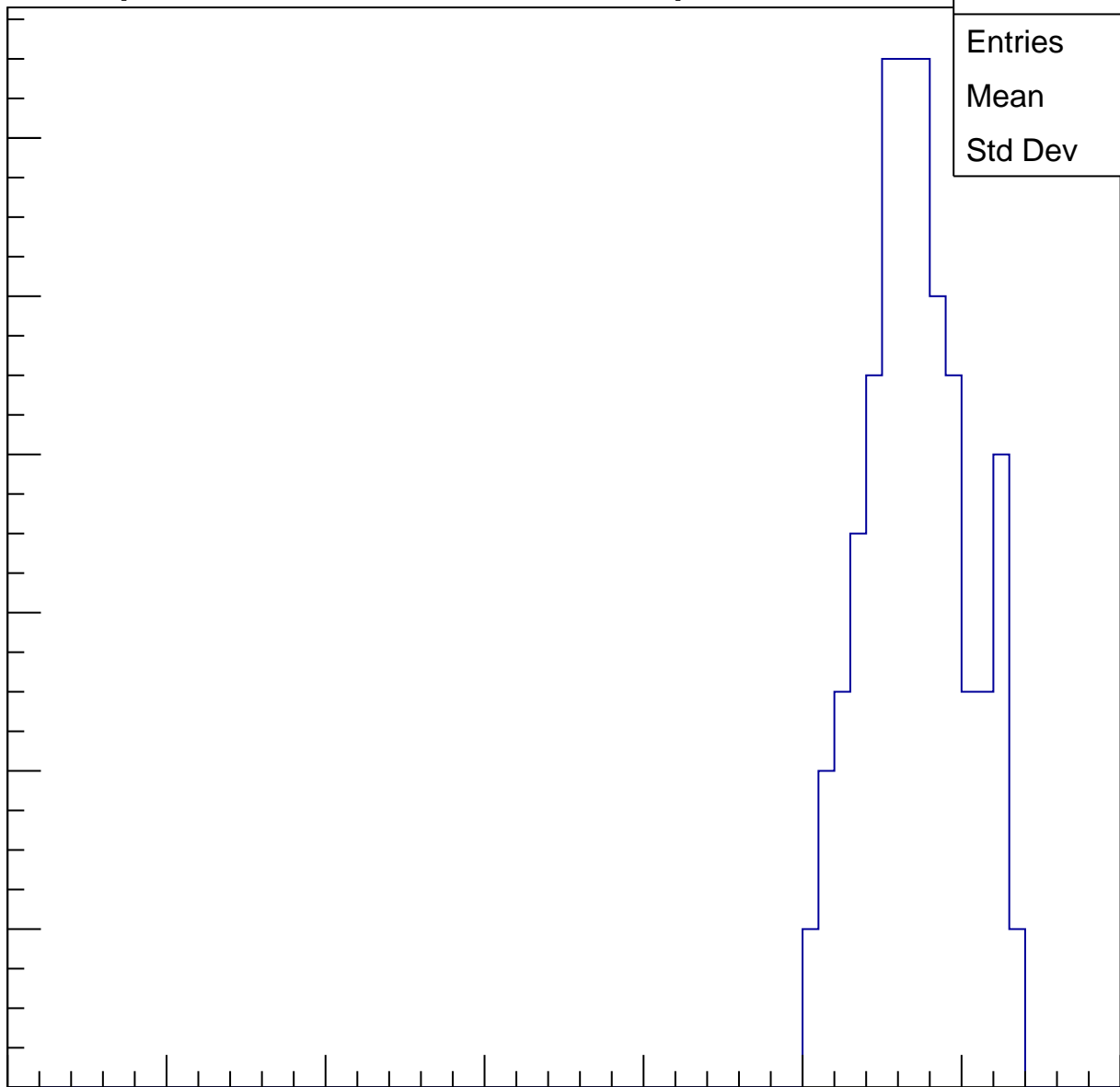
30

40

50

60

ampl



B1L001S, U24-ch95, adc5

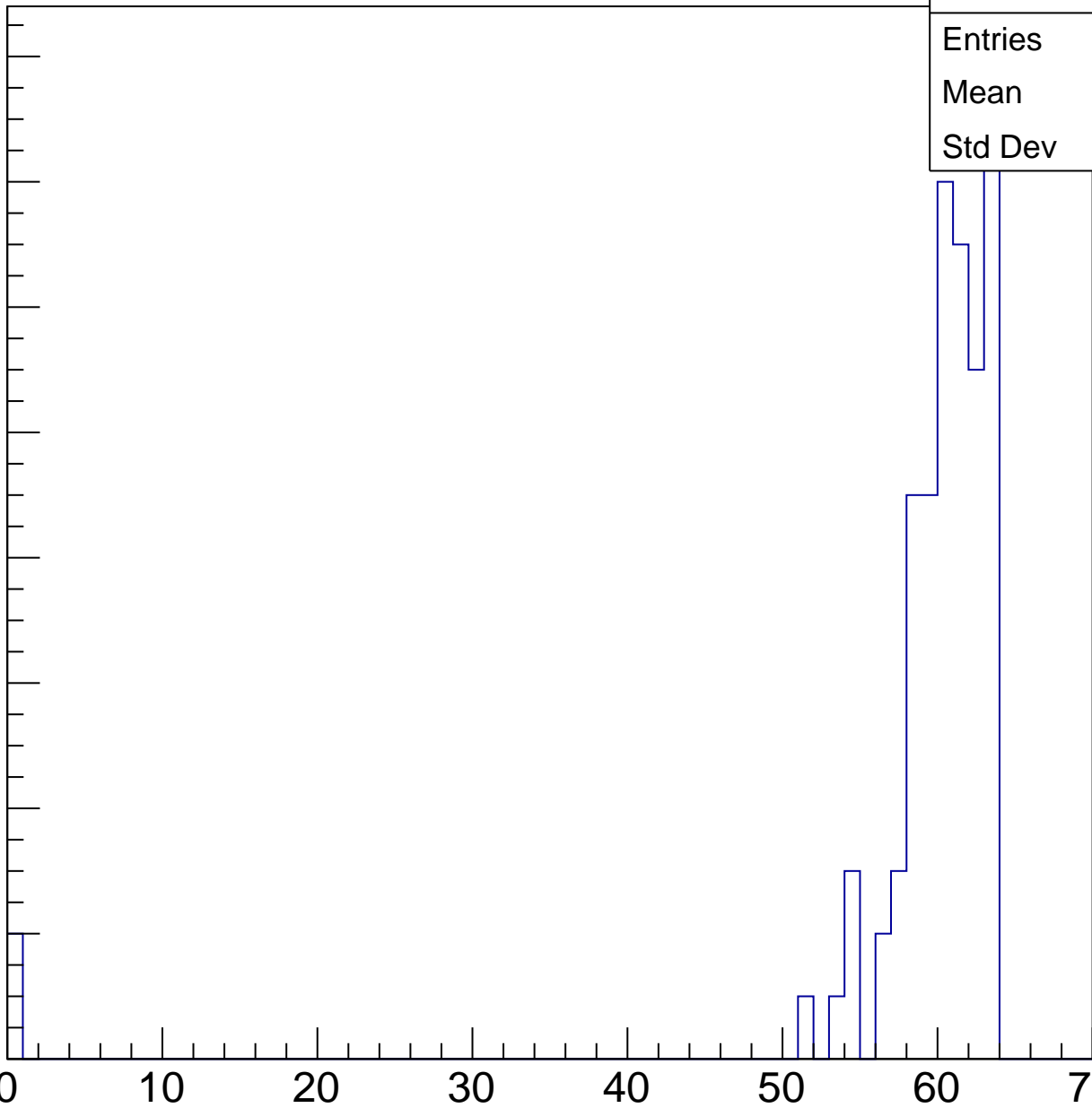
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.63
Std Dev	9.503

ampl



B1L001S, U24-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B1L001S, U24-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch96, adc0

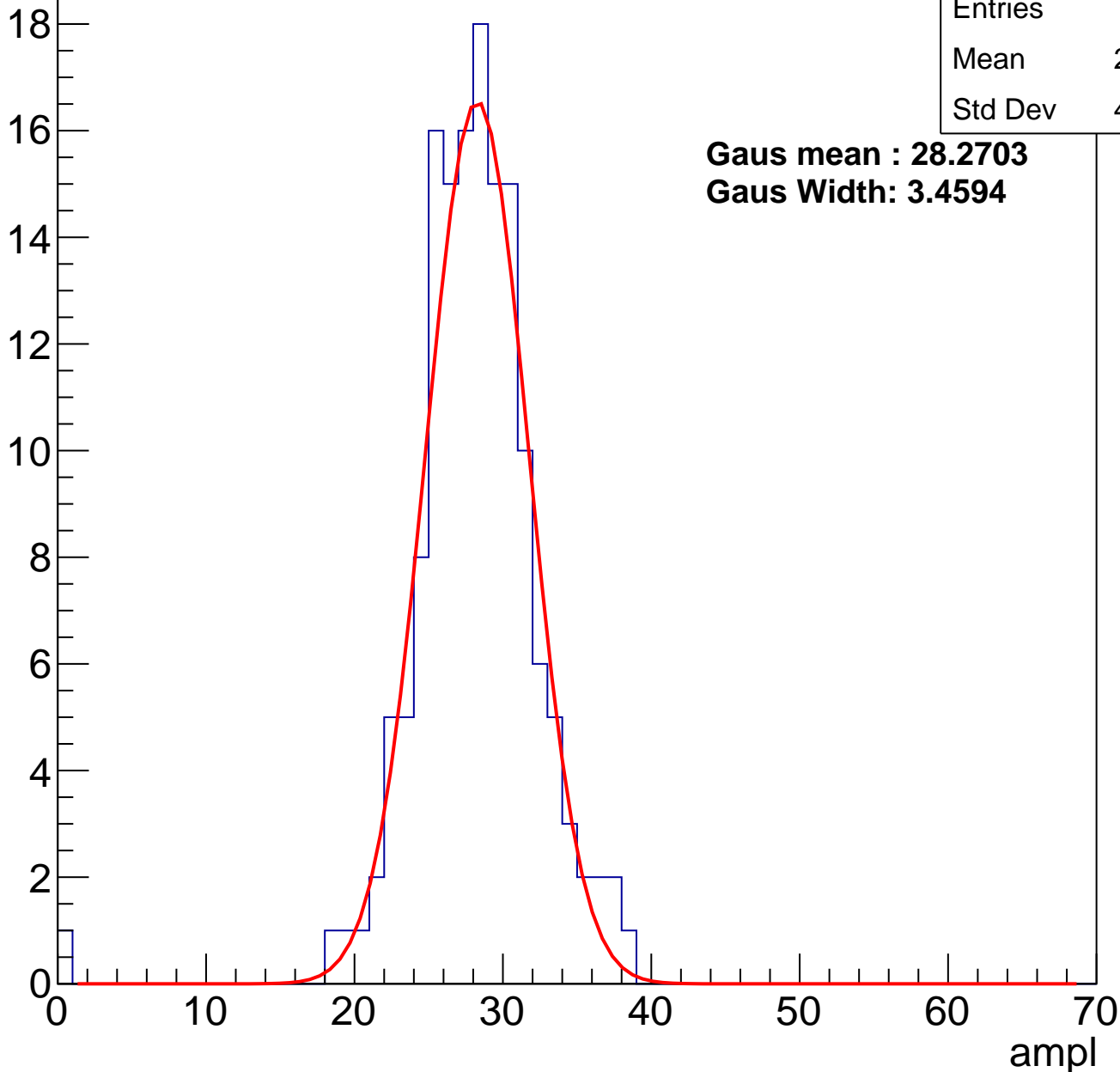
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	27.67
Std Dev	4.278

Gaus mean : 28.2703

Gaus Width: 3.4594

Entry



B1L001S, U24-ch96, adc1

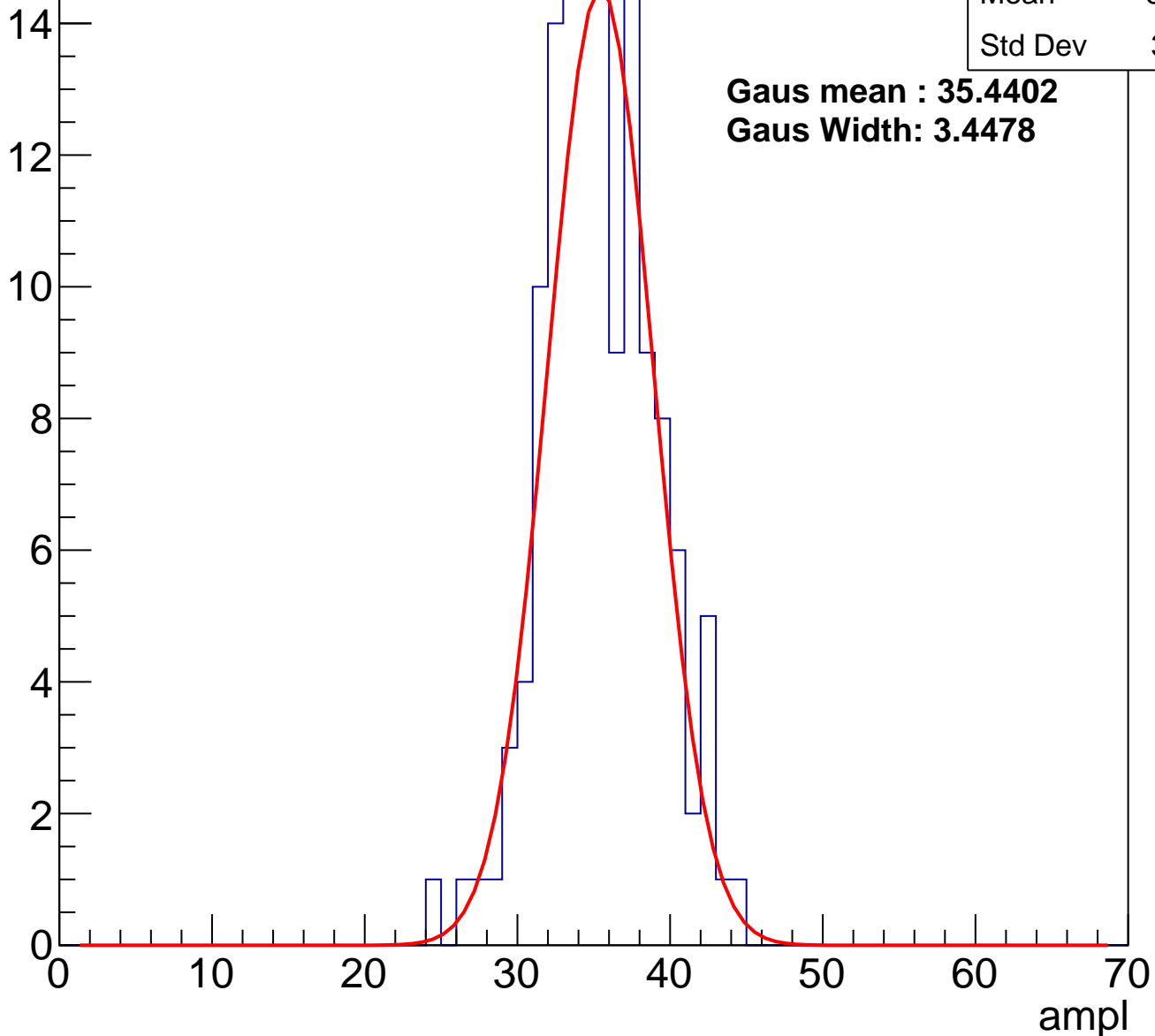
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	34.94
Std Dev	3.611

Gaus mean : 35.4402

Gaus Width: 3.4478

Entry



B1L001S, U24-ch96, adc2

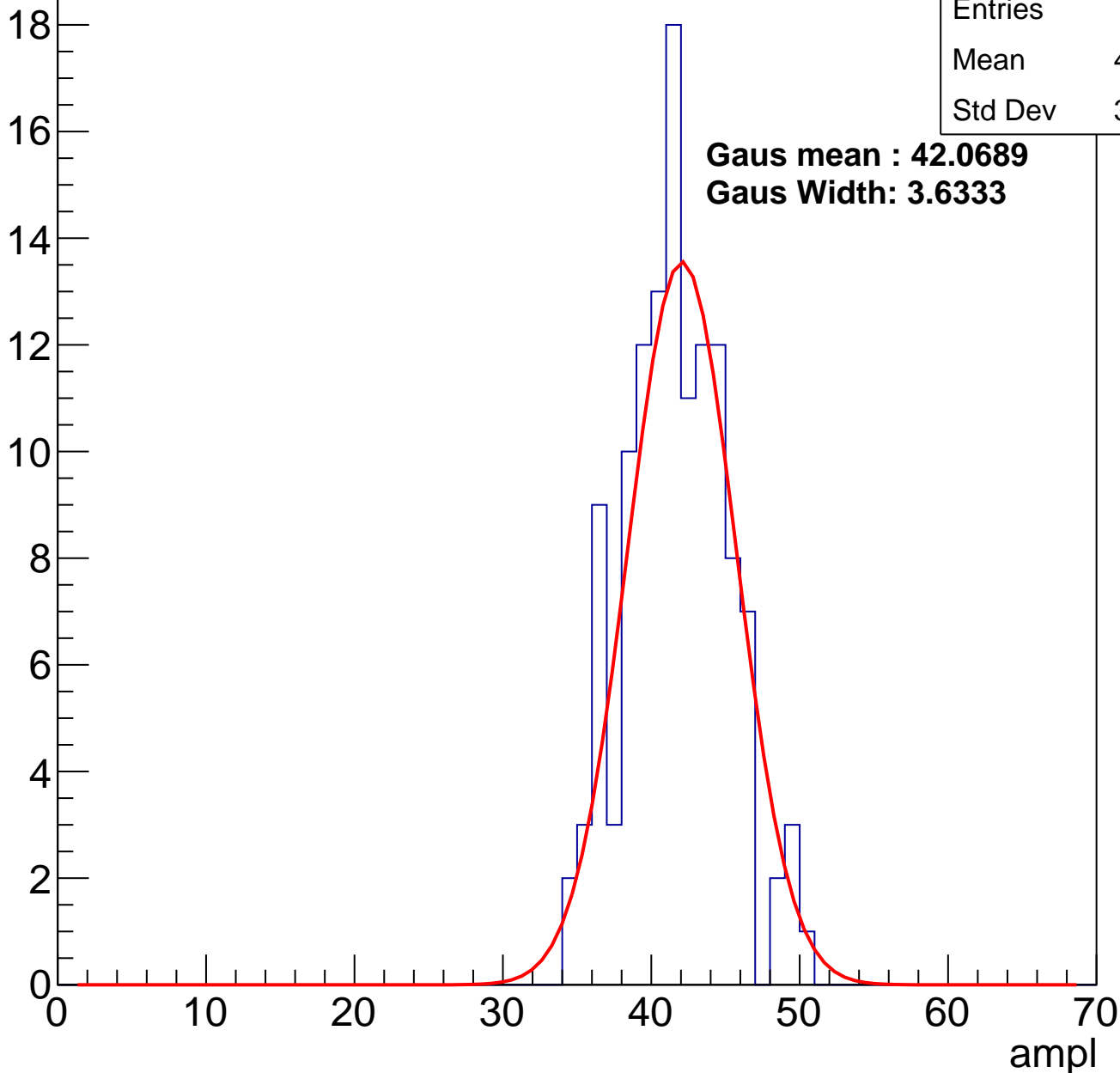
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	41.23
Std Dev	3.409

Gaus mean : 42.0689

Gaus Width: 3.6333

Entry

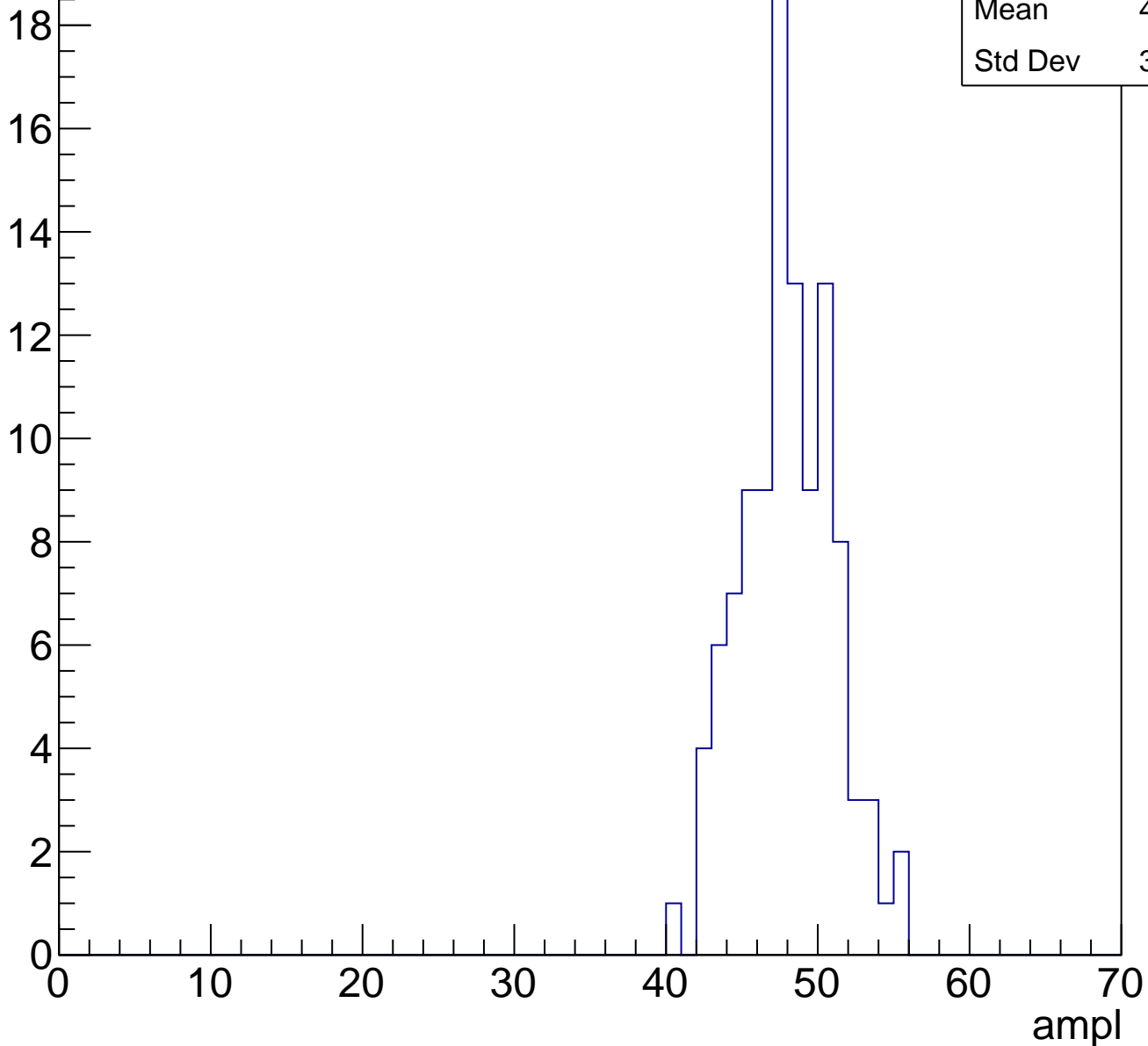


B1L001S, U24-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	47.55
Std Dev	3.018

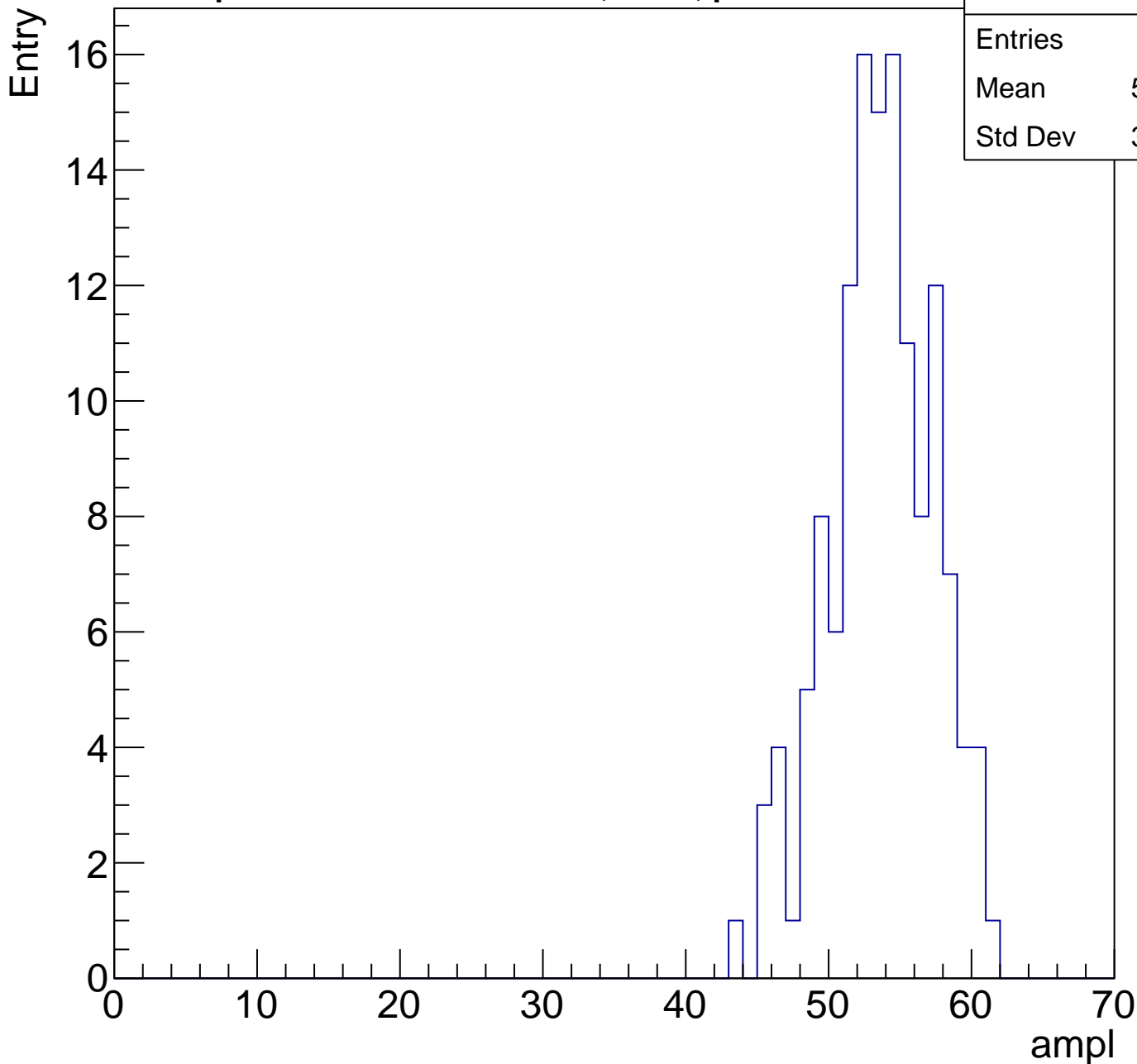
Entry



B1L001S, U24-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	53.16
Std Dev	3.659



B1L001S, U24-ch96, adc5

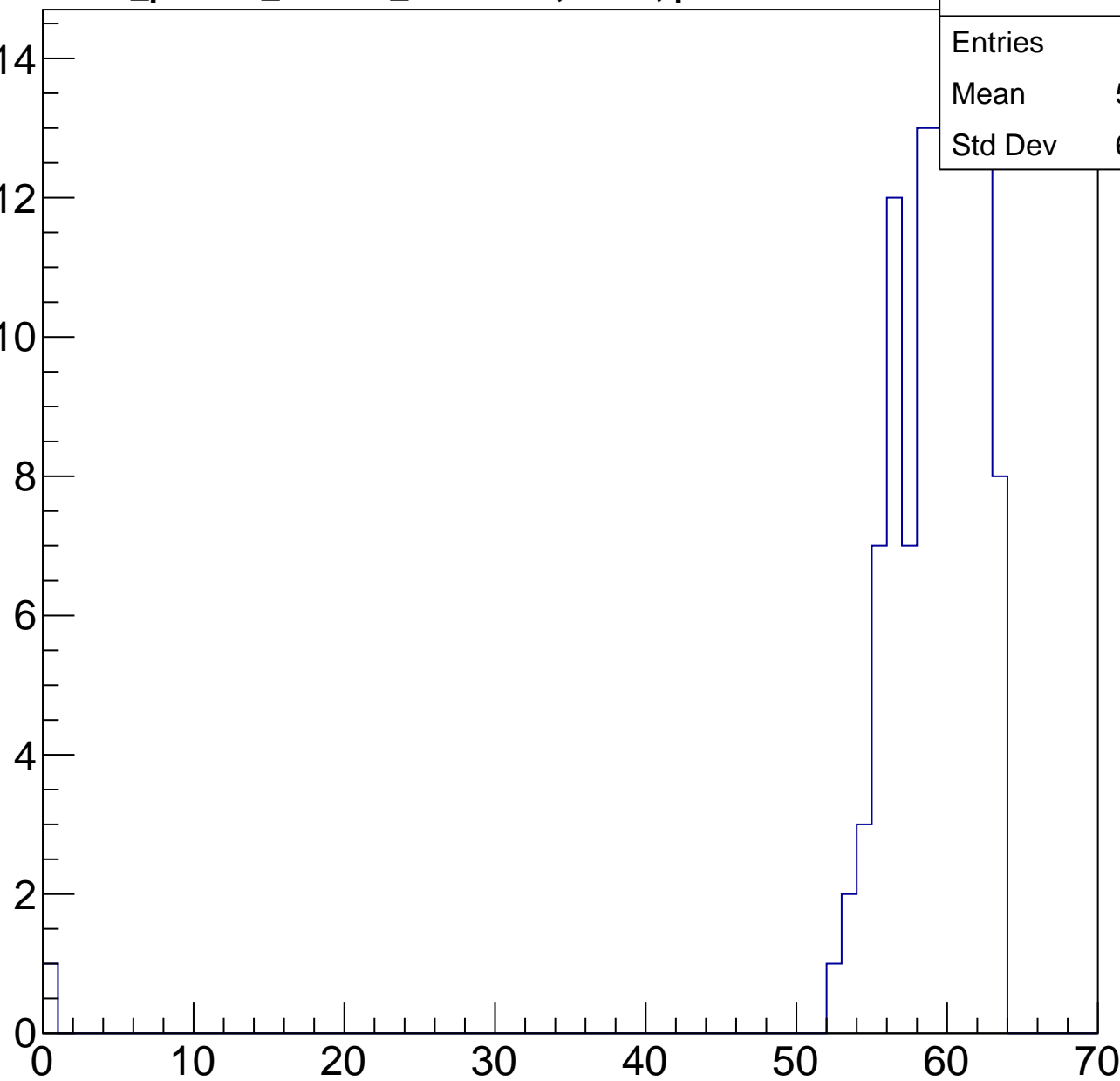
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	58.35
Std Dev	6.245

ampl



B1L001S, U24-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

30

Mean

59.6

Std Dev

11.17

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch97, adc0

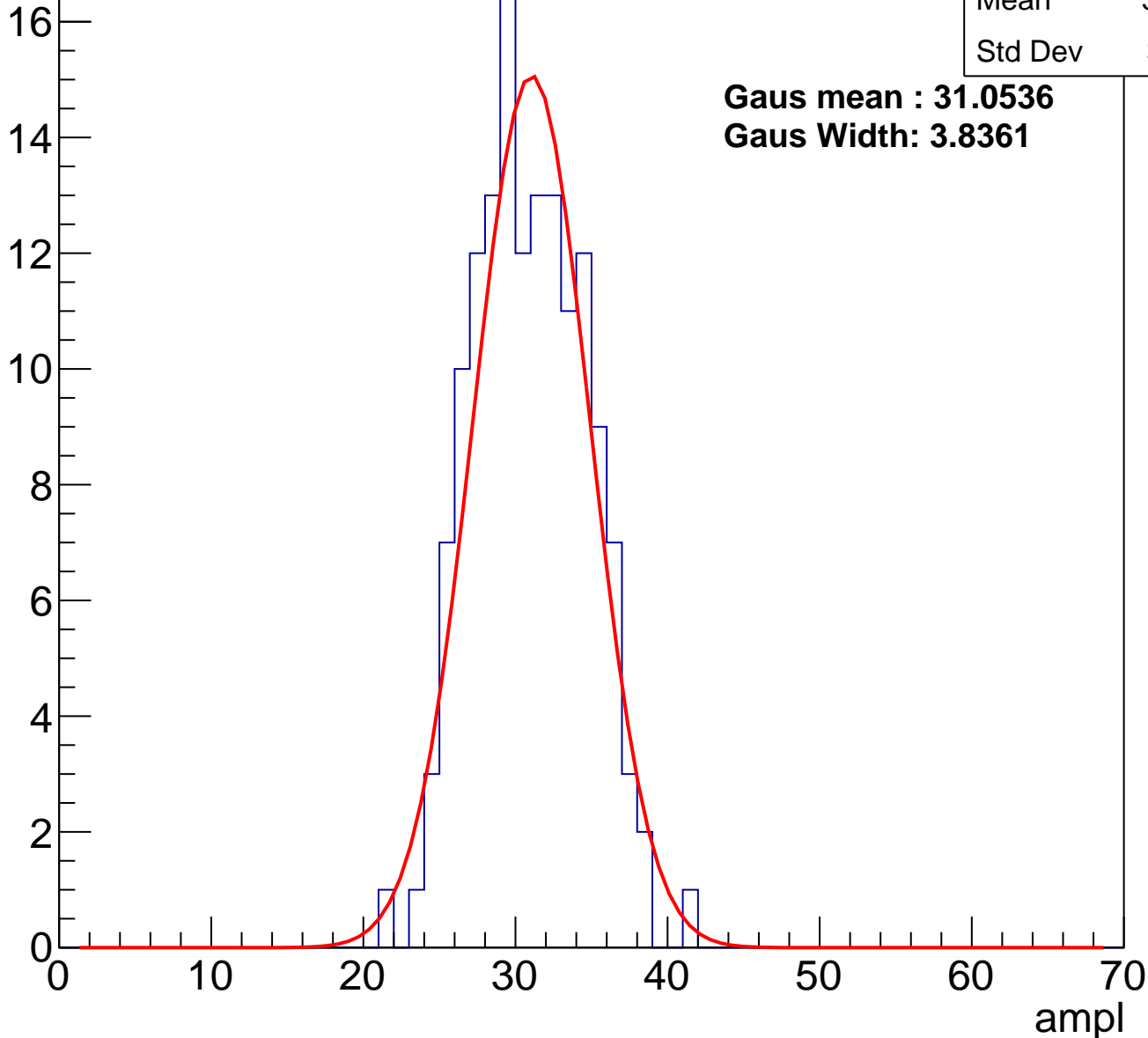
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	147
Mean	30.46
Std Dev	3.641

Gaus mean : 31.0536

Gaus Width: 3.8361



B1L001S, U24-ch97, adc1

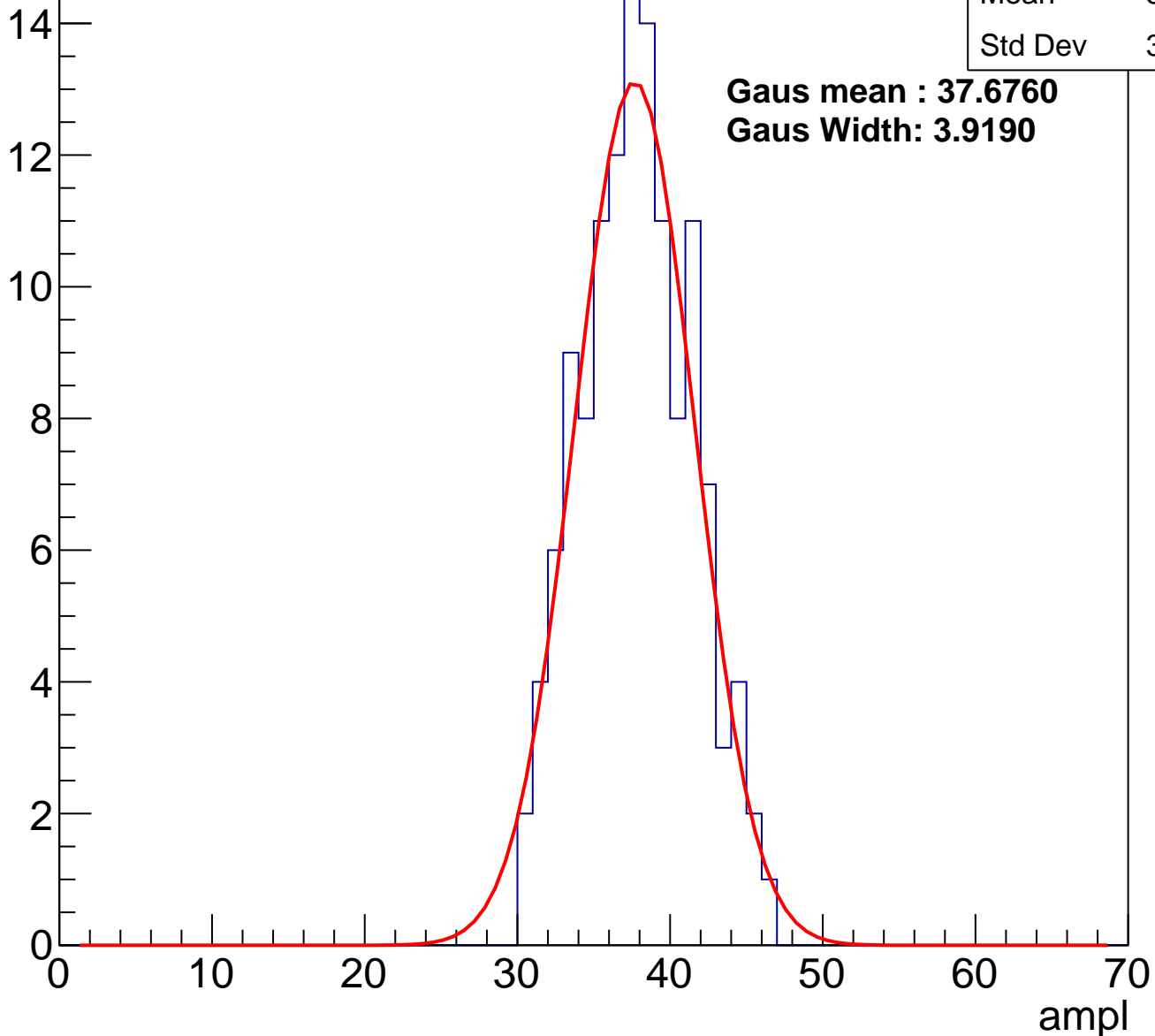
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	37.38
Std Dev	3.579

Gaus mean : 37.6760

Gaus Width: 3.9190

Entry



B1L001S, U24-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

106

Mean

43.23

Std Dev

3.34

Gaus mean : 43.4148

Gaus Width: 3.2198

0

10

20

30

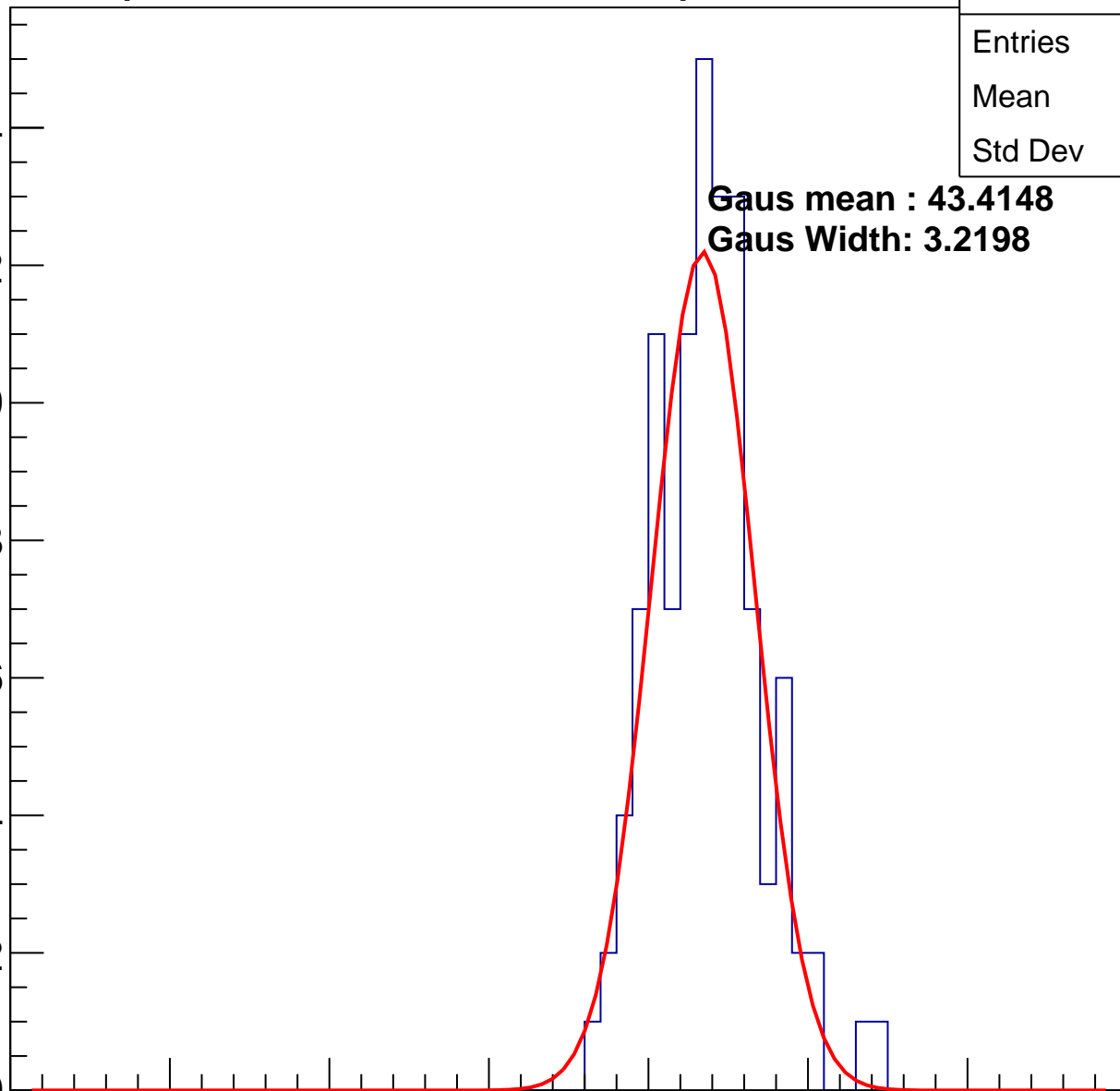
40

50

60

70

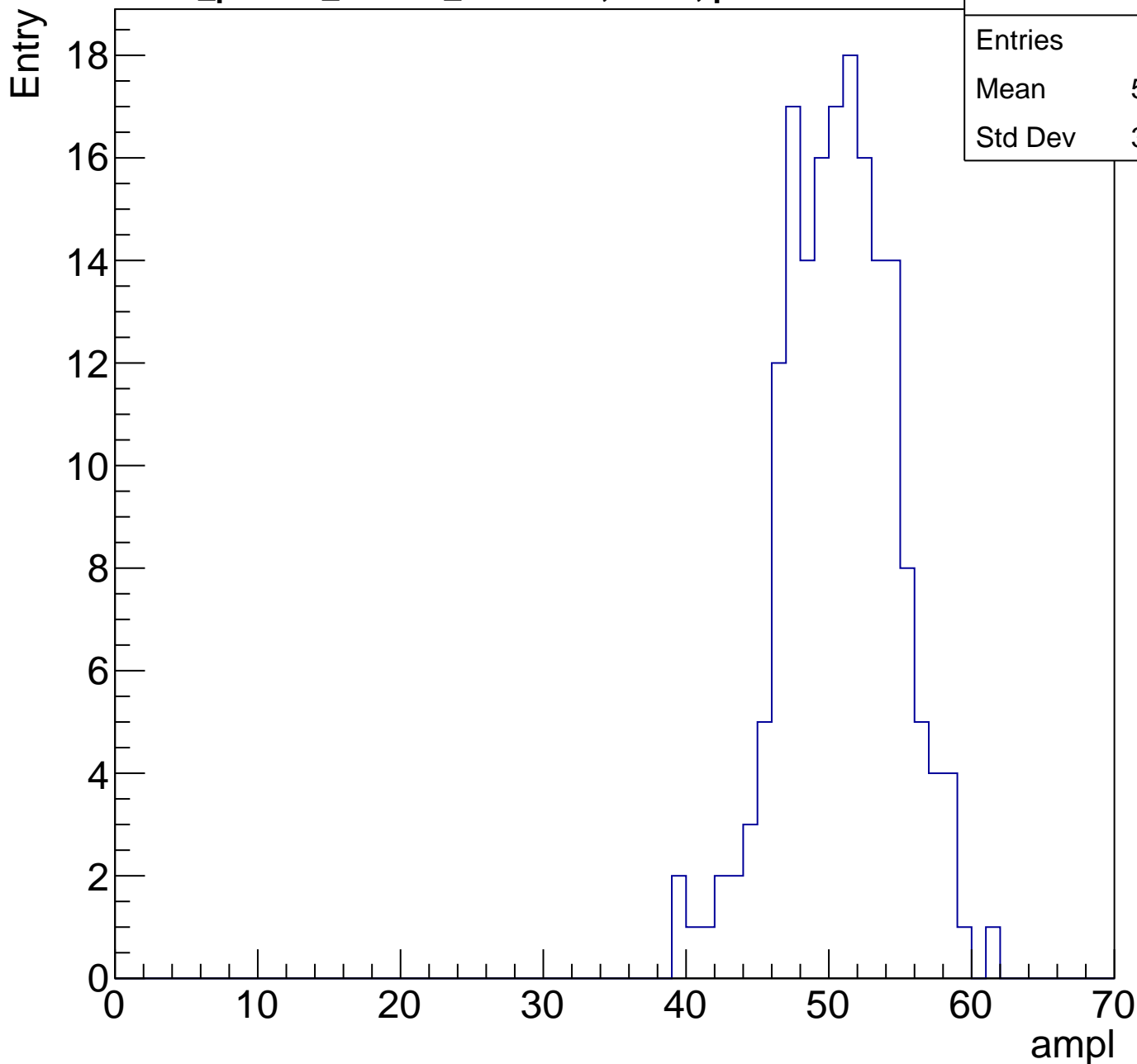
ampl



B1L001S, U24-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

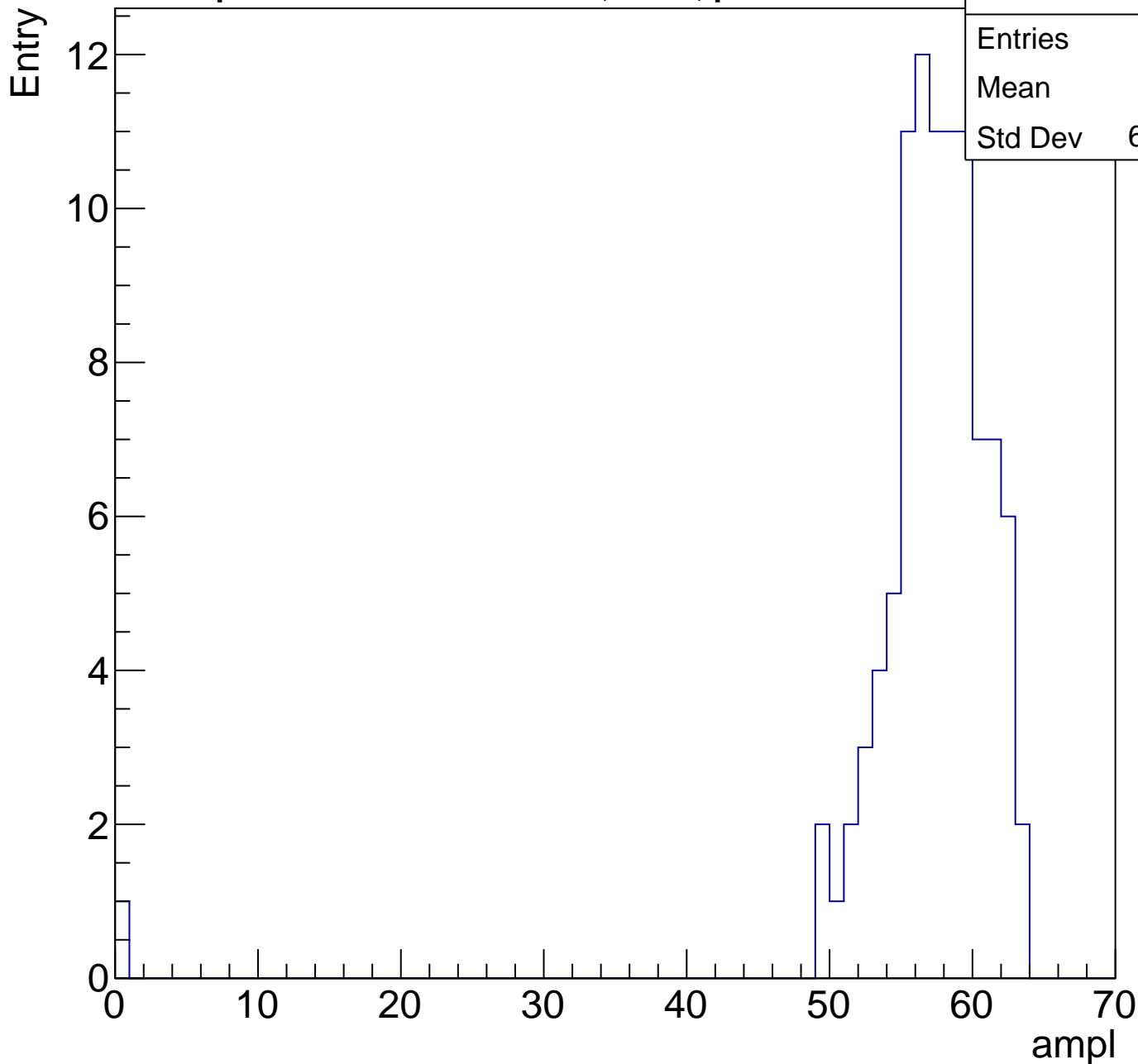
Entries	177
Mean	50.23
Std Dev	3.946



B1L001S, U24-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	56.5
Std Dev	6.588



B1L001S, U24-ch97, adc5

calib_packv5_042523_0143.root, FC#2, port C2

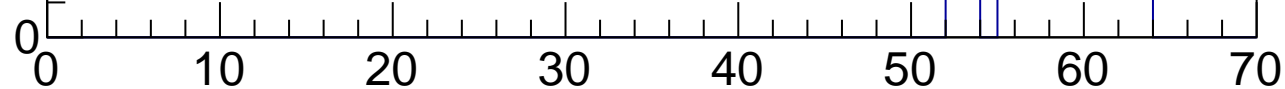
Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	60.29
Std Dev	2.424

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch98, adc0

calib_packv5_042523_0143.root, FC#2, port C2

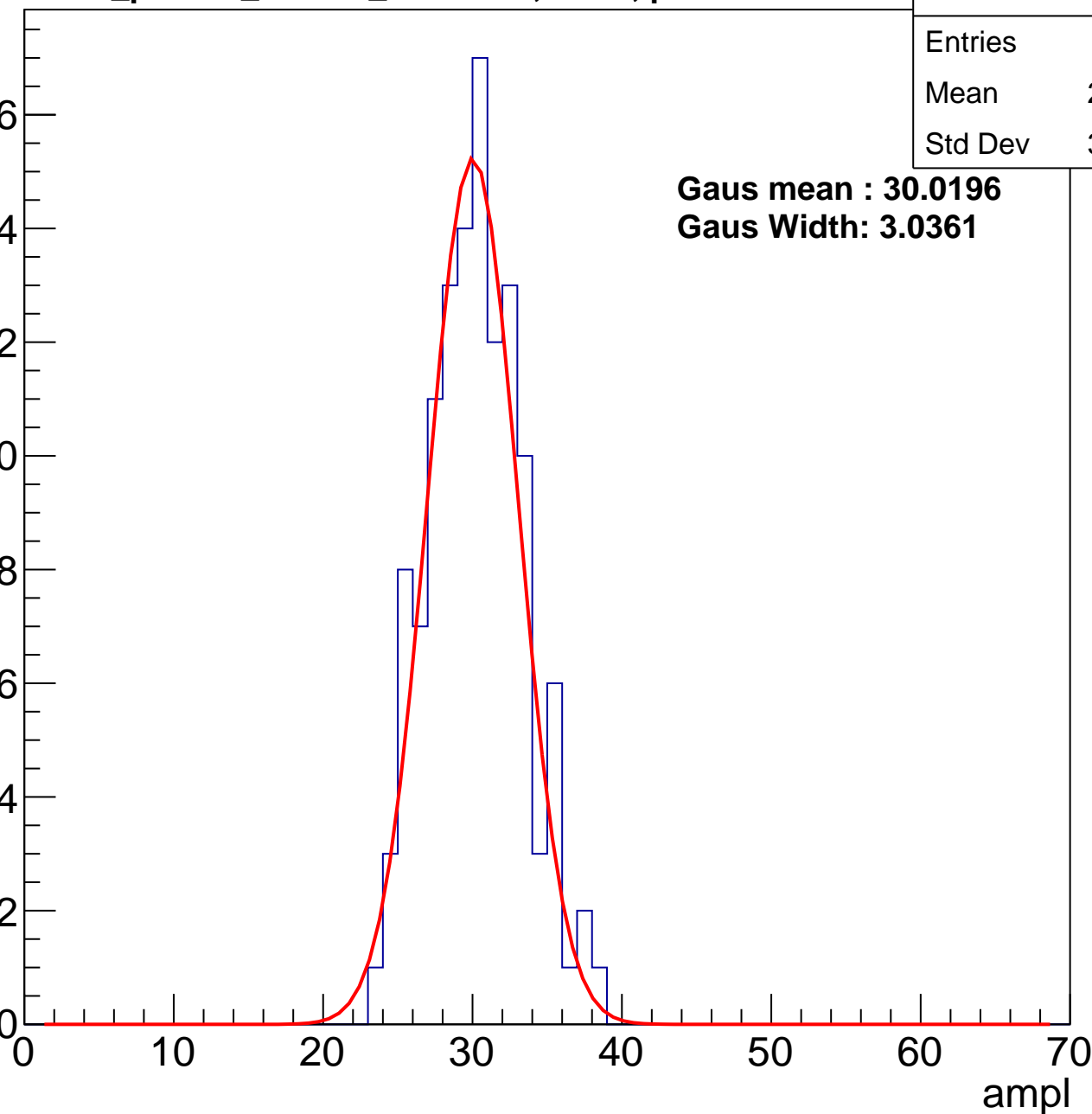
Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	29.77
Std Dev	3.107

Gaus mean : 30.0196

Gaus Width: 3.0361



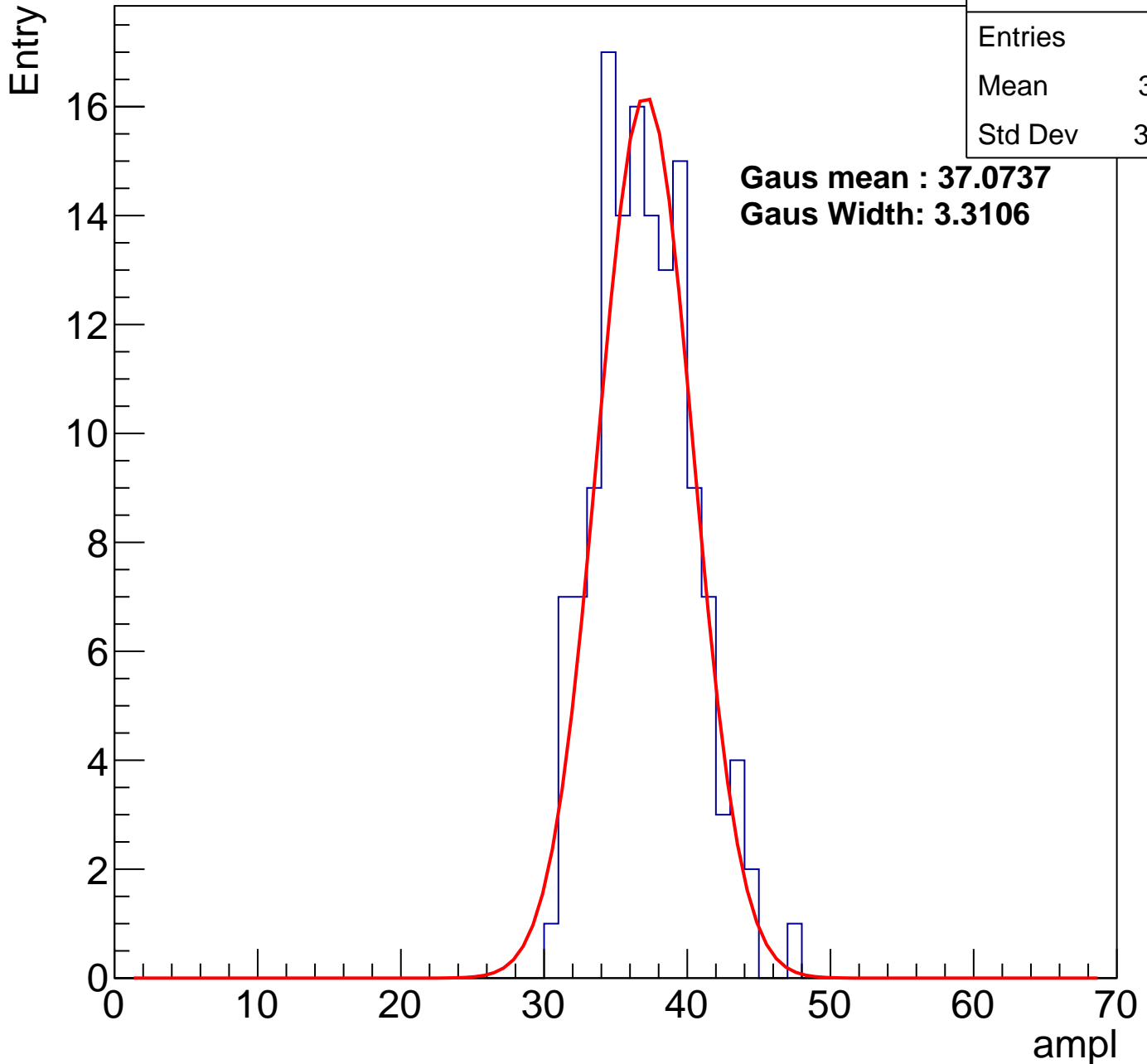
B1L001S, U24-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	36.61
Std Dev	3.293

Gaus mean : 37.0737

Gaus Width: 3.3106



B1L001S, U24-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	43.11
Std Dev	3.346

Gaus mean : 43.6184

Gaus Width: 3.6541

Entry

16

14

12

10

8

6

4

2

0

0

10

20

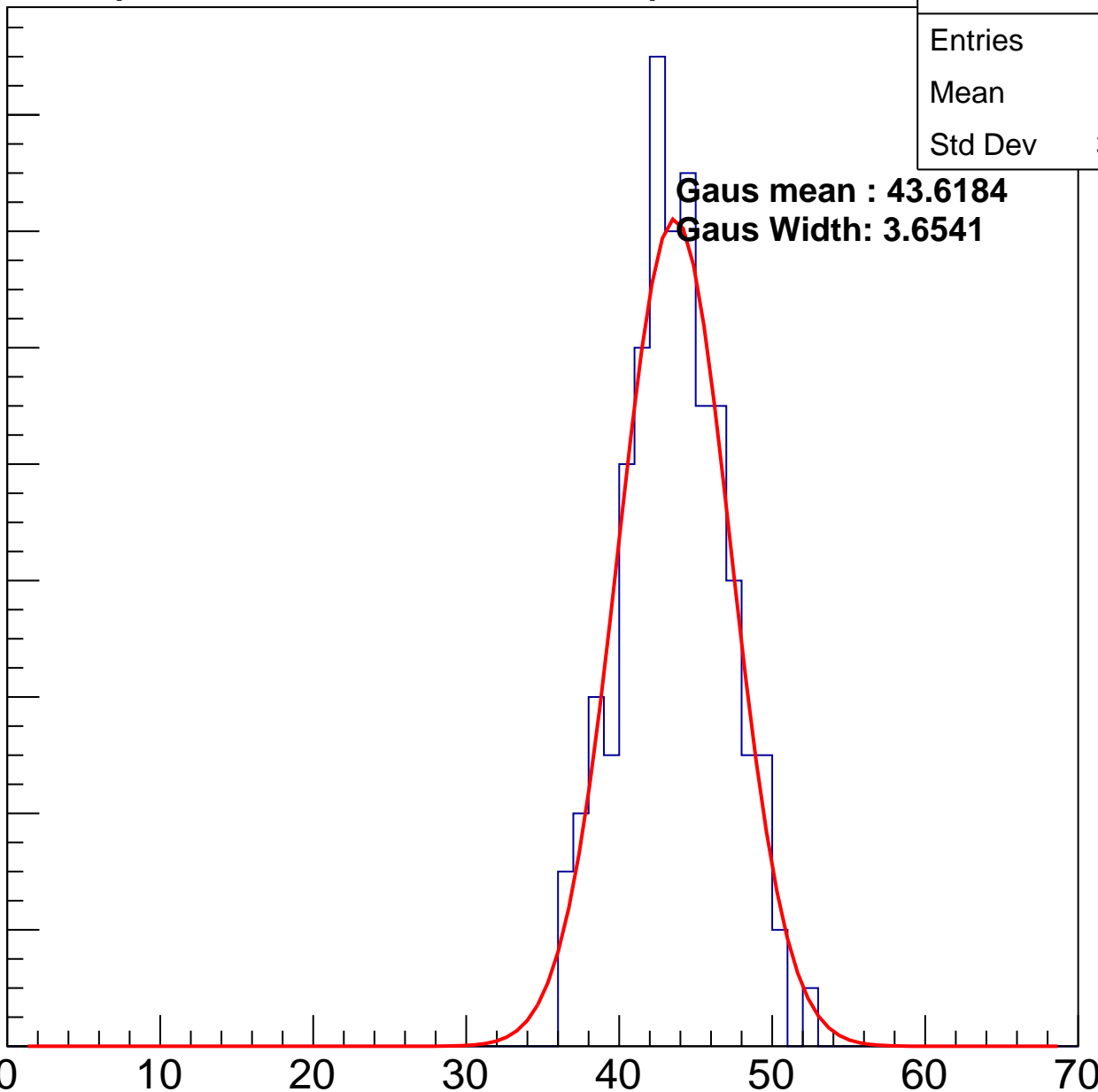
30

40

50

60

ampl



B1L001S, U24-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	49.38
Std Dev	3.169

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

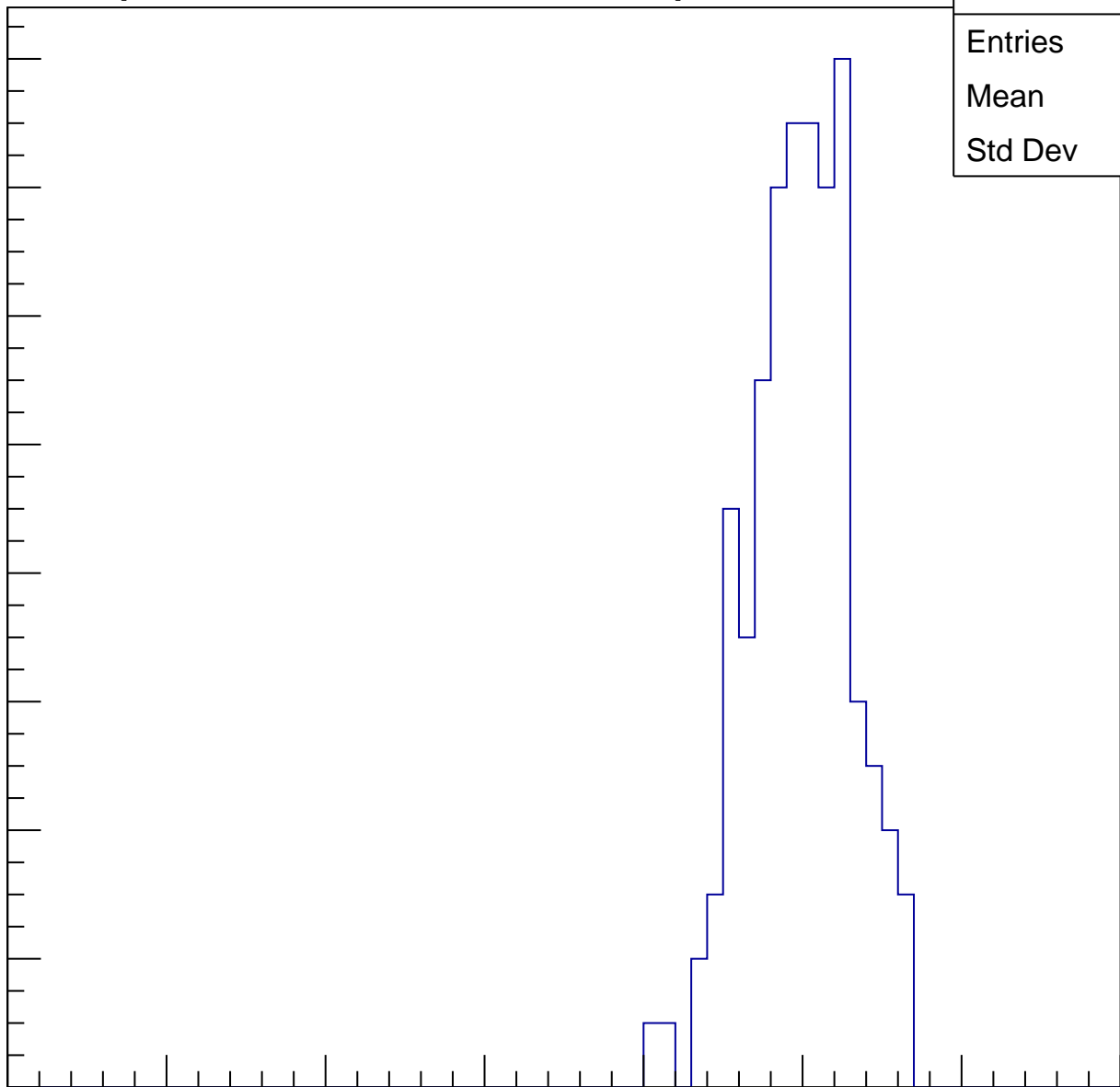
40

50

60

70

ampl



B1L001S, U24-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

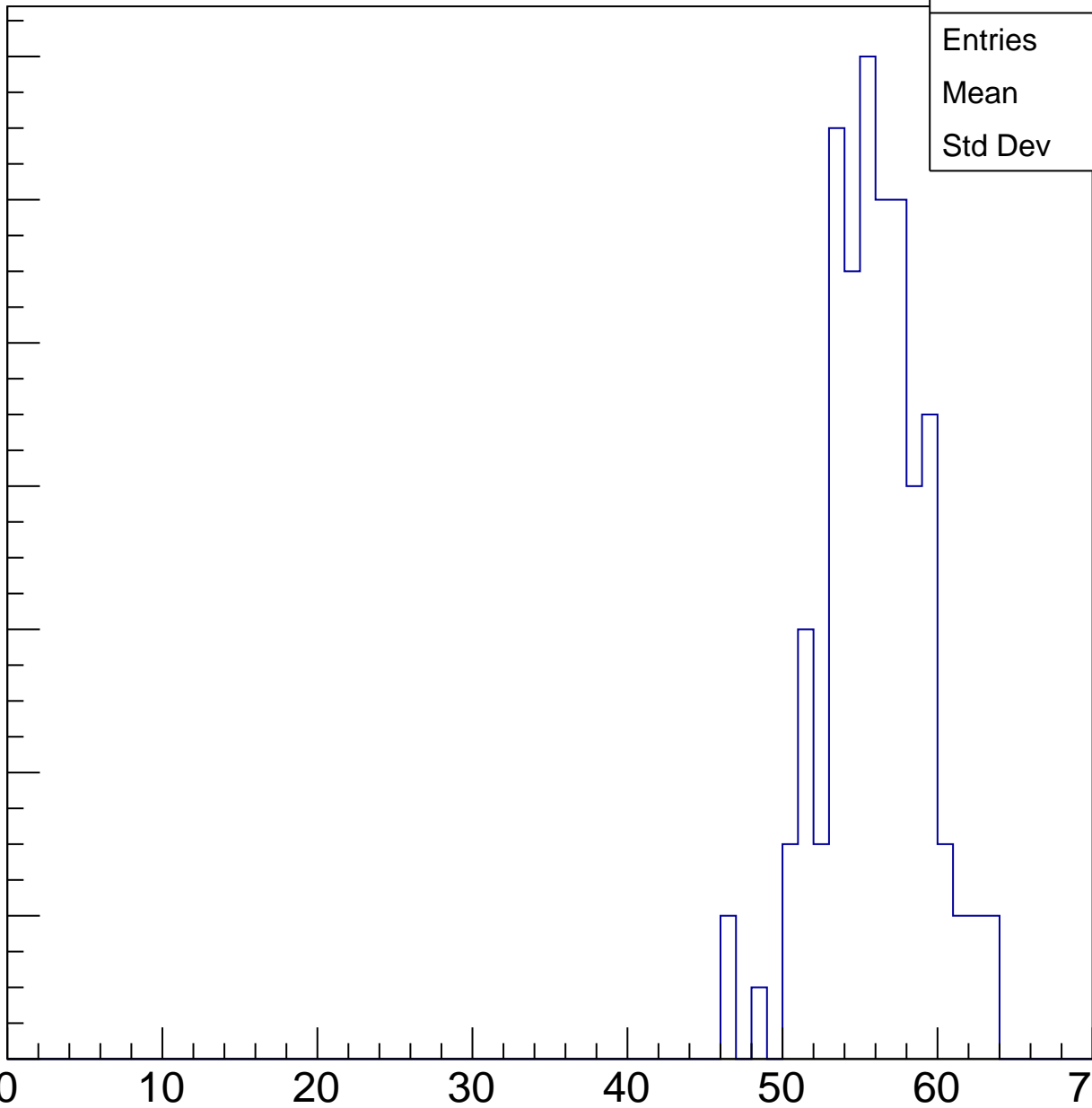
Entries	103
Mean	55.42
Std Dev	3.272

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch98, adc5

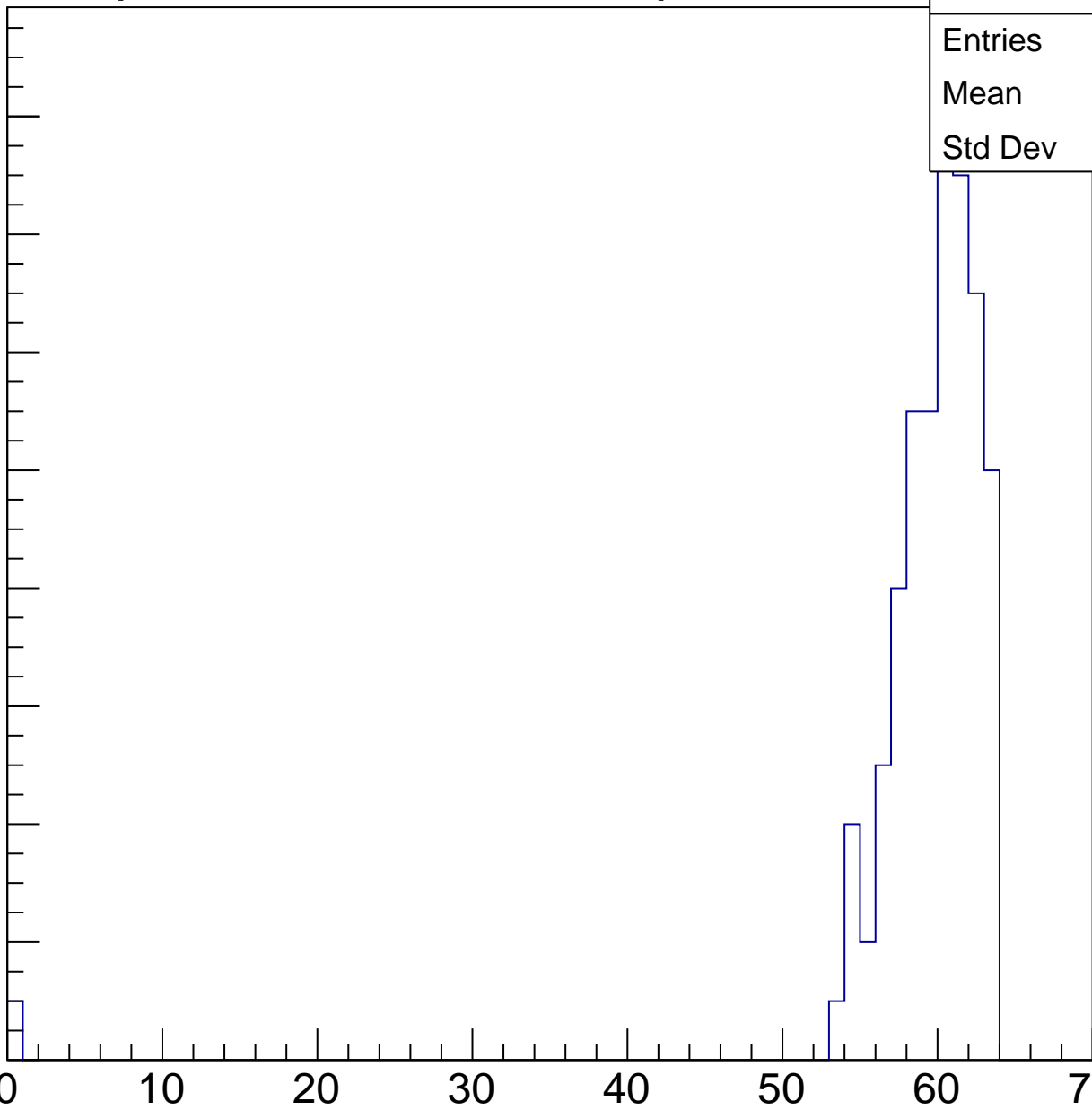
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.91
Std Dev	6.46

ampl

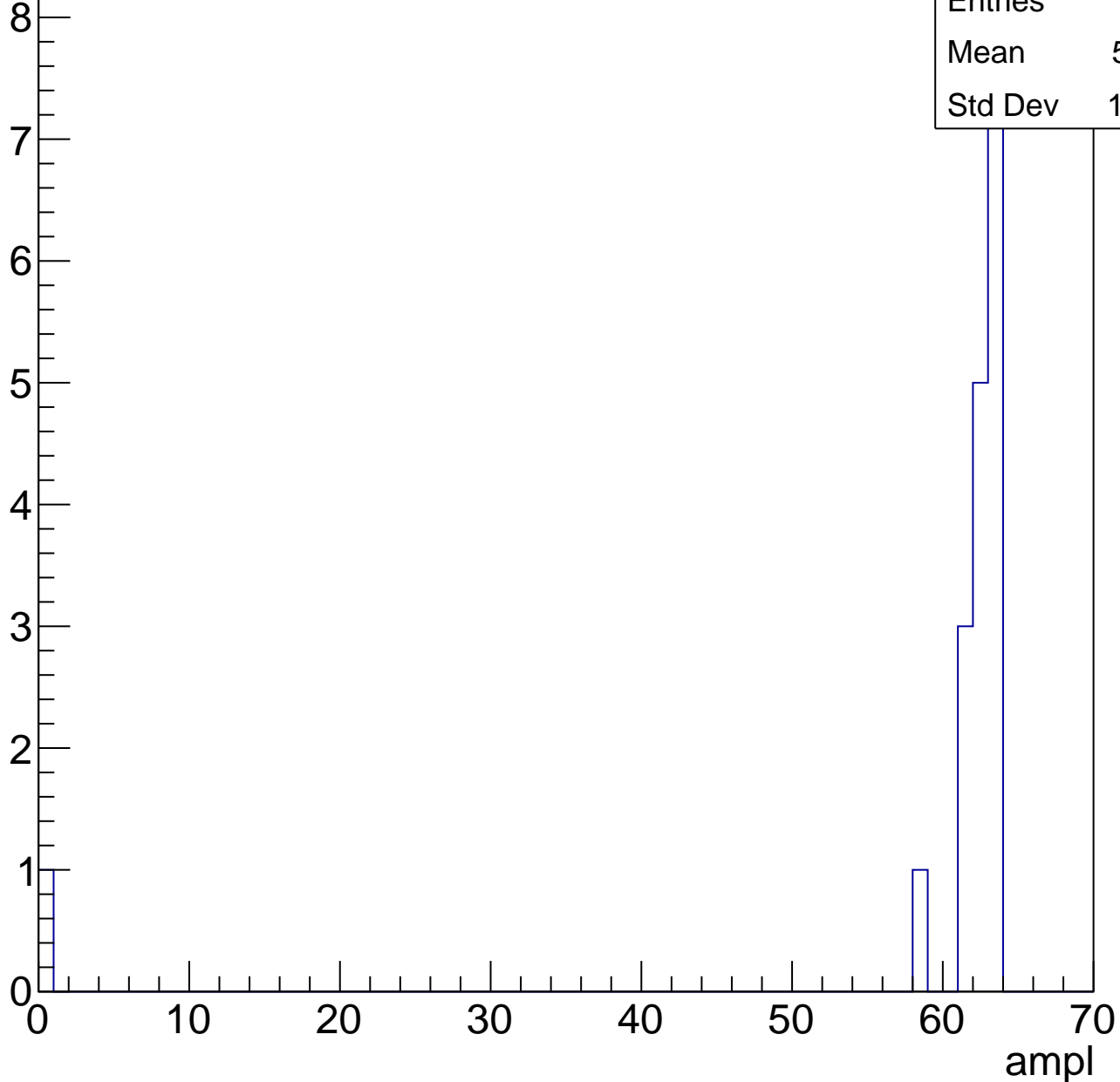


B1L001S, U24-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

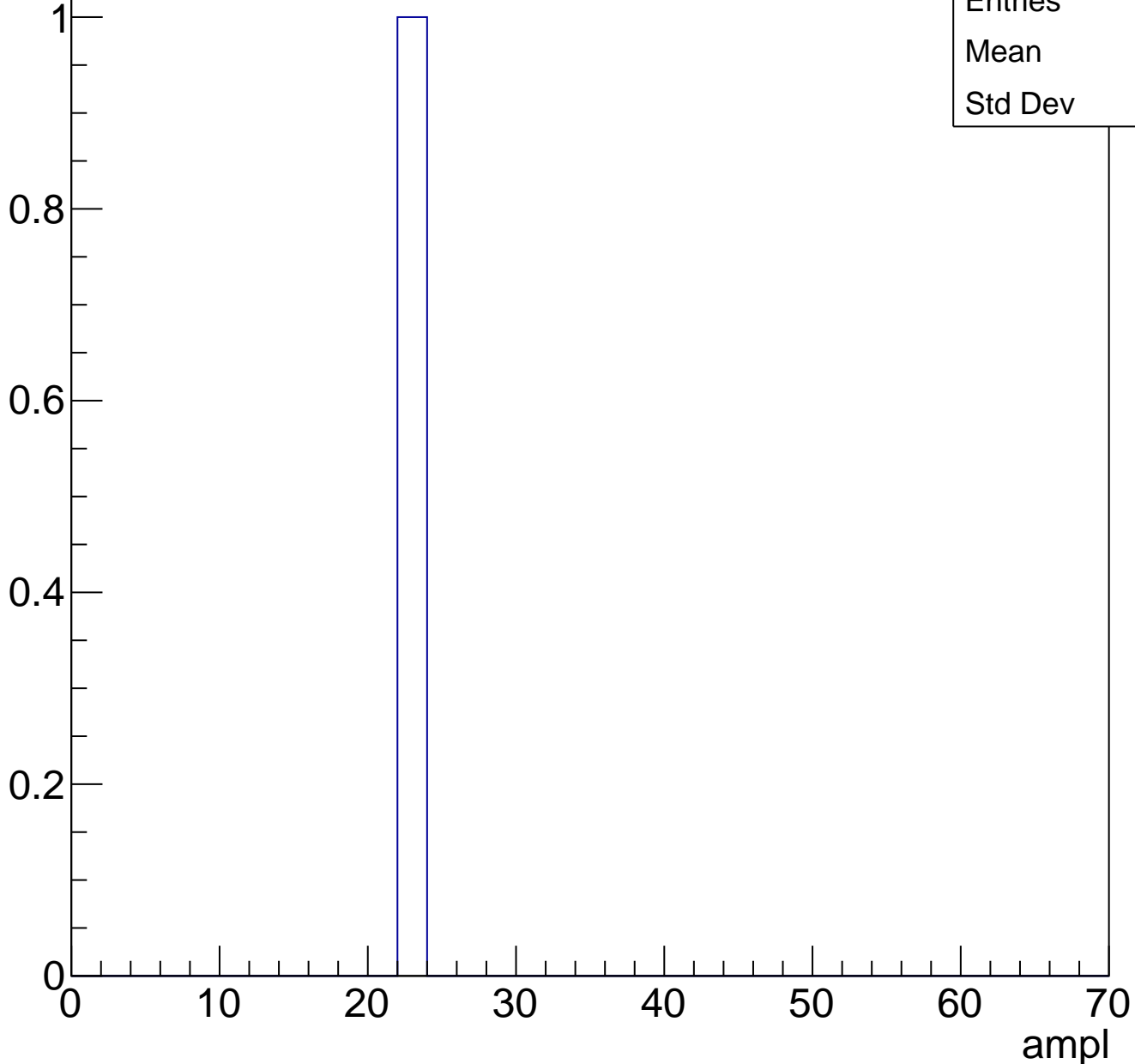
Entries	18
Mean	58.61
Std Dev	14.27



B1L001S, U24-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	22.5
Std Dev	0.5

B1L001S, U24-ch99, adc0

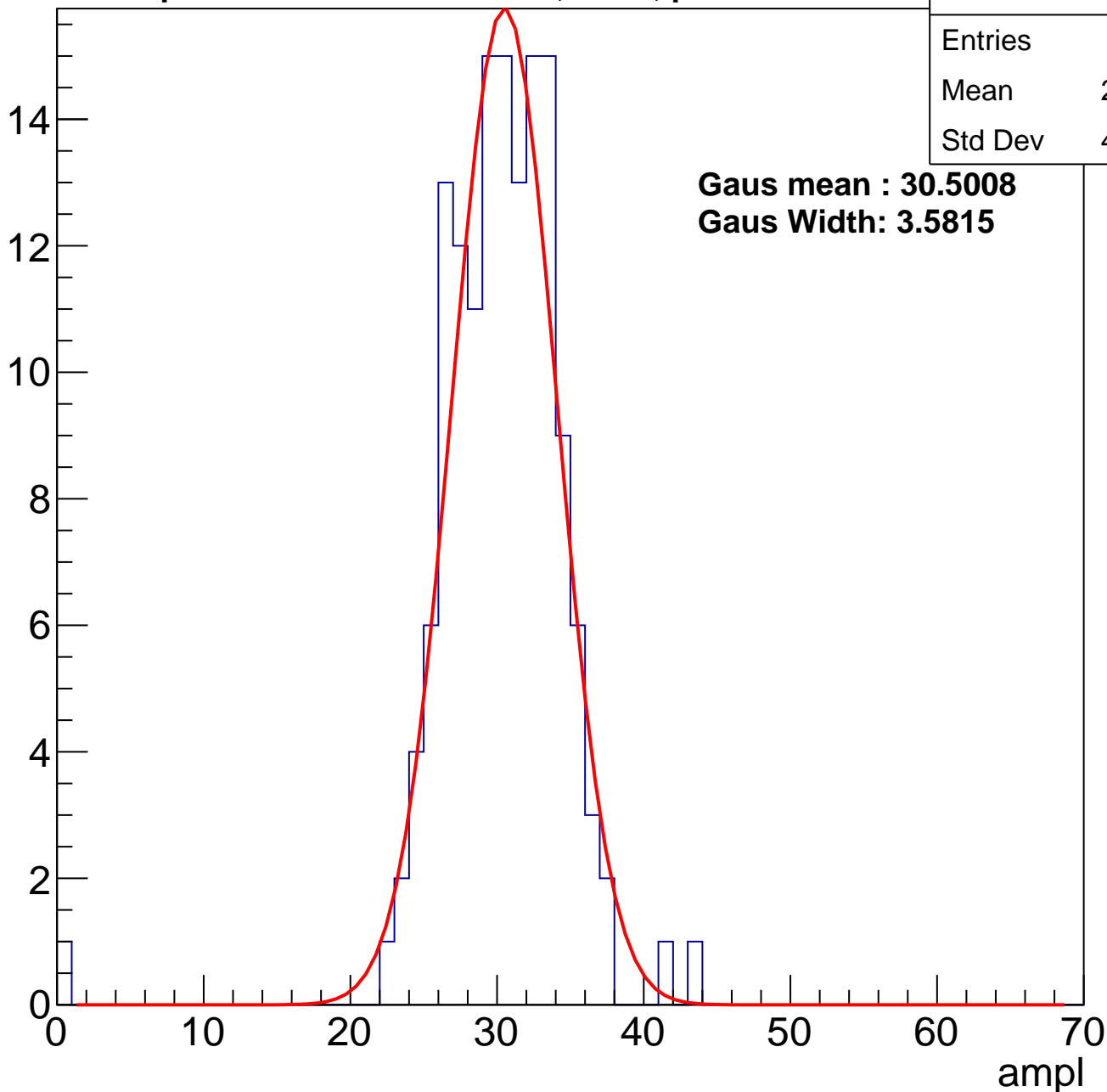
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.86
Std Dev	4.336

Gaus mean : 30.5008

Gaus Width: 3.5815

Entry



B1L001S, U24-ch99, adc1

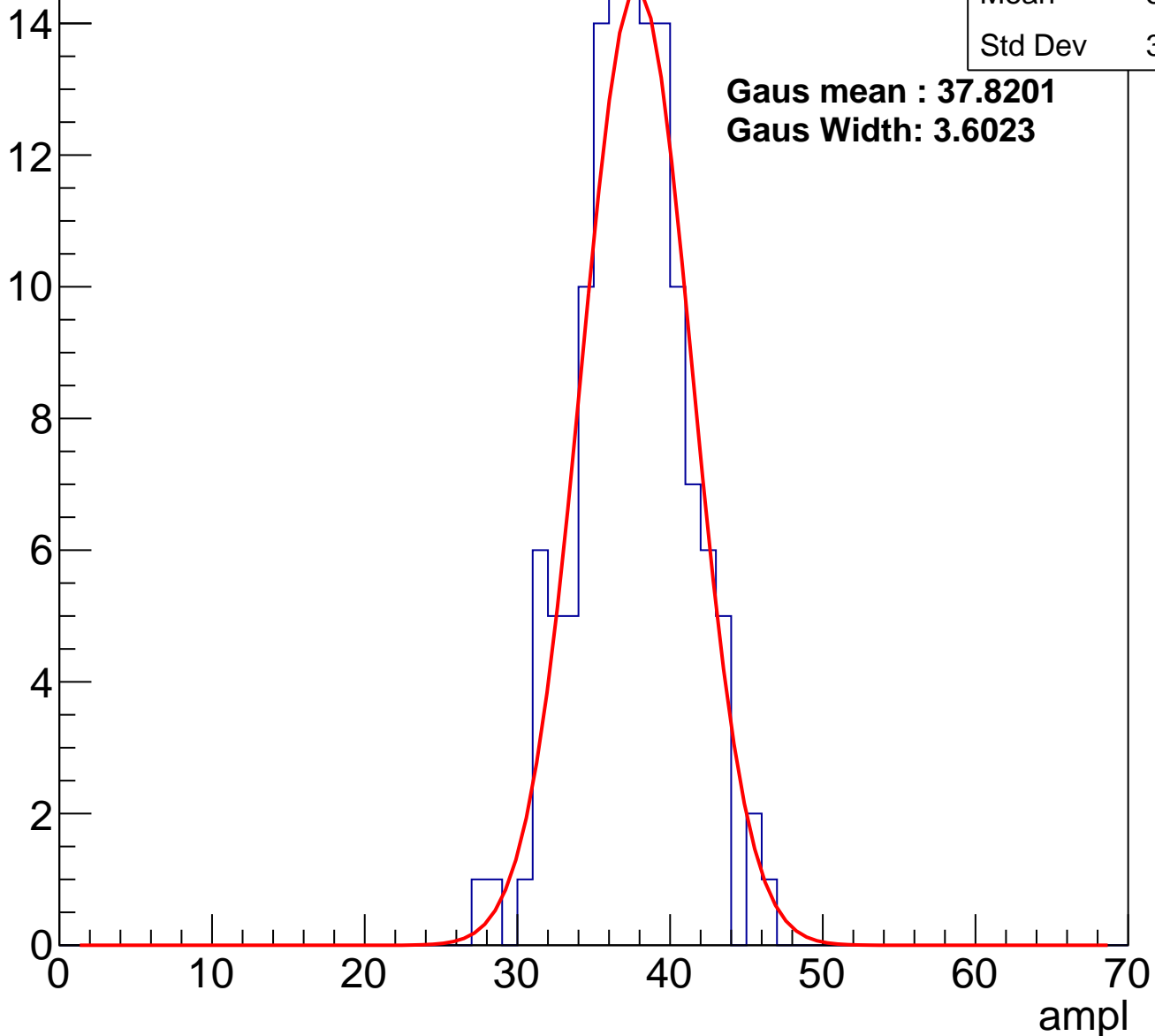
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	37.04
Std Dev	3.506

Gaus mean : 37.8201

Gaus Width: 3.6023

Entry



B1L001S, U24-ch99, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	43.29
Std Dev	3.693

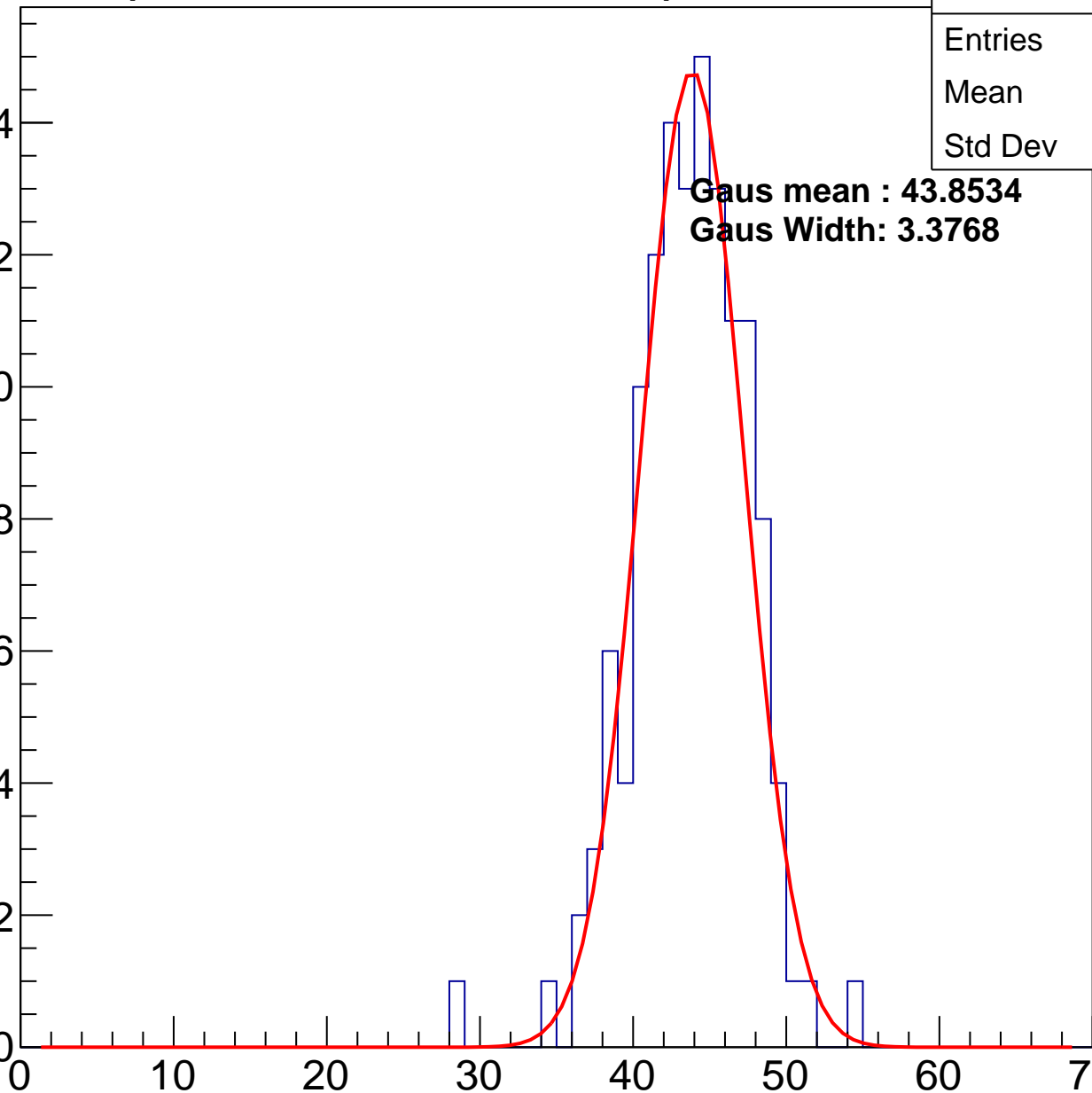
Gaus mean : 43.8534

Gaus Width: 3.3768

Entry

14
12
10
8
6
4
2
0

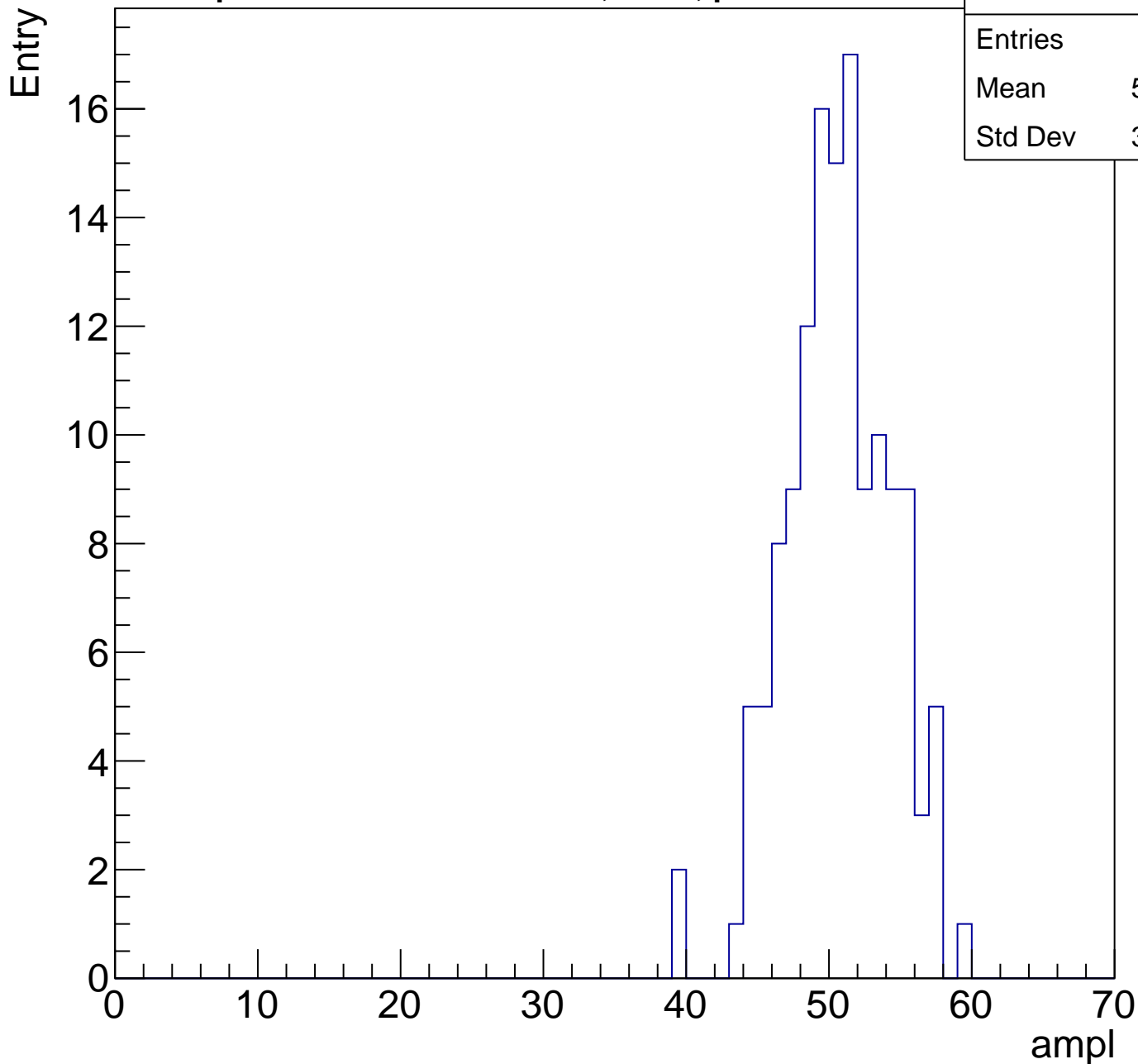
ampl



B1L001S, U24-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	50.18
Std Dev	3.649



B1L001S, U24-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	55.79
Std Dev	2.902

Entry

12

10

8

6

4

2

0

0

10

20

30

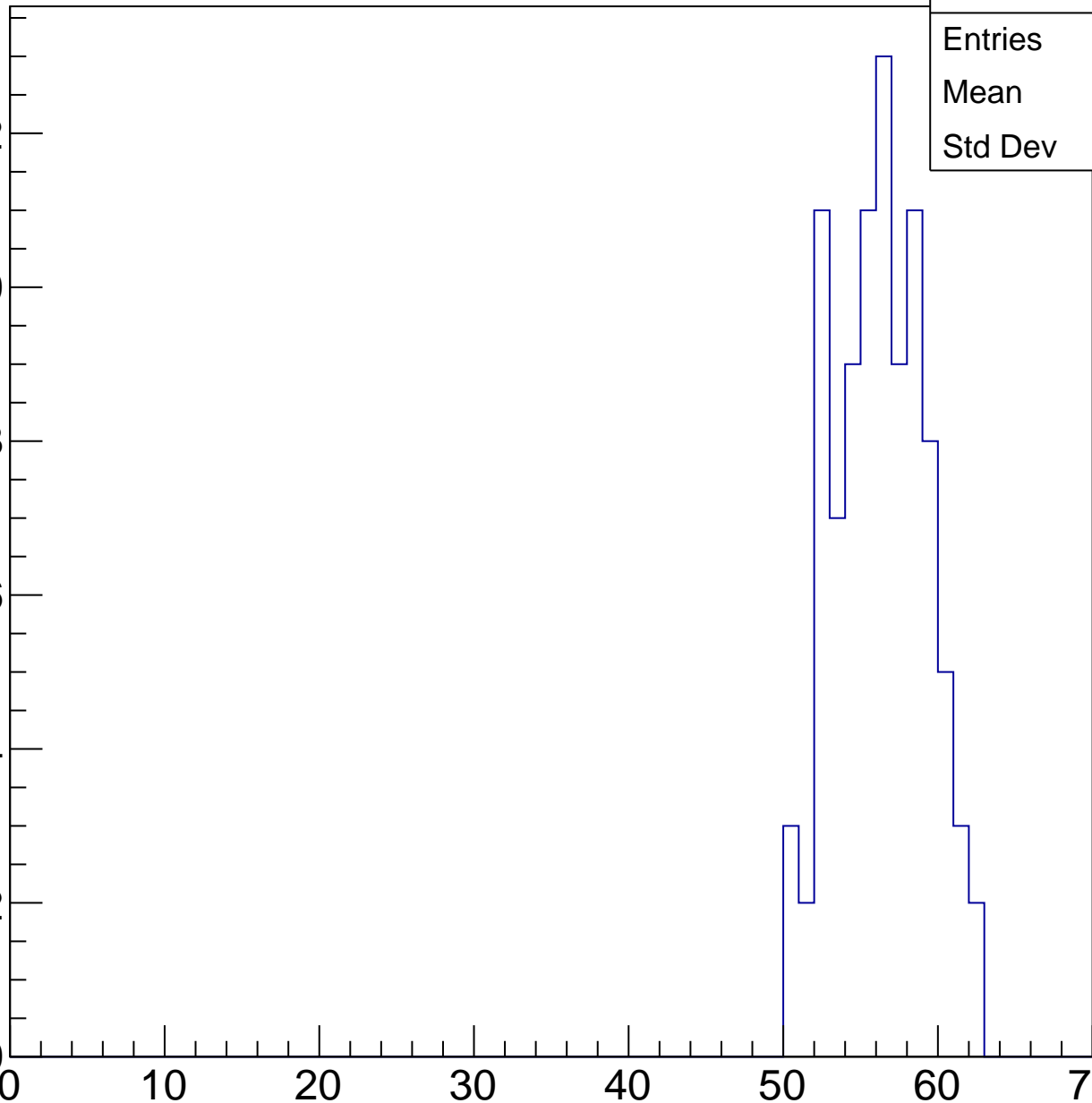
40

50

60

70

ampl



B1L001S, U24-ch99, adc5

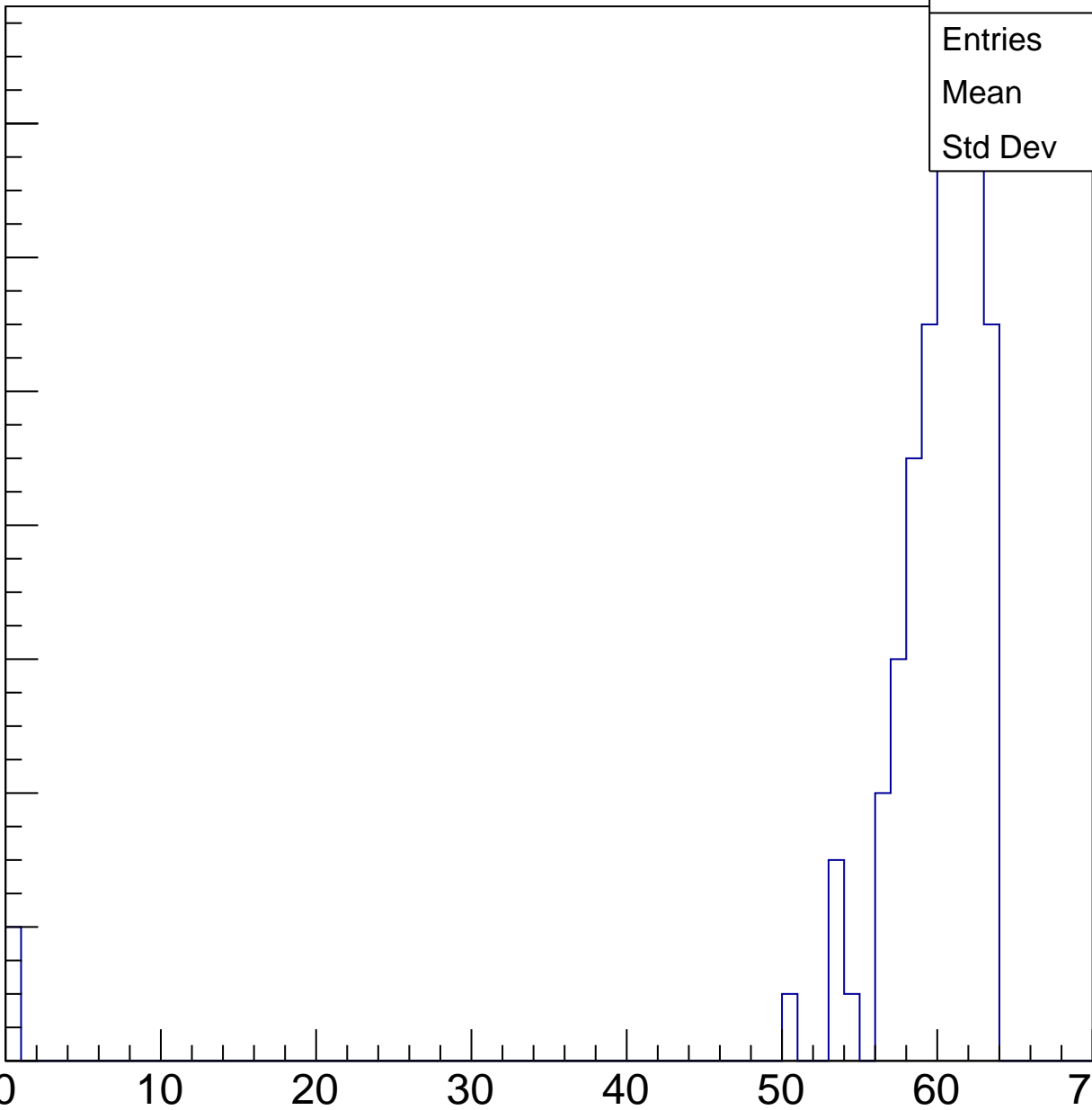
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	58.47
Std Dev	9.043

ampl

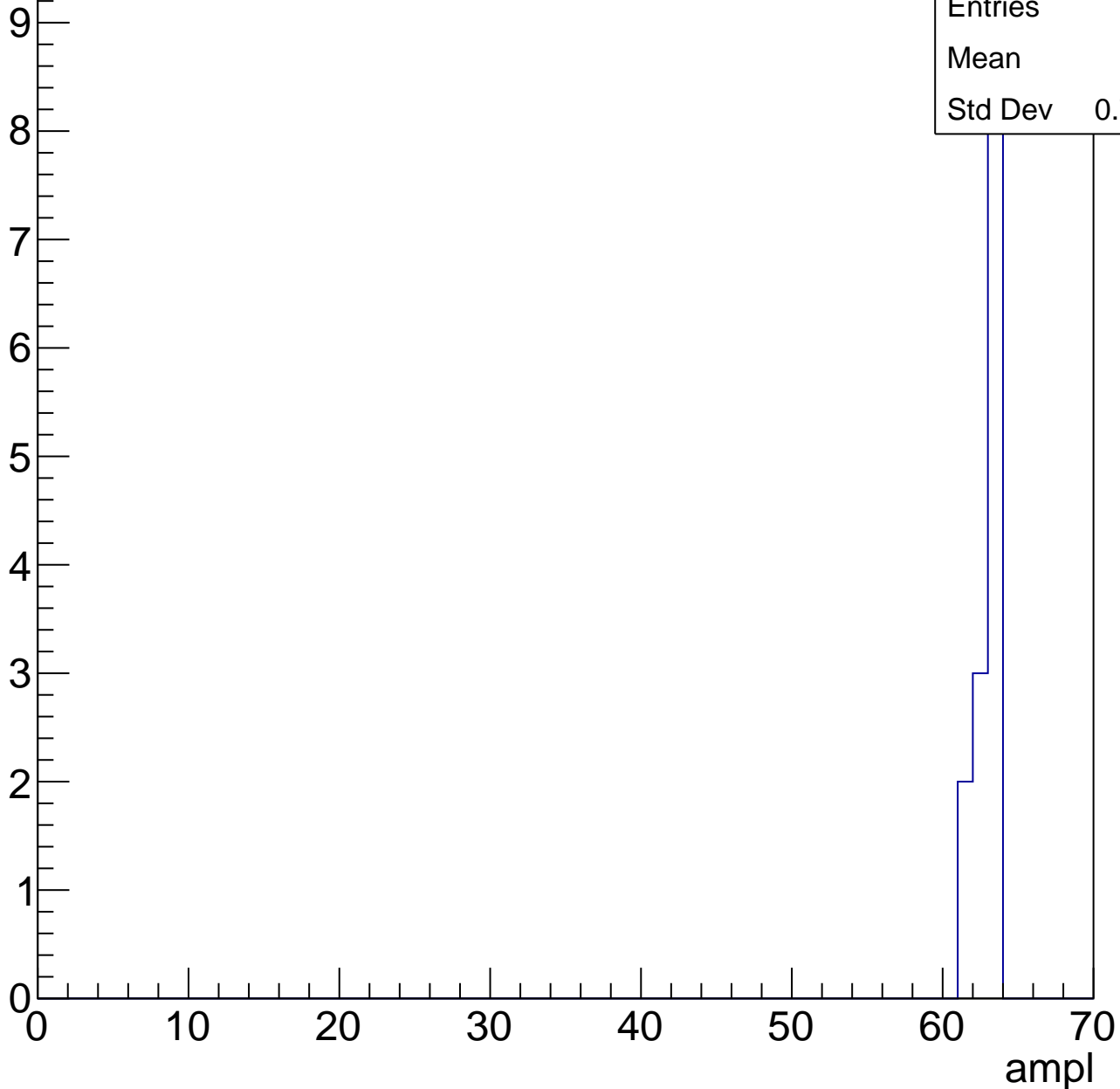


B1L001S, U24-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	62.5
Std Dev	0.7319



B1L001S, U24-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

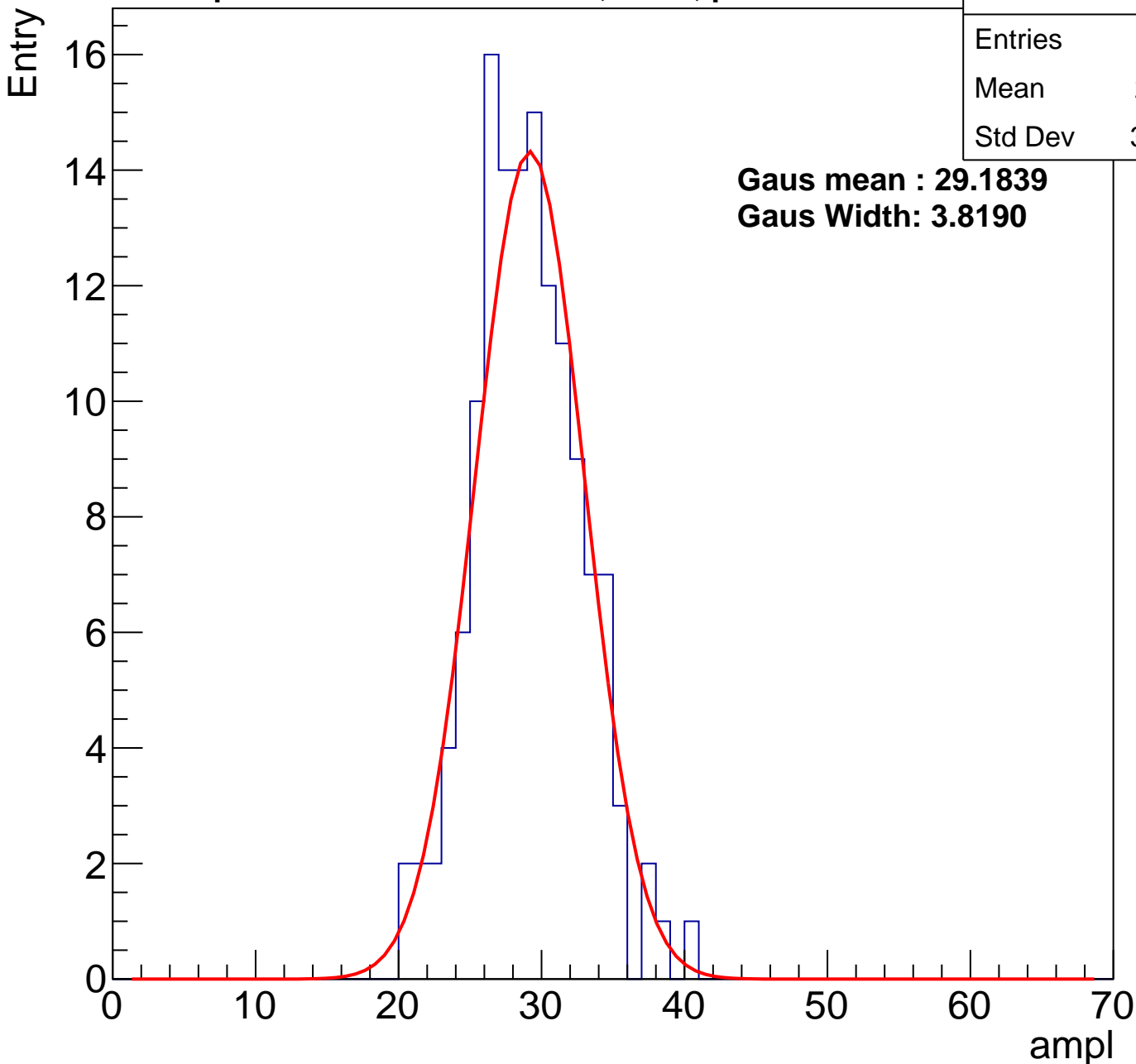
B1L001S, U24-ch100, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	28.61
Std Dev	3.707

Gaus mean : 29.1839

Gaus Width: 3.8190



B1L001S, U24-ch100, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	34.75
Std Dev	3.36

Gaus mean : 35.0412

Gaus Width: 3.3725

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

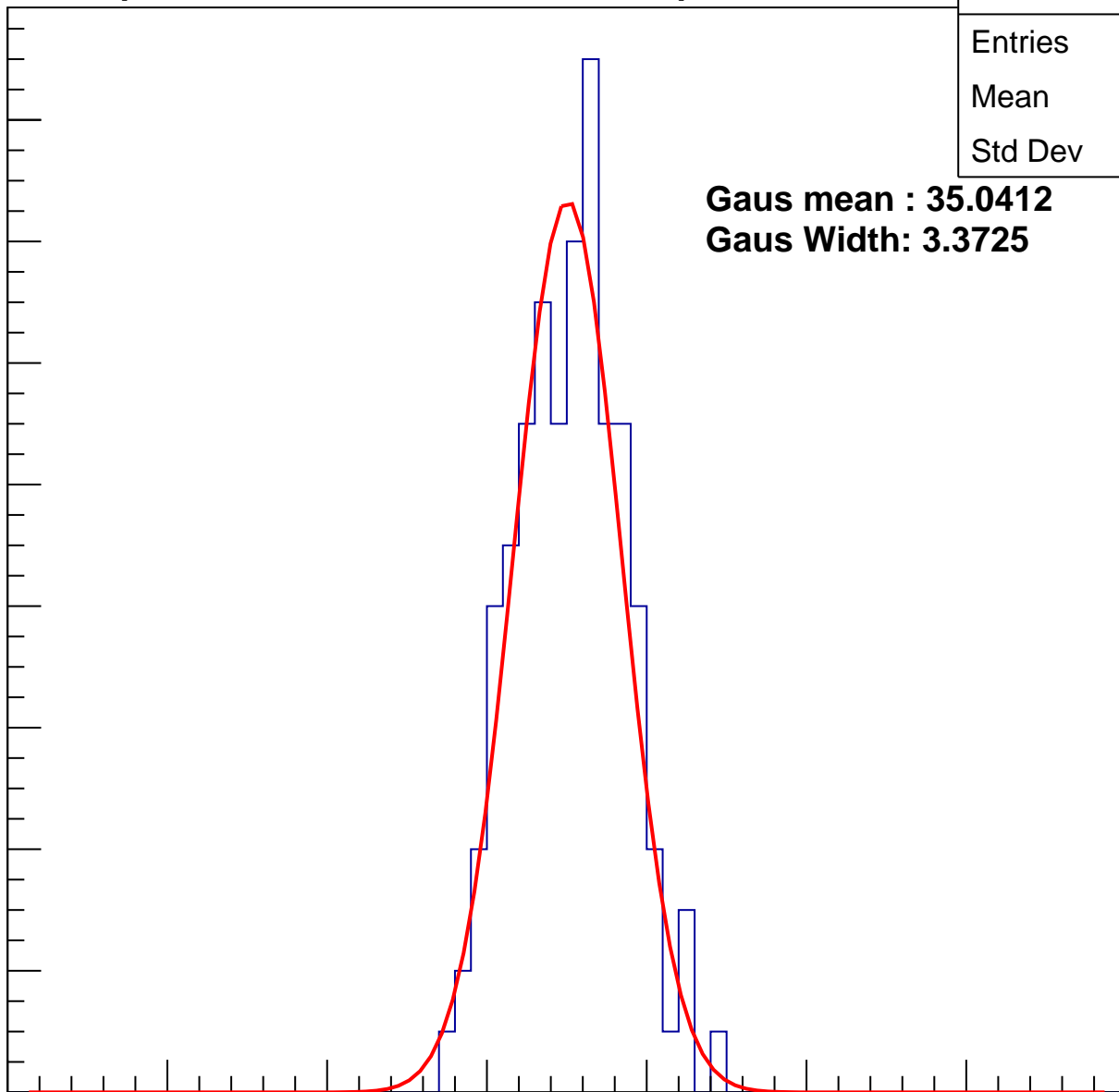
40

50

60

70

ampl



B1L001S, U24-ch100, adc2

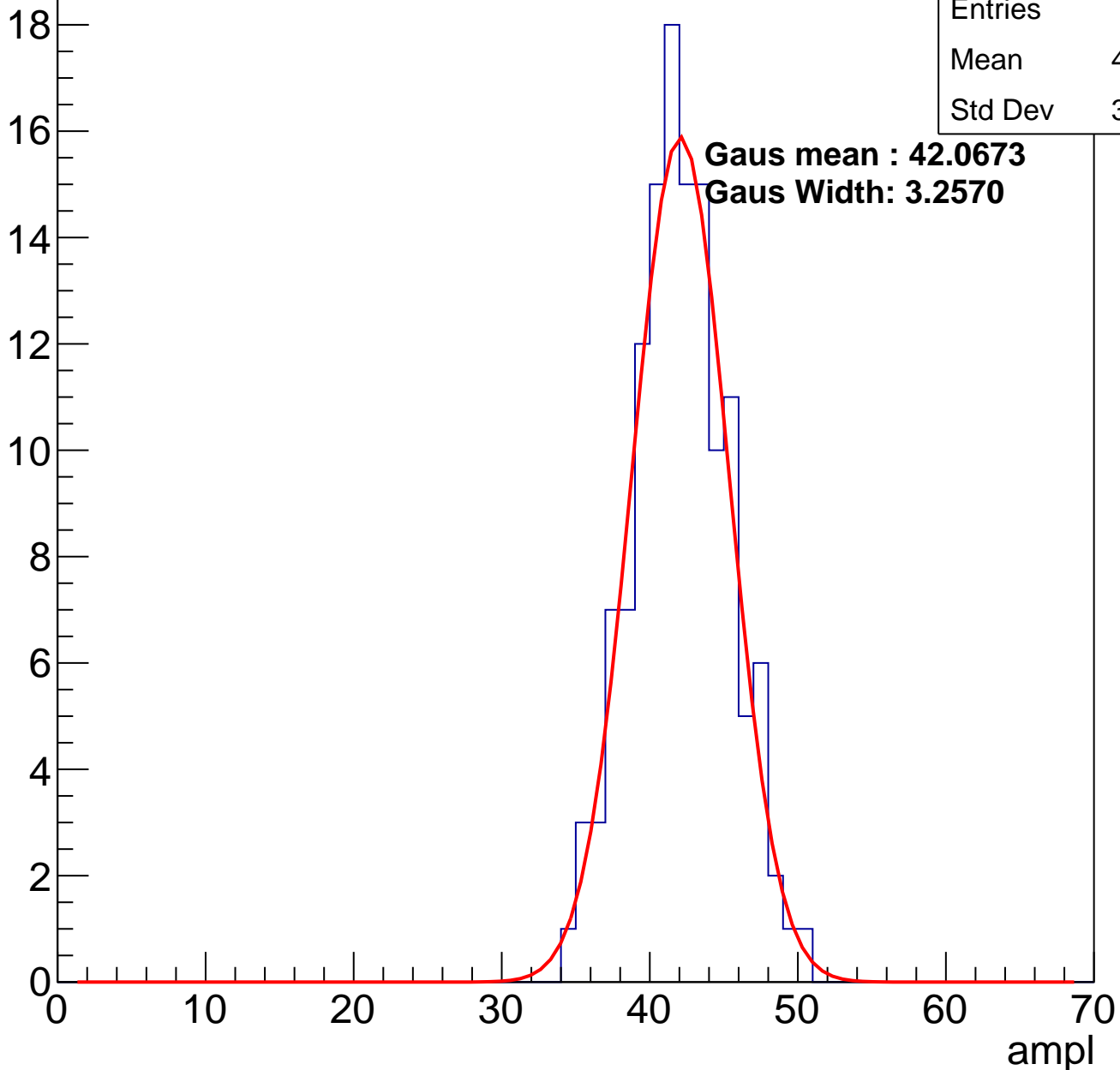
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	41.63
Std Dev	3.189

Entry

Gaus mean : 42.0673

Gaus Width: 3.2570



B1L001S, U24-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	48.58
Std Dev	3.477

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

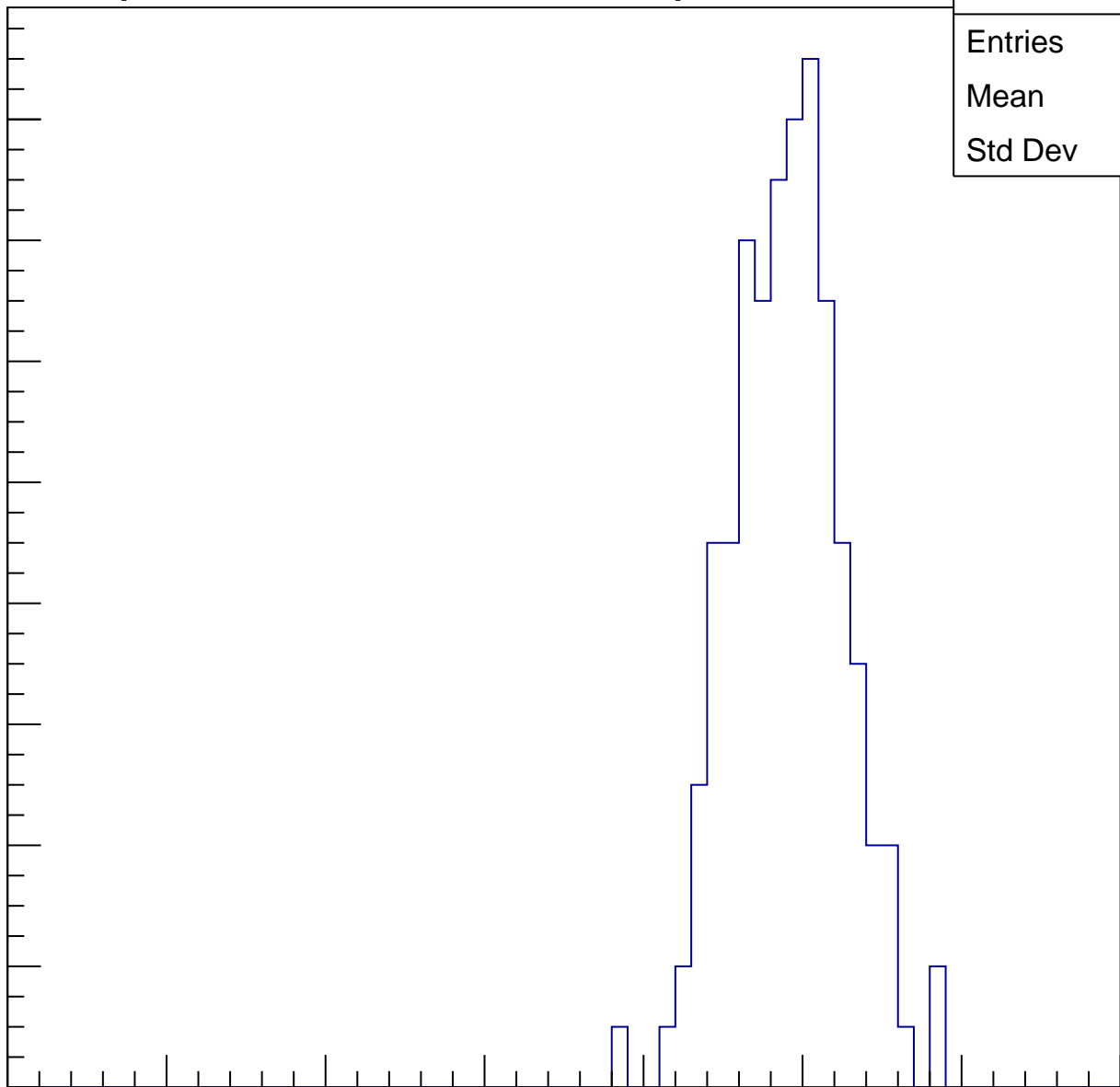
40

50

60

70

ampl



B1L001S, U24-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	54.53
Std Dev	3.11

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch100, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

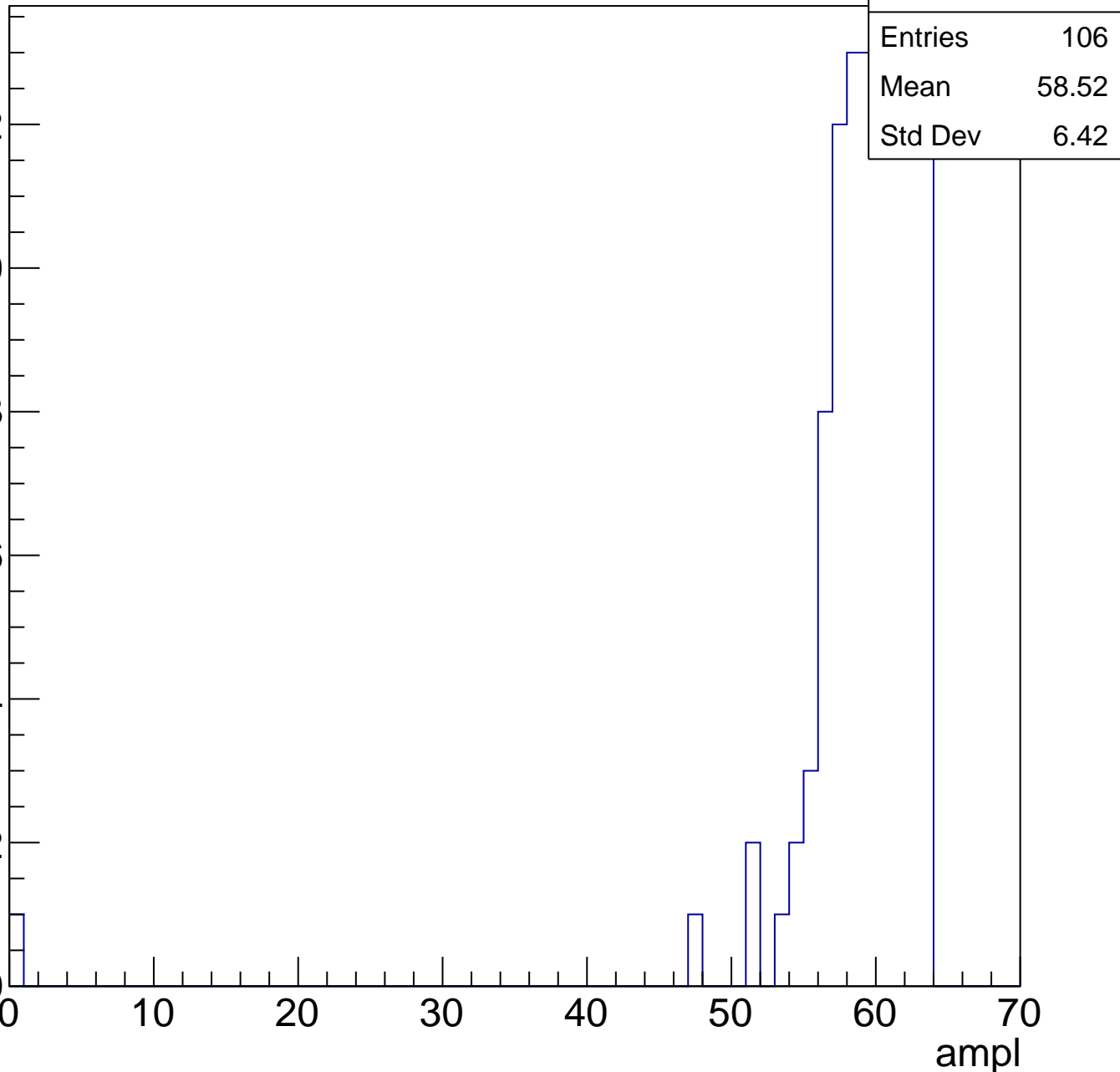
40

50

60

ampl

Entries	106
Mean	58.52
Std Dev	6.42

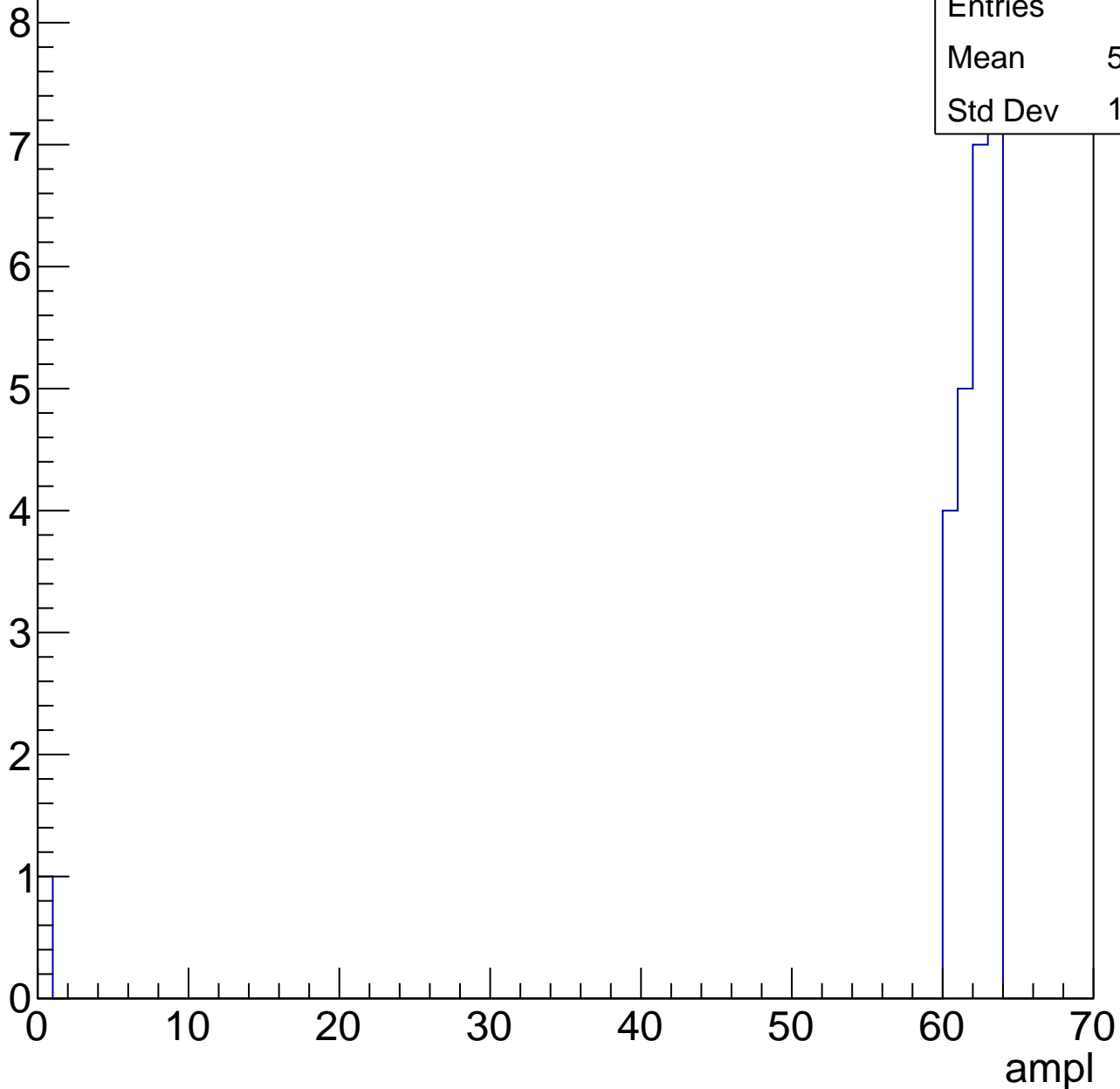


B1L001S, U24-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	25
Mean	59.32
Std Dev	12.15



B1L001S, U24-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch101, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.87
Std Dev	3.359

Gaus mean : 30.5030

Gaus Width: 3.0594

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

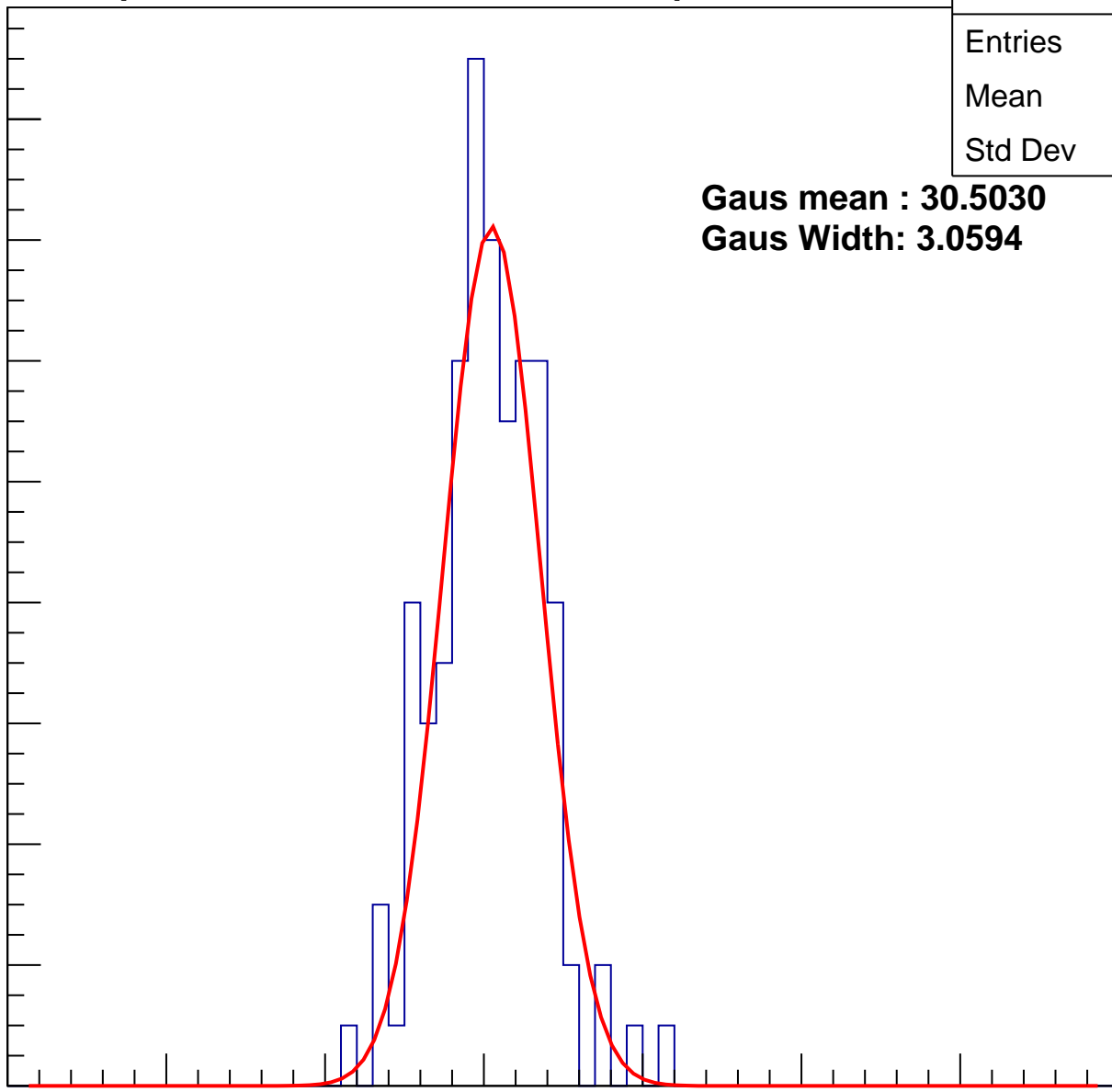
40

50

60

70

ampl



B1L001S, U24-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	36.64
Std Dev	3.217

Gaus mean : 37.0041

Gaus Width: 3.1958

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

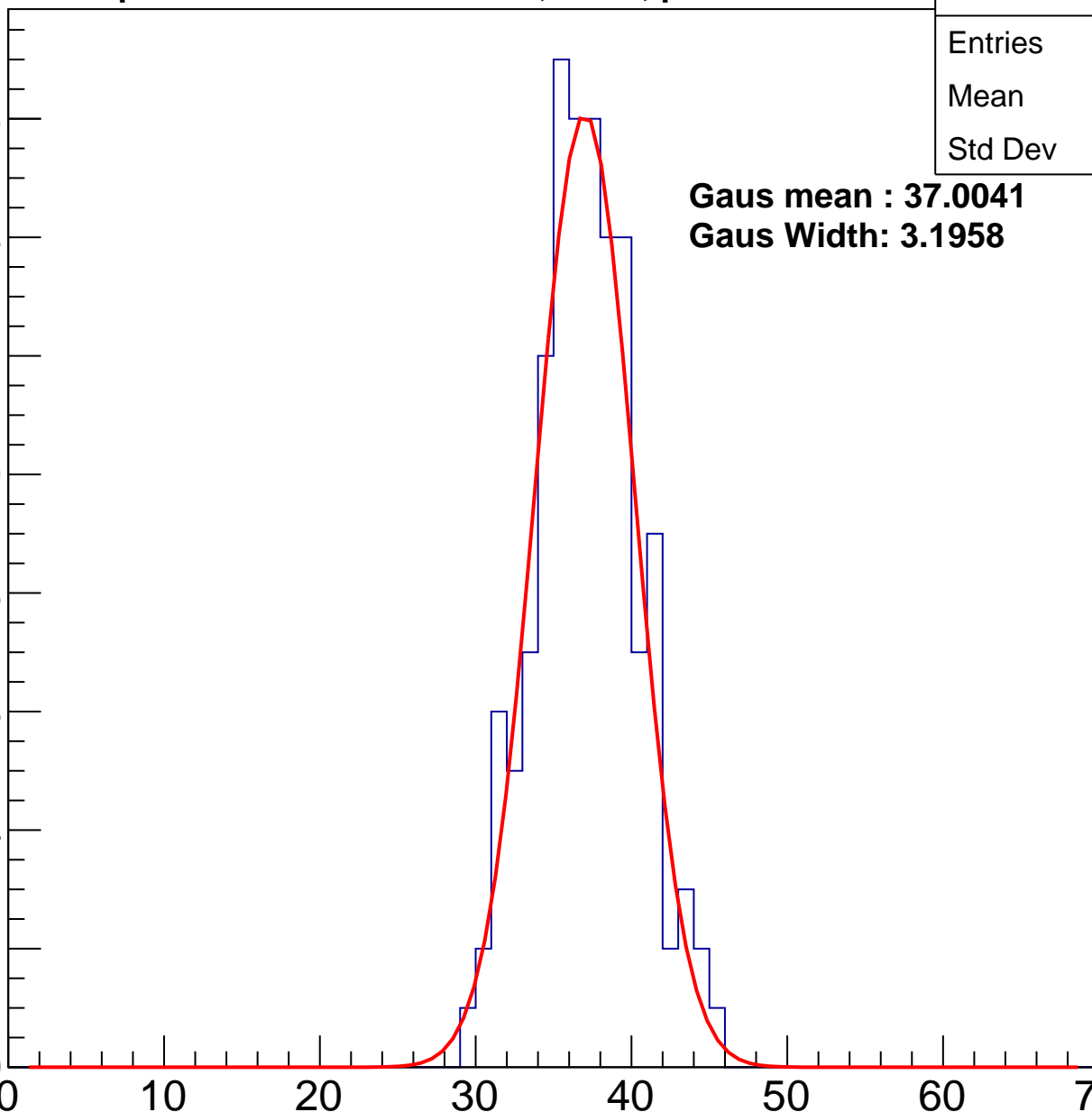
40

50

60

70

ampl



B1L001S, U24-ch101, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

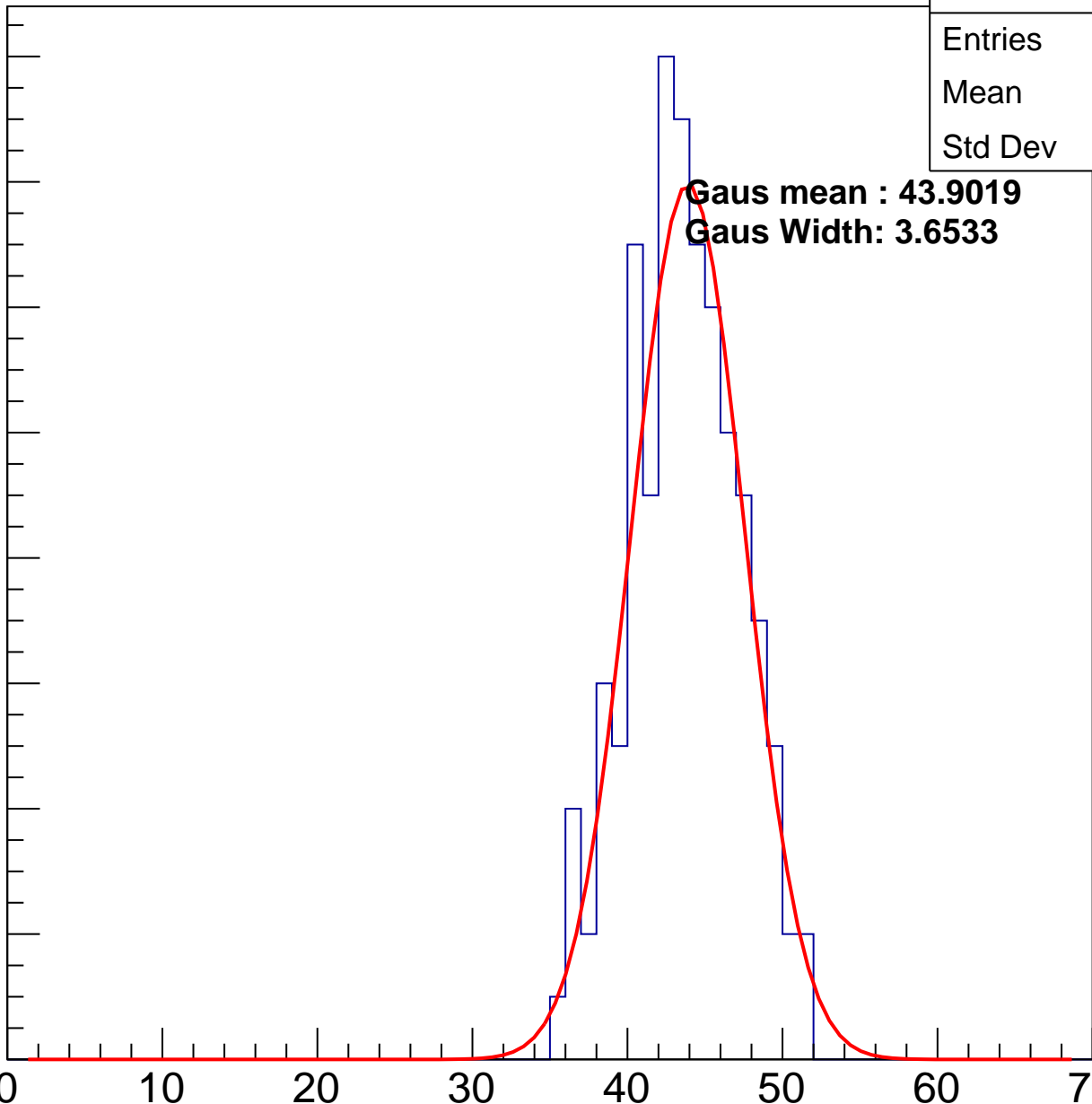
16
14
12
10
8
6
4
2
0

Entries	131
Mean	43.21
Std Dev	3.496

Gaus mean : 43.9019

Gaus Width: 3.6533

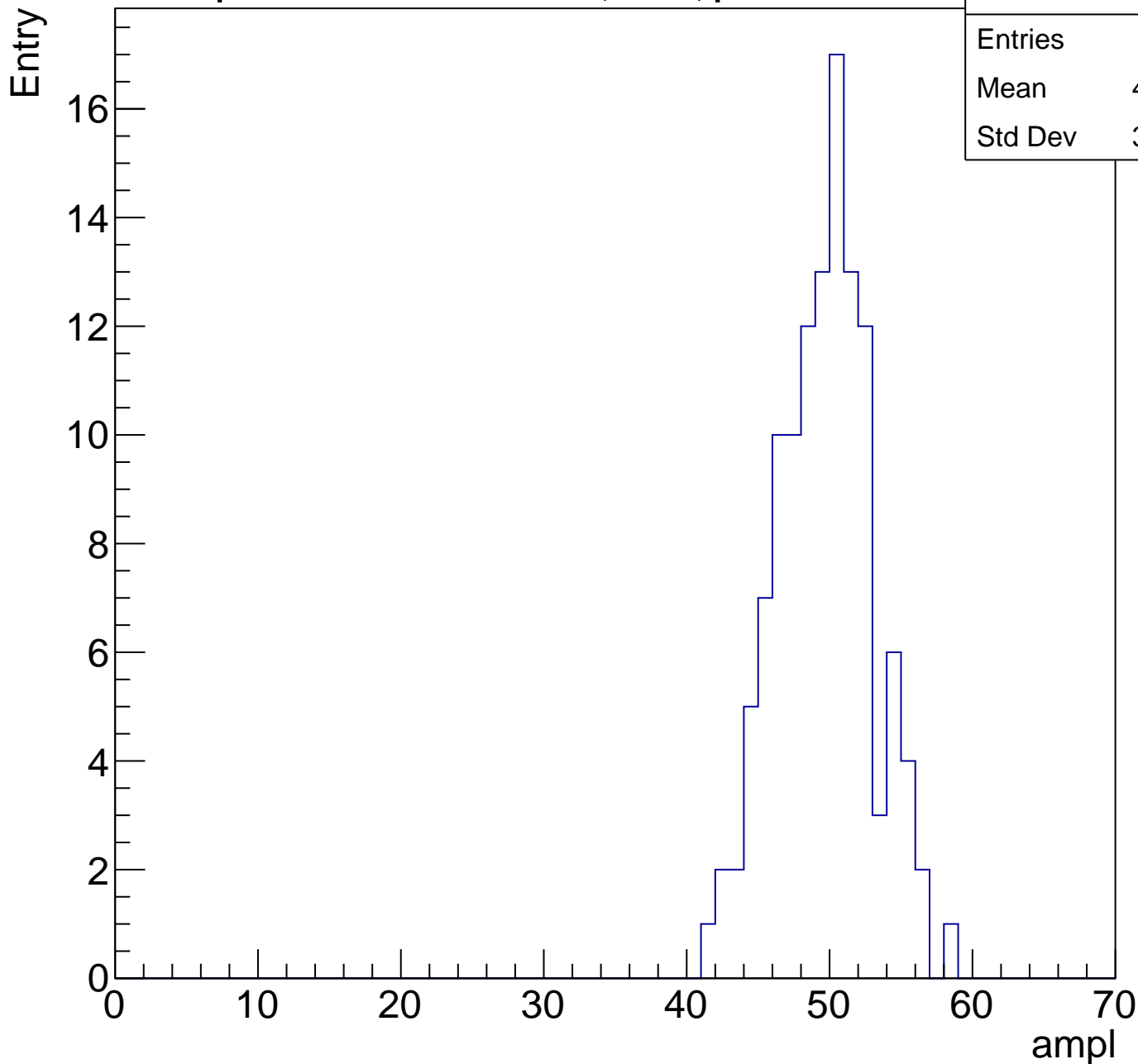
0 10 20 30 40 50 60 70
ampl



B1L001S, U24-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

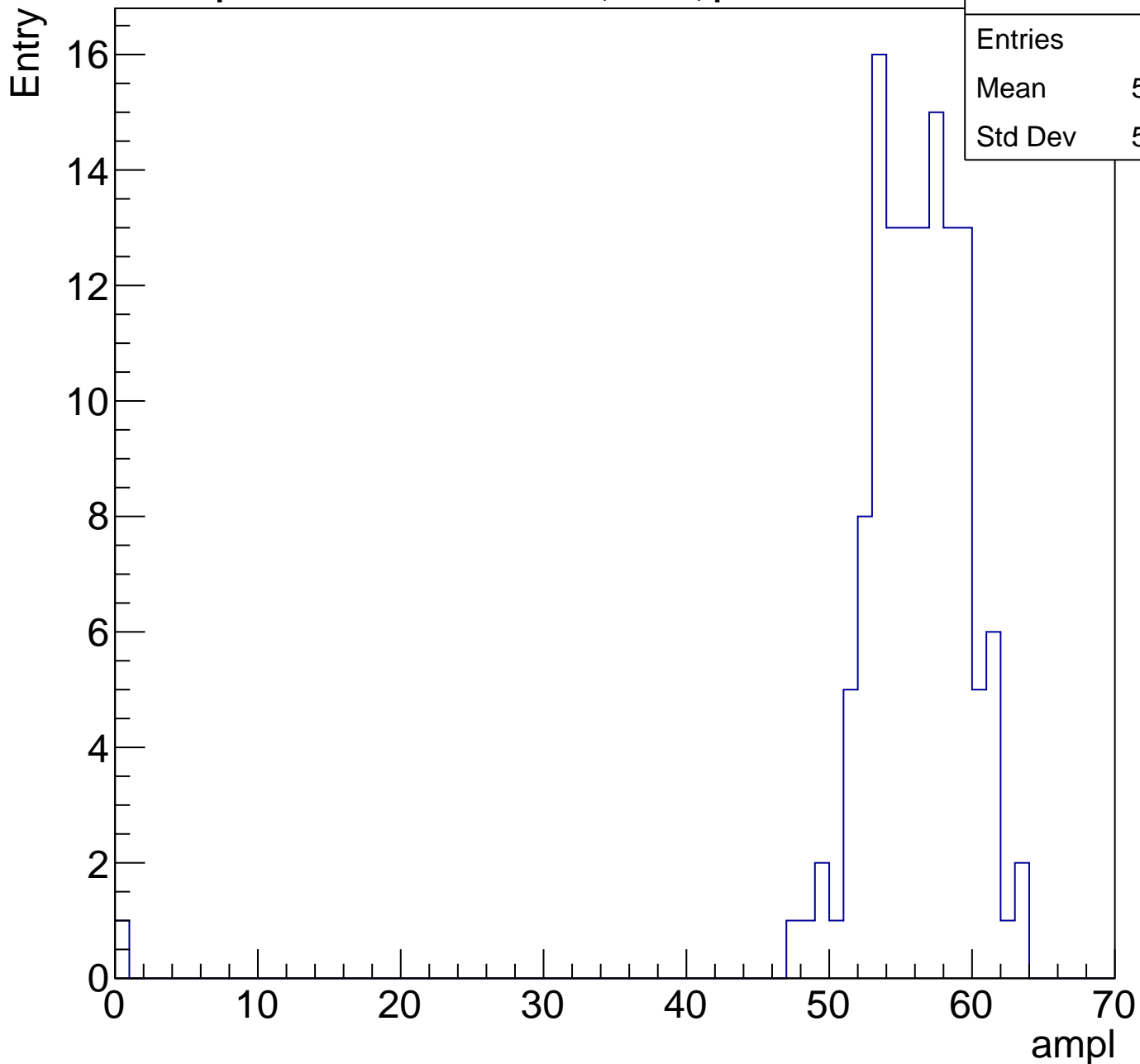
Entries	120
Mean	49.16
Std Dev	3.302



B1L001S, U24-ch101, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	55.33
Std Dev	5.818



B1L001S, U24-ch101, adc5

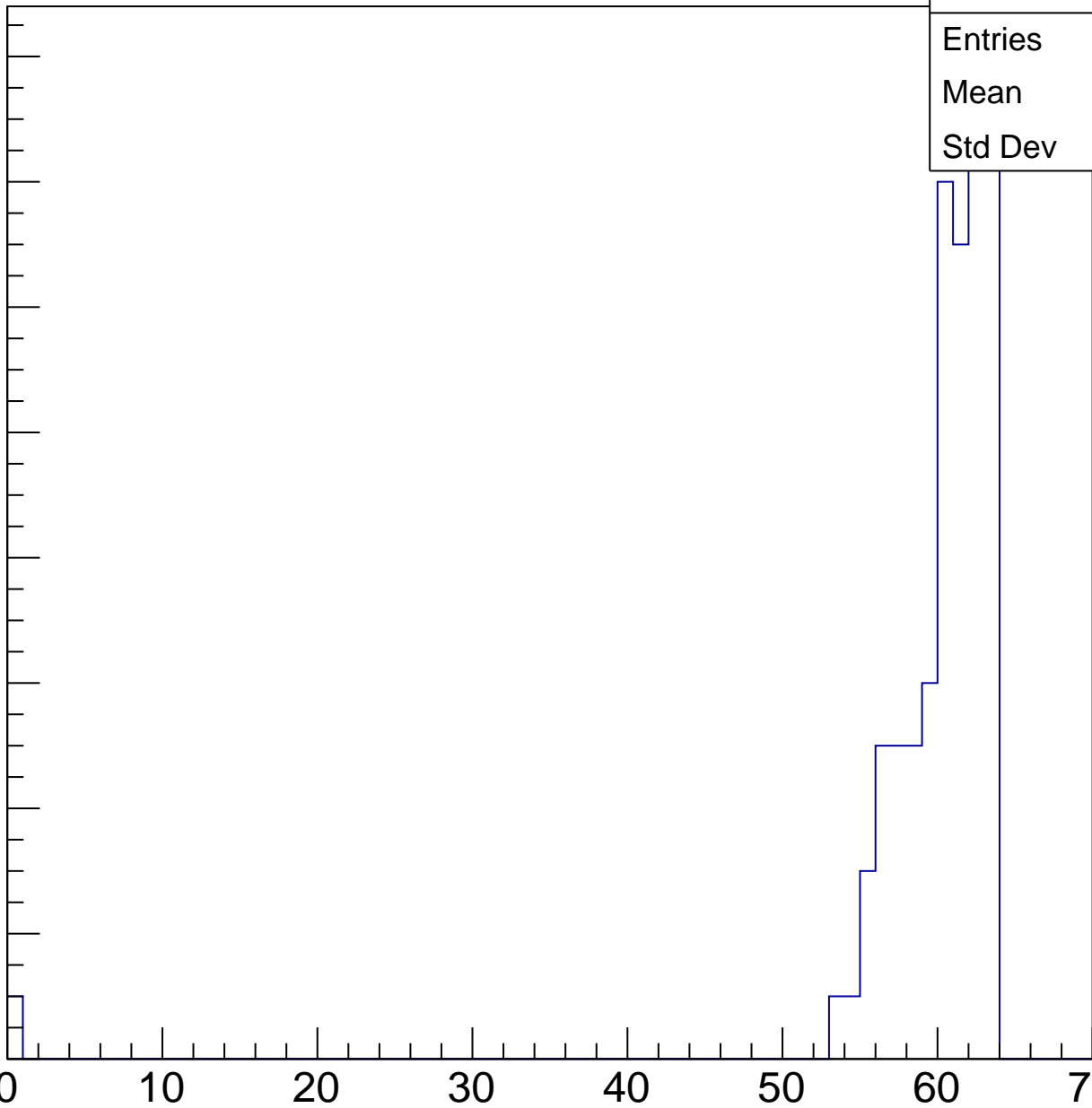
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.47
Std Dev	6.911

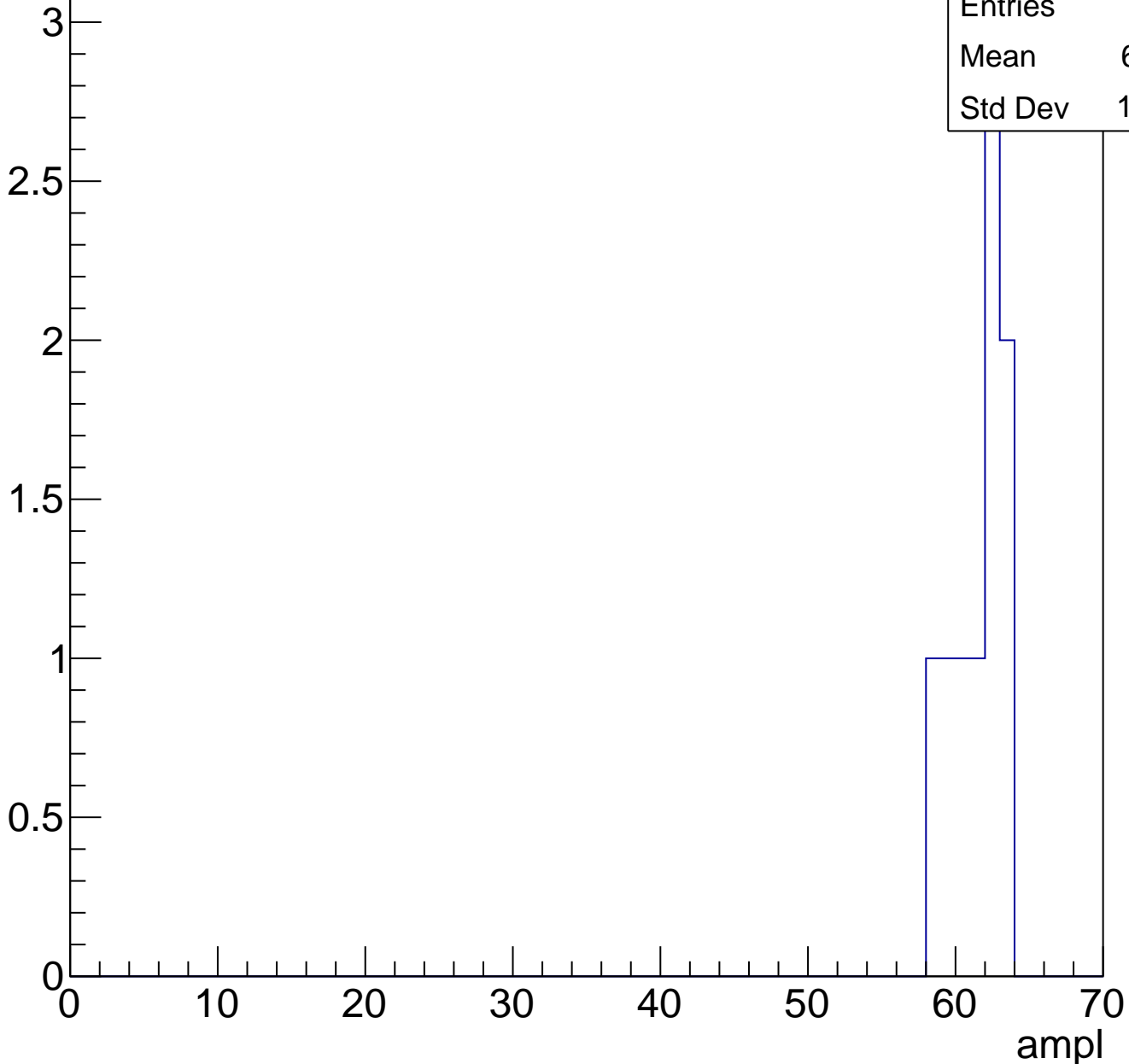
ampl



B1L001S, U24-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch102, adc0

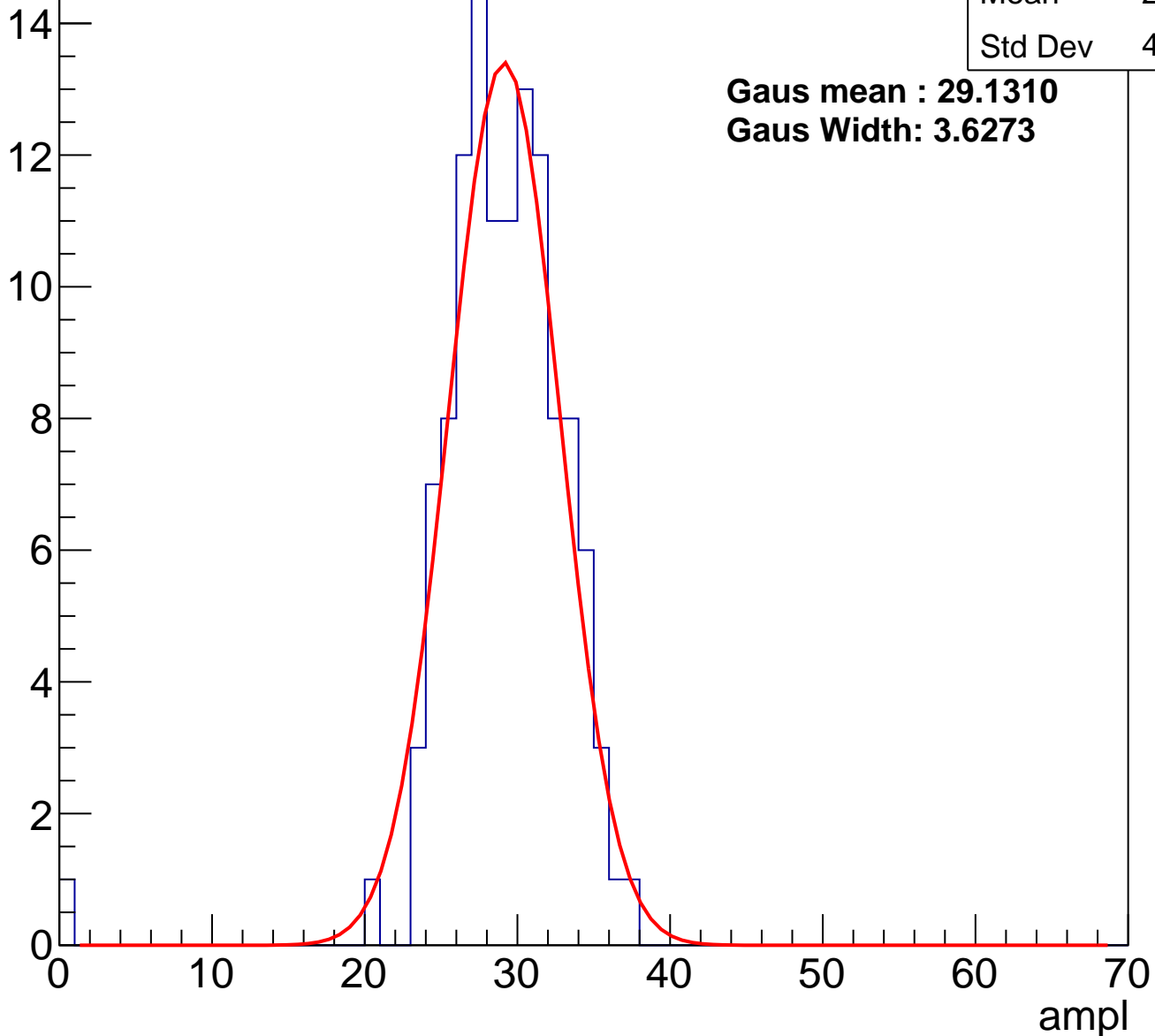
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	28.64
Std Dev	4.194

Gaus mean : 29.1310

Gaus Width: 3.6273

Entry



B1L001S, U24-ch102, adc1

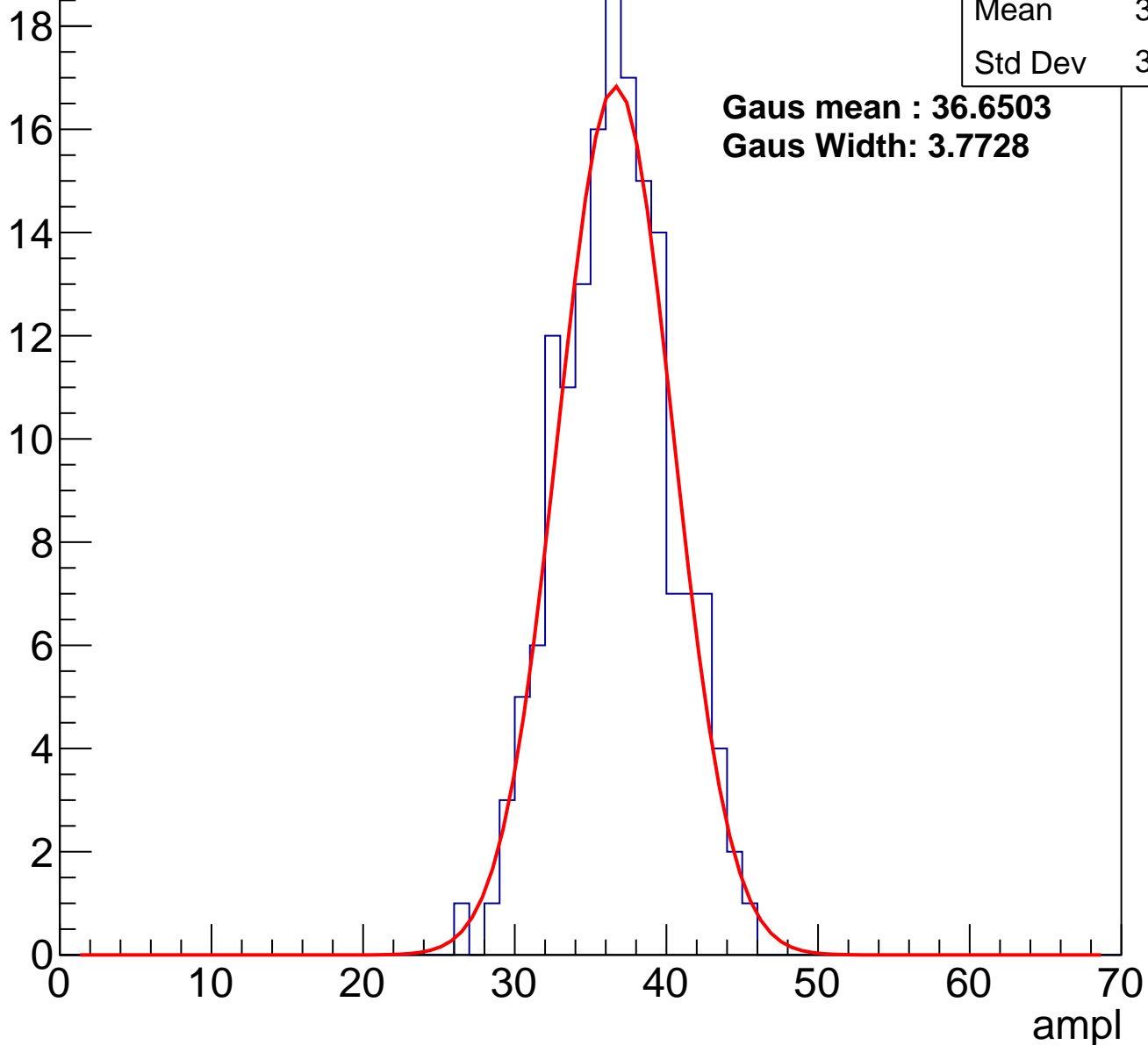
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	36.16
Std Dev	3.648

Gaus mean : 36.6503

Gaus Width: 3.7728

Entry



B1L001S, U24-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

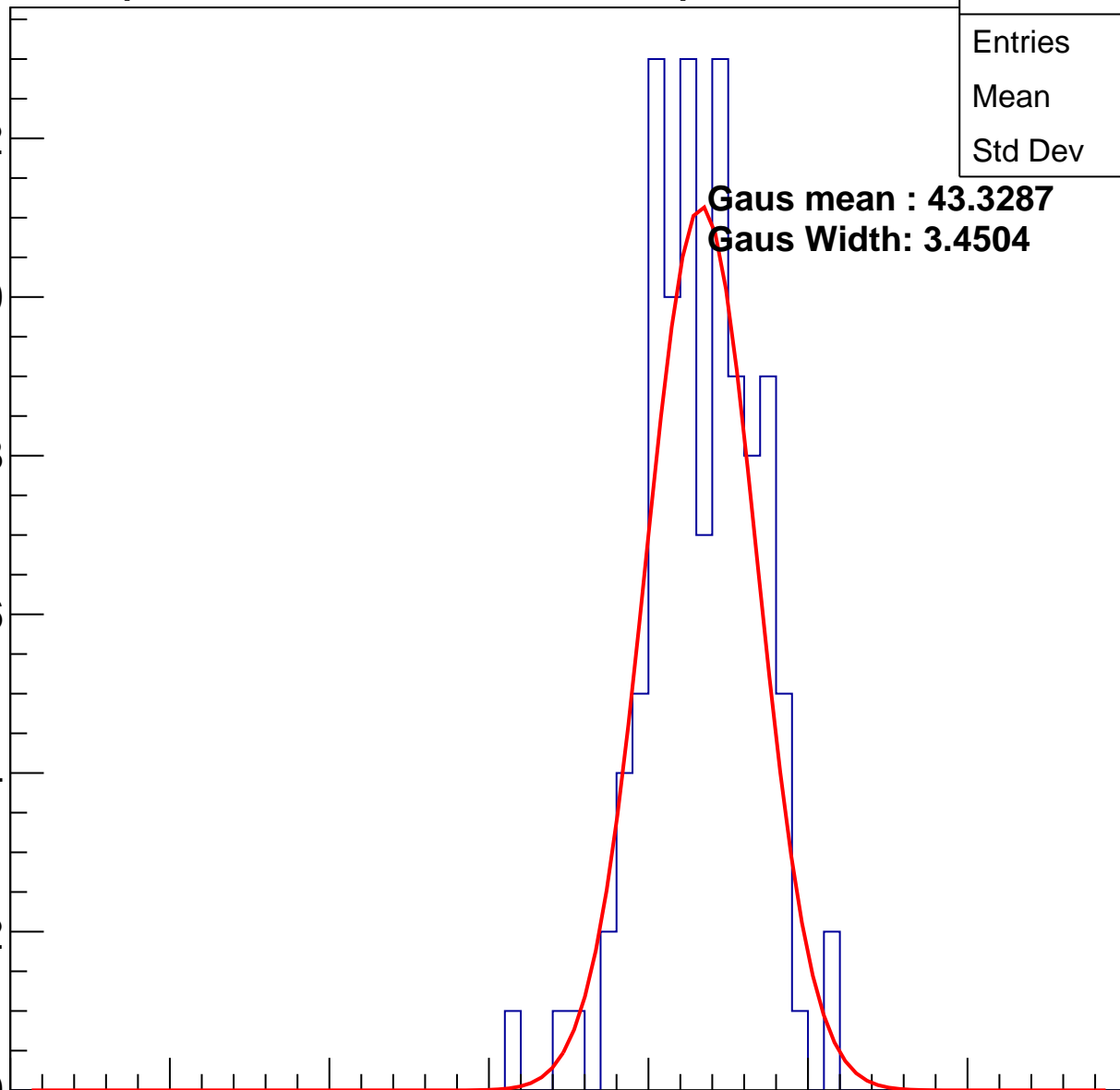
70

ampl

Entries	104
Mean	42.86
Std Dev	3.485

Gaus mean : 43.3287

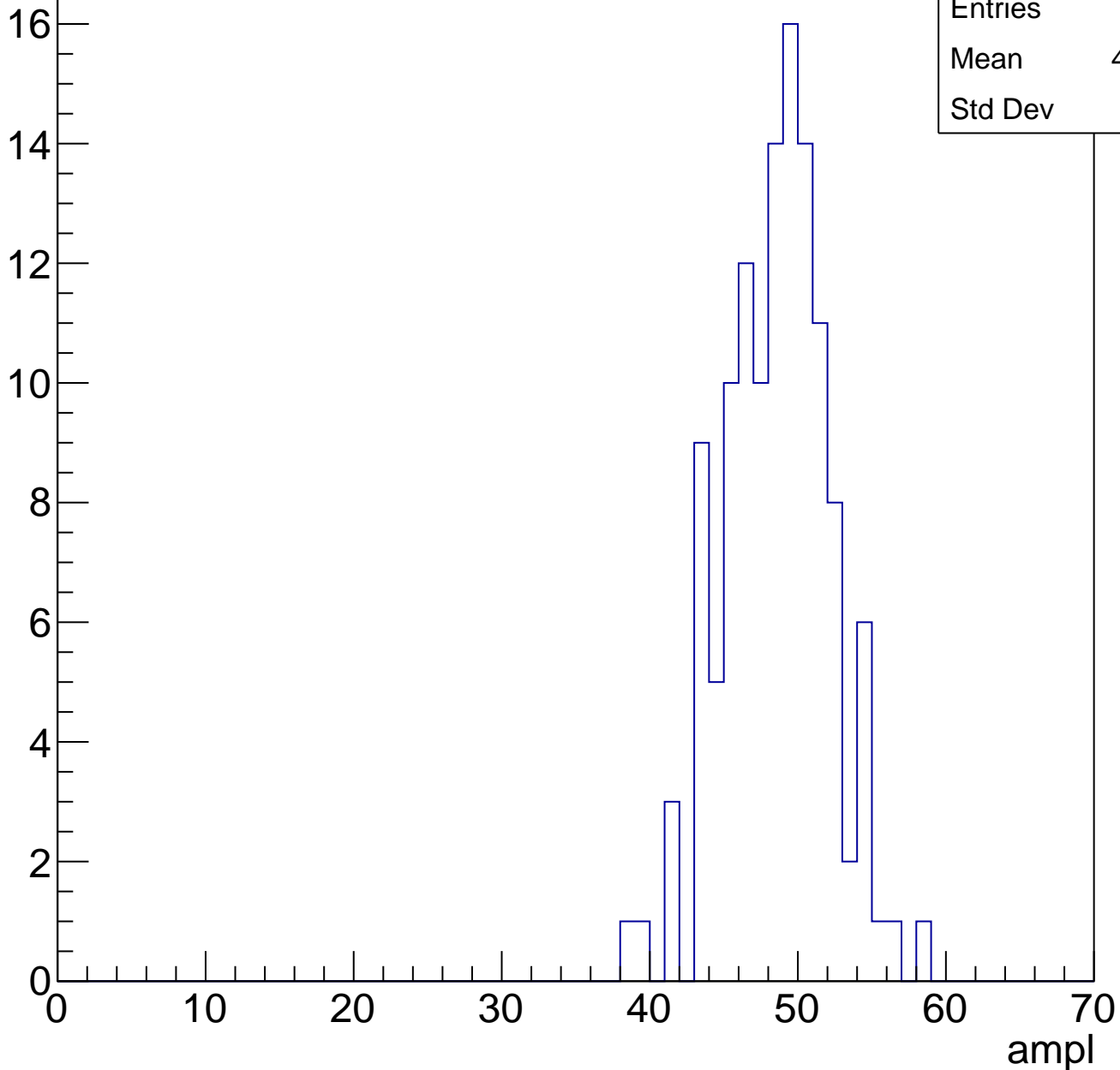
Gaus Width: 3.4504



B1L001S, U24-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

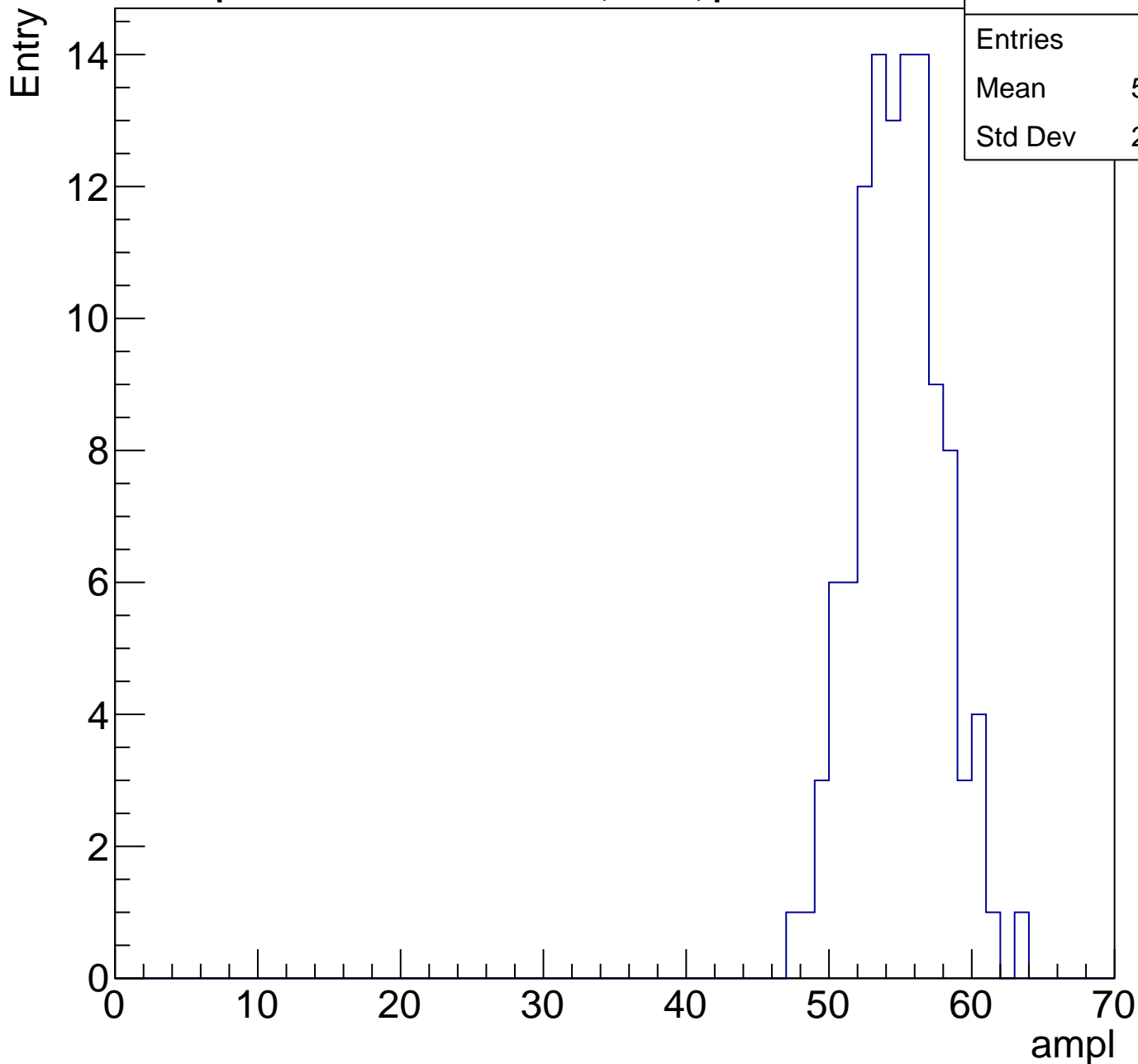
Entry



B1L001S, U24-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	54.44
Std Dev	2.983



B1L001S, U24-ch102, adc5

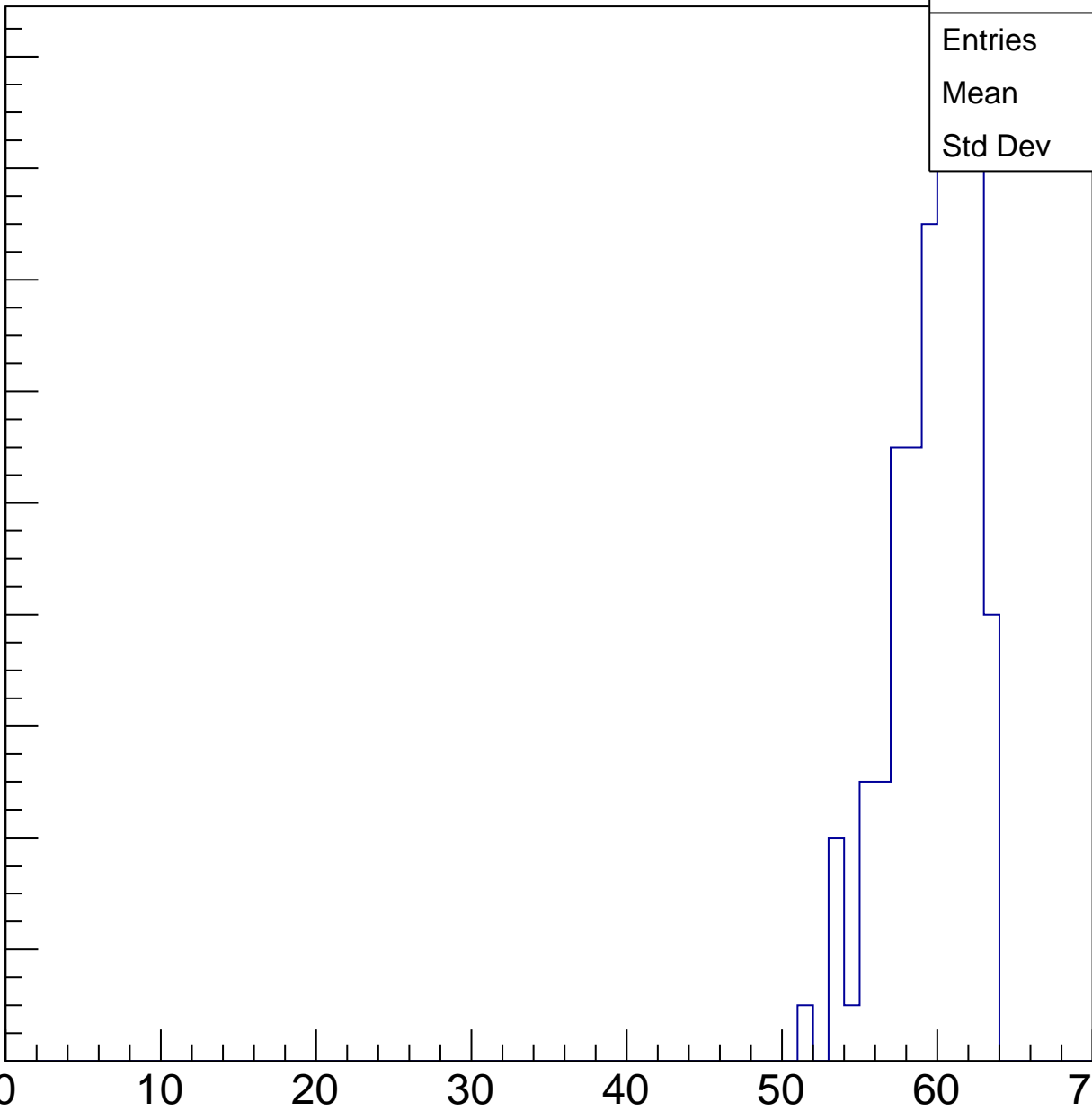
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	112
Mean	59.28
Std Dev	2.636

ampl



B1L001S, U24-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	19
Mean	55.63
Std Dev	19.12

Entry

10

8

6

4

2

0

0

10

20

30

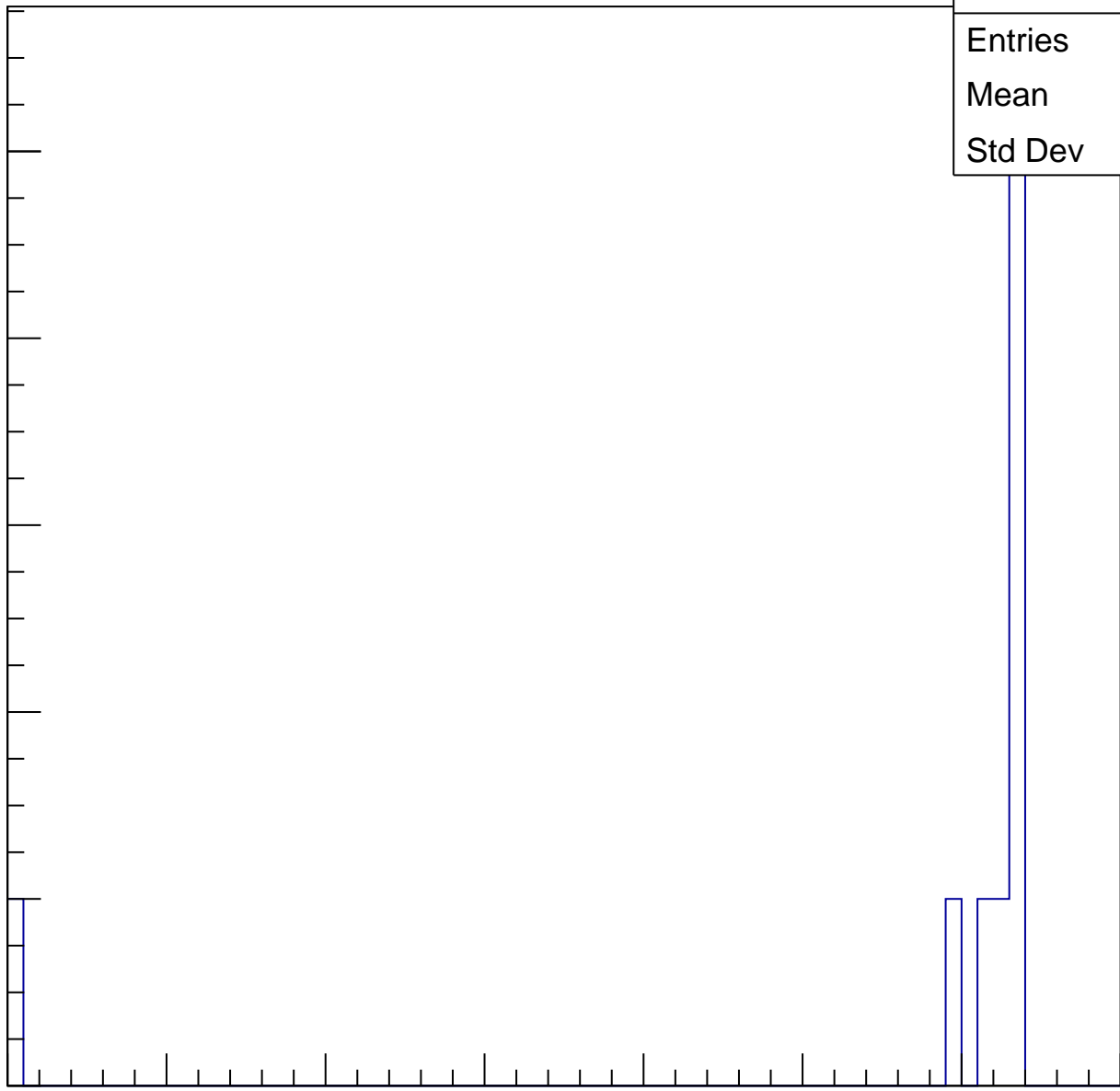
40

50

60

70

ampl



B1L001S, U24-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U24-ch103, adc0

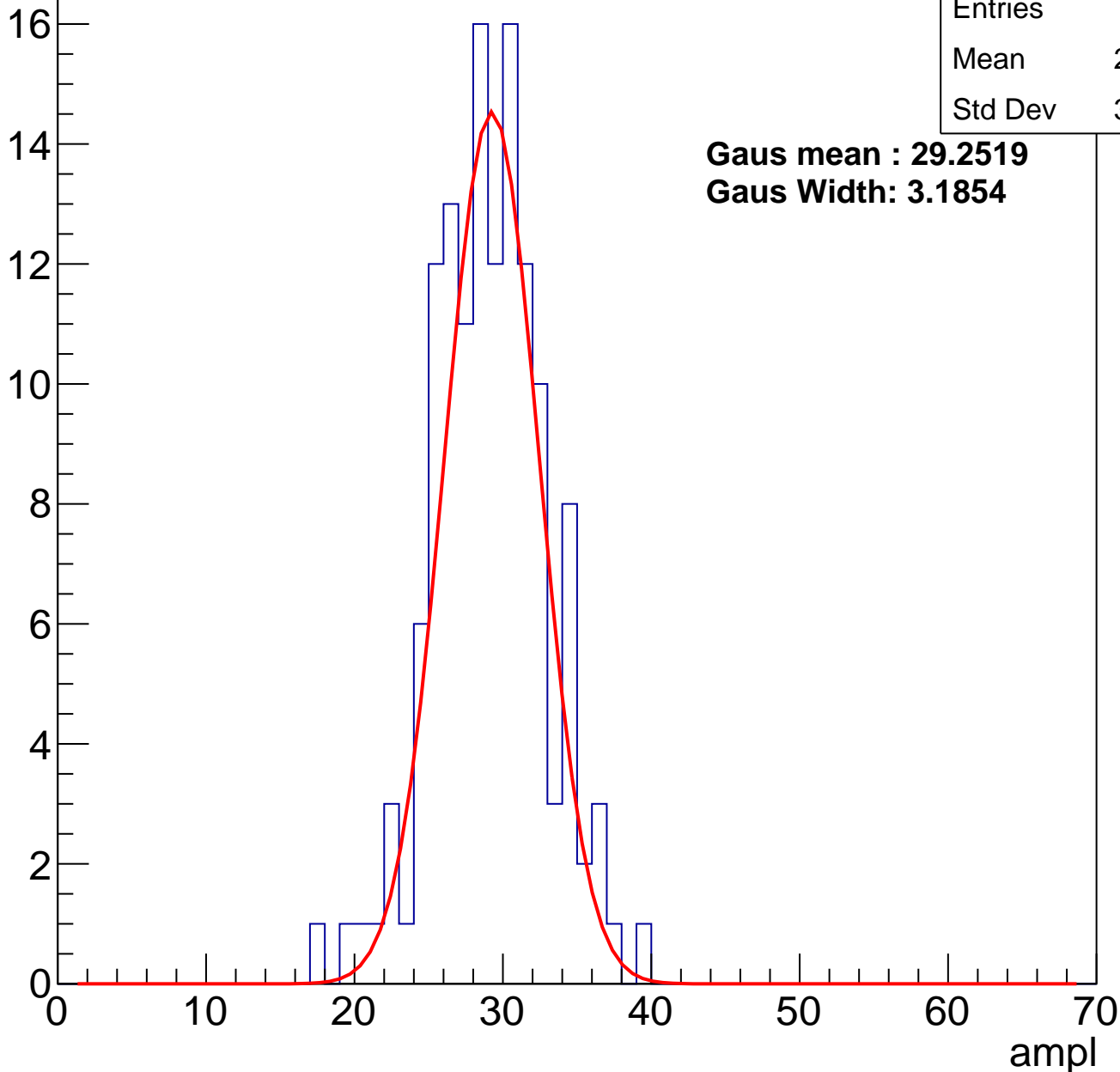
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.64
Std Dev	3.724

Gaus mean : 29.2519

Gaus Width: 3.1854

Entry



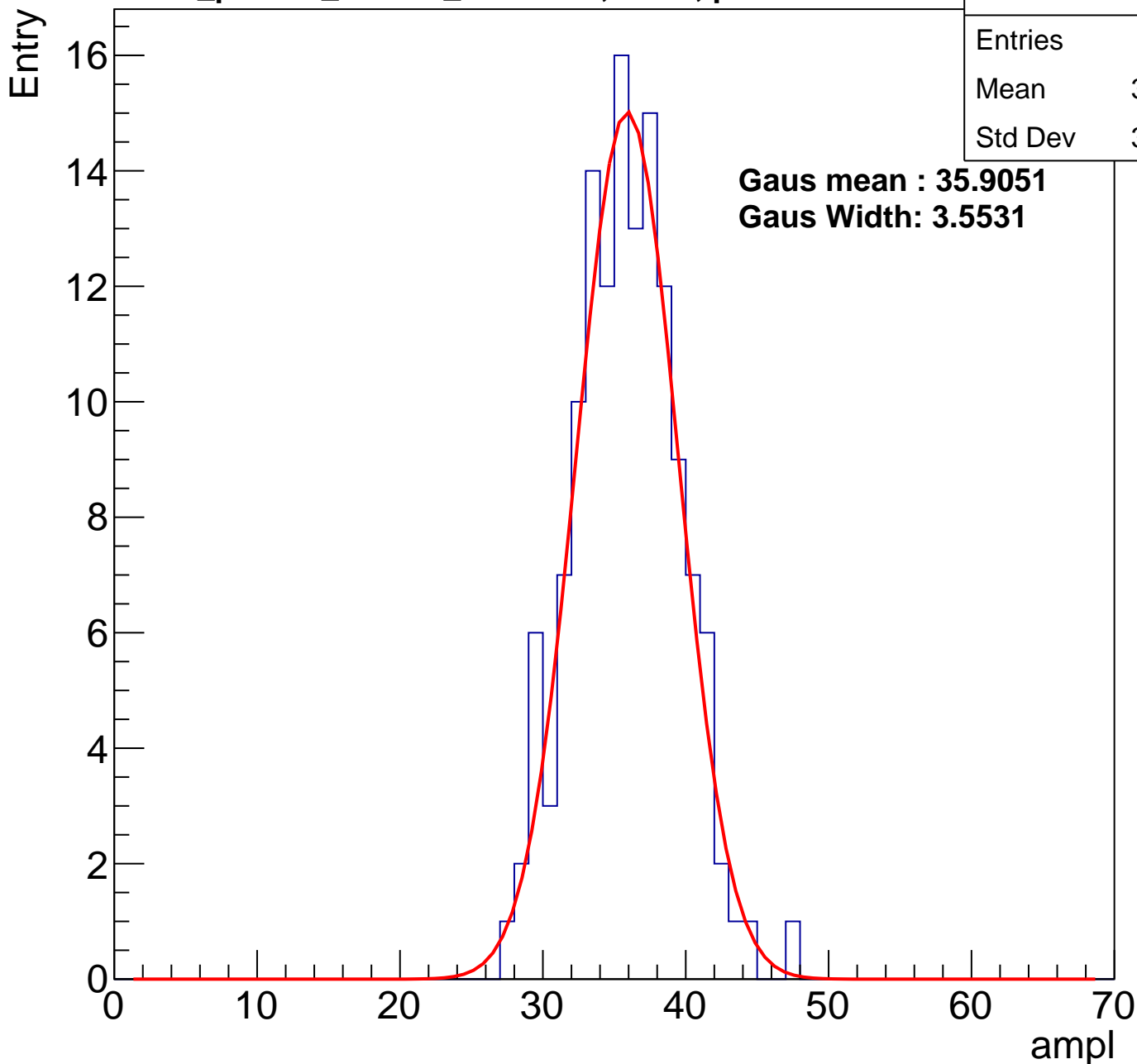
B1L001S, U24-ch103, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.42
Std Dev	3.613

Gaus mean : 35.9051

Gaus Width: 3.5531



B1L001S, U24-ch103, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

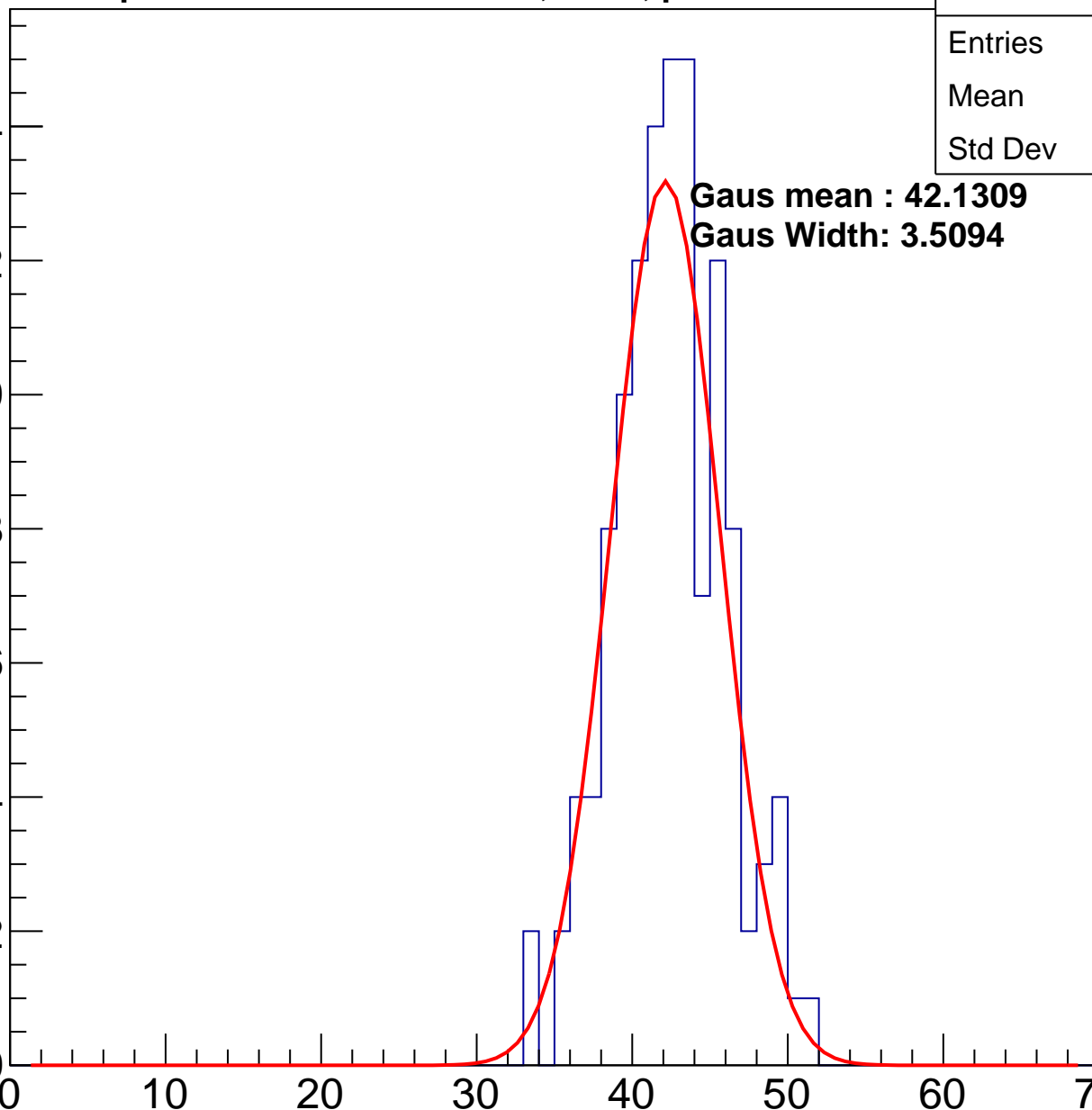
70

ampl

Entries	124
Mean	41.95
Std Dev	3.549

Gaus mean : 42.1309

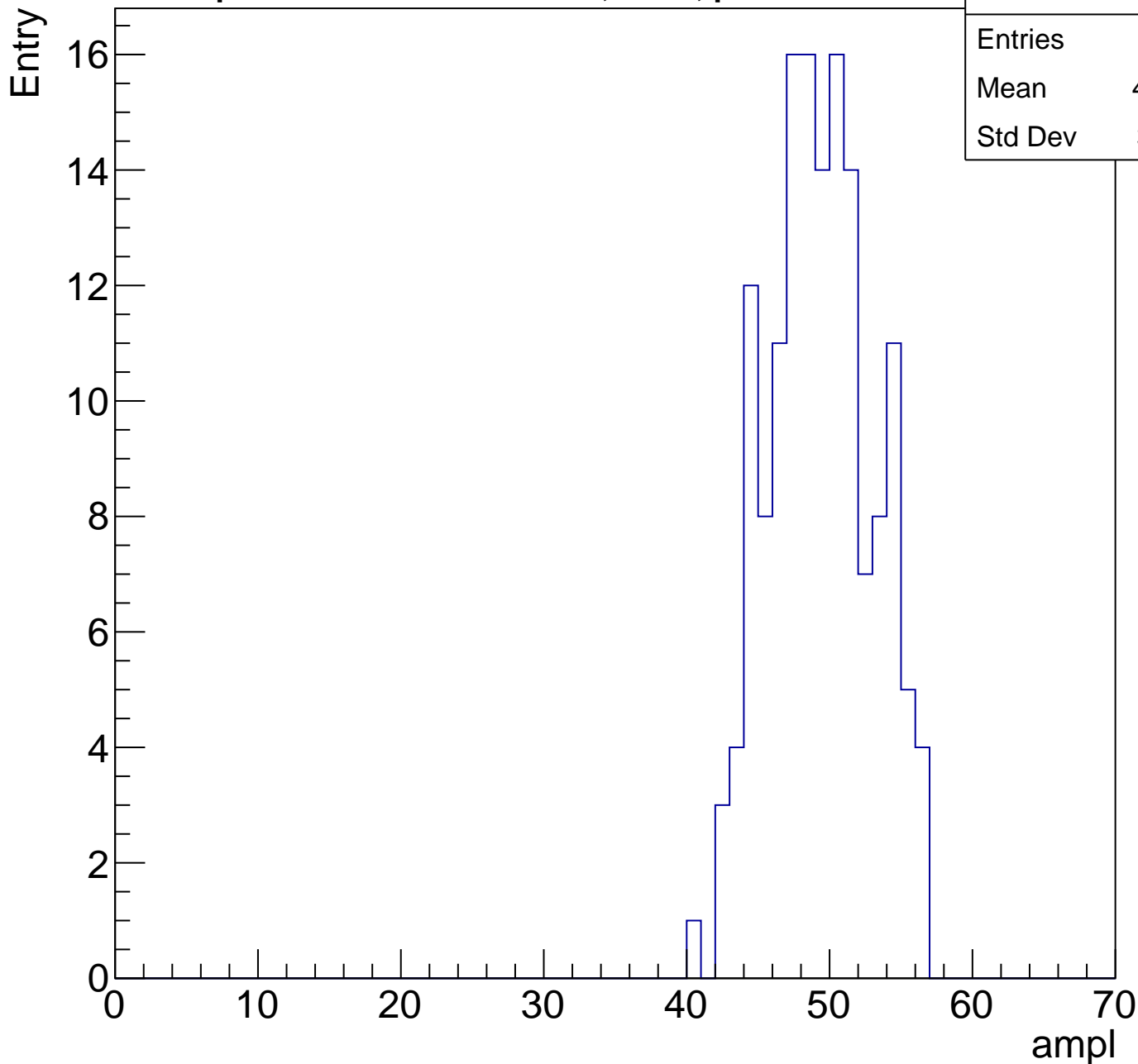
Gaus Width: 3.5094



B1L001S, U24-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

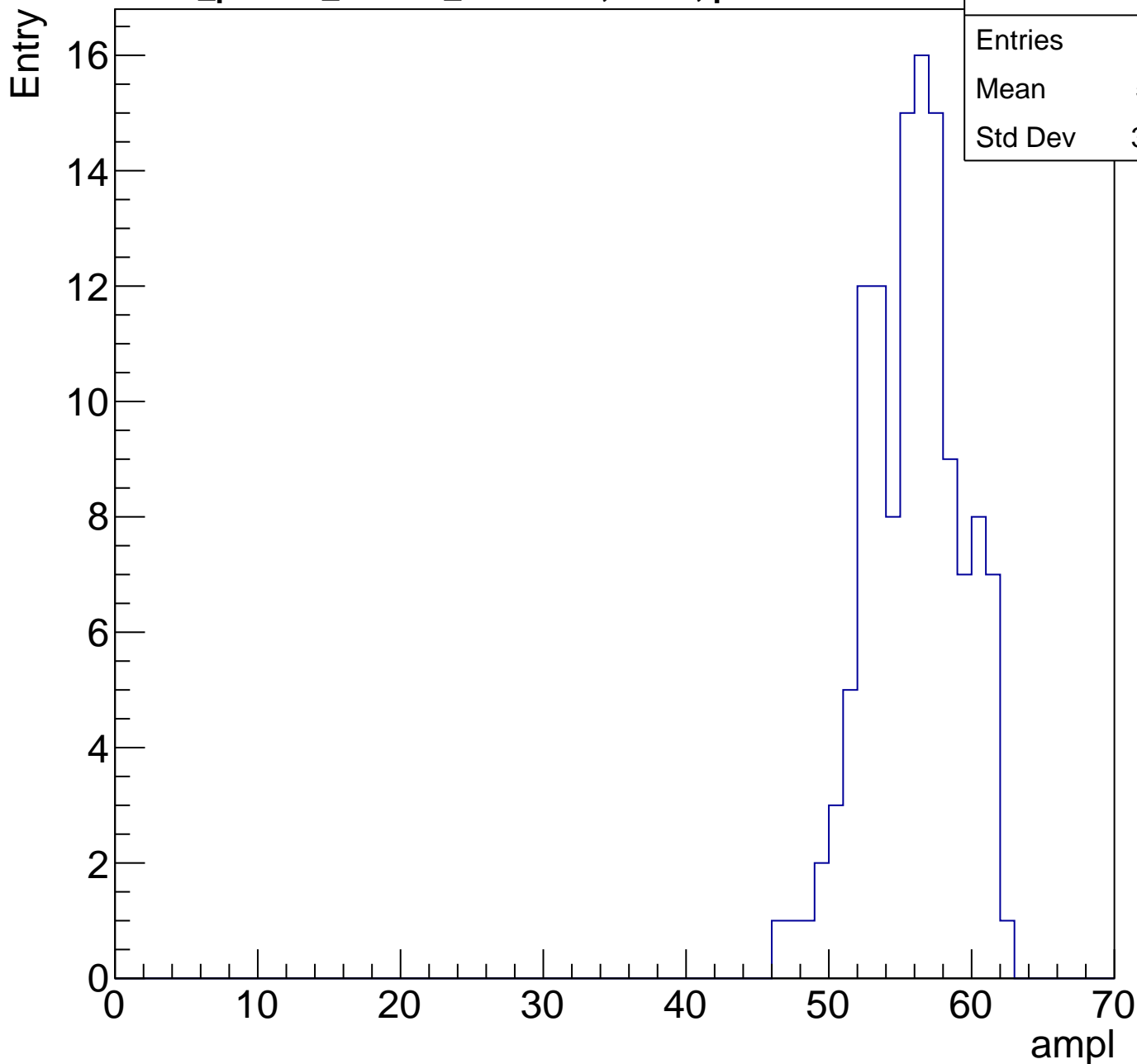
Entries	150
Mean	48.89
Std Dev	3.551



B1L001S, U24-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	55.41
Std Dev	3.293



B1L001S, U24-ch103, adc5

calib_packv5_042523_0143.root, FC#2, port C2

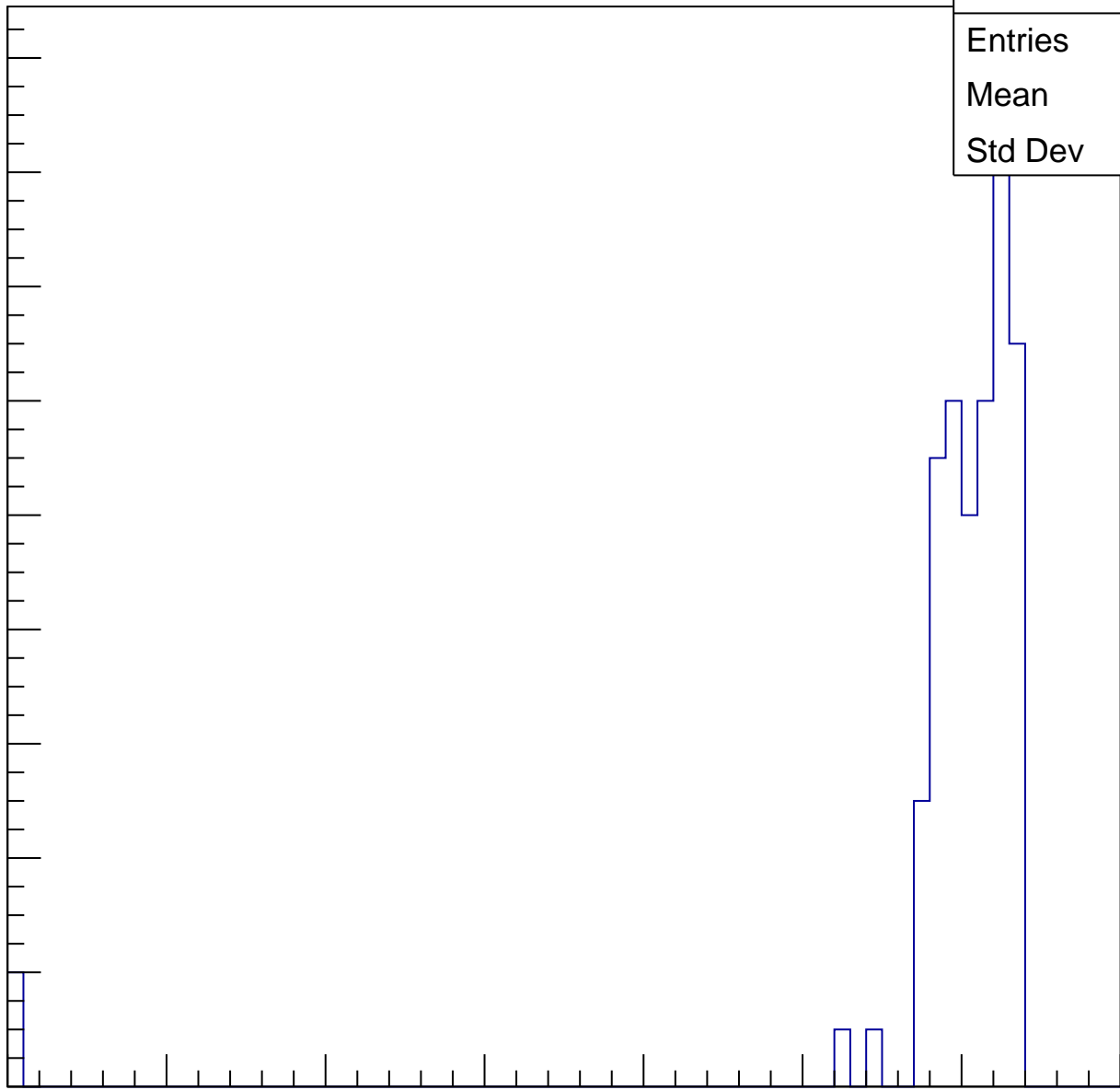
Entries	85
Mean	58.87
Std Dev	9.389

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

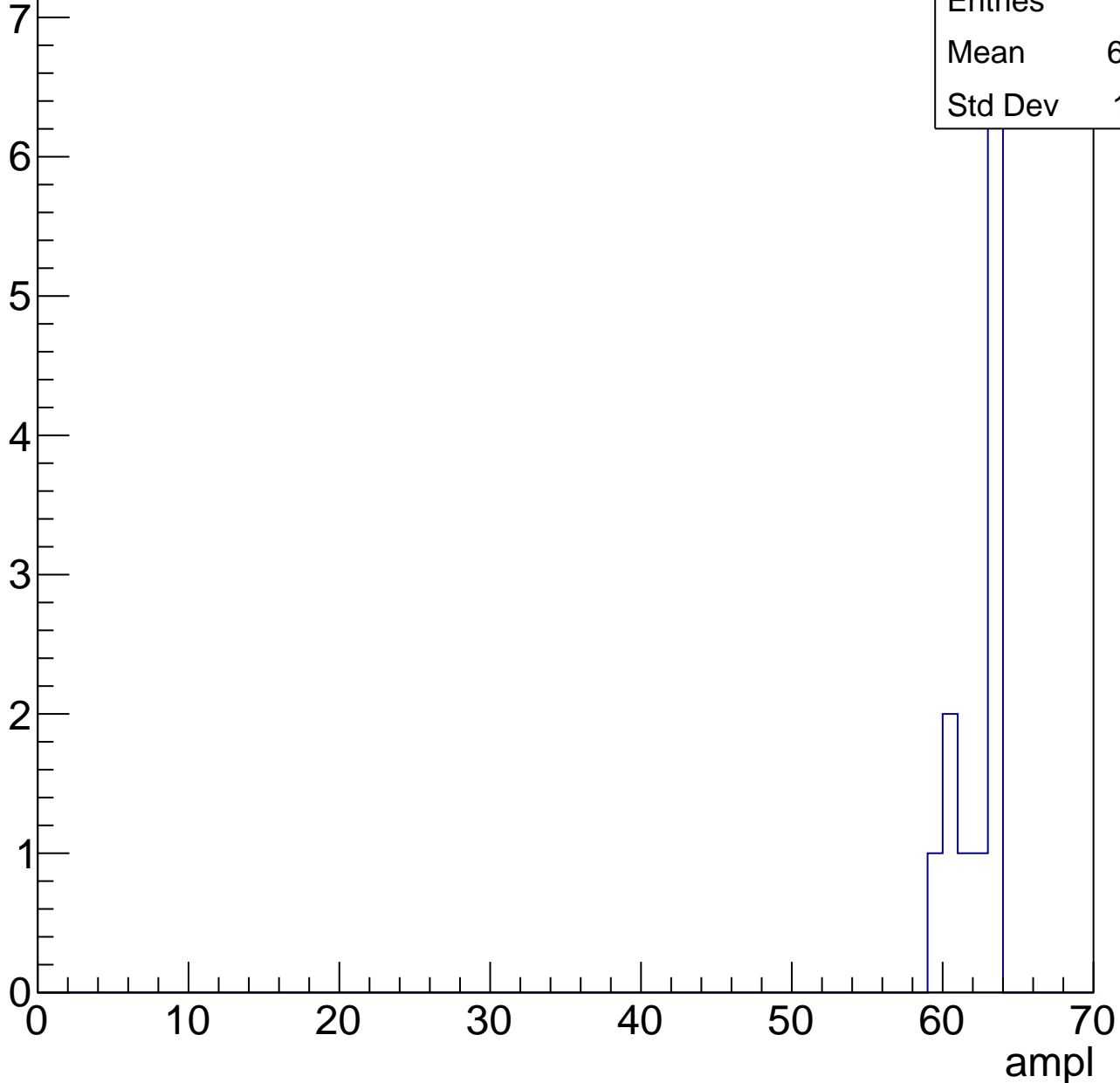


B1L001S, U24-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.92
Std Dev	1.441



B1L001S, U24-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

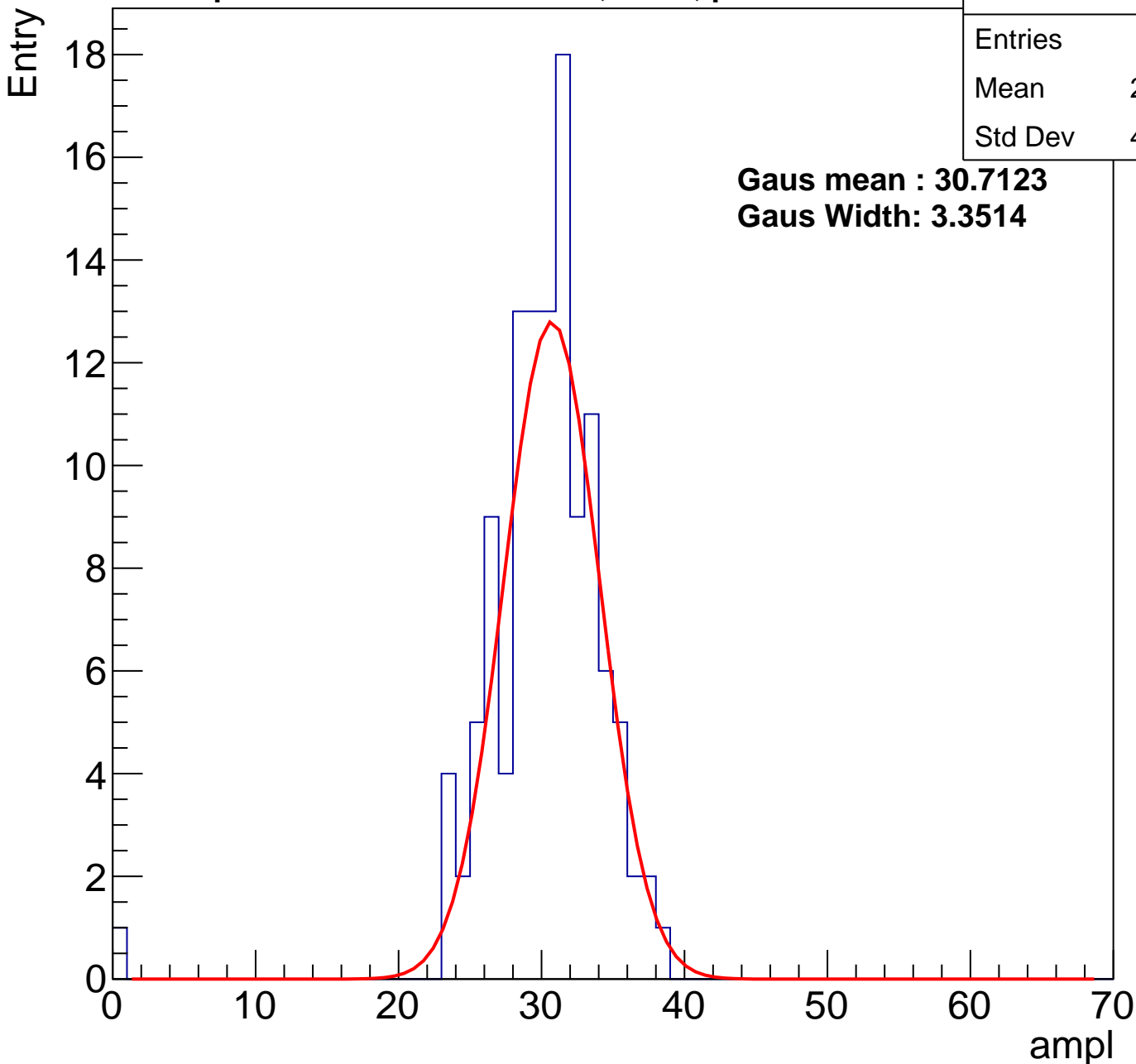
B1L001S, U24-ch104, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.75
Std Dev	4.257

Gaus mean : 30.7123

Gaus Width: 3.3514

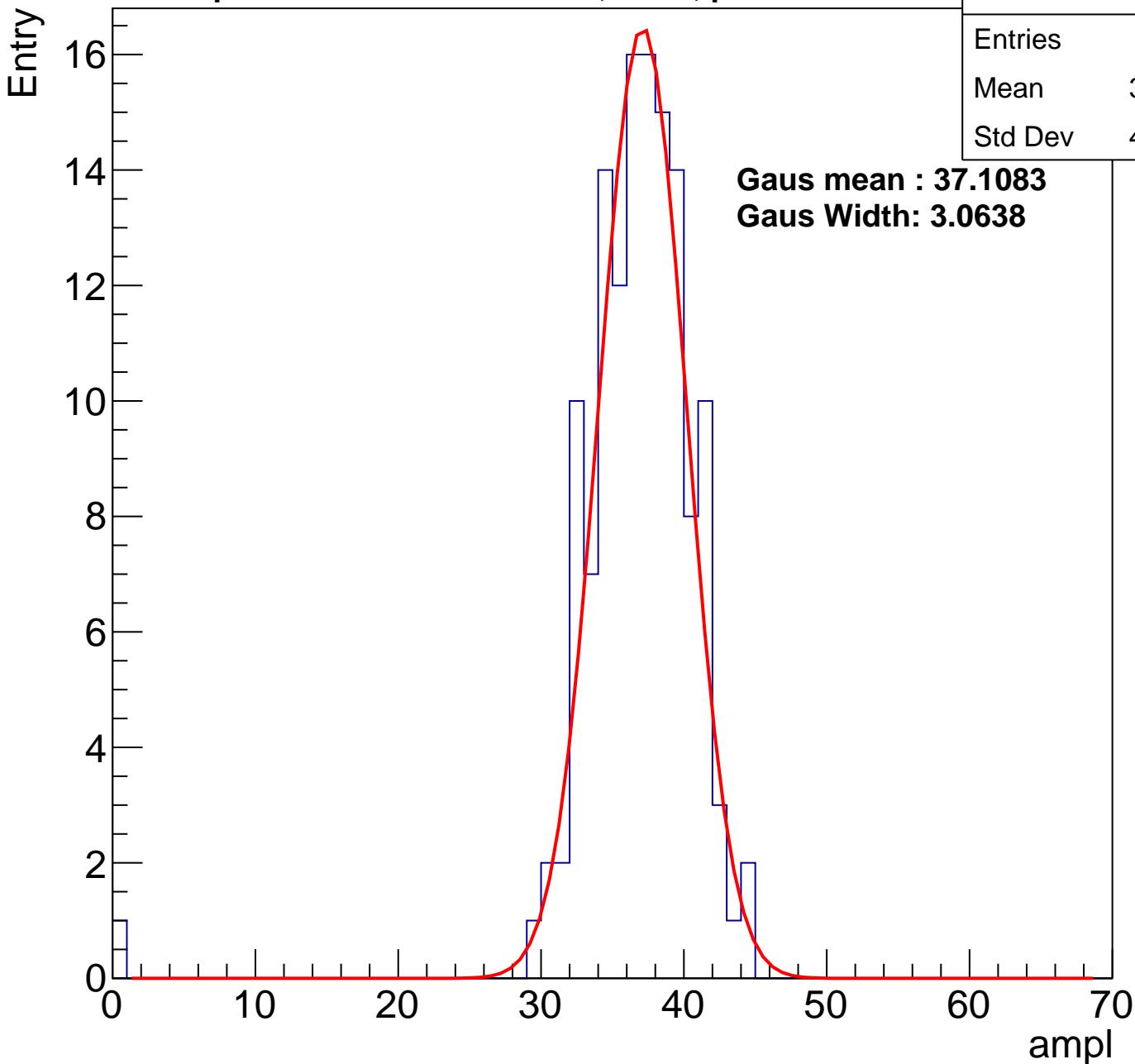


B1L001S, U24-ch104, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	36.34
Std Dev	4.412

Gaus mean : 37.1083
Gaus Width: 3.0638



B1L001S, U24-ch104, adc2

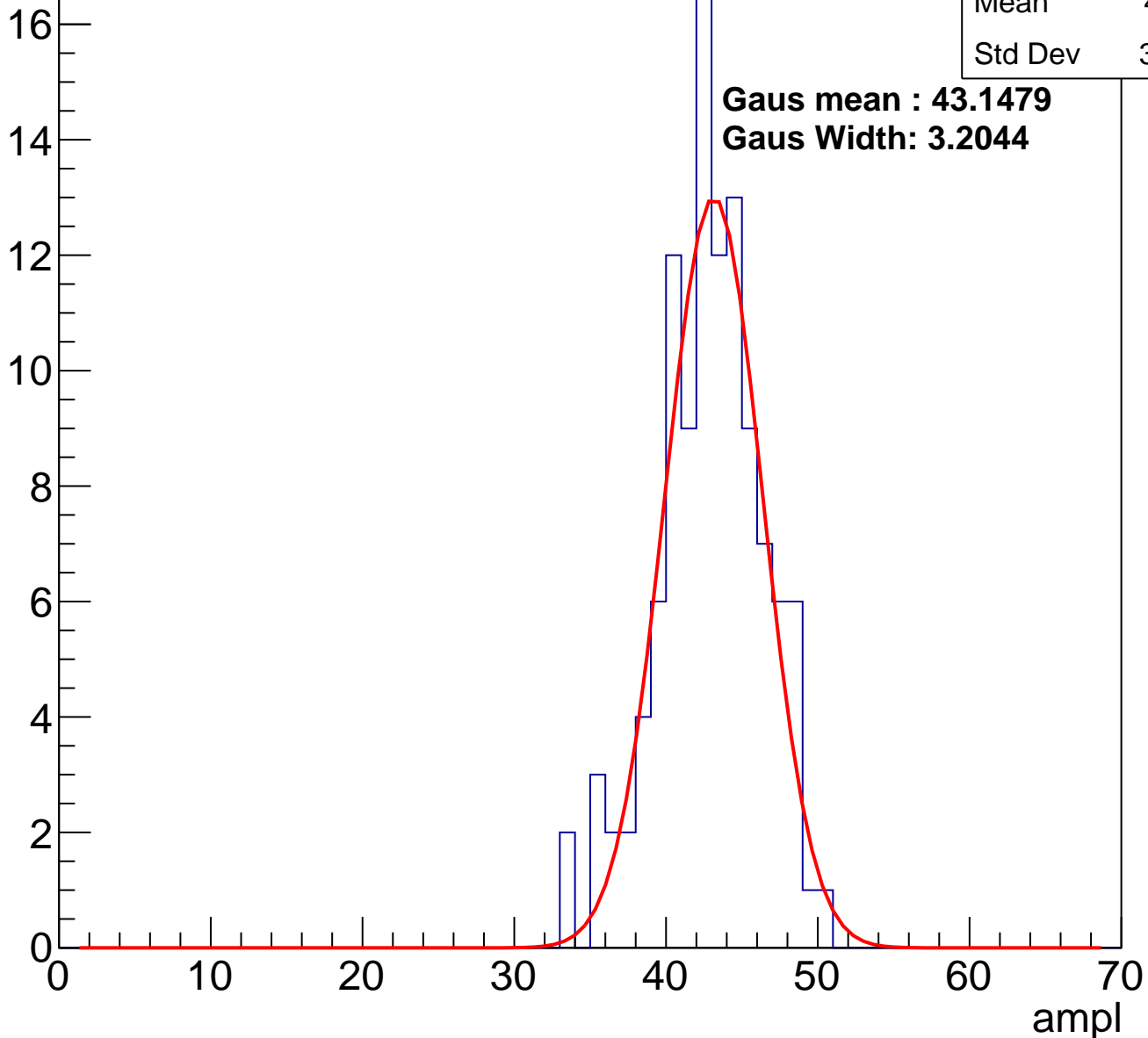
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	112
Mean	42.41
Std Dev	3.437

Gaus mean : 43.1479

Gaus Width: 3.2044

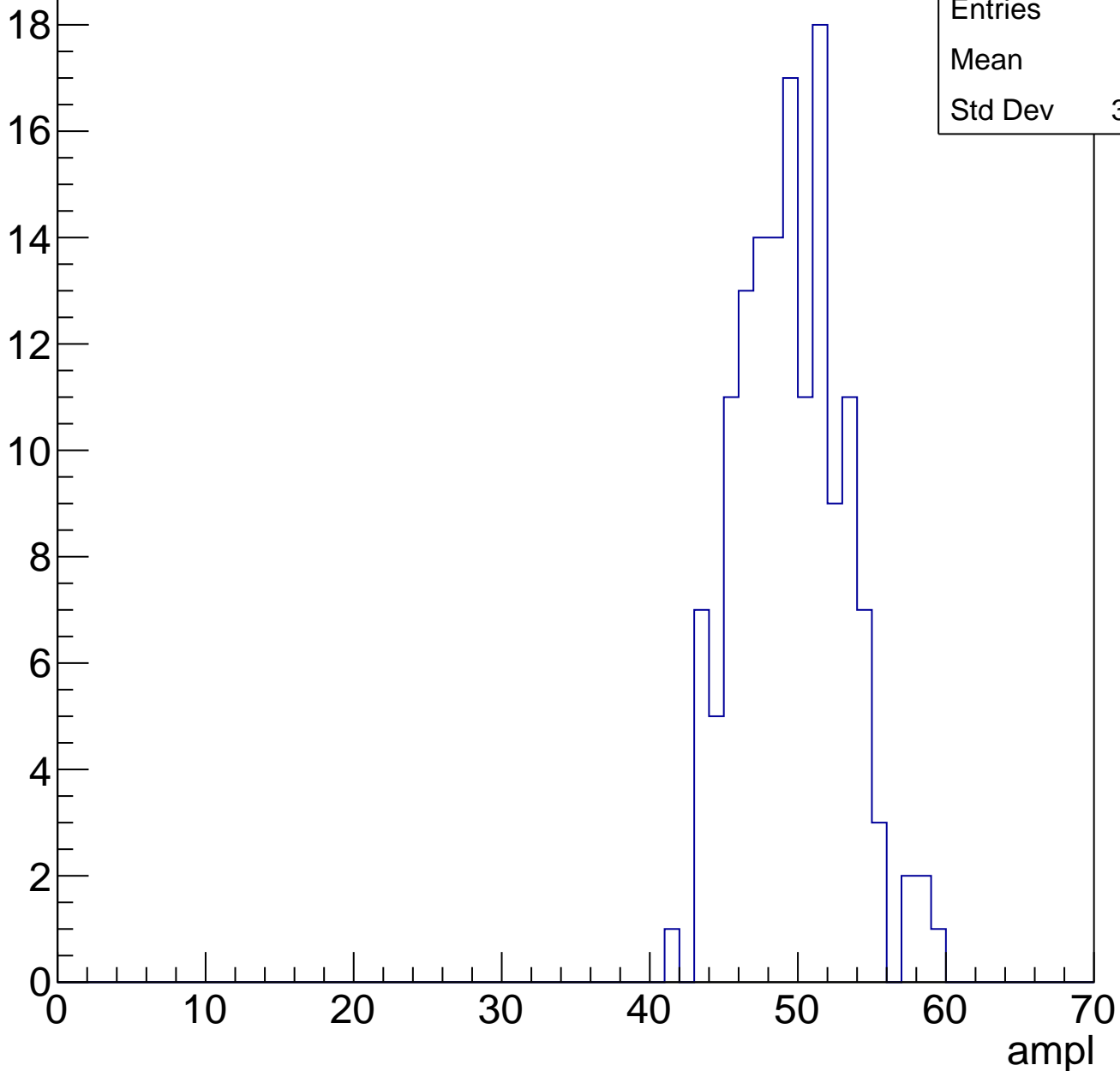


B1L001S, U24-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	49.1
Std Dev	3.526

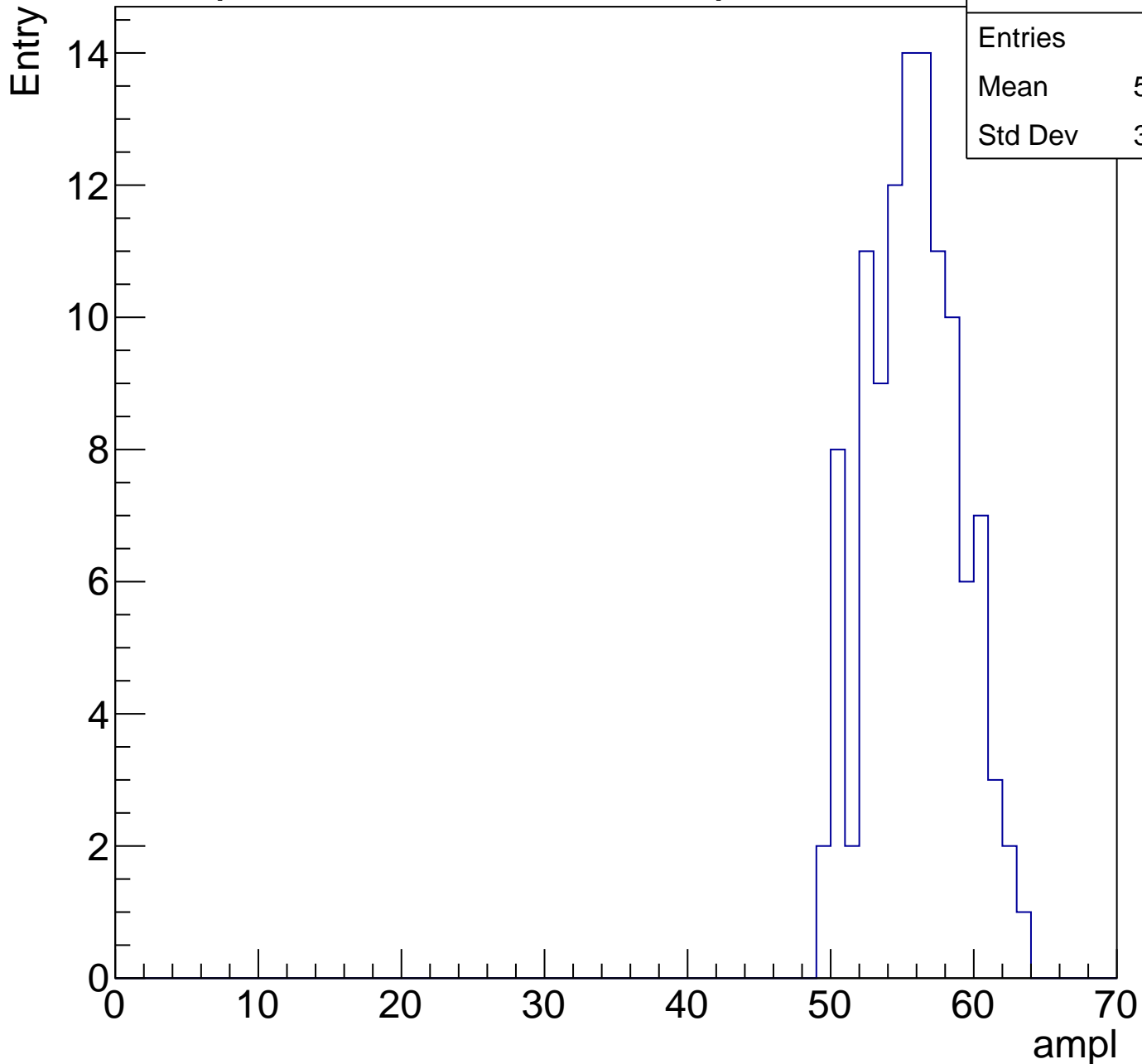
Entry



B1L001S, U24-ch104, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	55.38
Std Dev	3.168



B1L001S, U24-ch104, adc5

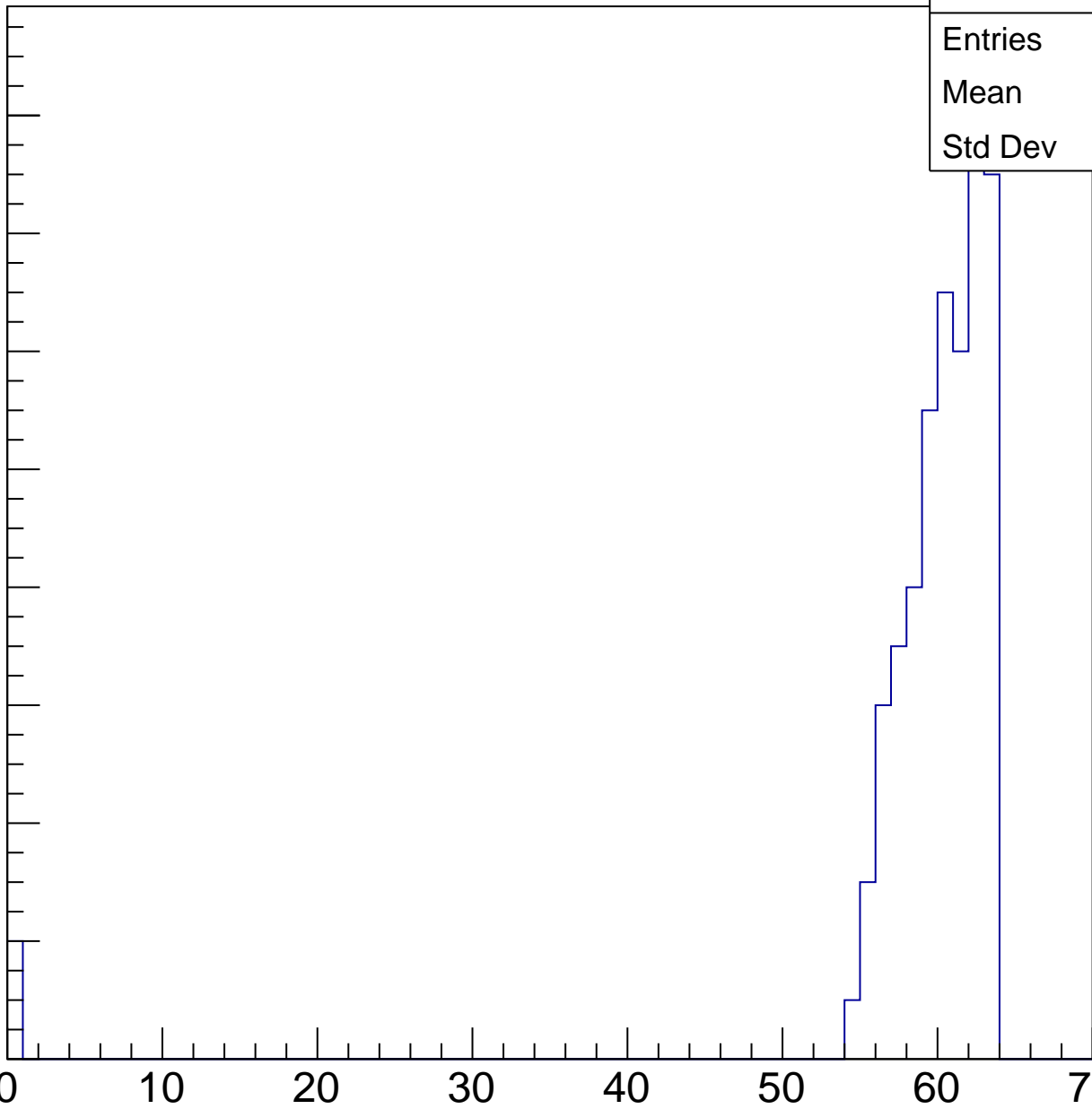
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.72
Std Dev	8.927

ampl

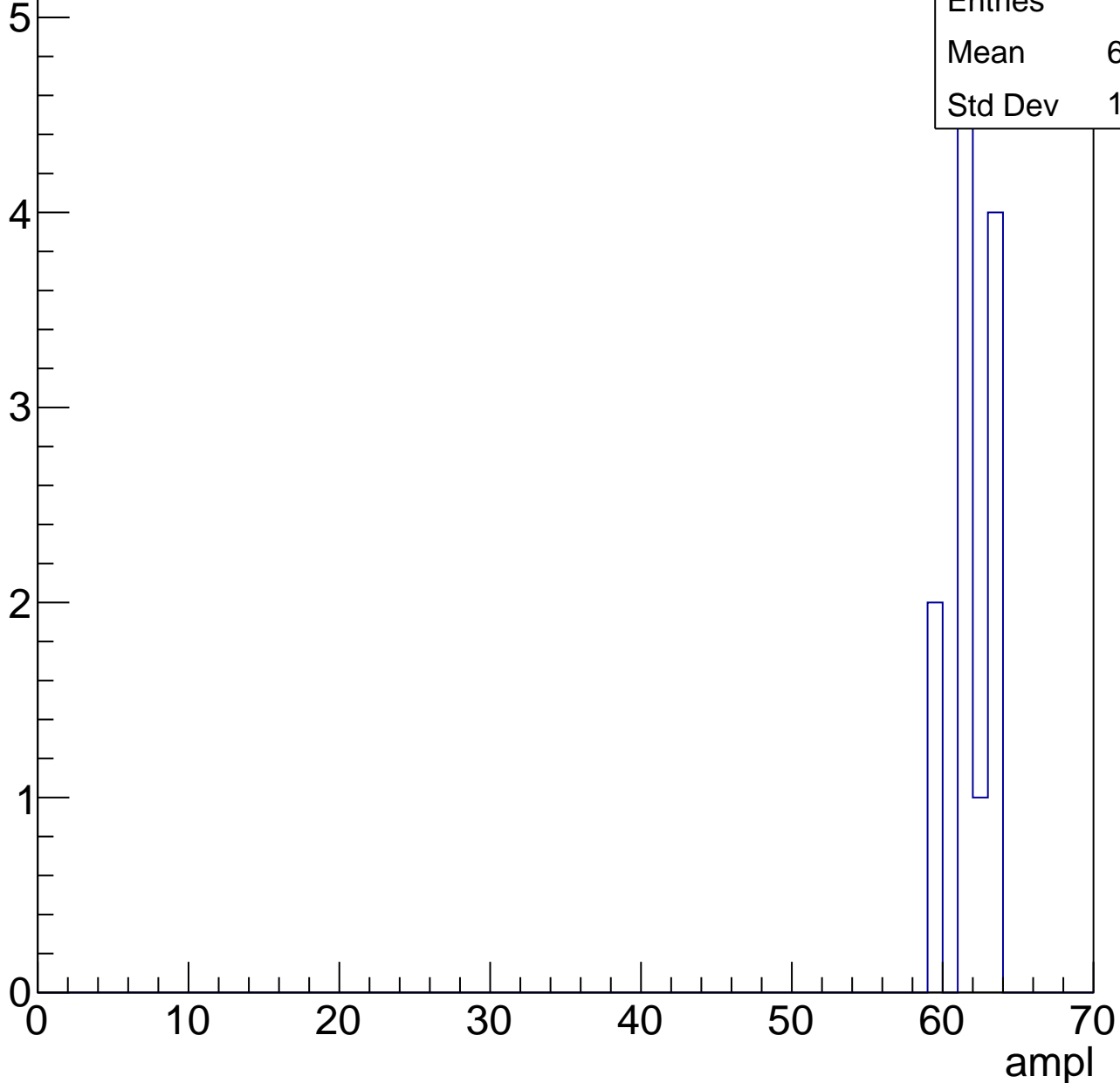


B1L001S, U24-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

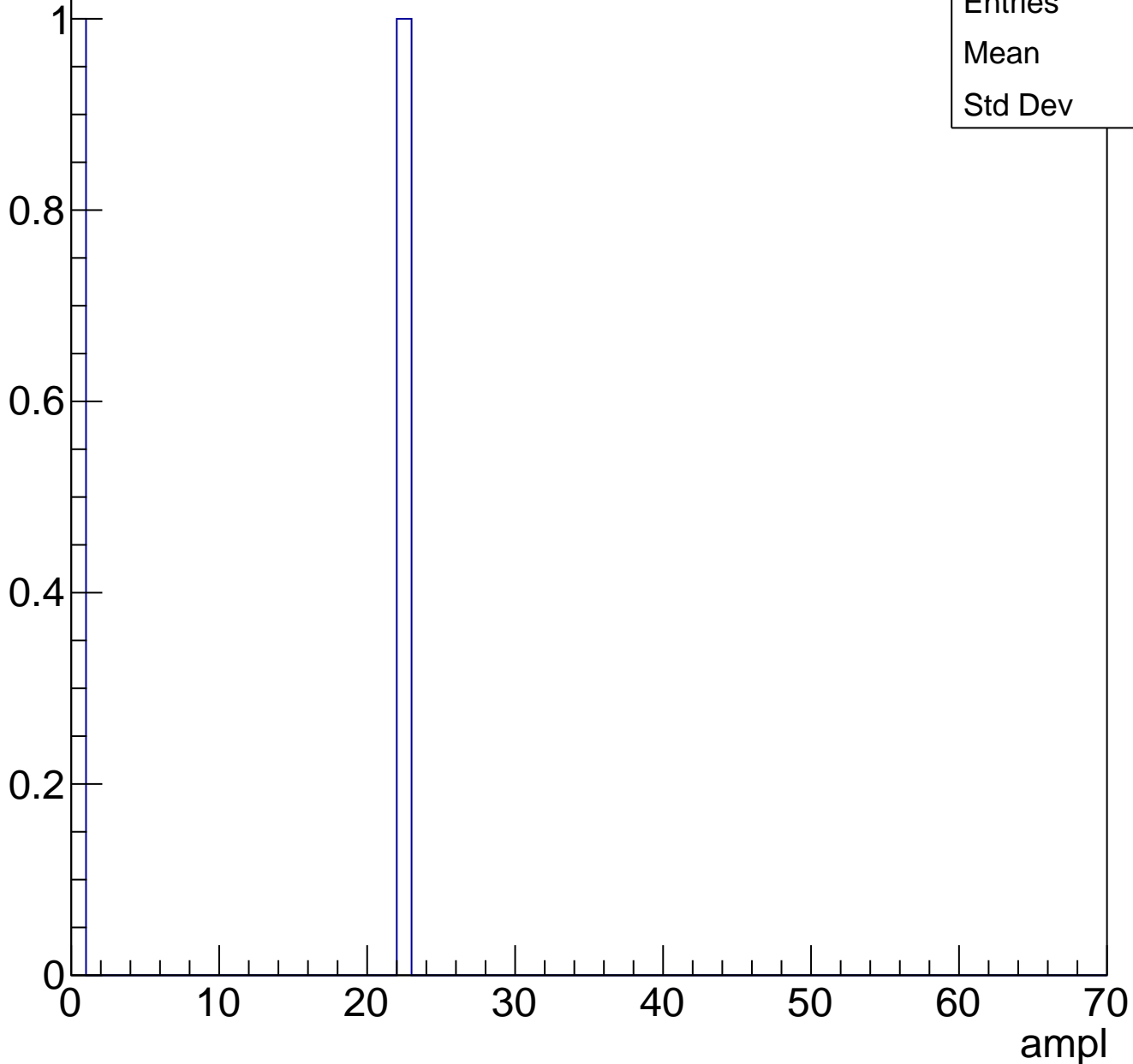
Entries	12
Mean	61.42
Std Dev	1.382



B1L001S, U24-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch105, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.51
Std Dev	3.719

Gaus mean : 29.7167

Gaus Width: 3.3322

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

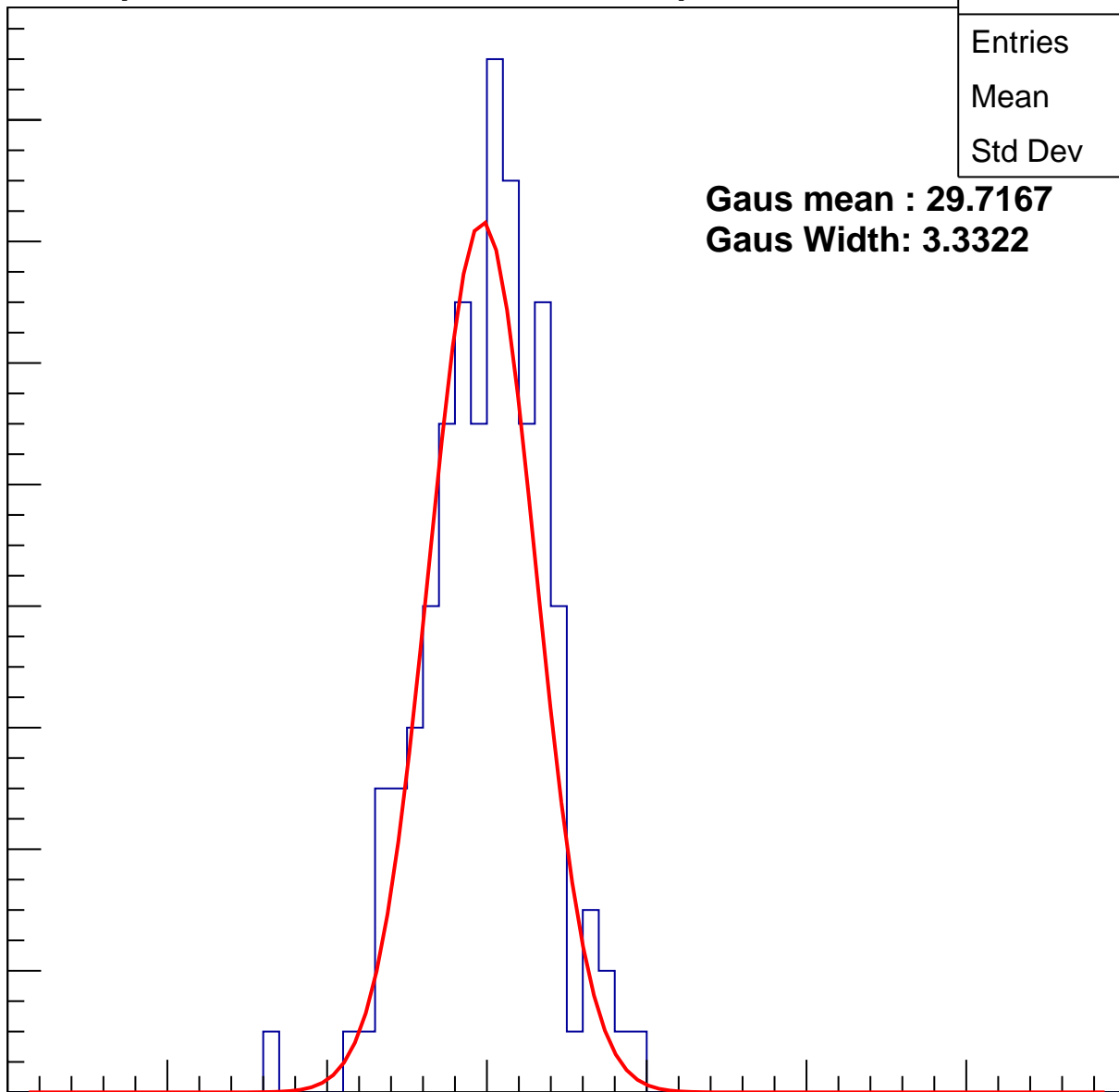
40

50

60

70

ampl



B1L001S, U24-ch105, adc1

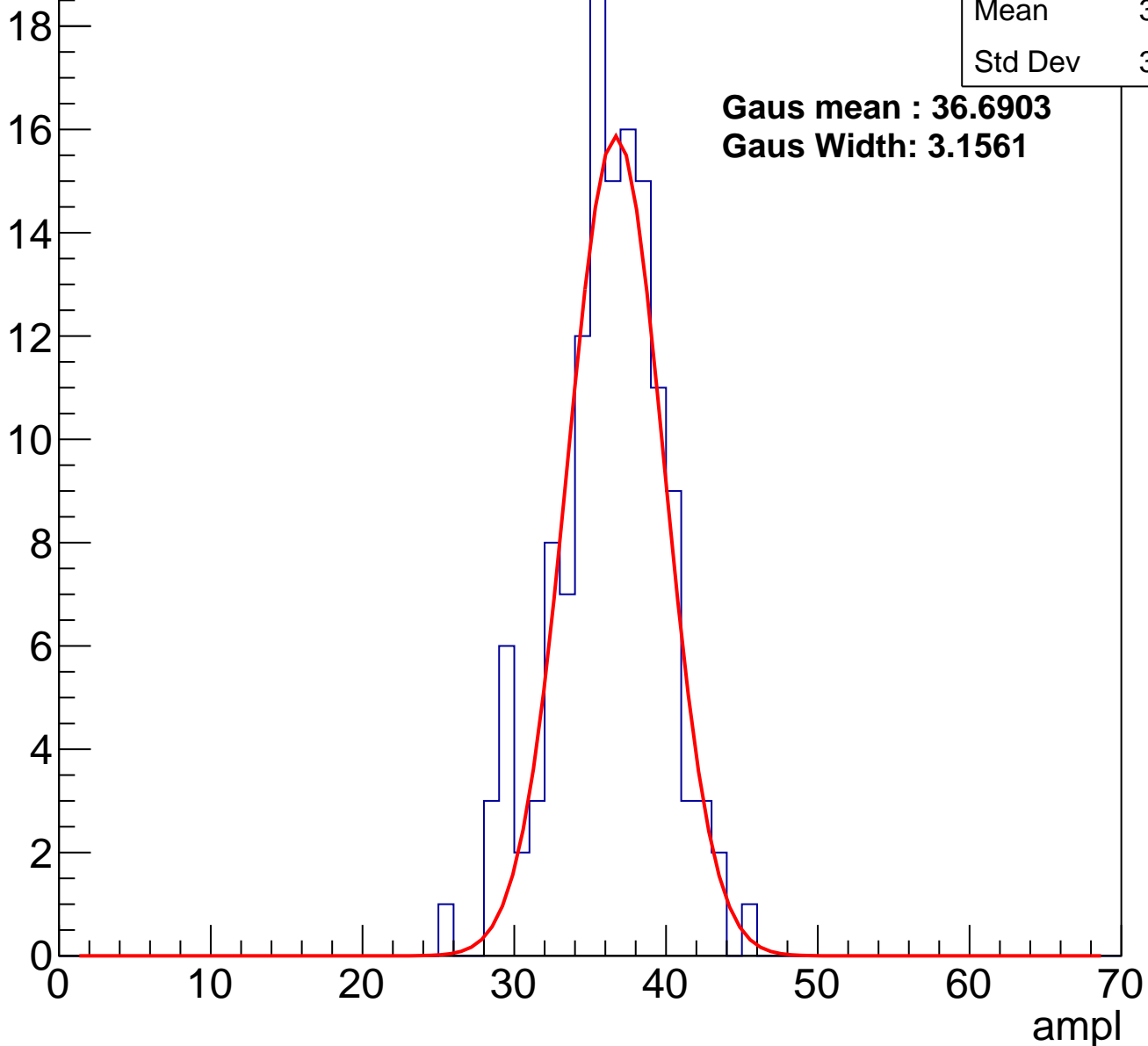
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	35.79
Std Dev	3.522

Gaus mean : 36.6903

Gaus Width: 3.1561

Entry



B1L001S, U24-ch105, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.37
Std Dev	3.538

Gaus mean : 43.4591

Gaus Width: 3.1940

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

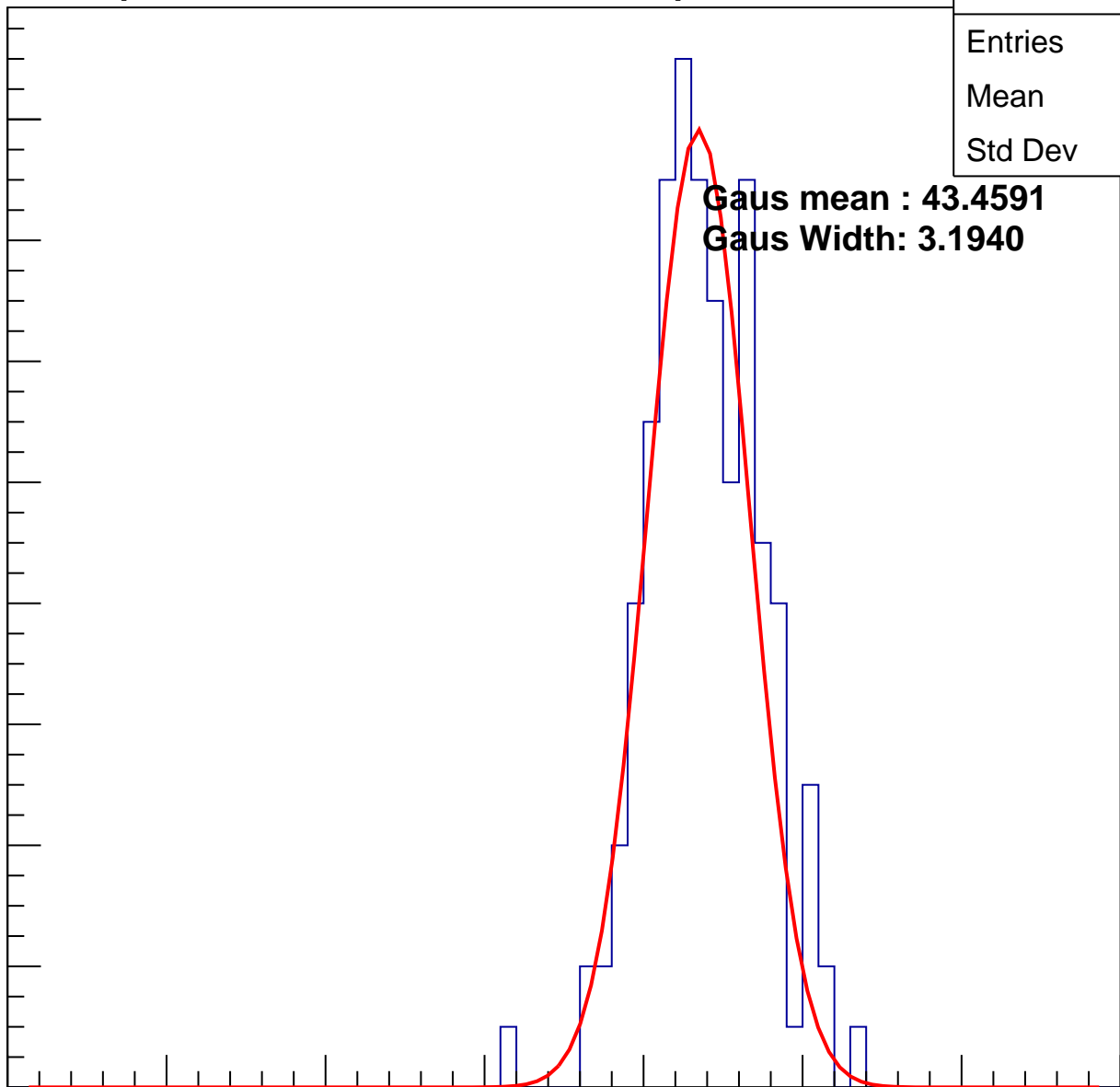
40

50

60

70

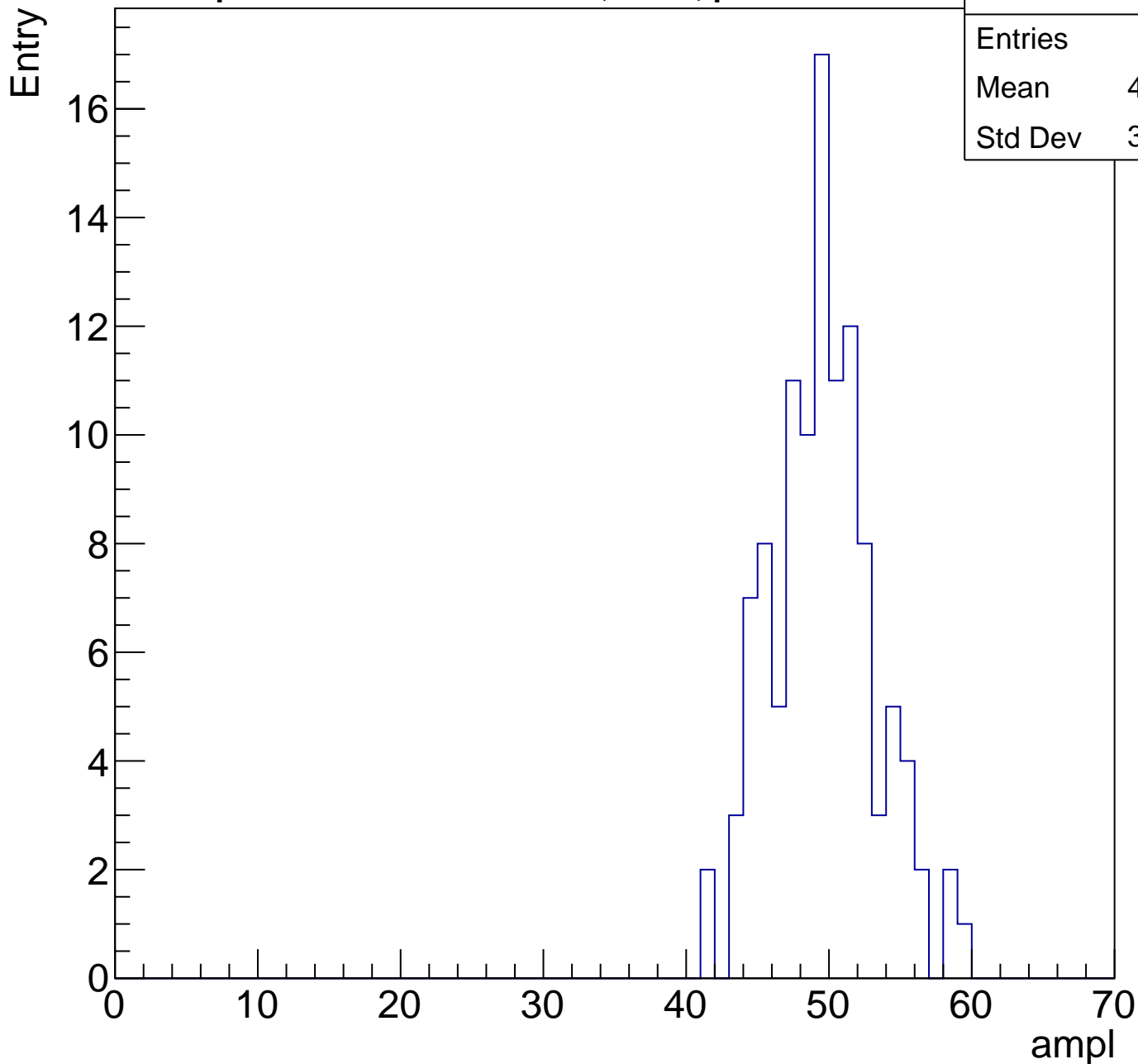
ampl



B1L001S, U24-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

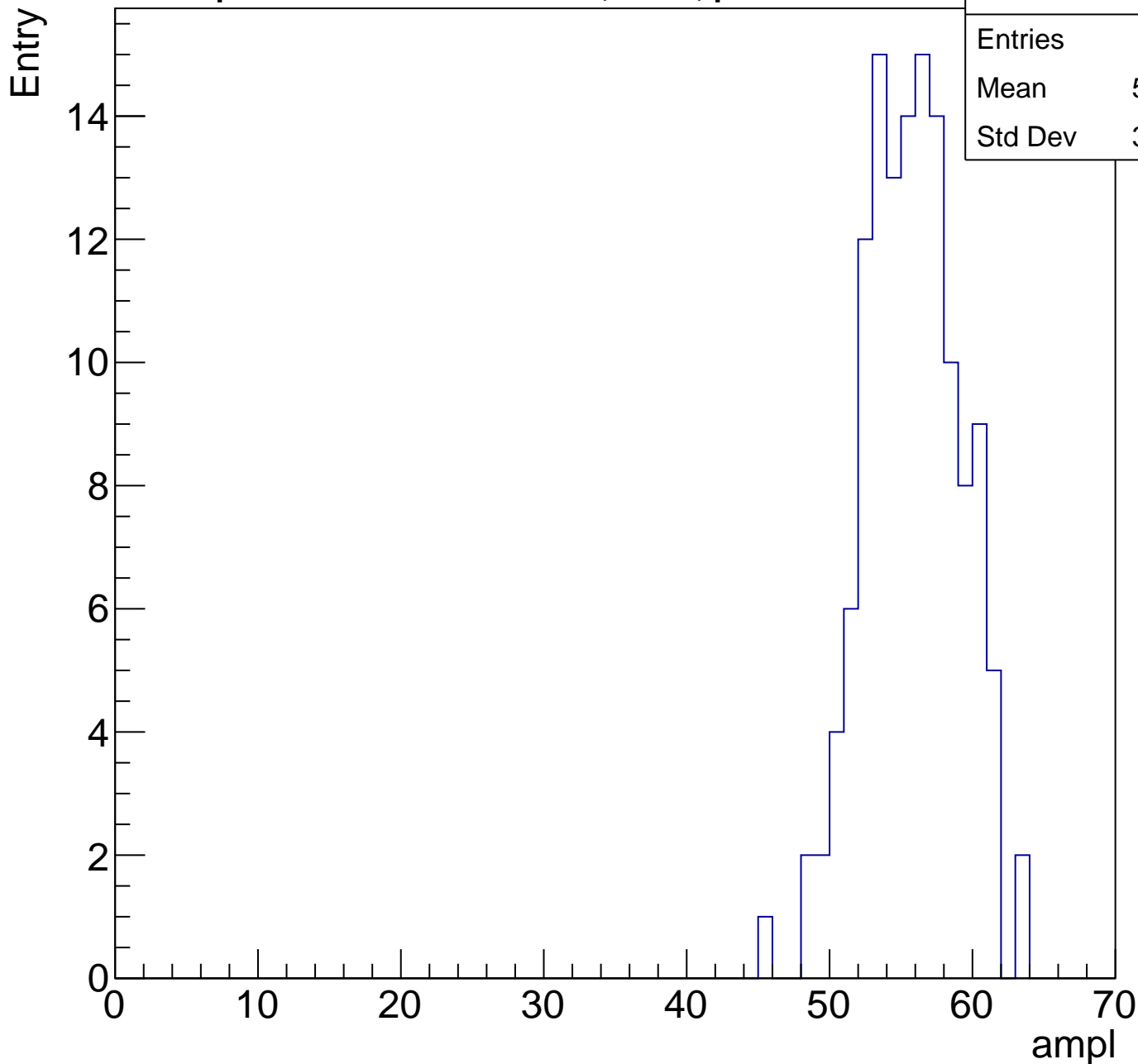
Entries	111
Mean	49.13
Std Dev	3.607



B1L001S, U24-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	55.28
Std Dev	3.324



B1L001S, U24-ch105, adc5

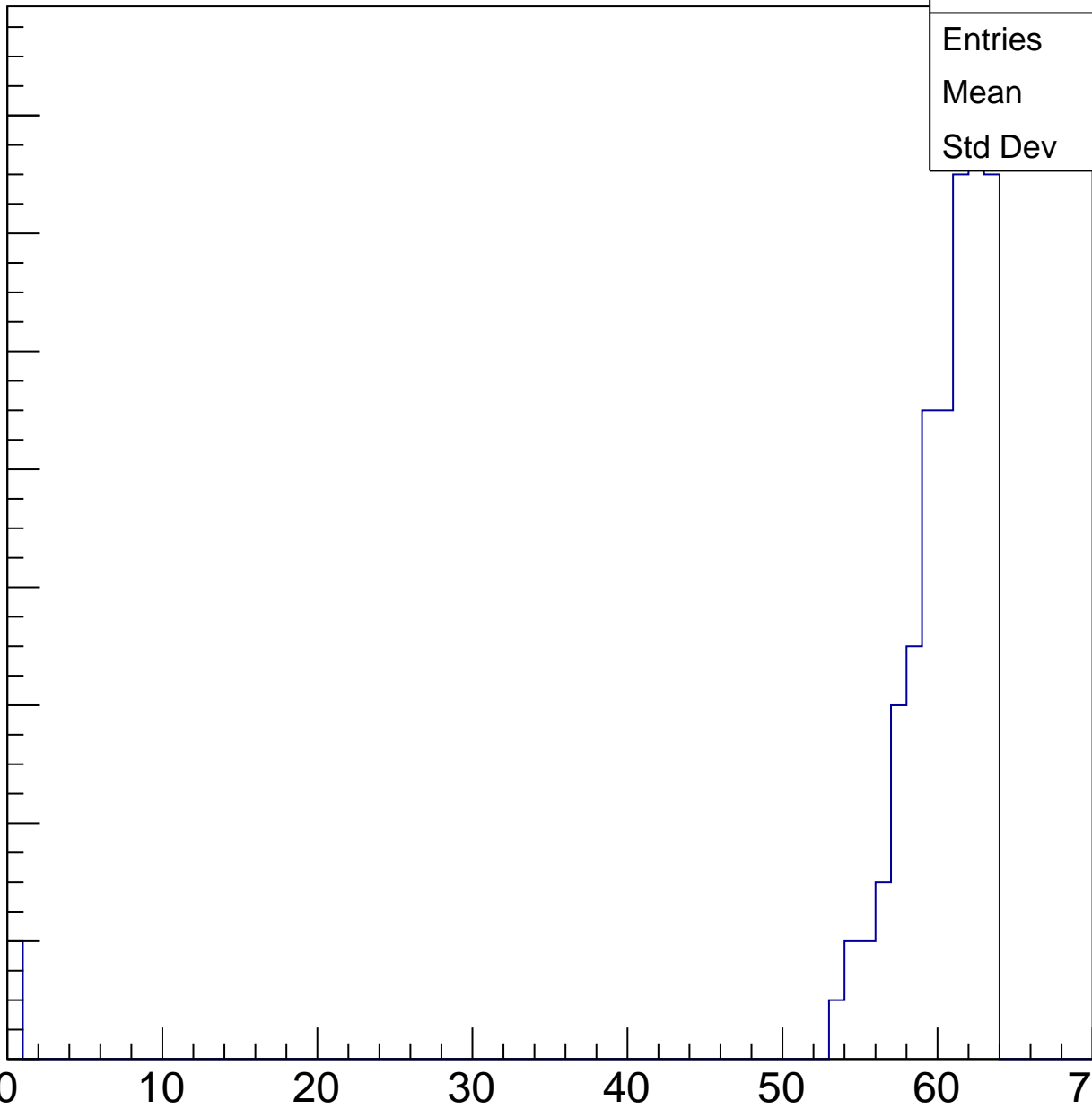
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.8
Std Dev	9.088

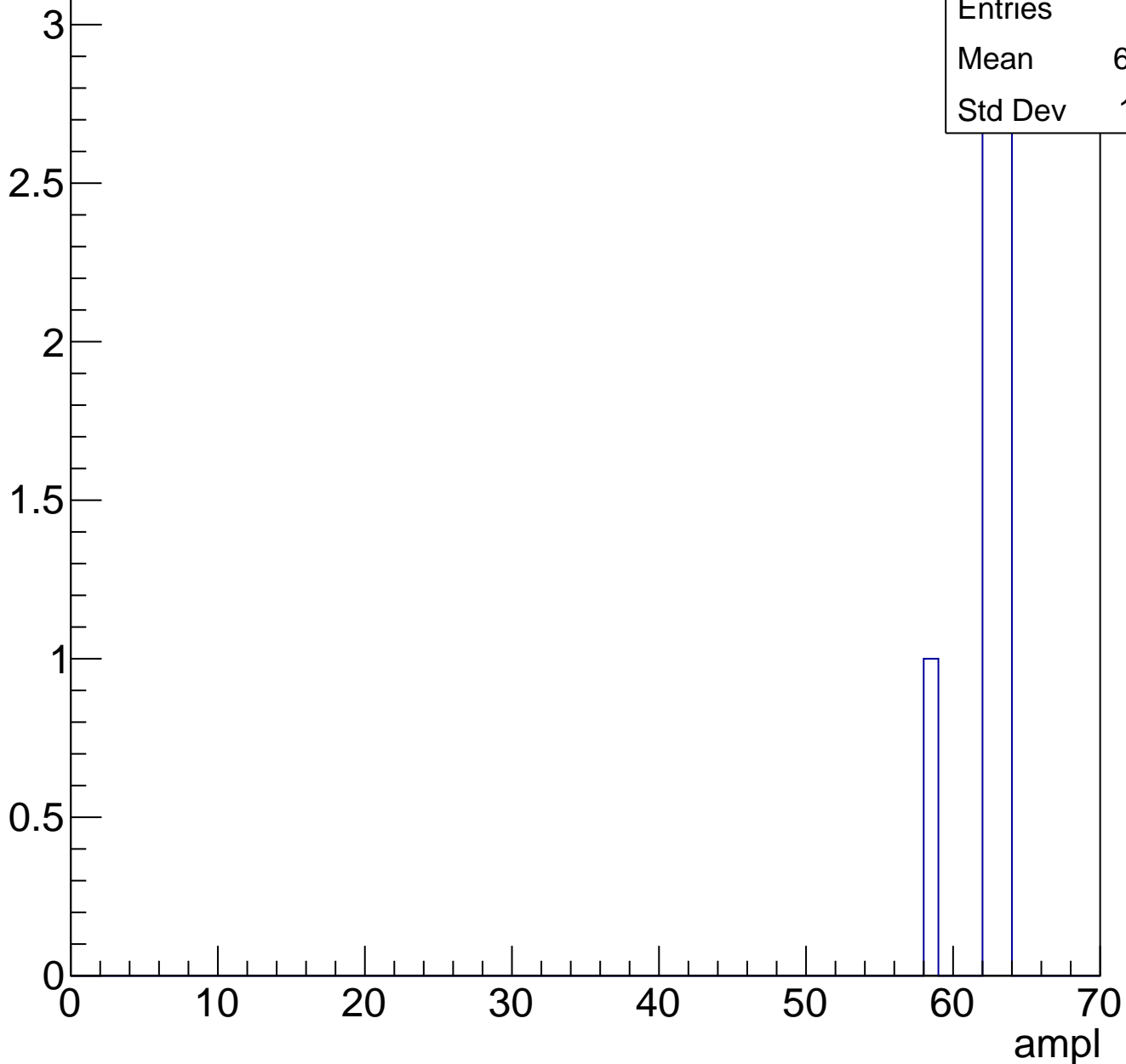
ampl



B1L001S, U24-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch106, adc0

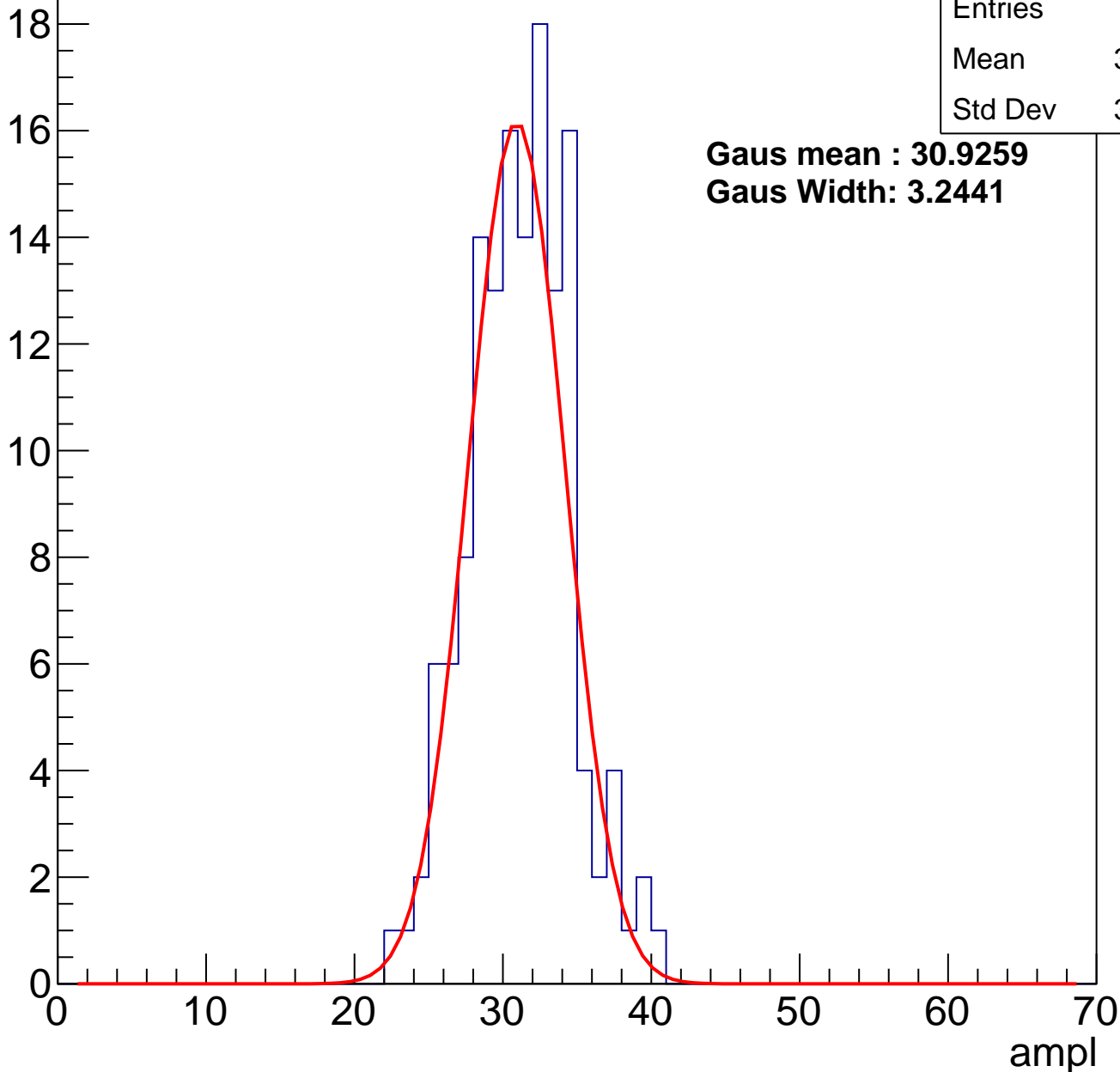
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.73
Std Dev	3.409

Gaus mean : 30.9259

Gaus Width: 3.2441

Entry



B1L001S, U24-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	38.12
Std Dev	3.704

Gaus mean : 38.7749

Gaus Width: 3.6316

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

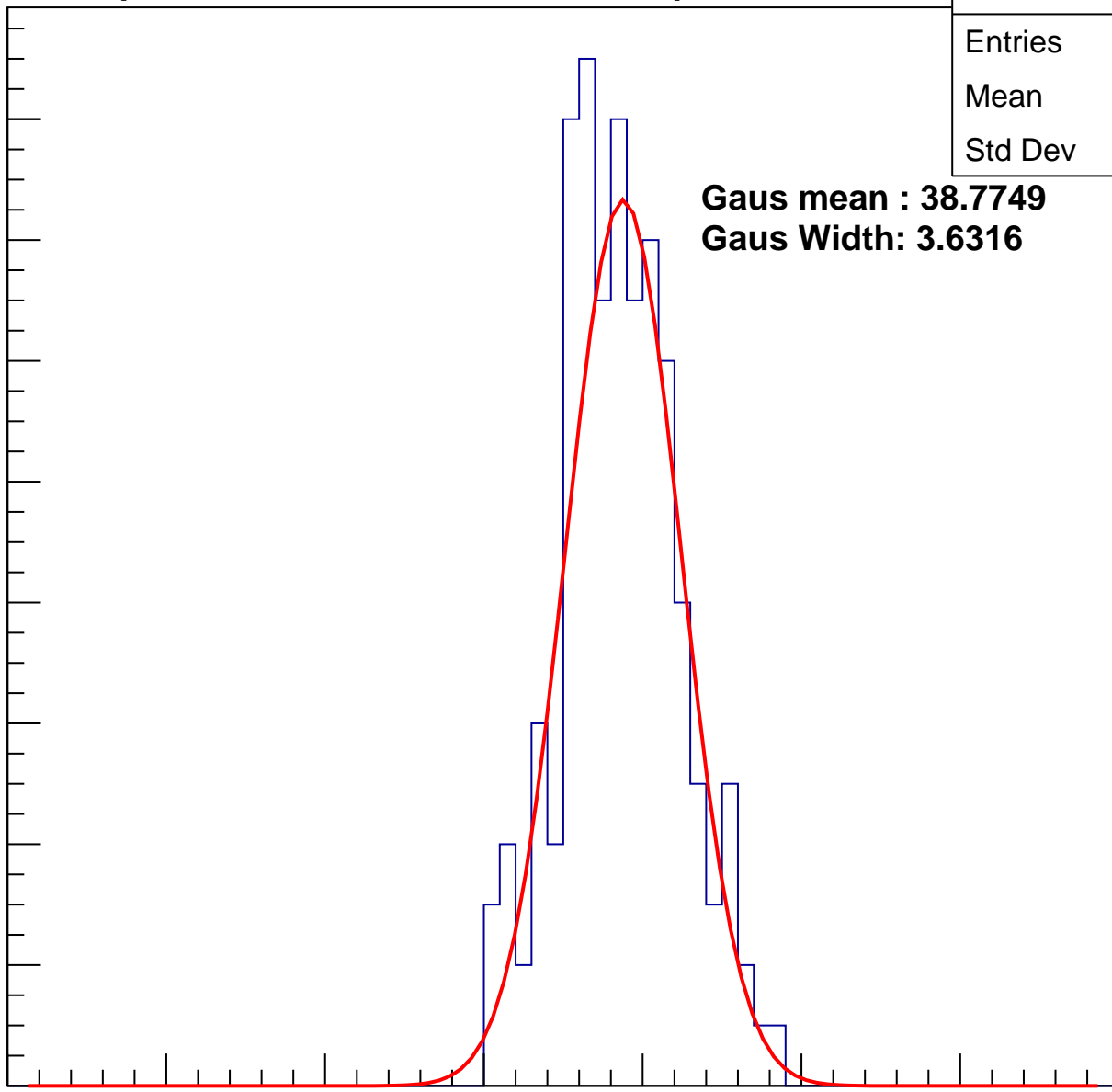
40

50

60

70

ampl



B1L001S, U24-ch106, adc2

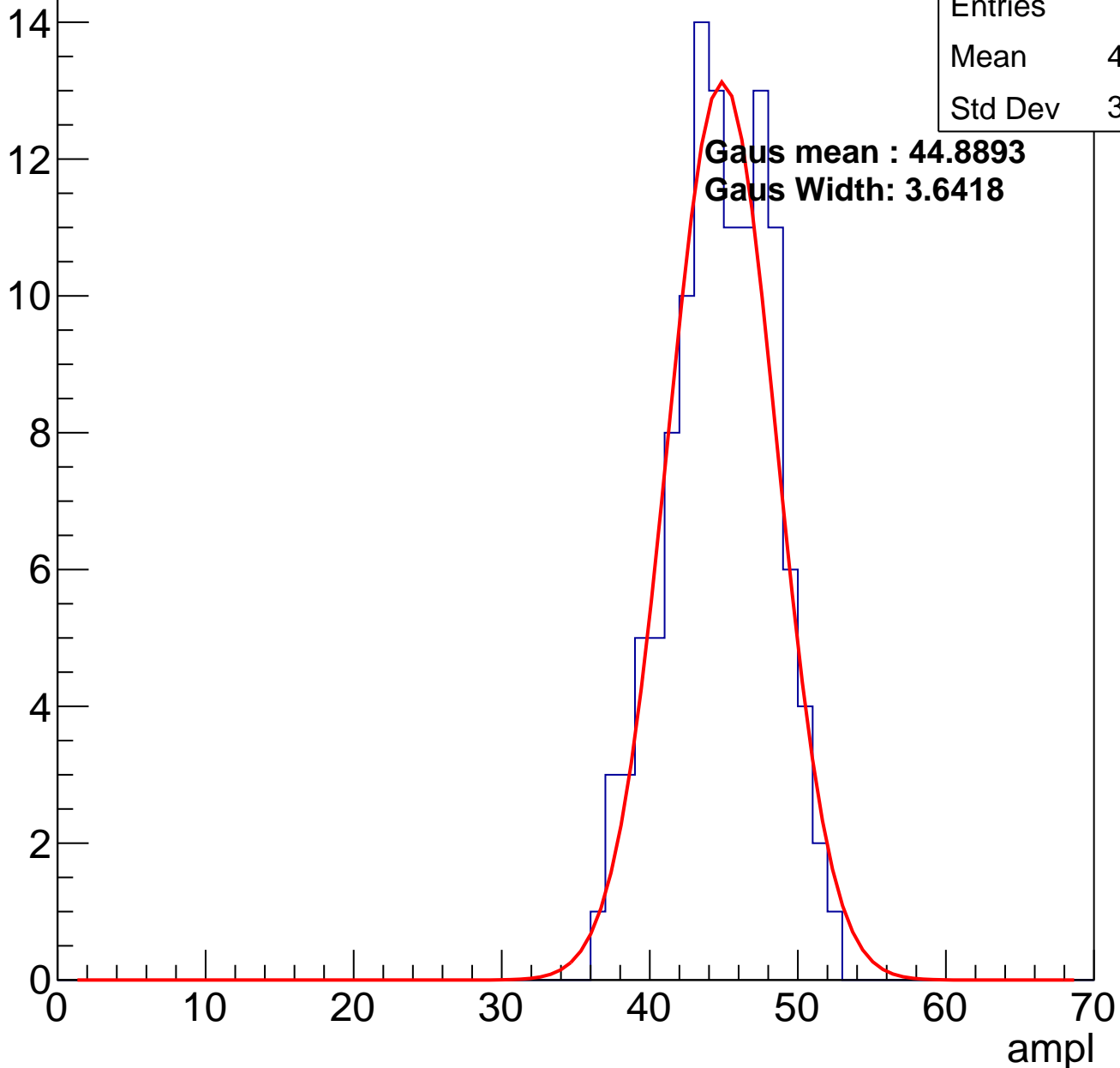
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	44.35
Std Dev	3.449

Gaus mean : 44.8893

Gaus Width: 3.6418

Entry

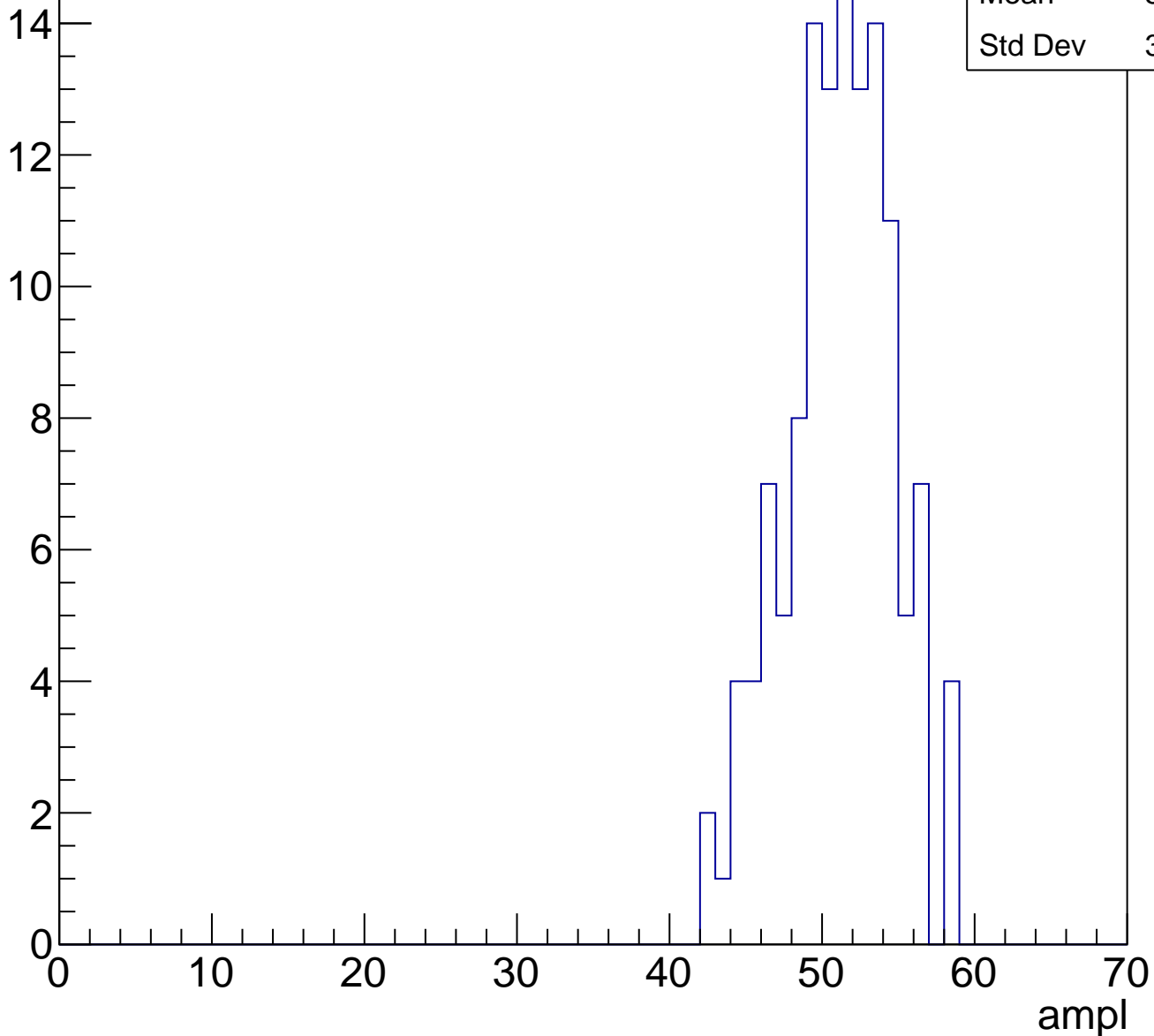


B1L001S, U24-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	50.68
Std Dev	3.513

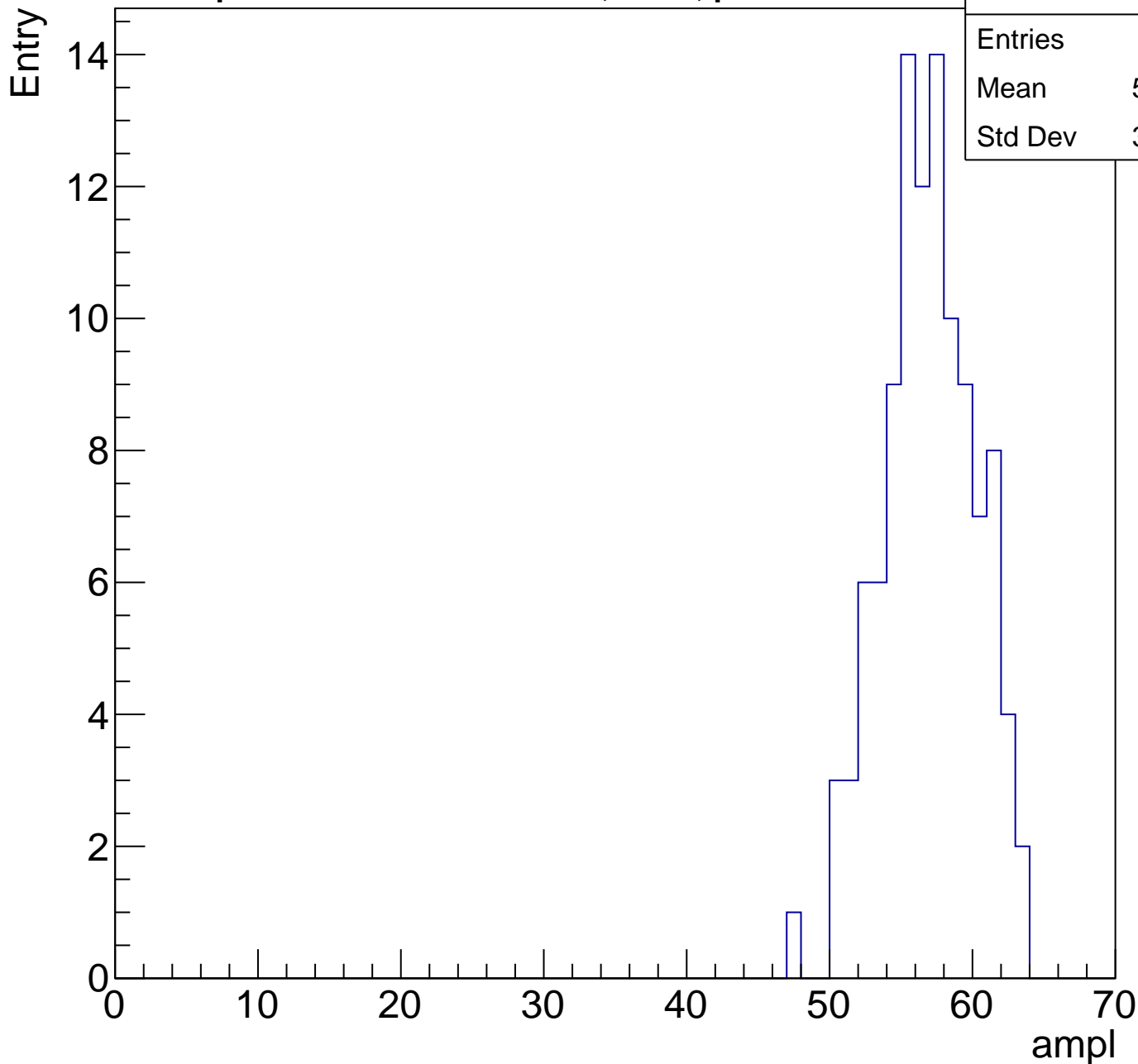
Entry



B1L001S, U24-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	56.47
Std Dev	3.233



B1L001S, U24-ch106, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

79

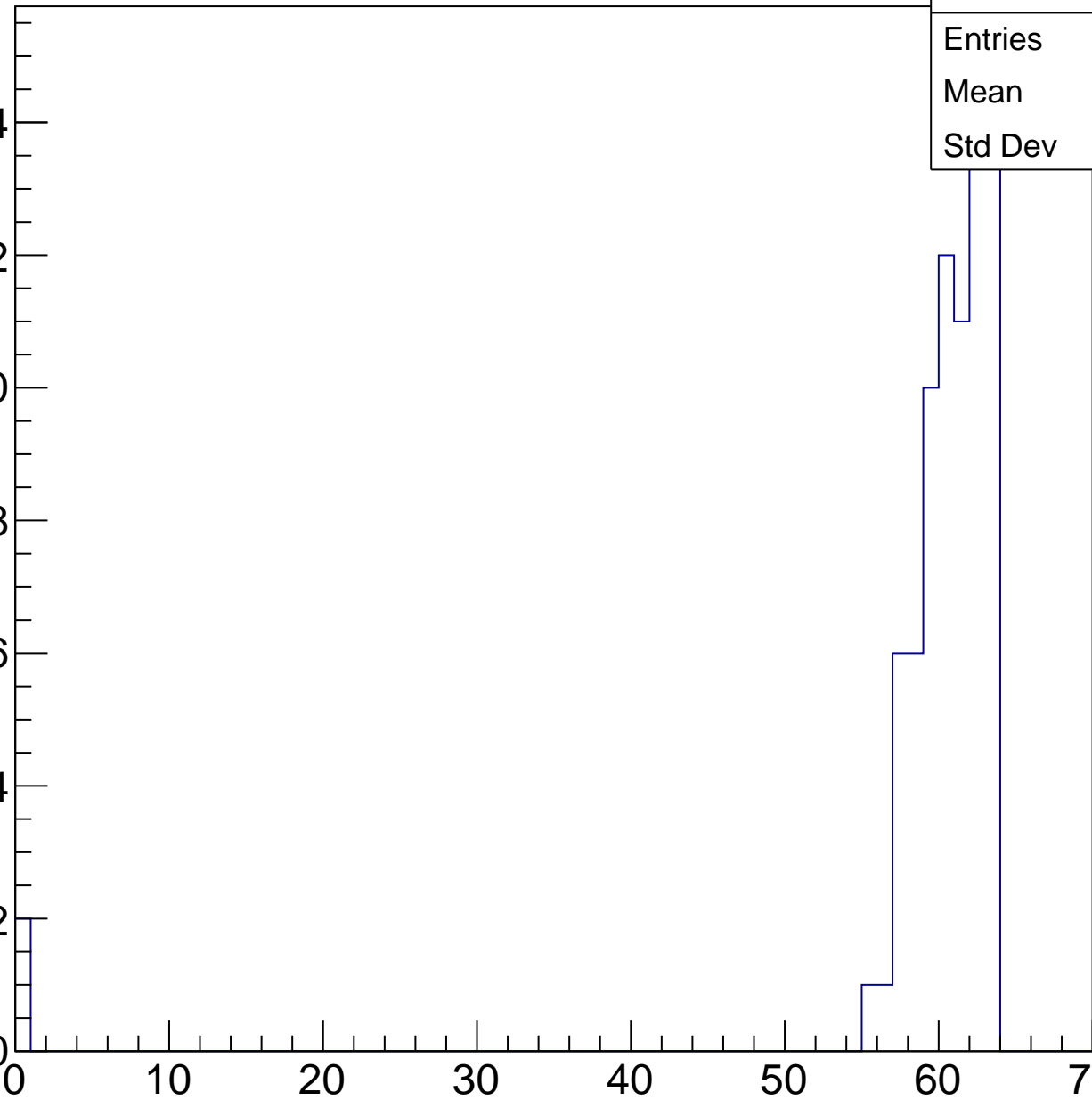
Mean

58.95

Std Dev

9.71

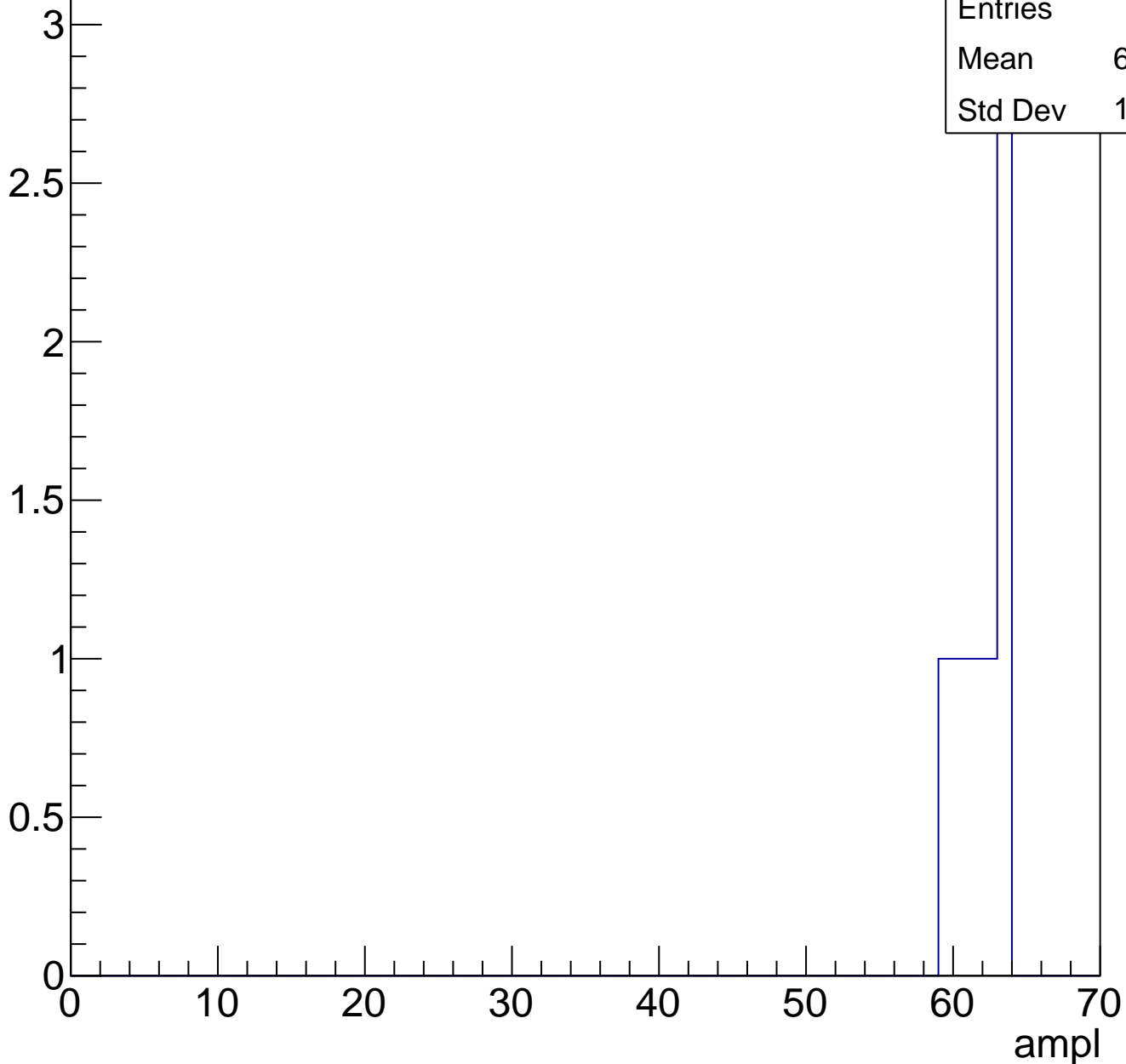
ampl



B1L001S, U24-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23
Std Dev	1

ampl

B1L001S, U24-ch107, adc0

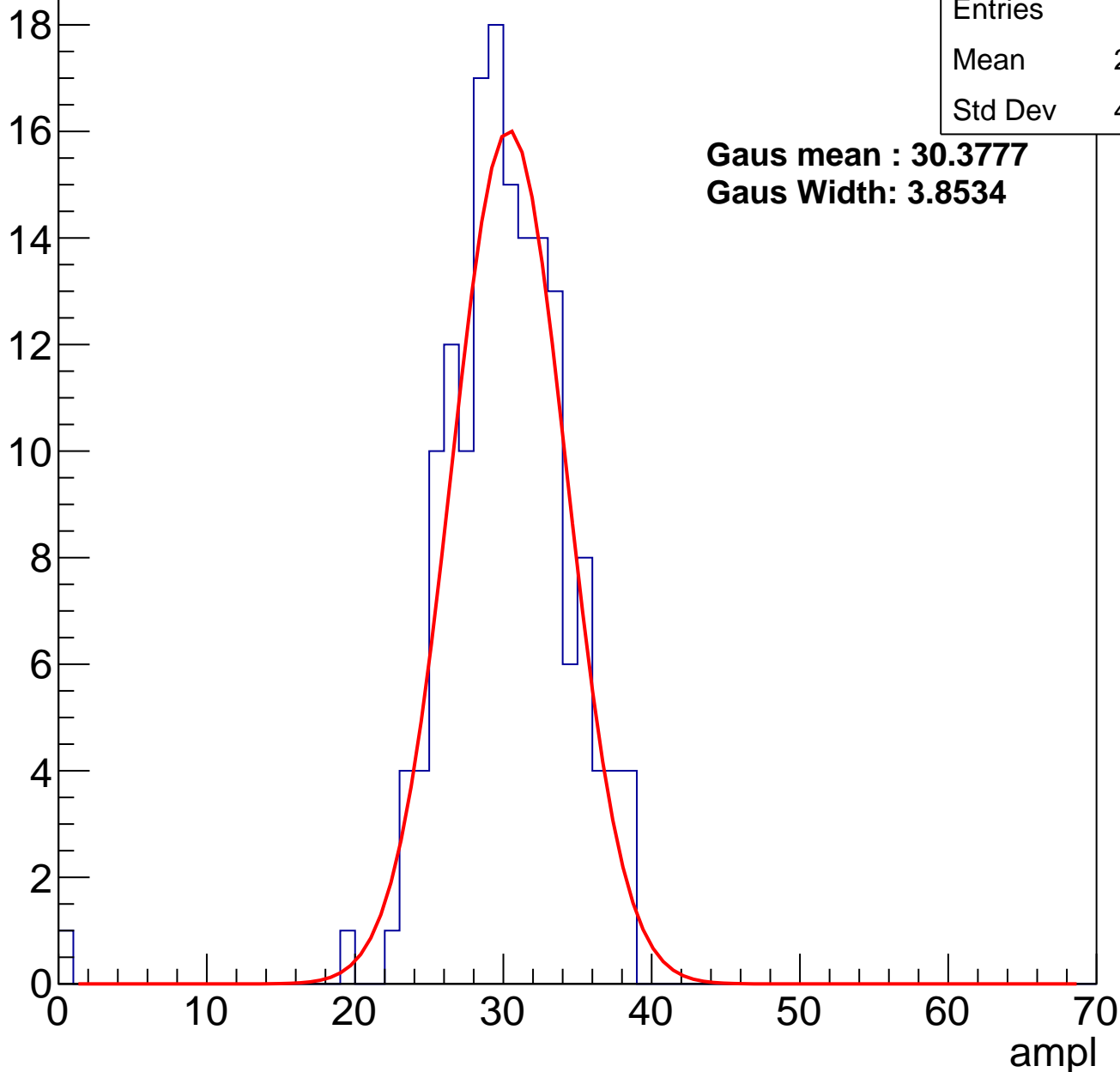
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	29.68
Std Dev	4.405

Gaus mean : 30.3777

Gaus Width: 3.8534

Entry

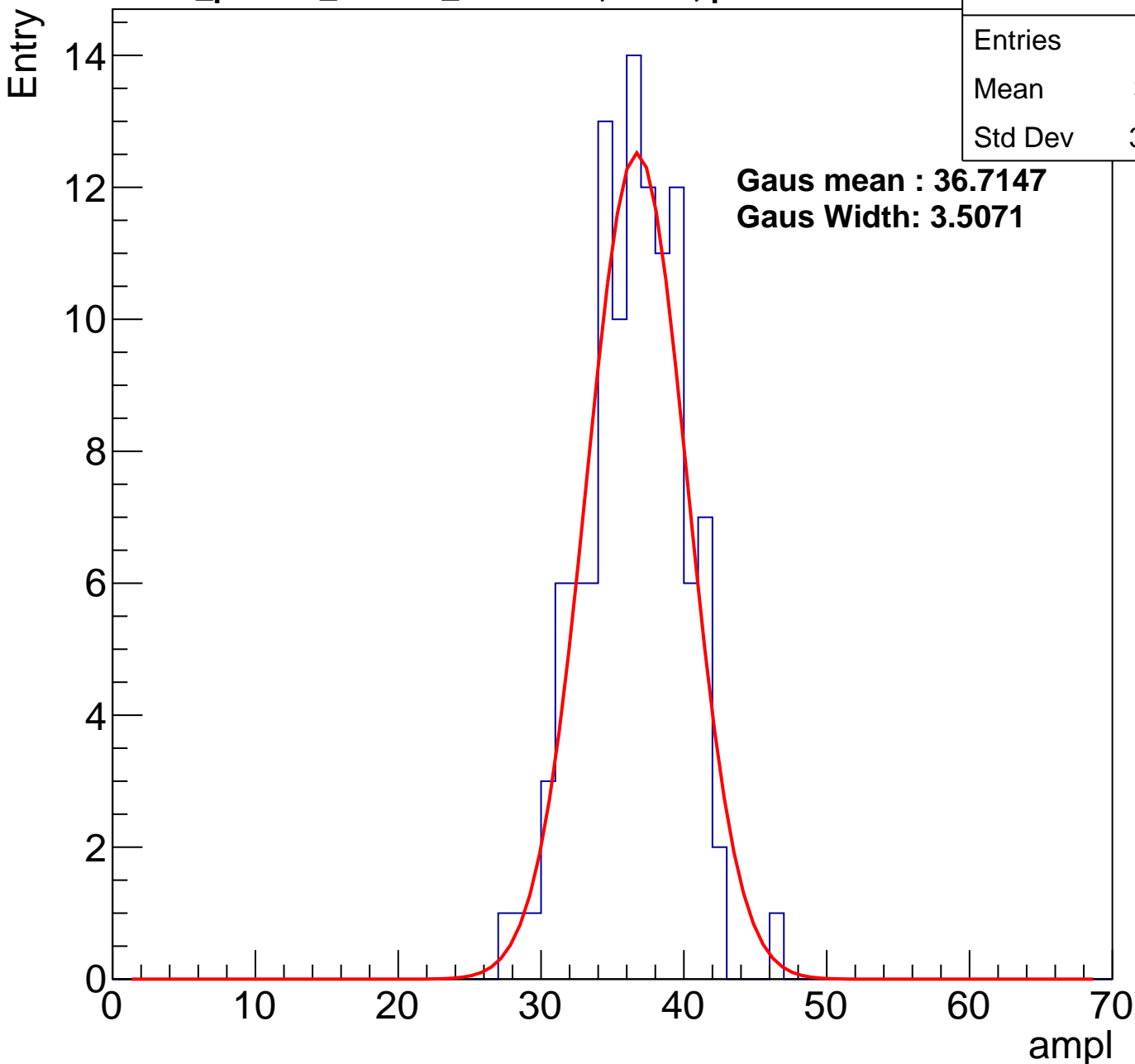


B1L001S, U24-ch107, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	36.01
Std Dev	3.369

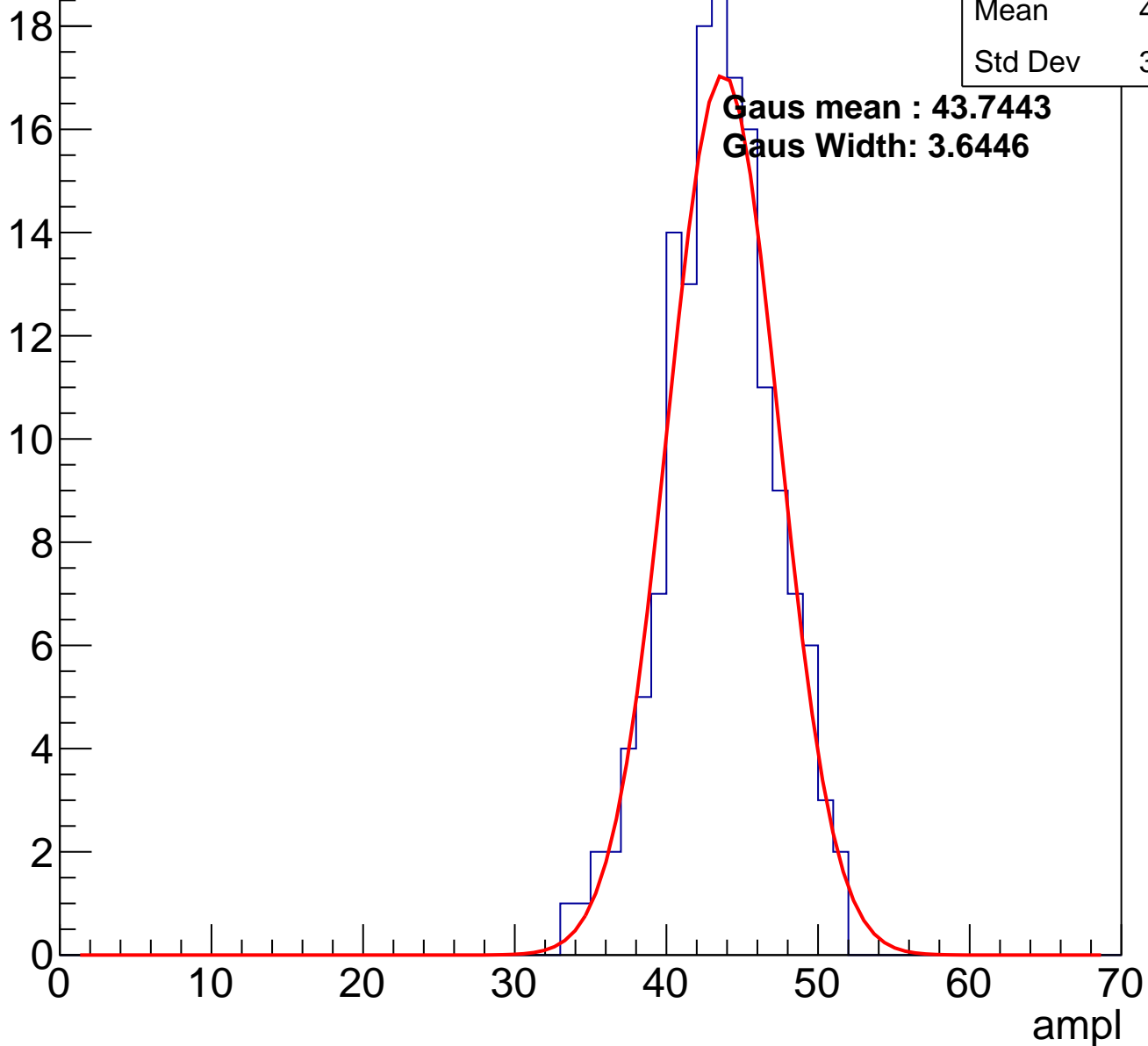
Gaus mean : 36.7147
Gaus Width: 3.5071



B1L001S, U24-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

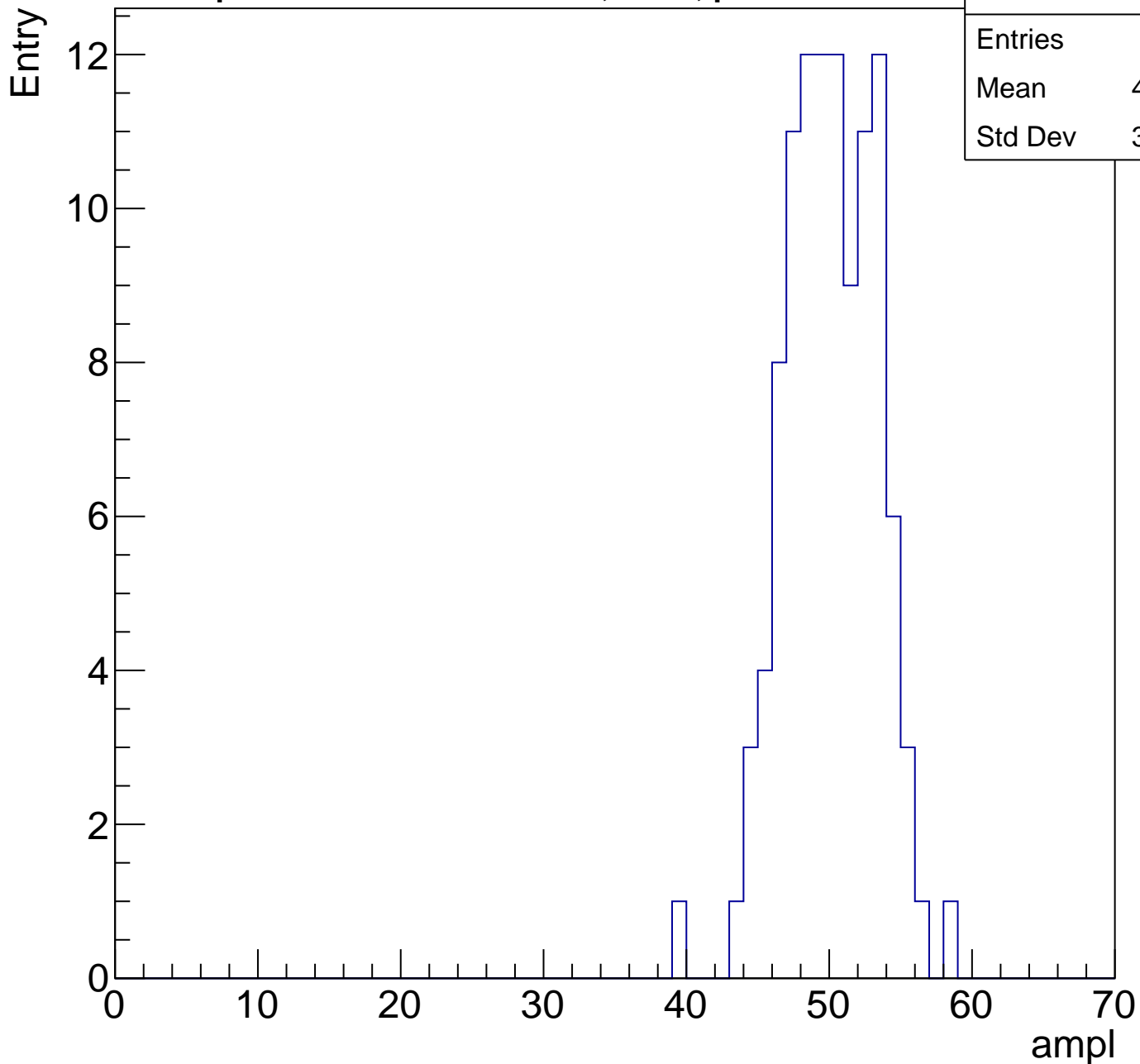
Entry



B1L001S, U24-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

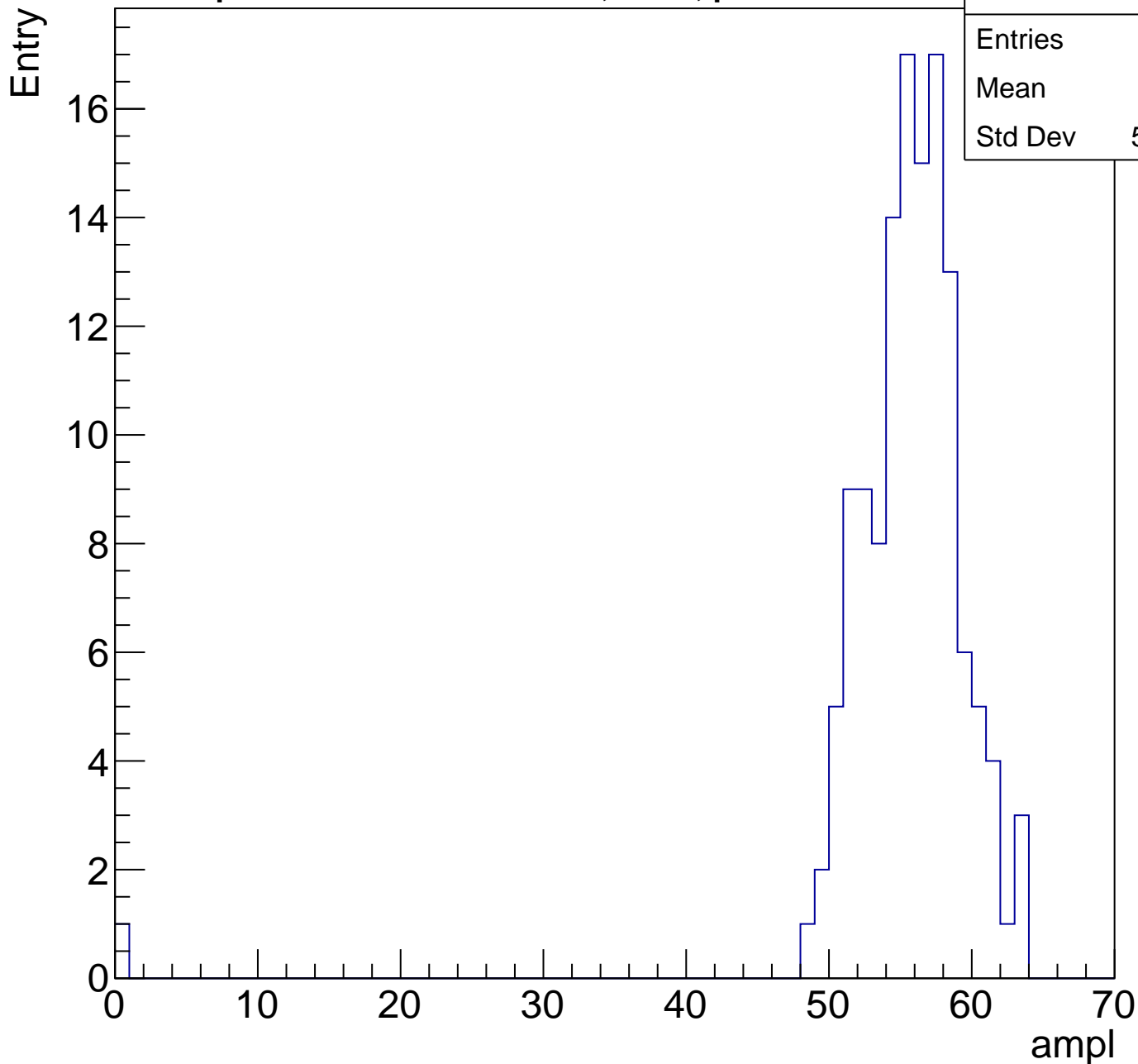
Entries	107
Mean	49.65
Std Dev	3.183



B1L001S, U24-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	55
Std Dev	5.783



B1L001S, U24-ch107, adc5

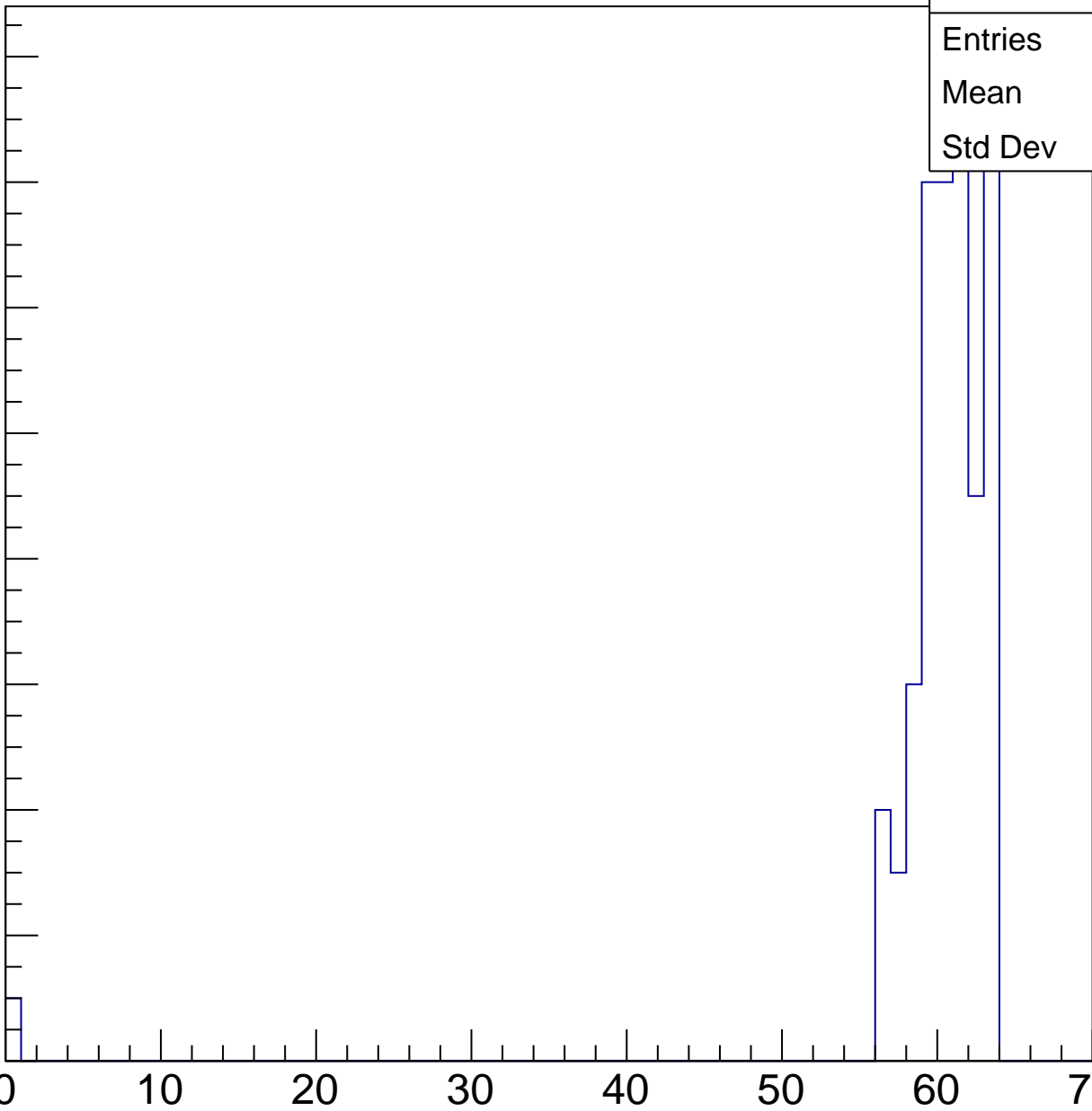
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.63
Std Dev	6.908

ampl

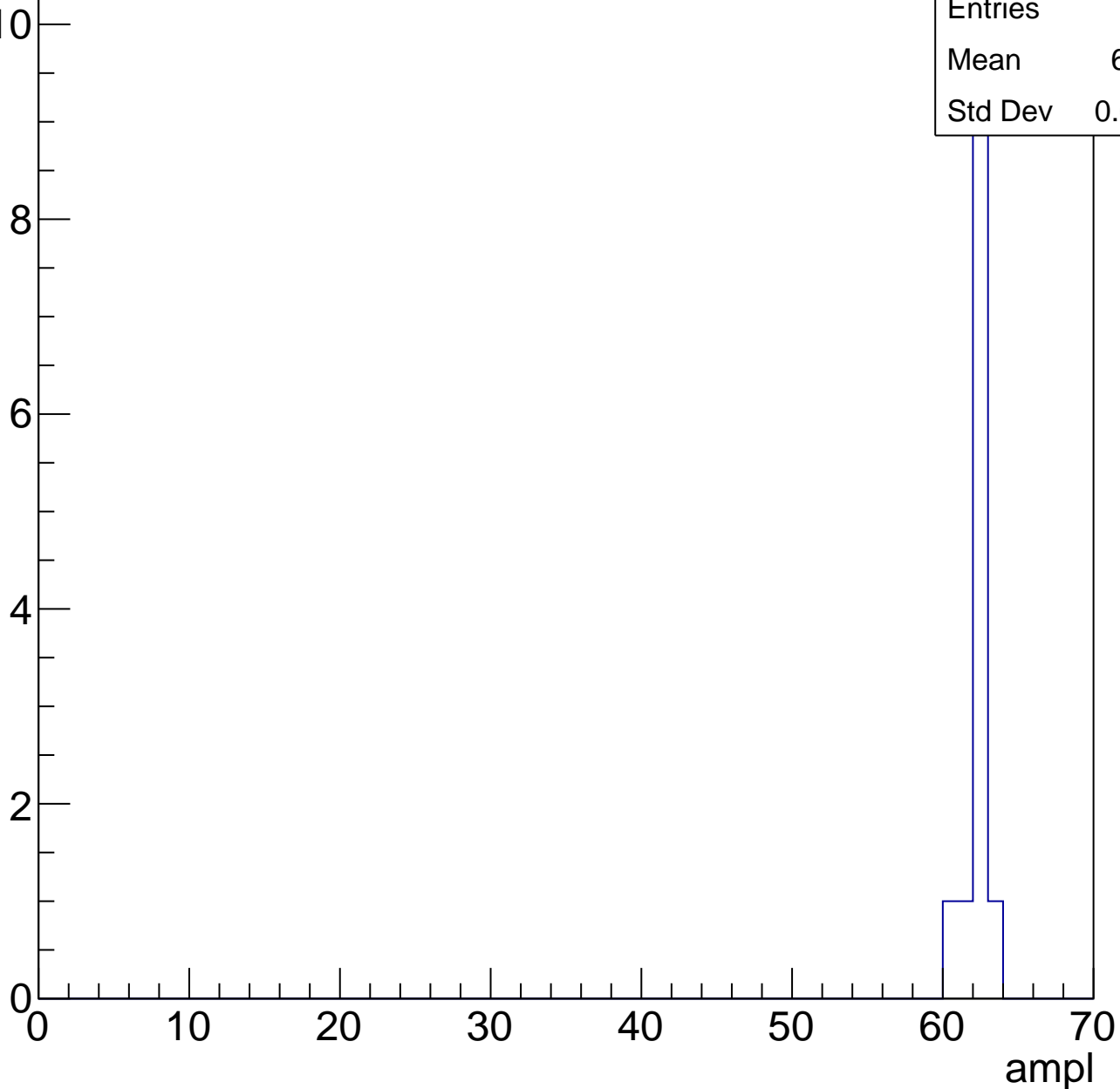


B1L001S, U24-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	61.85
Std Dev	0.6617



B1L001S, U24-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch108, adc0

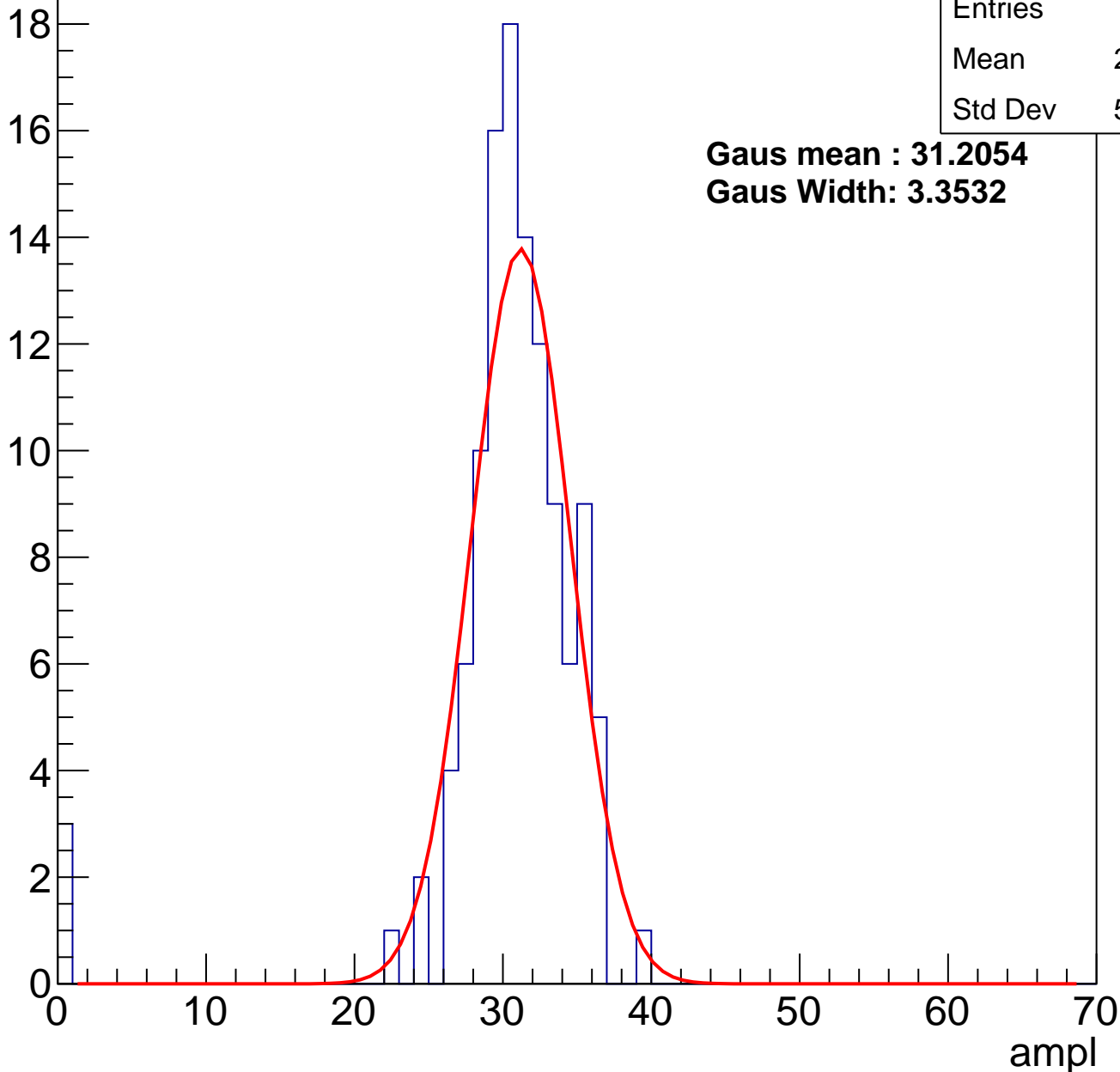
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.94
Std Dev	5.674

Gaus mean : 31.2054

Gaus Width: 3.3532

Entry



B1L001S, U24-ch108, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	37.14
Std Dev	3.523

Gaus mean : 37.6556

Gaus Width: 3.5792

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

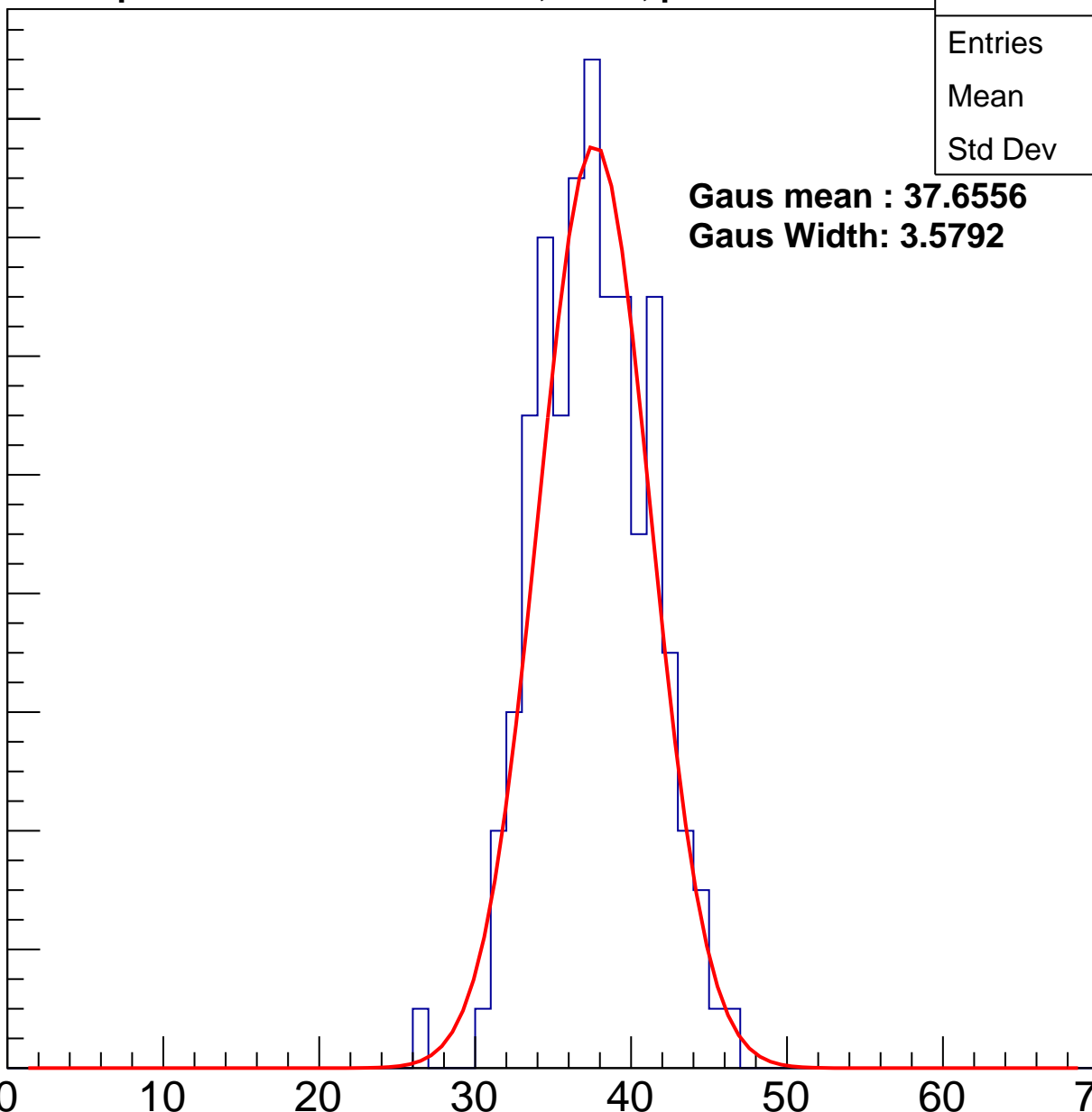
40

50

60

70

ampl



B1L001S, U24-ch108, adc2

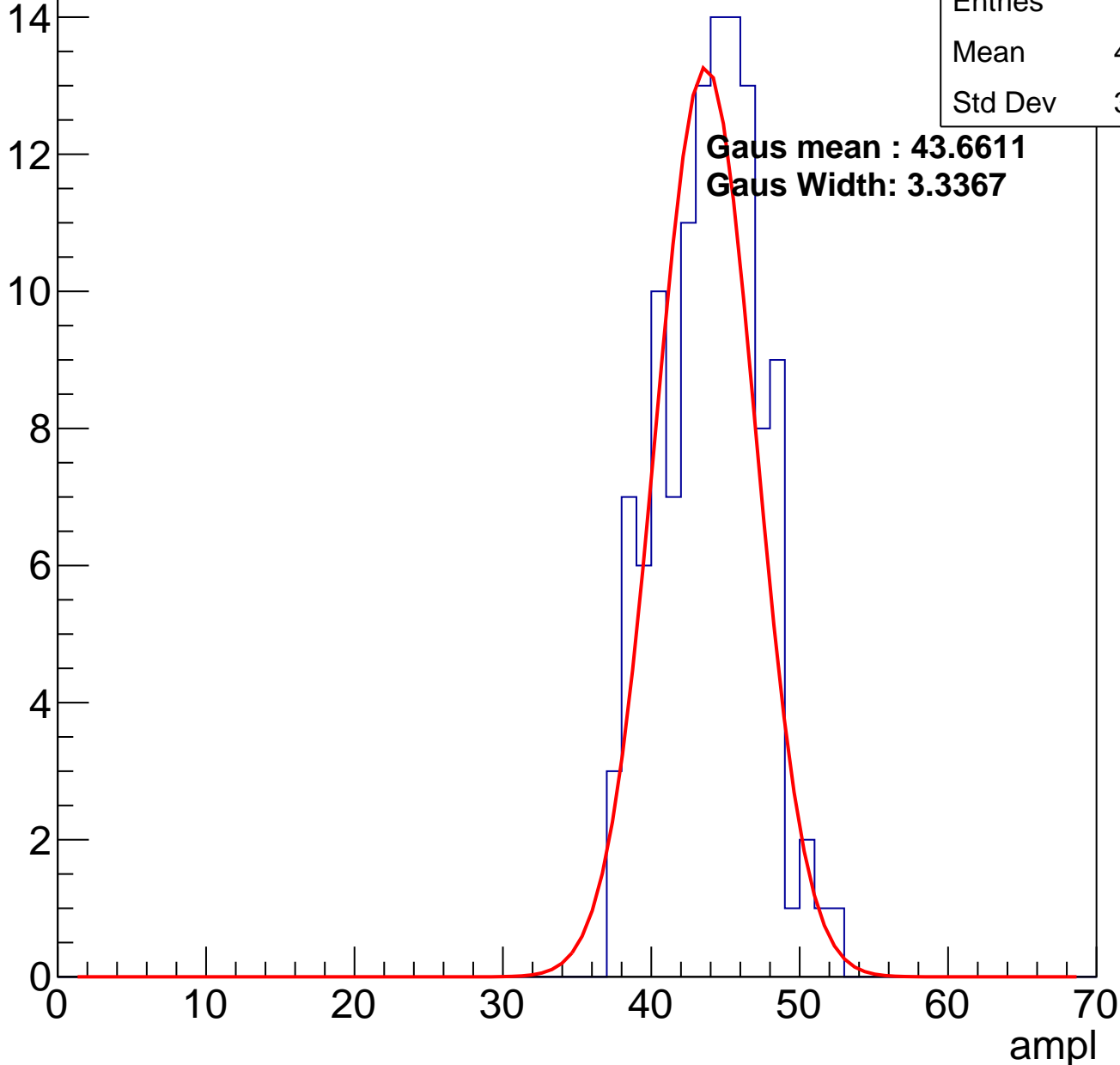
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	43.52
Std Dev	3.284

Gaus mean : 43.6611

Gaus Width: 3.3367

Entry



B1L001S, U24-ch108, adc3

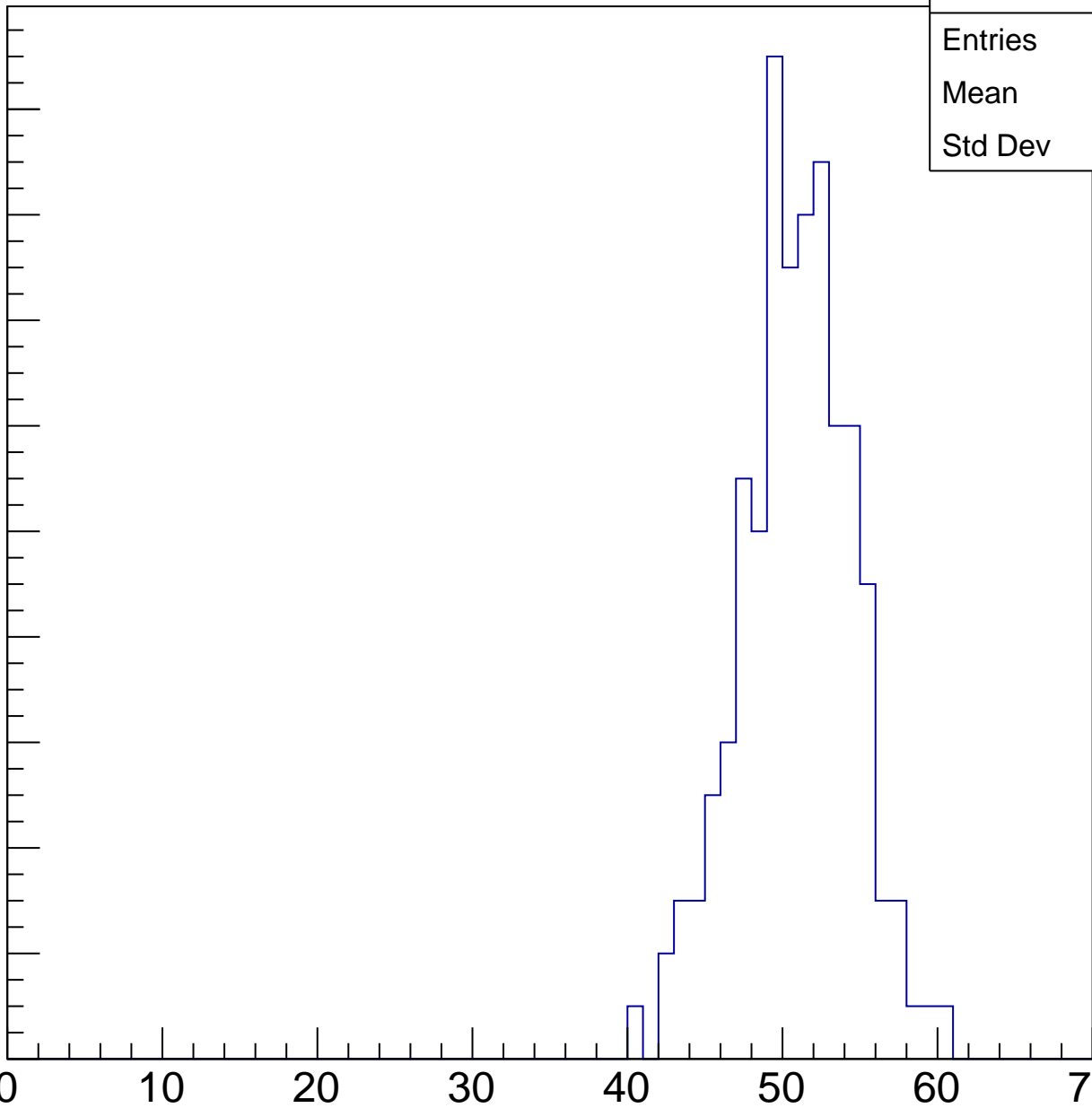
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	150
Mean	50.39
Std Dev	3.604

ampl

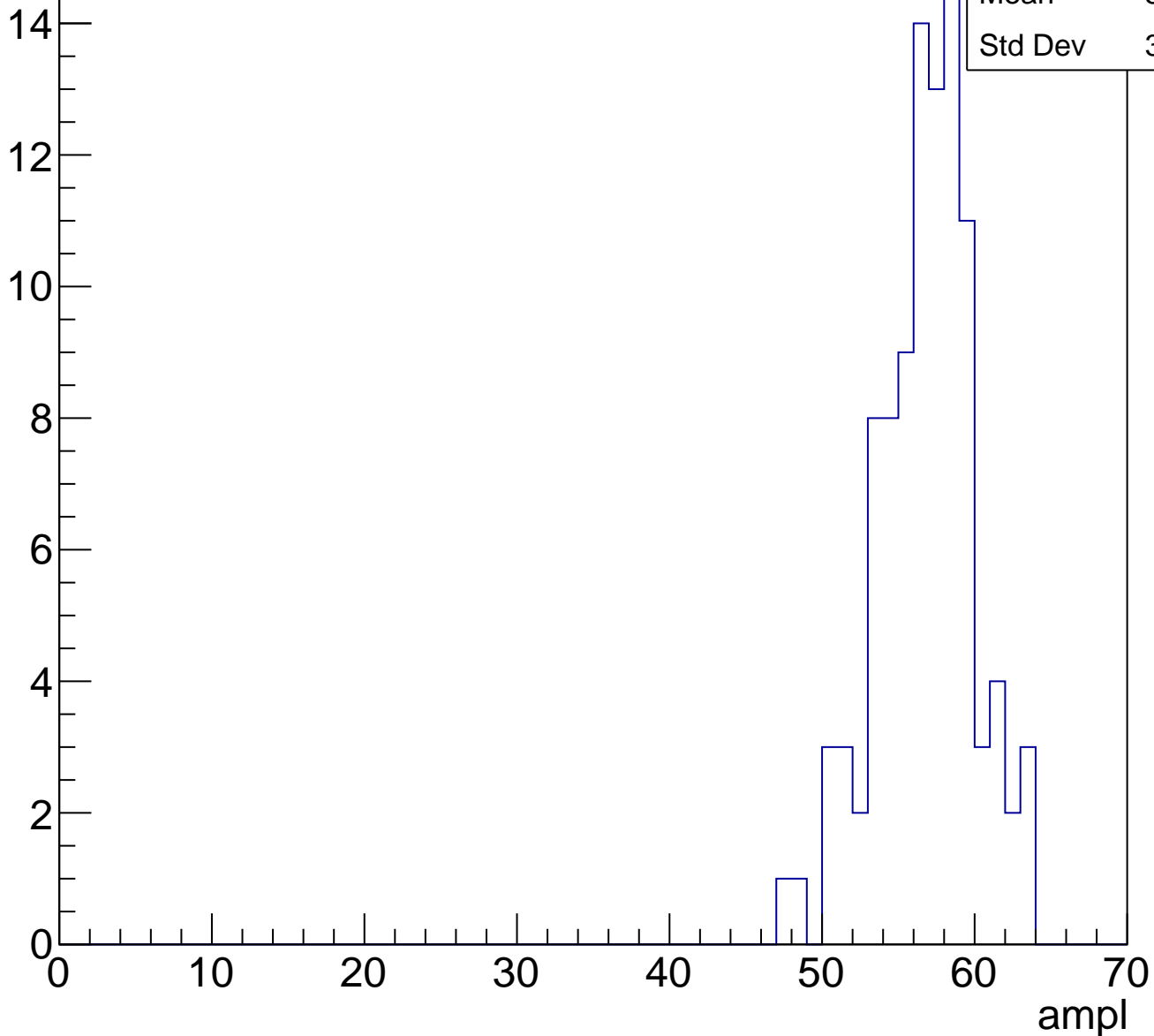


B1L001S, U24-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	56.34
Std Dev	3.163

Entry



B1L001S, U24-ch108, adc5

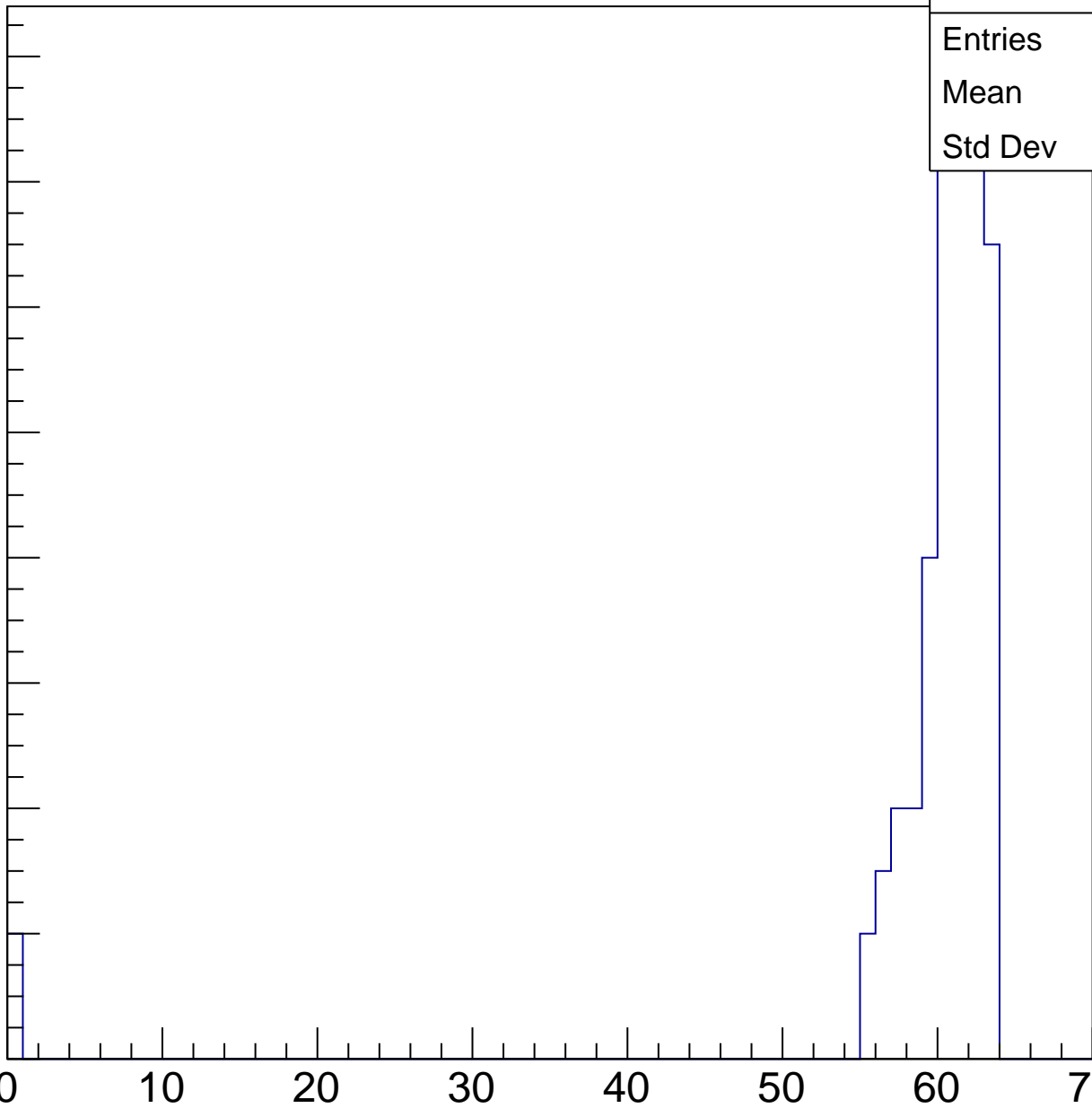
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.99
Std Dev	9.486

ampl



B1L001S, U24-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U24-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

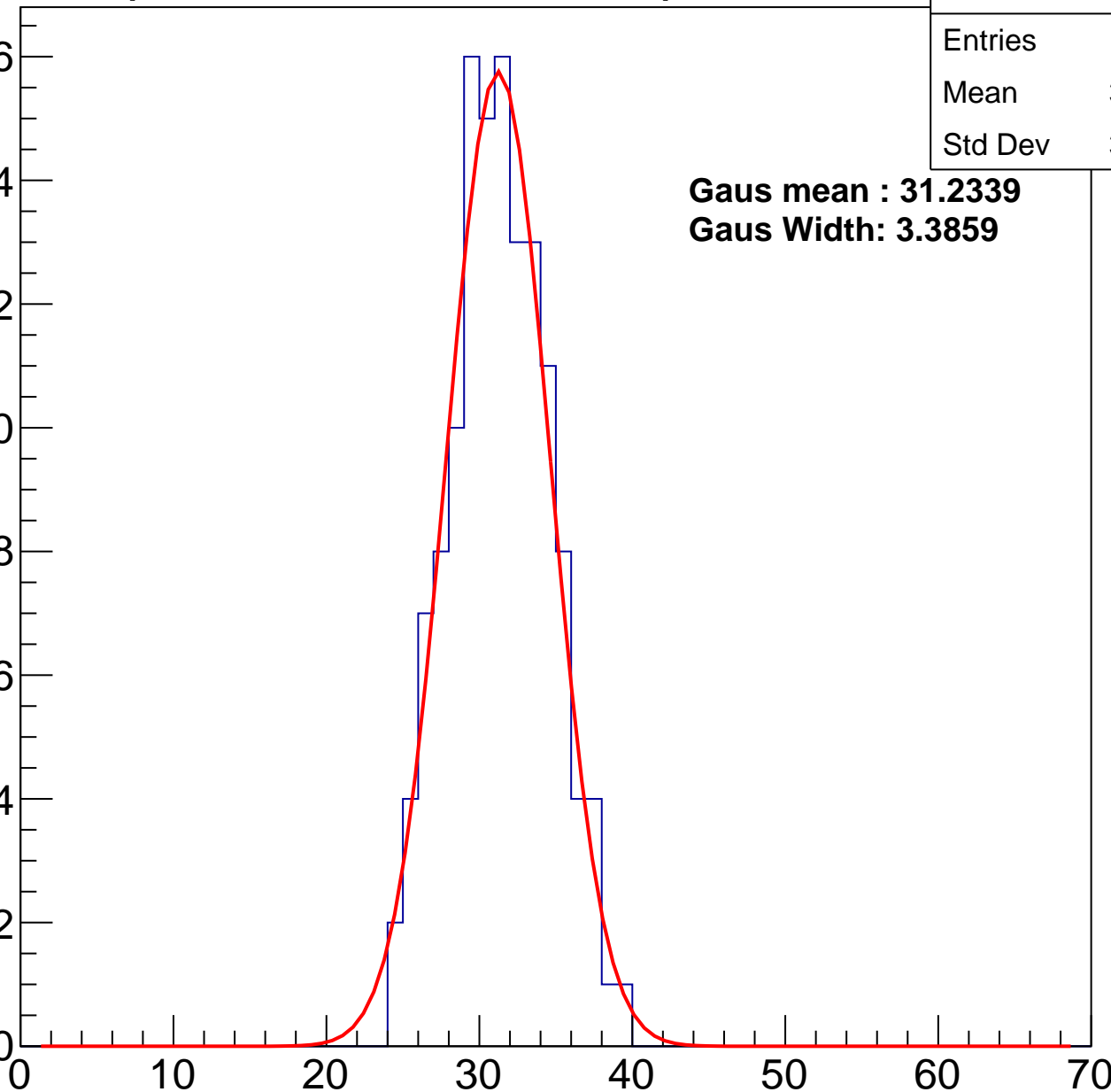
16
14
12
10
8
6
4
2
0

Entries	133
Mean	30.86
Std Dev	3.198

Gaus mean : 31.2339

Gaus Width: 3.3859

ampl



B1L001S, U24-ch109, adc1

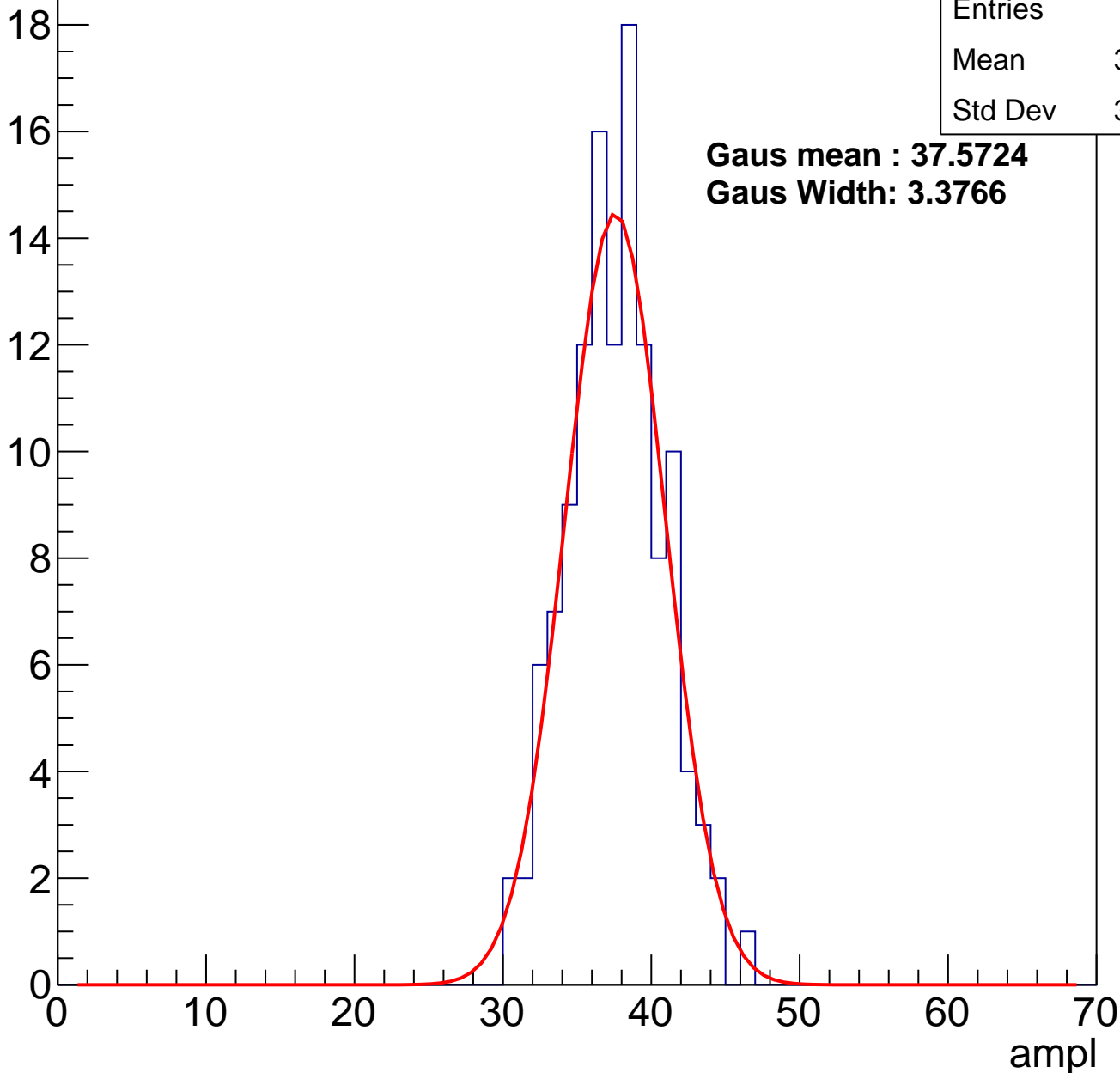
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	37.13
Std Dev	3.188

Gaus mean : 37.5724

Gaus Width: 3.3766

Entry



B1L001S, U24-ch109, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

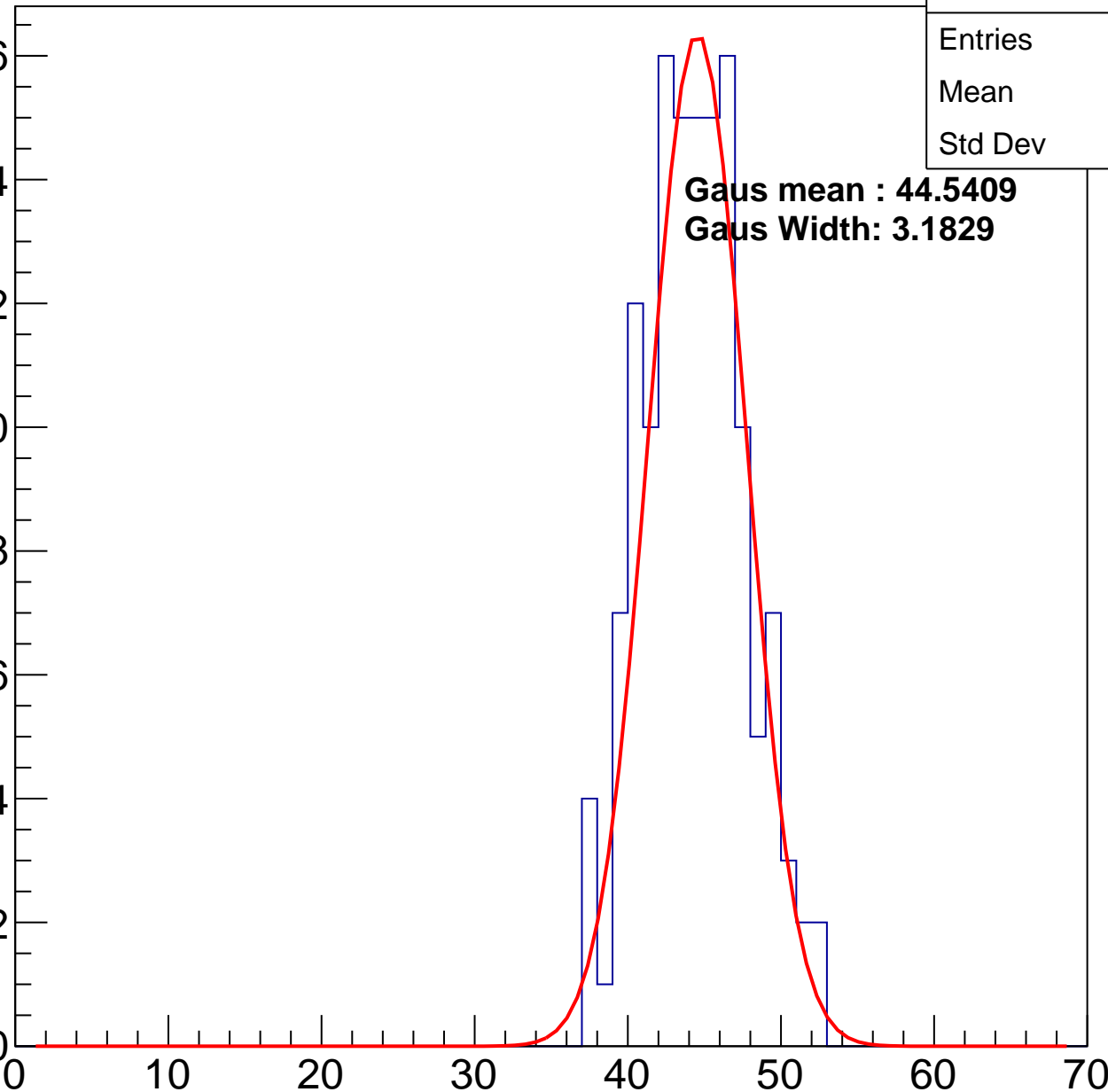
16
14
12
10
8
6
4
2
0

Entries	140
Mean	43.9
Std Dev	3.3

Gaus mean : 44.5409

Gaus Width: 3.1829

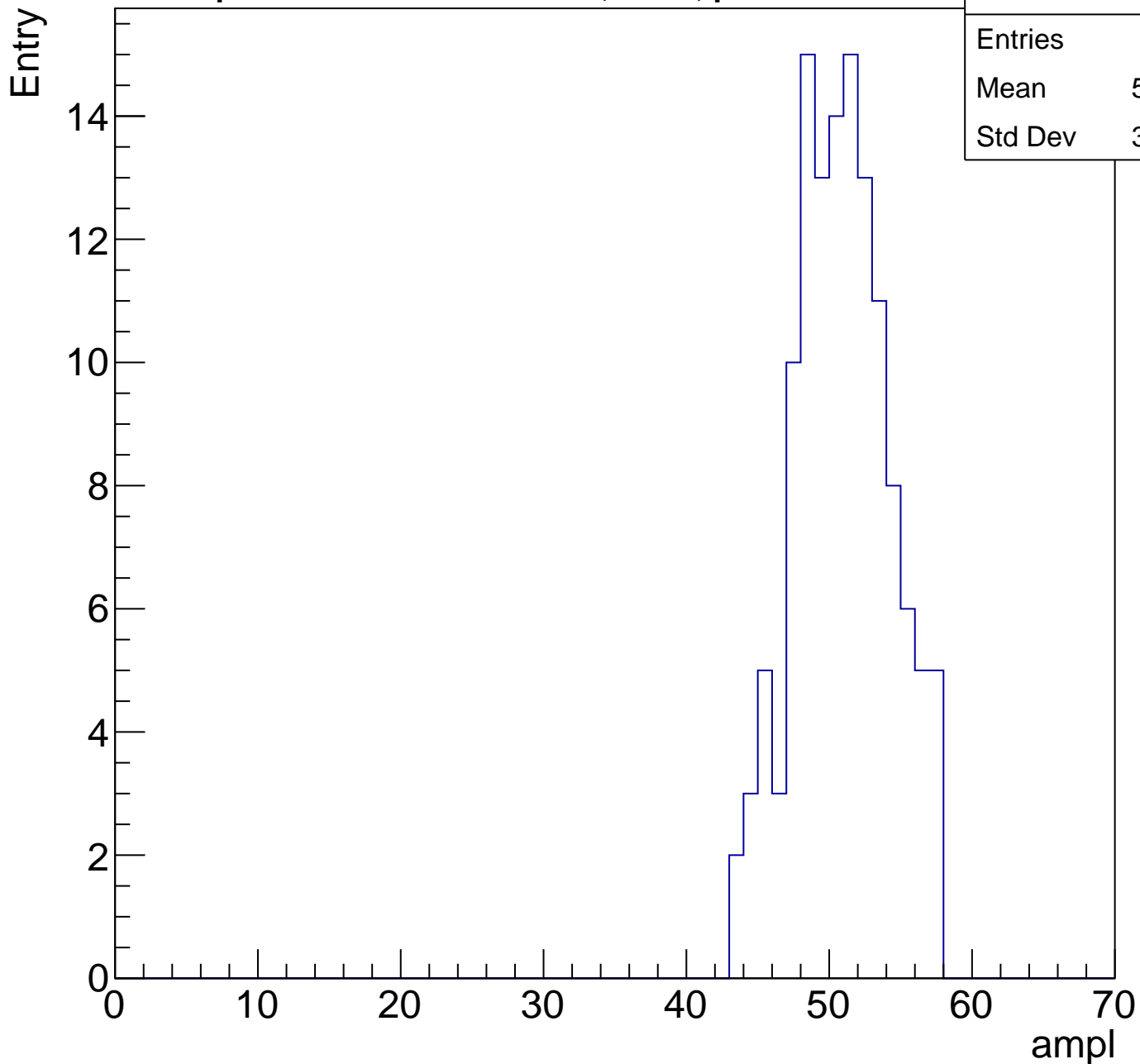
ampl



B1L001S, U24-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	50.46
Std Dev	3.288



B1L001S, U24-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

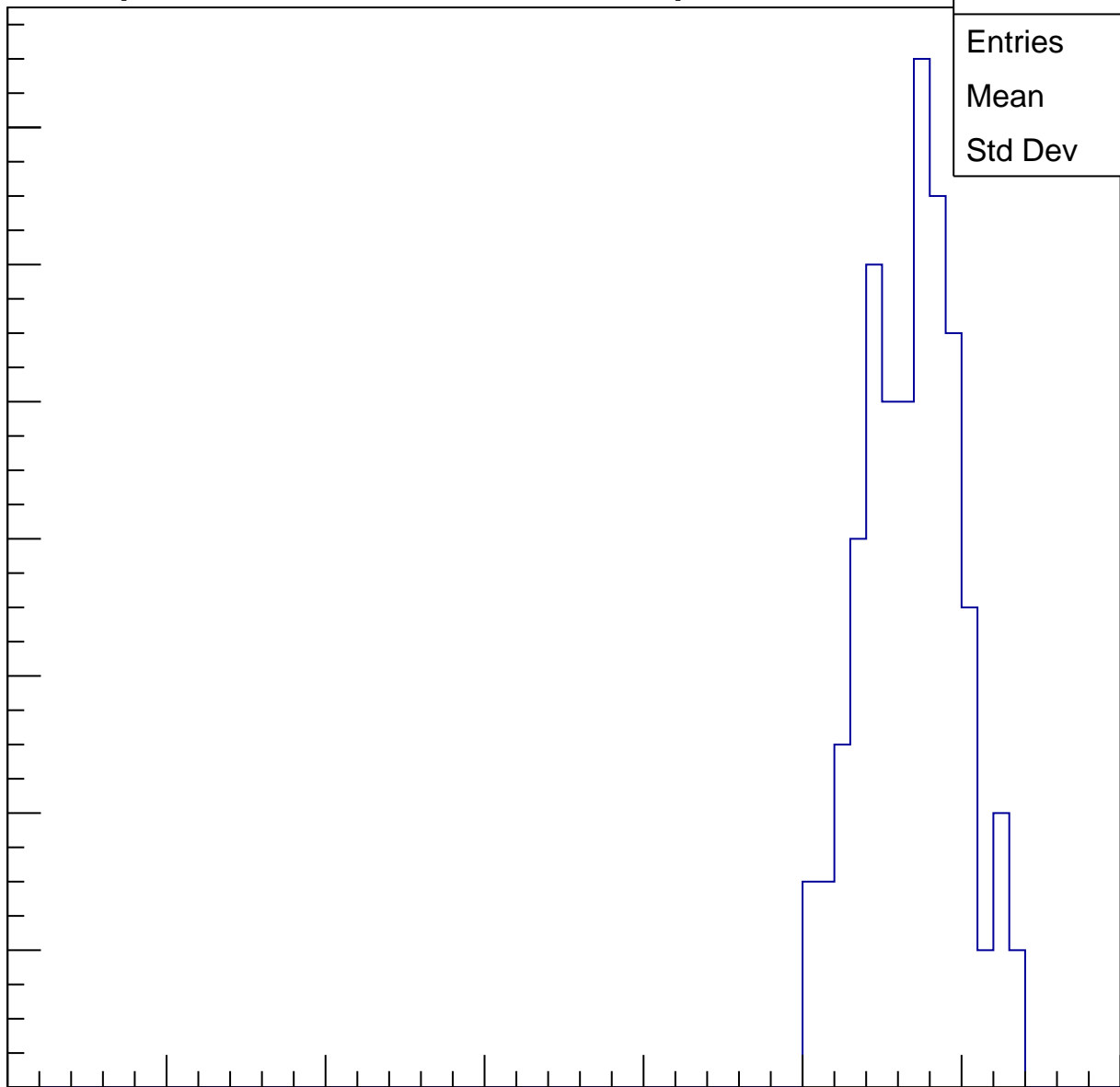
Entries	105
Mean	56.37
Std Dev	3.006

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

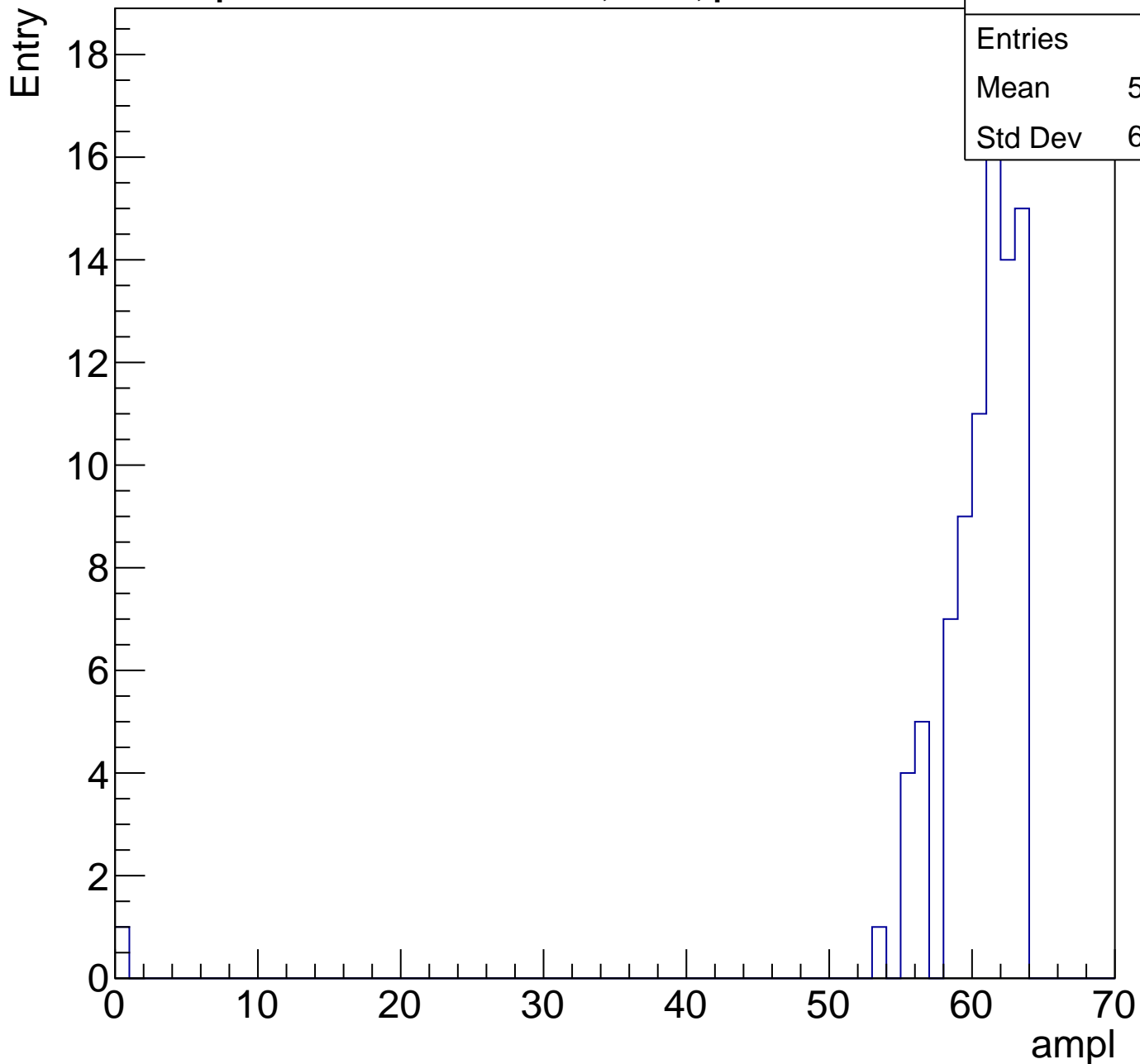
ampl



B1L001S, U24-ch109, adc5

calib_packv5_042523_0143.root, FC#2, port C2

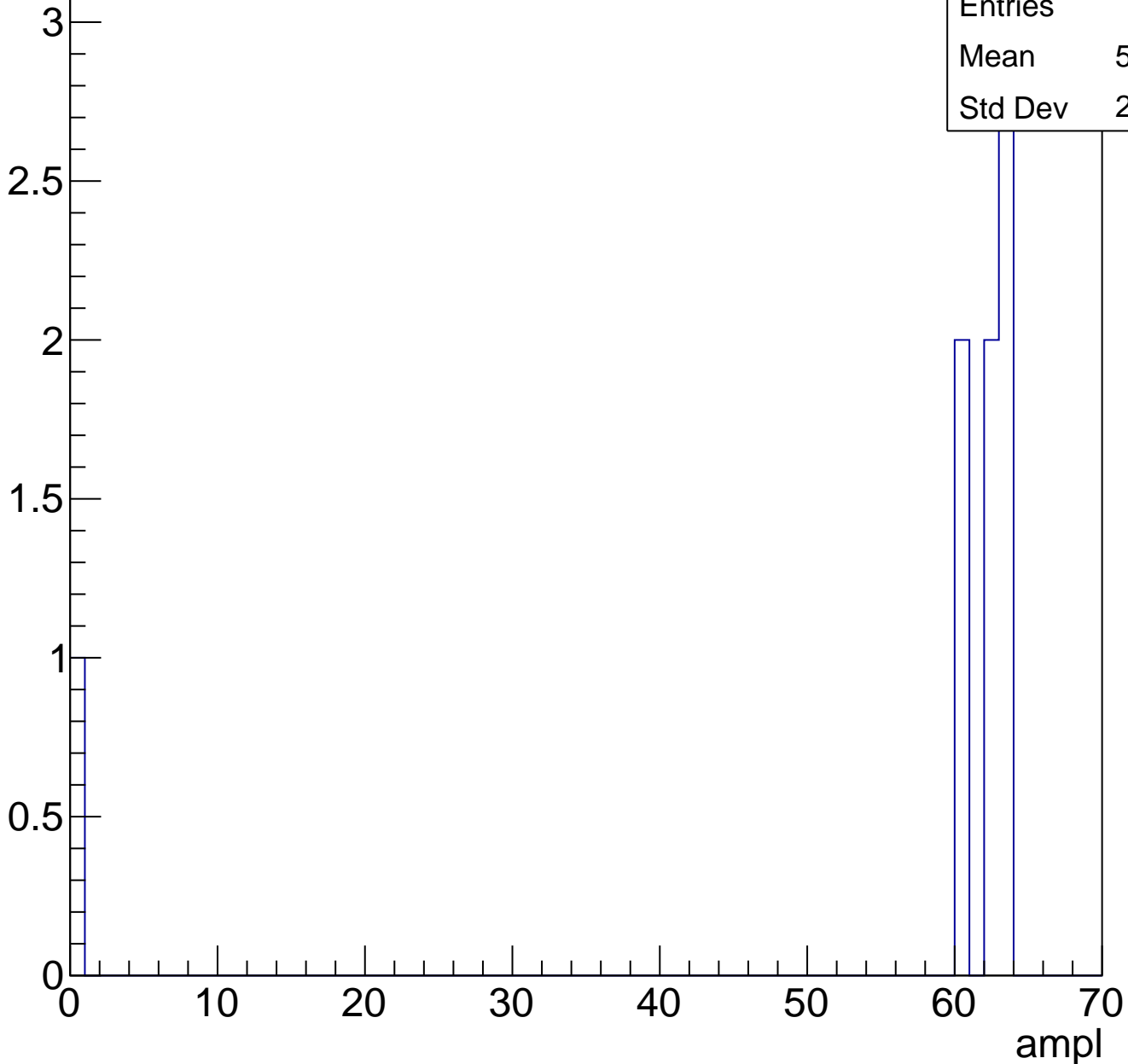
Entries	85
Mean	59.54
Std Dev	6.909



B1L001S, U24-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch110, adc0

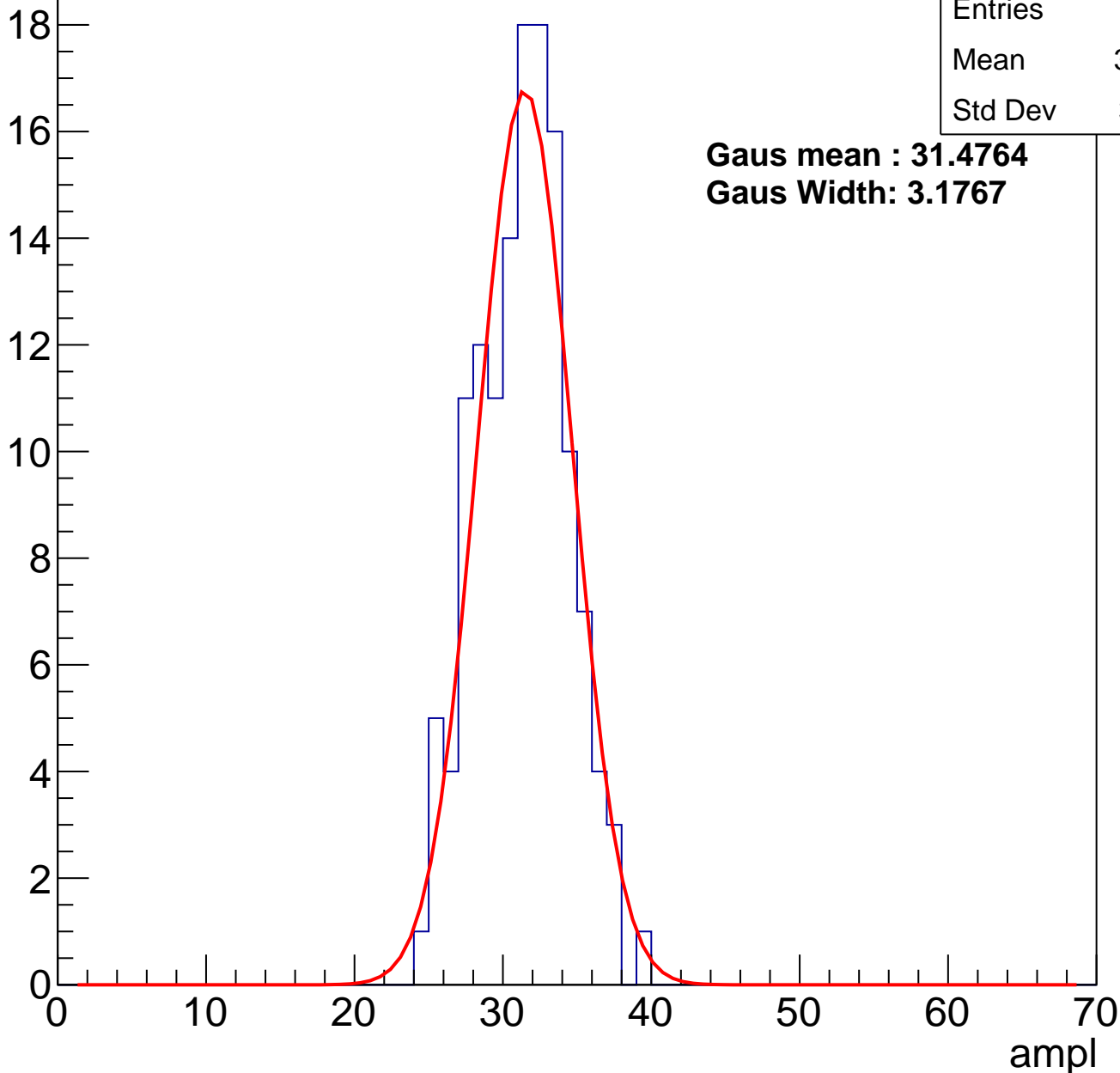
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.86
Std Dev	3.011

Gaus mean : 31.4764

Gaus Width: 3.1767

Entry



B1L001S, U24-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	37.27
Std Dev	3.435

Gaus mean : 37.8213

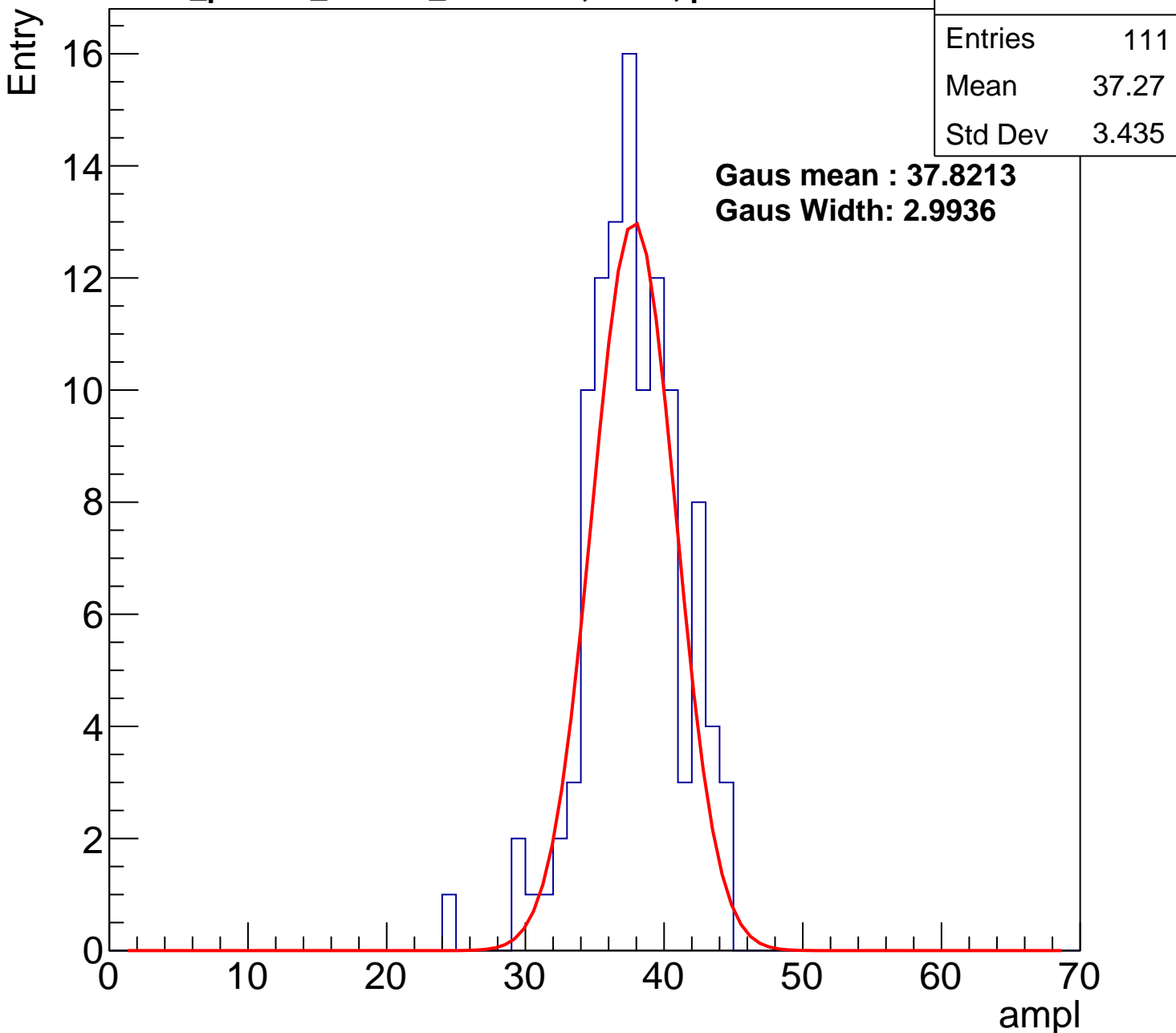
Gaus Width: 2.9936

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	43.06
Std Dev	3.37

Gaus mean : 43.5509

Gaus Width: 3.0530

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

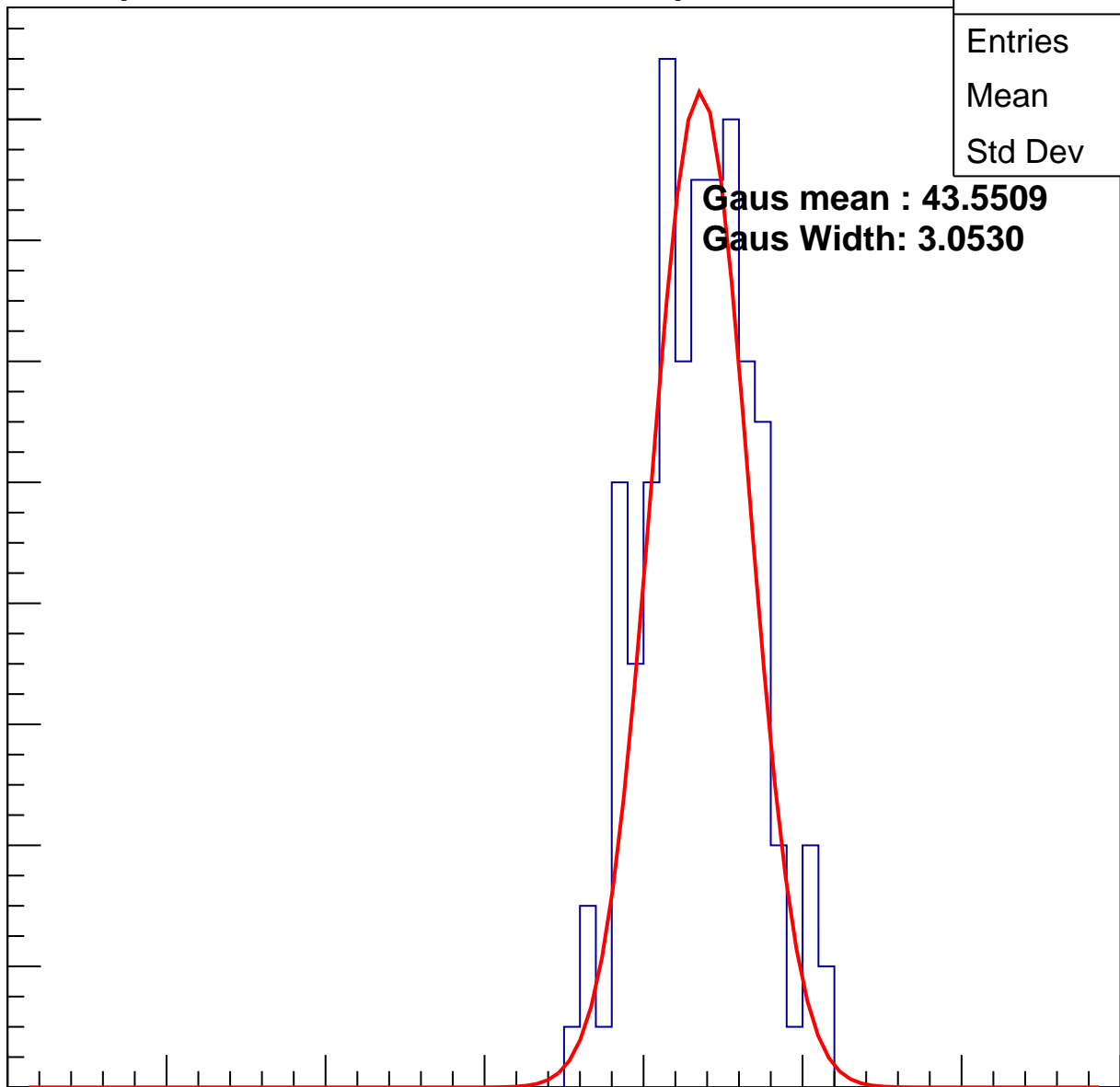
40

50

60

70

ampl



B1L001S, U24-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

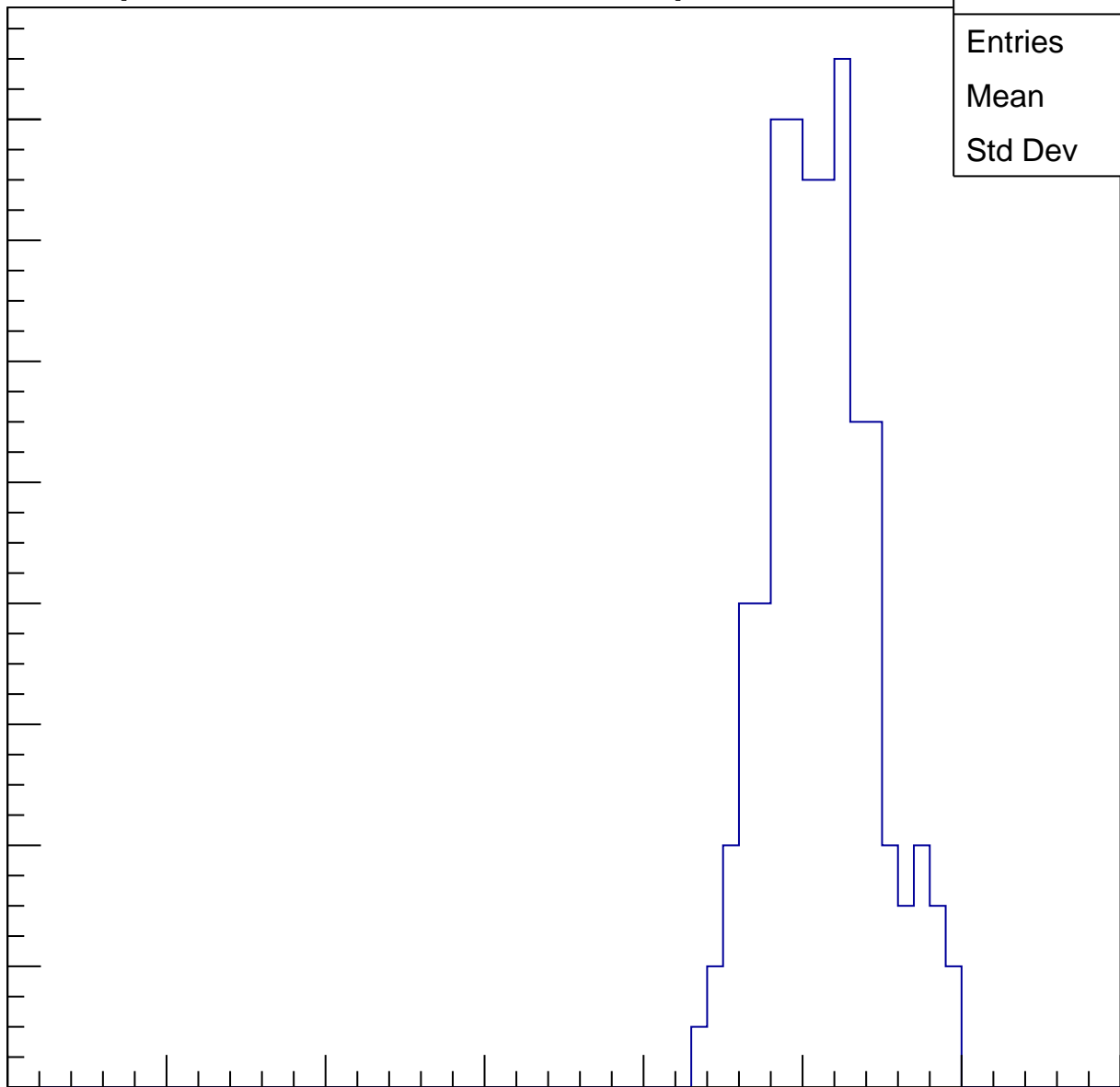
Entries	140
Mean	50.65
Std Dev	3.336

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch110, adc4

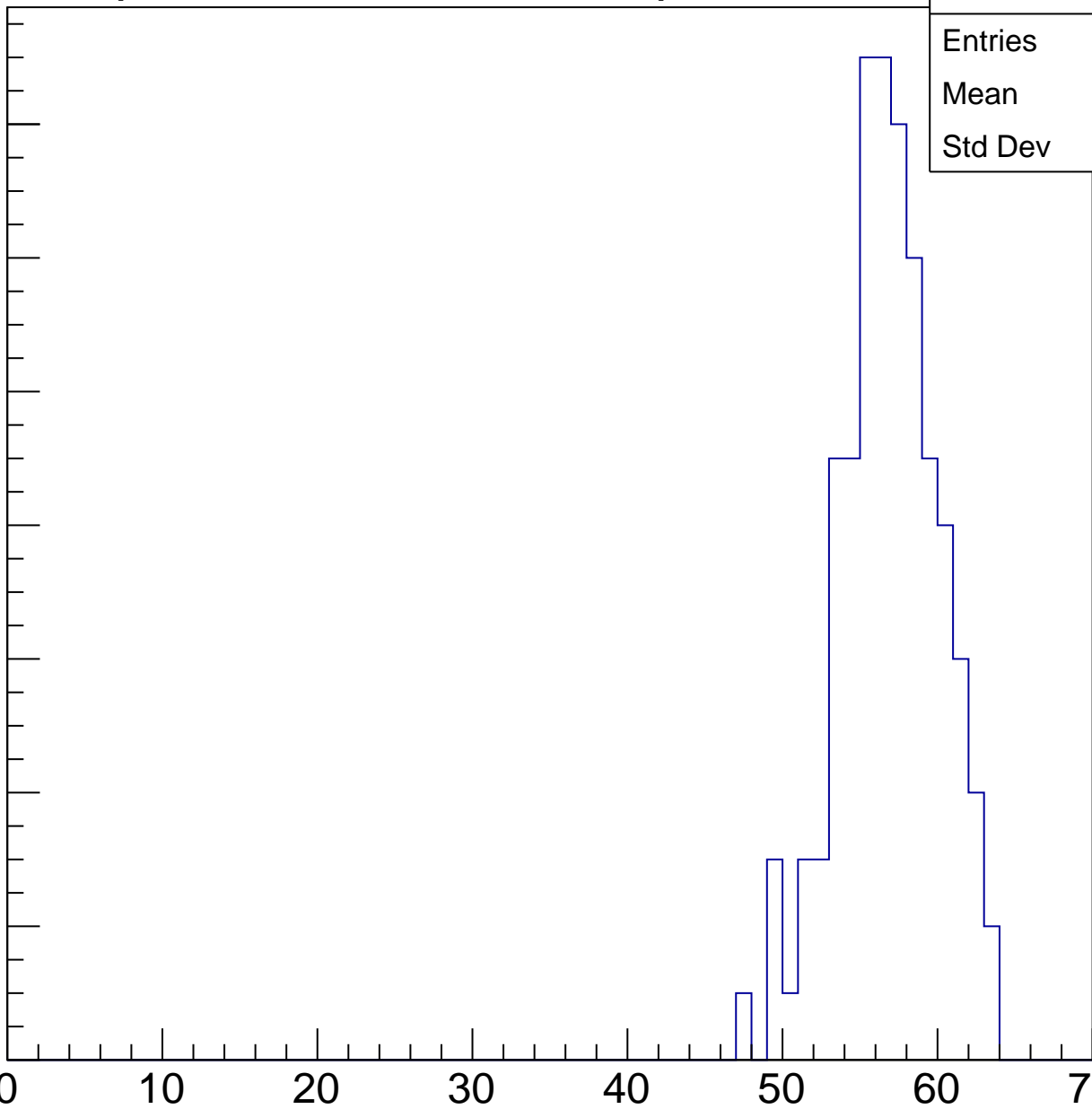
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	56.37
Std Dev	3.199

ampl



B1L001S, U24-ch110, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	70
Mean	59.71
Std Dev	7.443

ampl

0

10

20

30

40

50

60

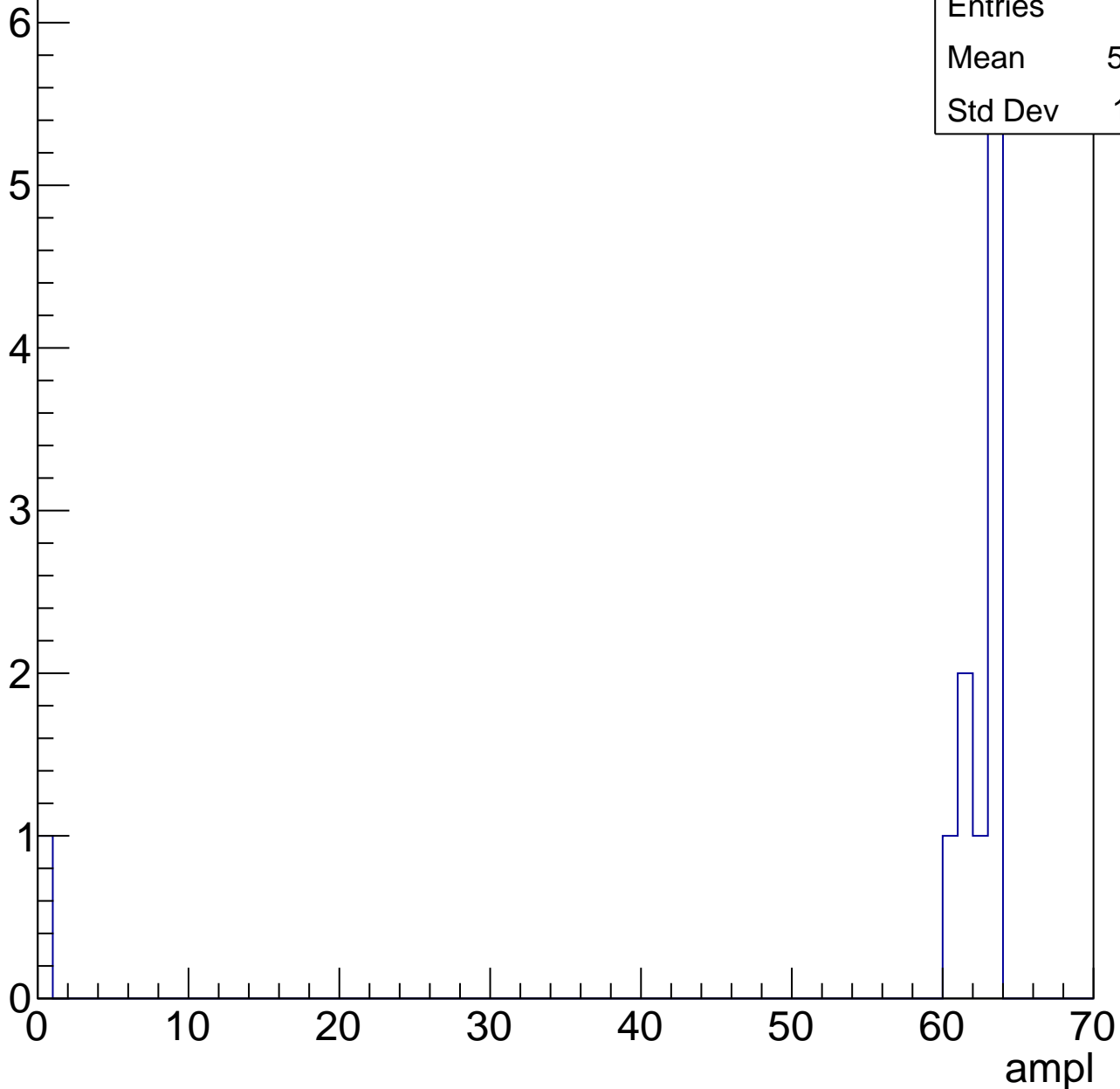
70

B1L001S, U24-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

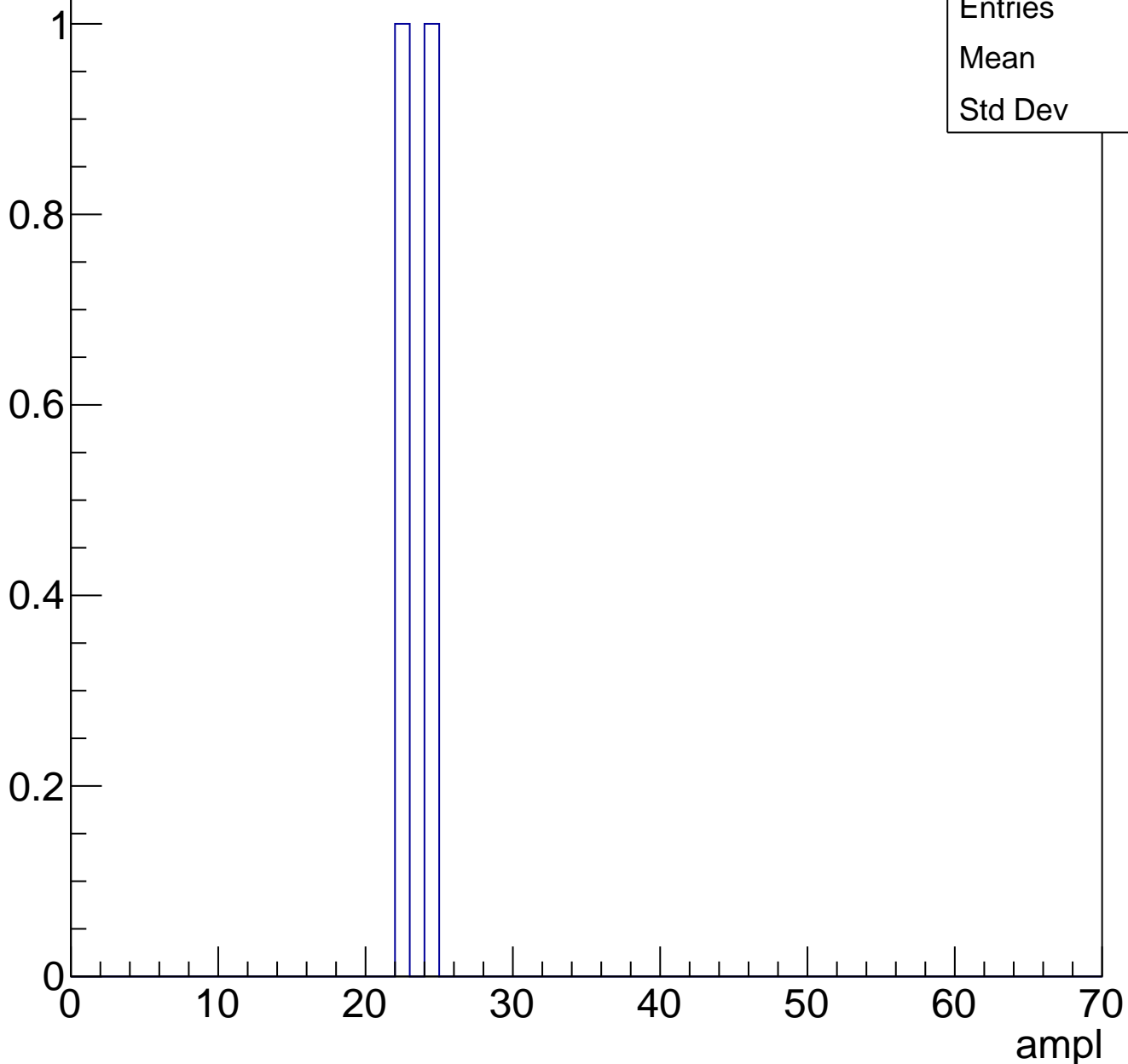
Entries	11
Mean	56.55
Std Dev	17.91



B1L001S, U24-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	23
Std Dev	1

B1L001S, U24-ch111, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	31.67
Std Dev	3.283

Gaus mean : 31.9952

Gaus Width: 3.5244

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

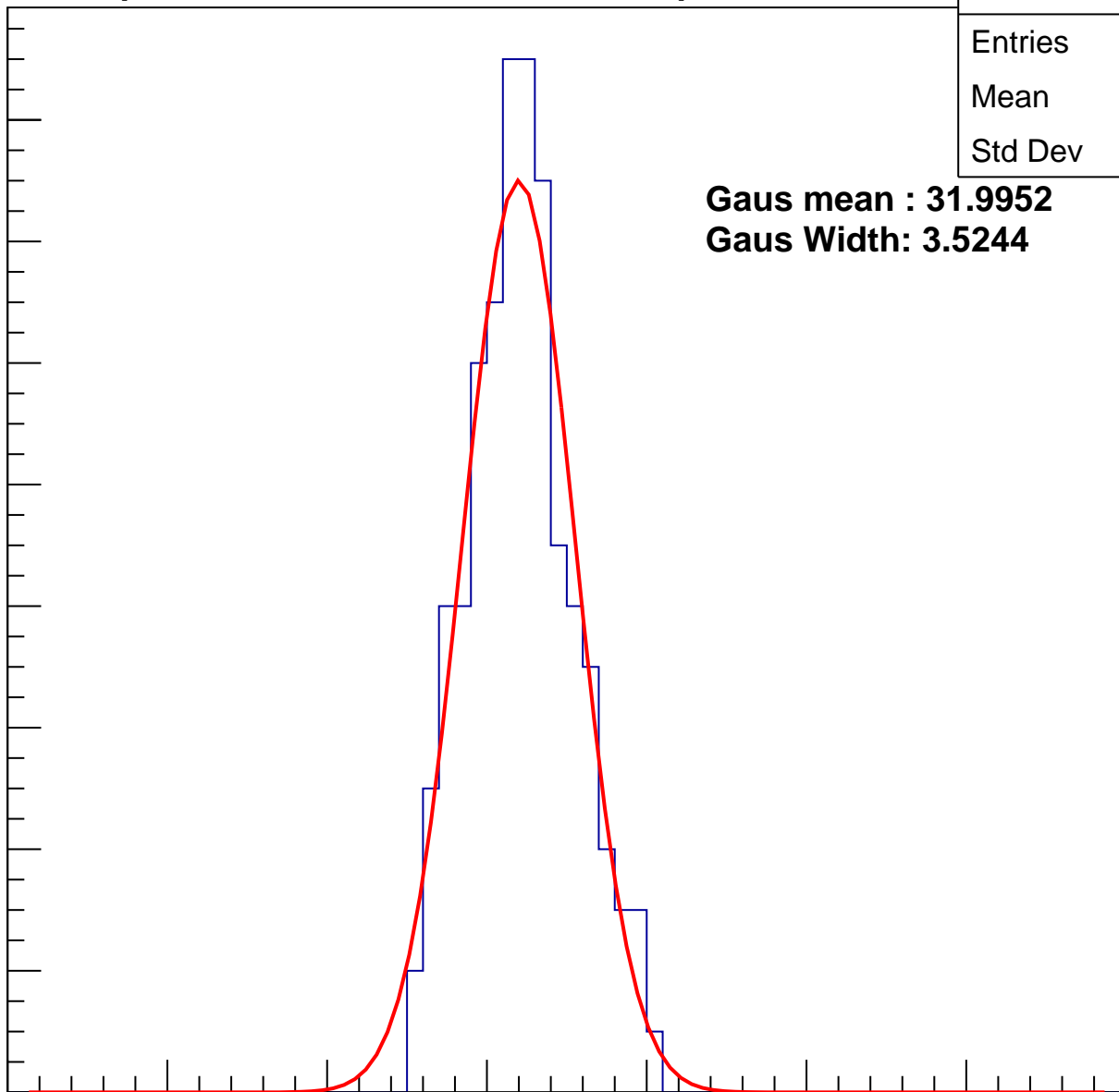
40

50

60

70

ampl



B1L001S, U24-ch111, adc1

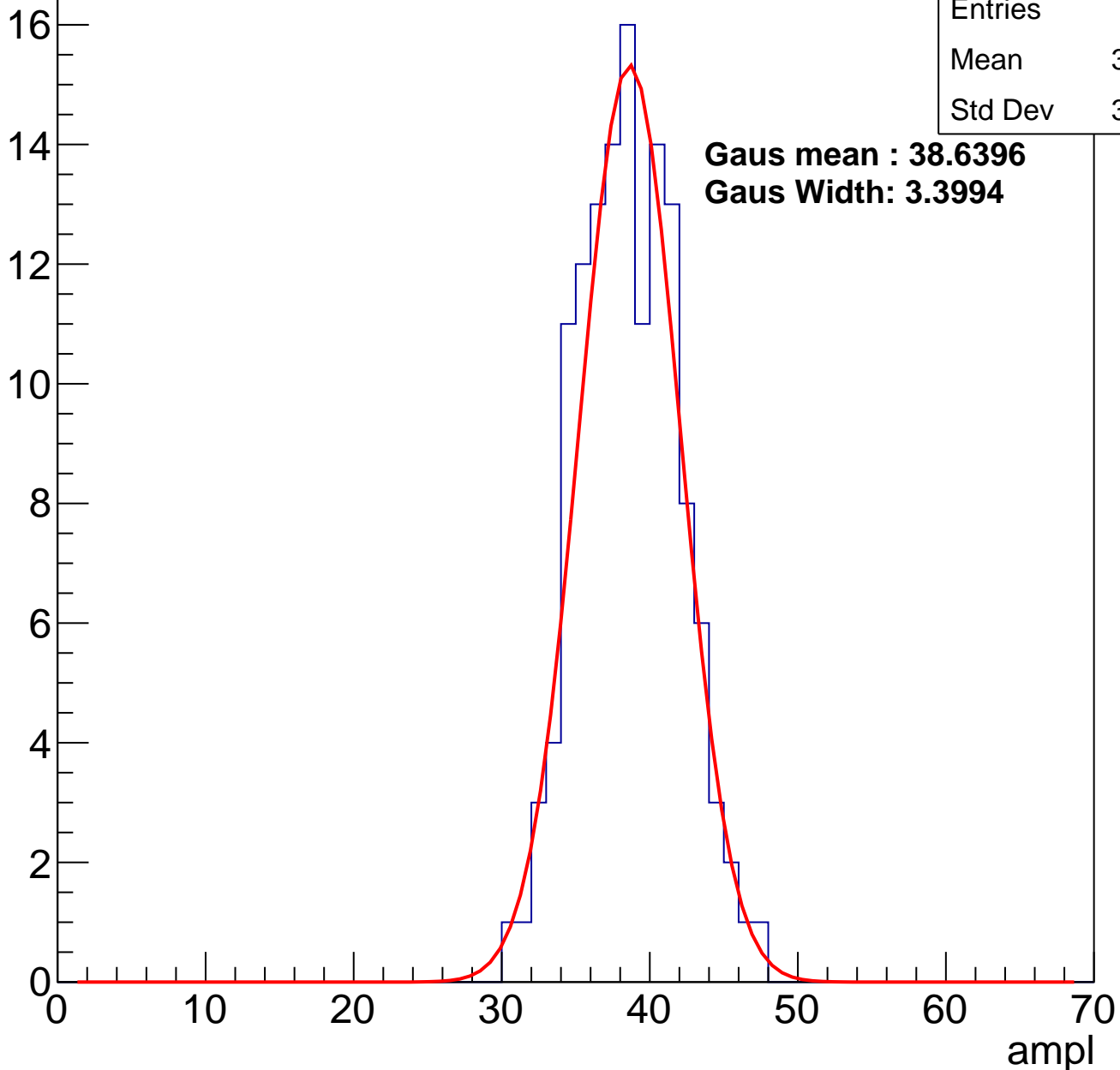
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	38.12
Std Dev	3.326

Gaus mean : 38.6396

Gaus Width: 3.3994

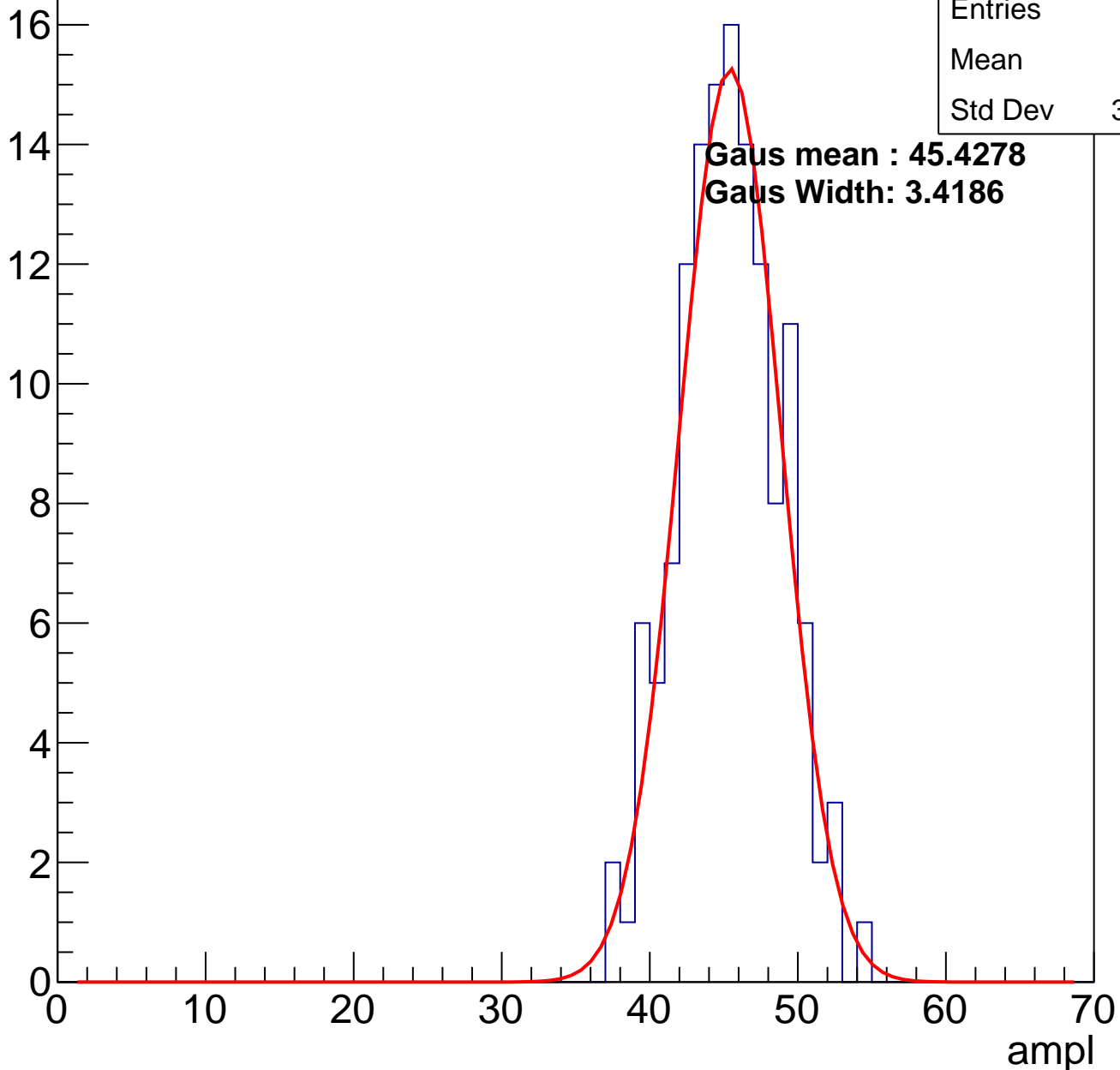
Entry



B1L001S, U24-ch111, adc2

calib_packv5_042523_0143.root, FC#2, port C2

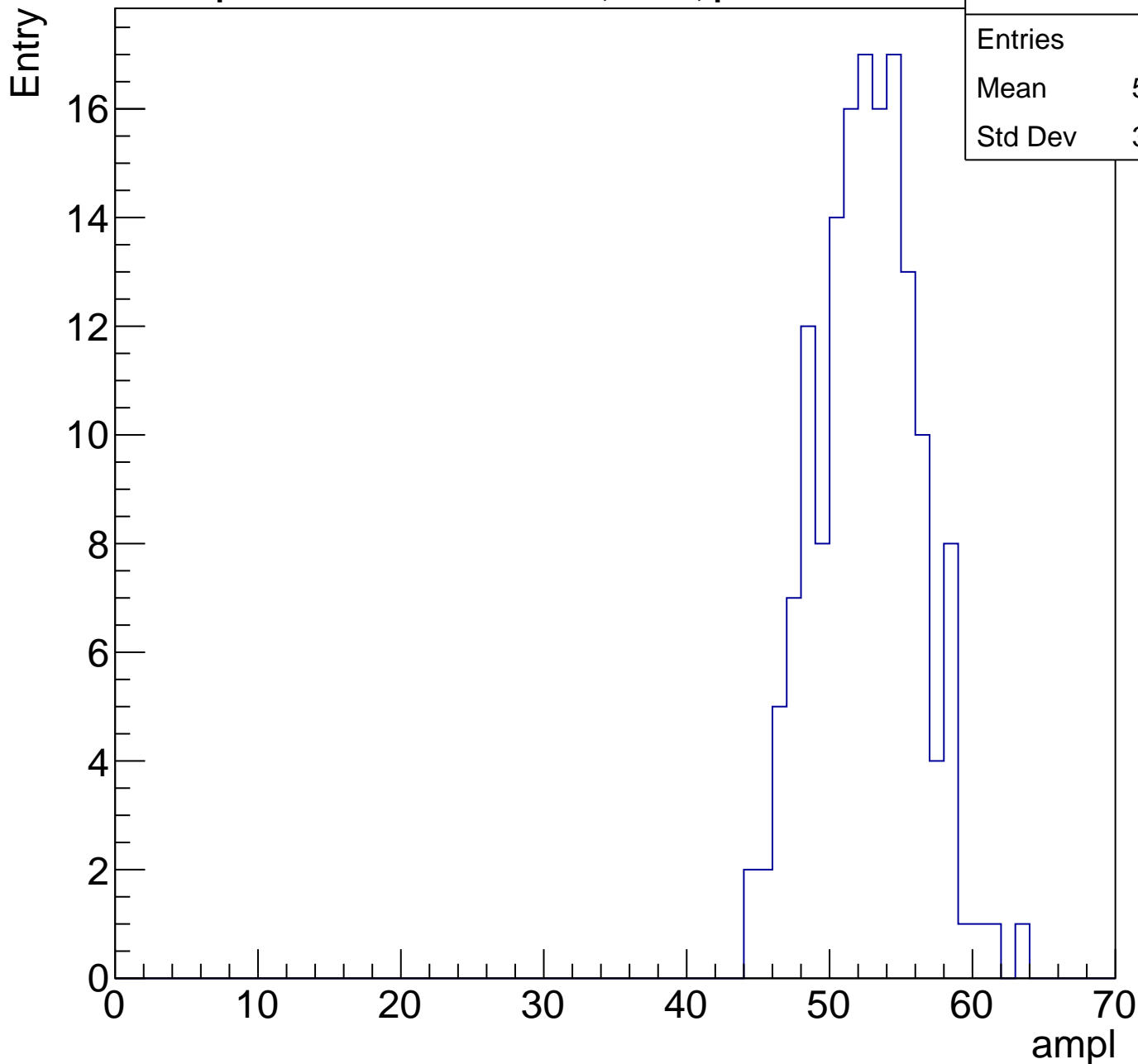
Entry



B1L001S, U24-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	52.14
Std Dev	3.572

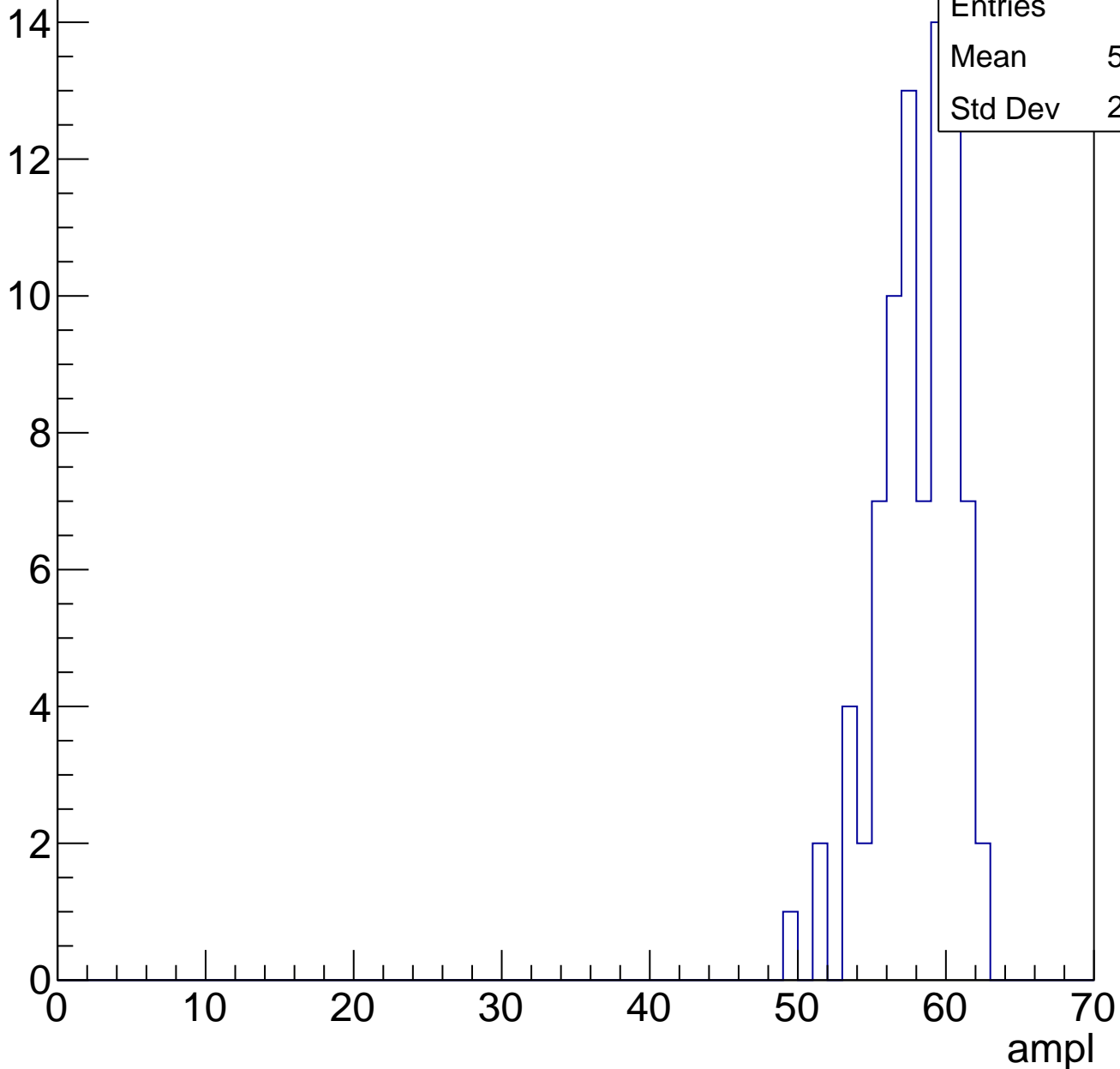


B1L001S, U24-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	83
Mean	57.59
Std Dev	2.639

Entry



B1L001S, U24-ch111, adc5

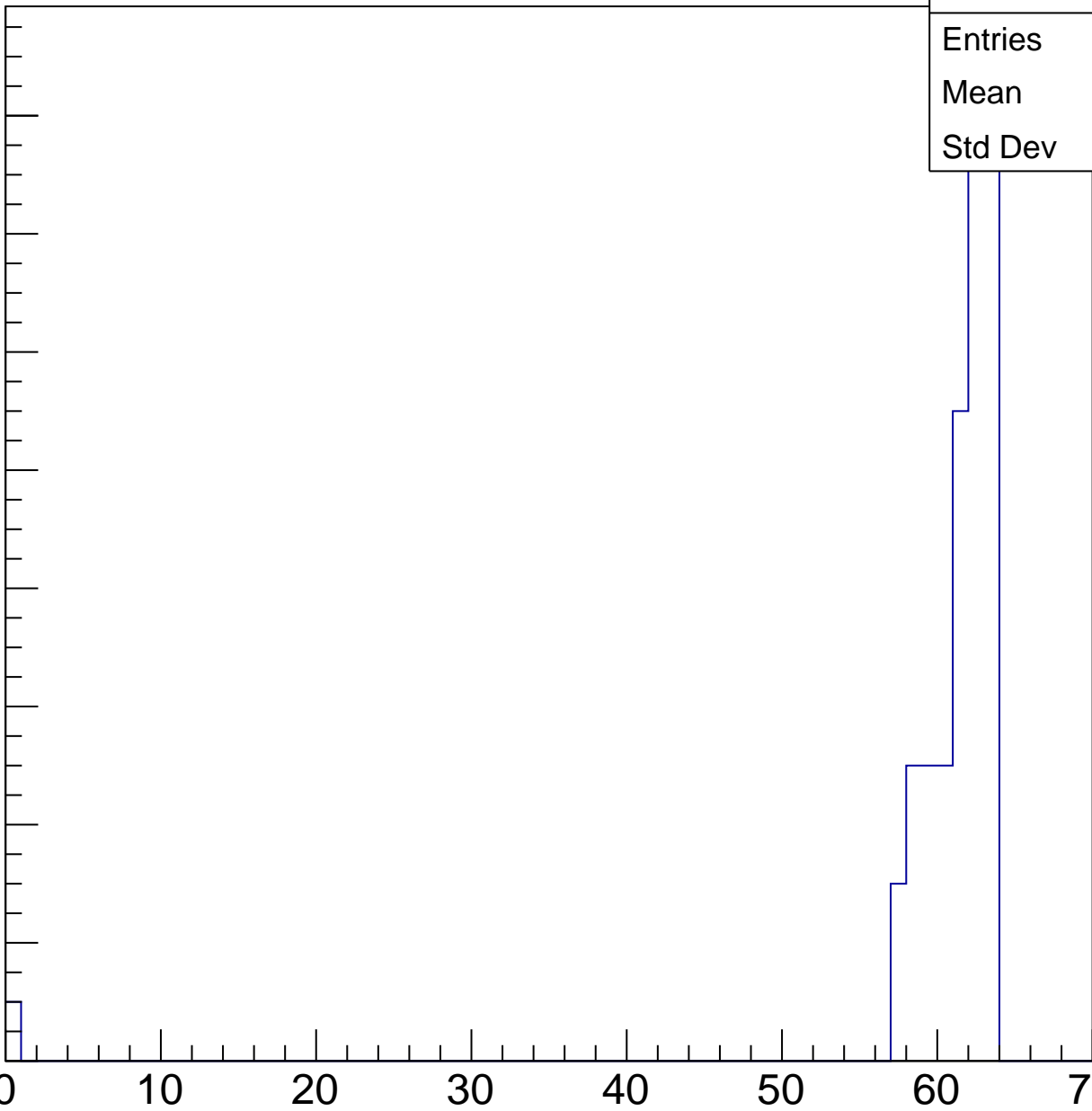
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	60.16
Std Dev	7.846

ampl



B1L001S, U24-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

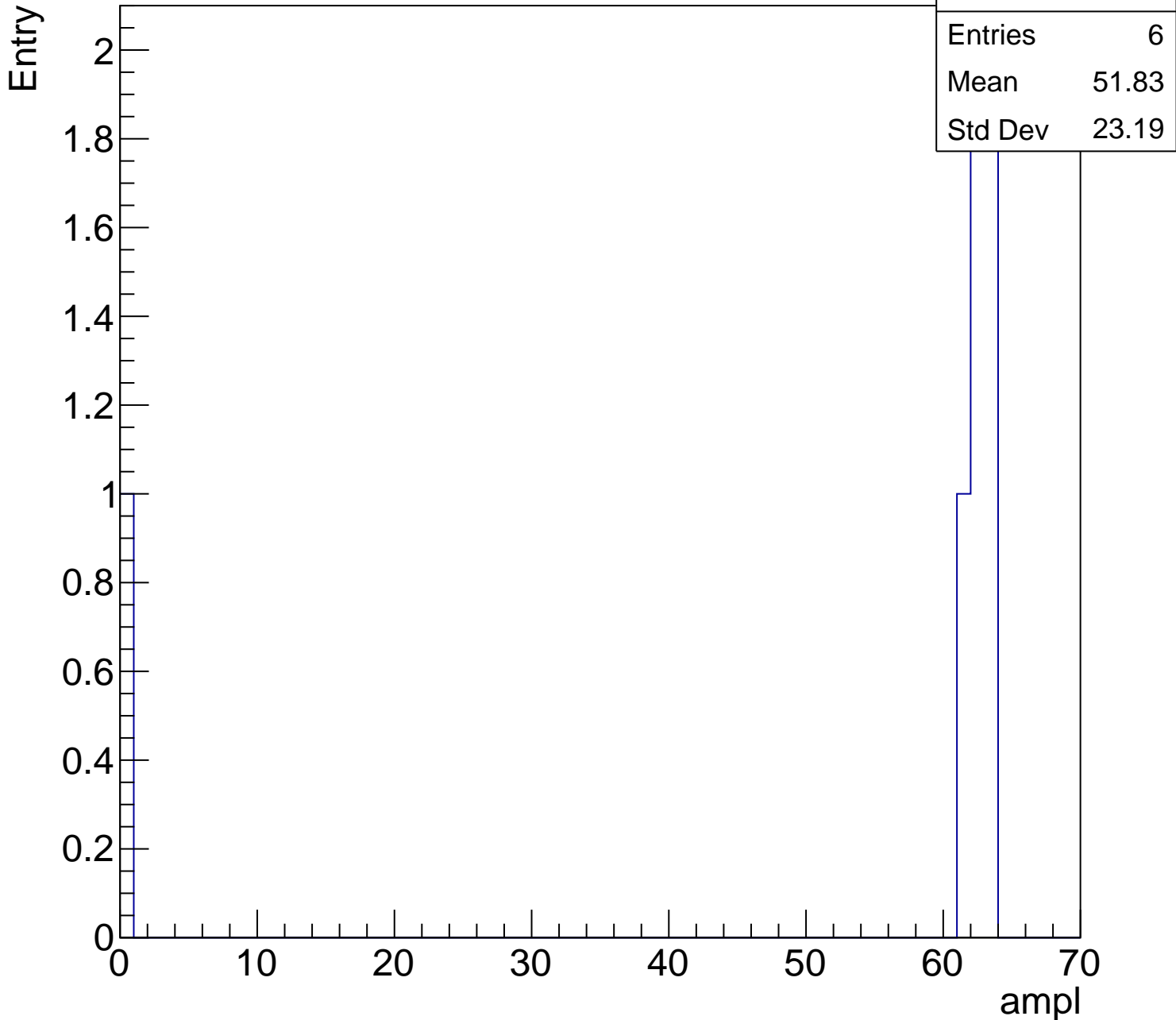
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

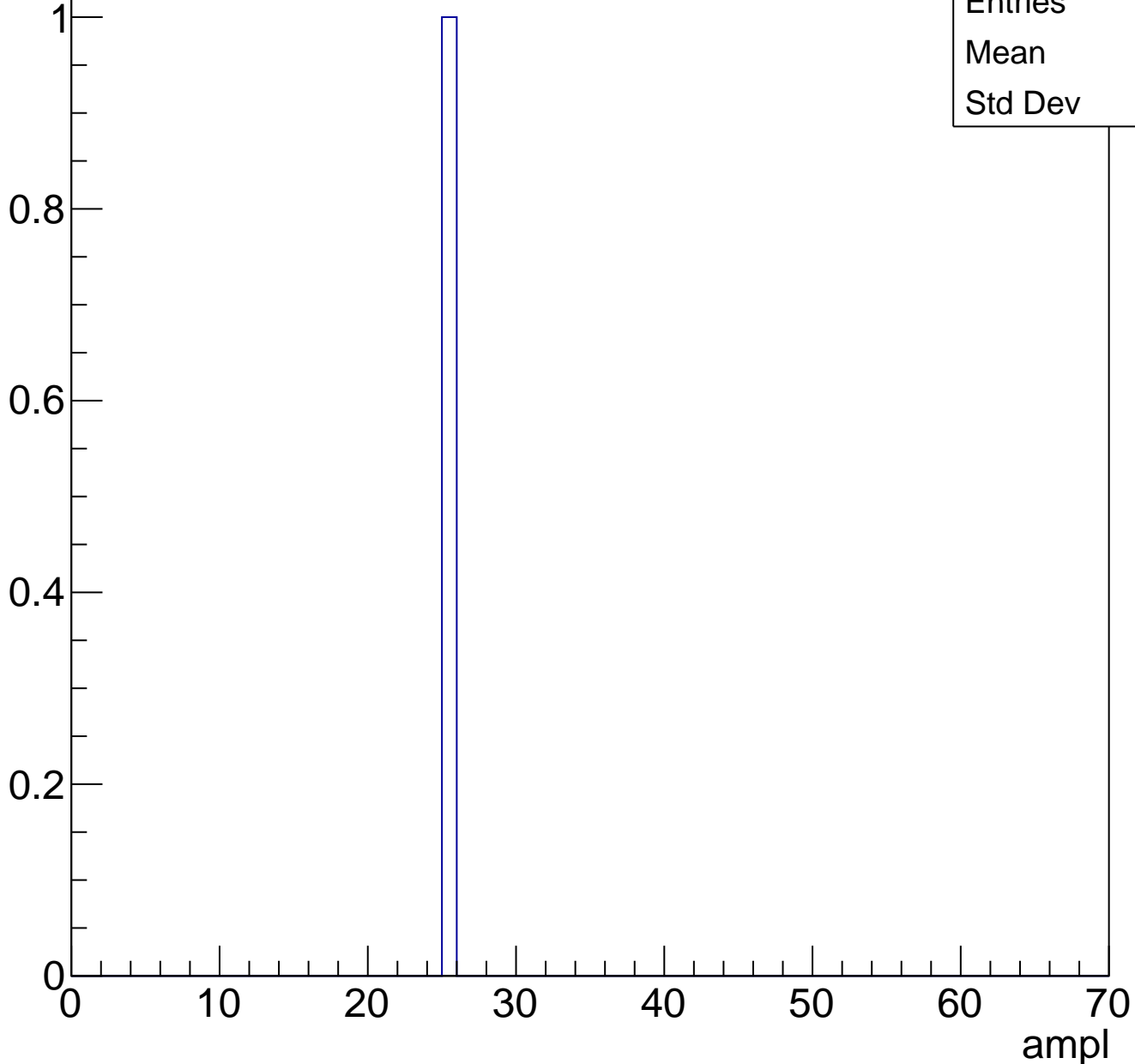
ampl



B1L001S, U24-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	25
Std Dev	0

B1L001S, U24-ch112, adc0

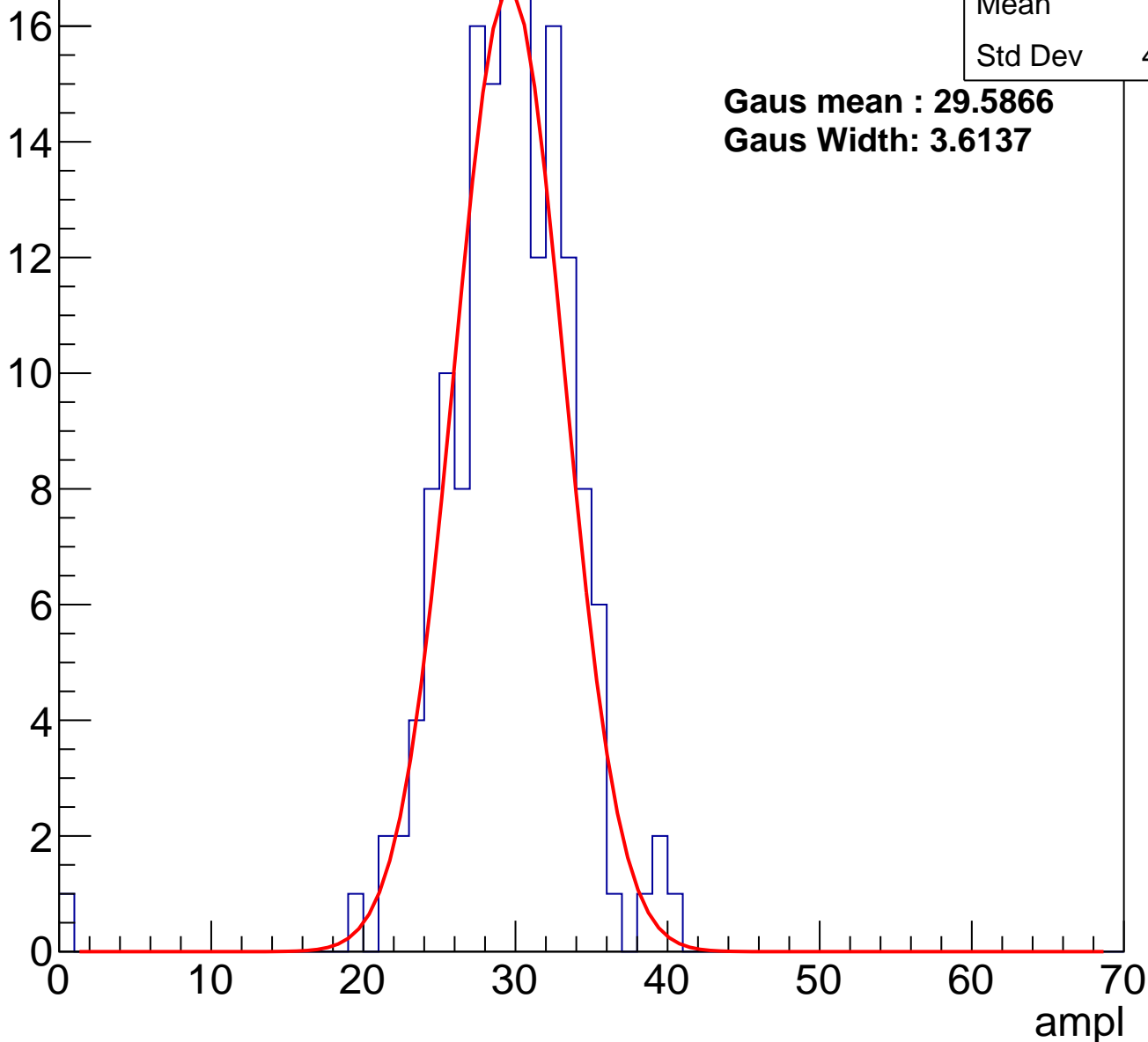
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	29.1
Std Dev	4.393

Gaus mean : 29.5866

Gaus Width: 3.6137

Entry



B1L001S, U24-ch112, adc1

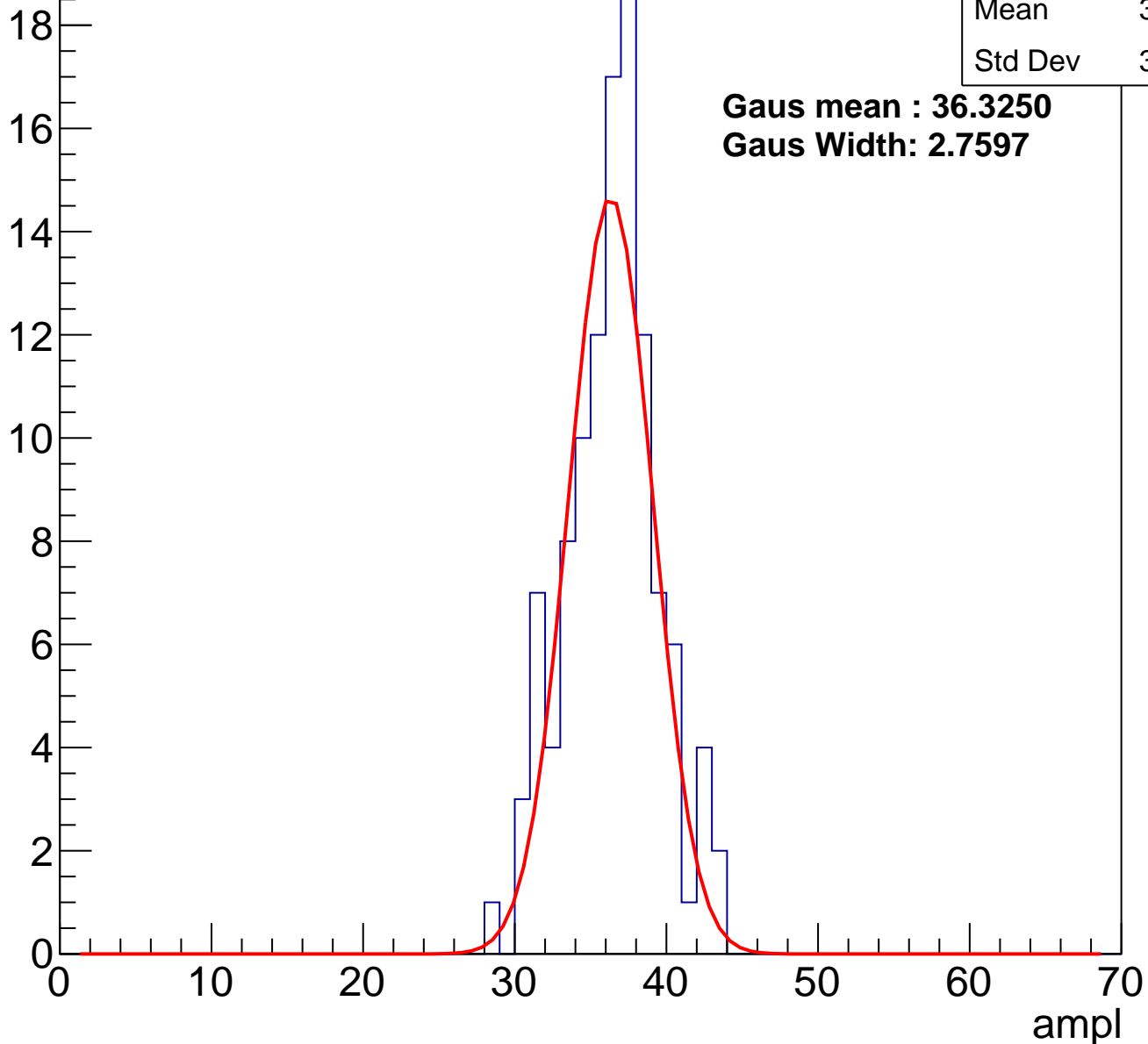
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	35.98
Std Dev	3.016

Gaus mean : 36.3250

Gaus Width: 2.7597

Entry



B1L001S, U24-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

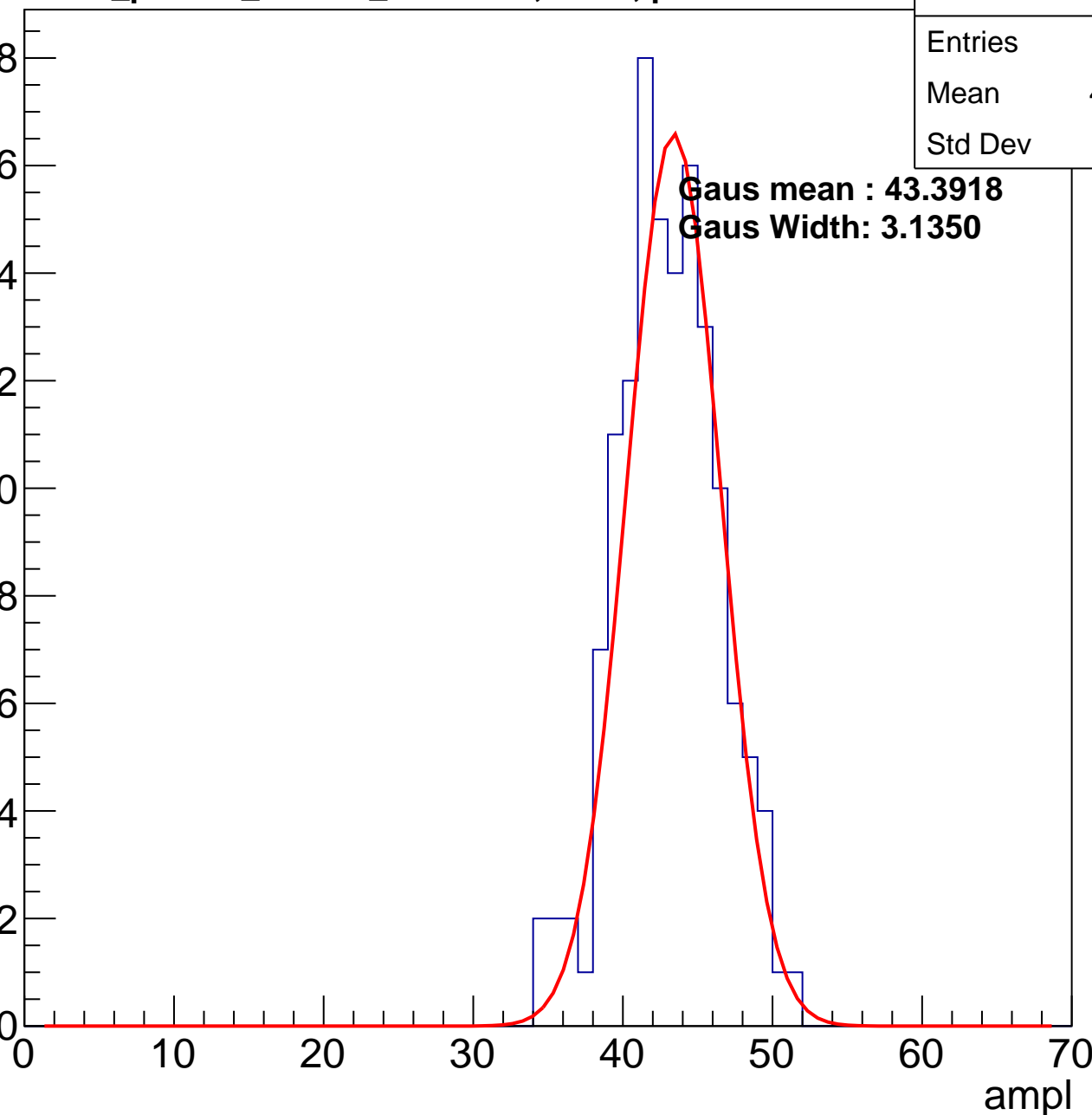
Entry

18
16
14
12
10
8
6
4
2
0

Entries	140
Mean	42.57
Std Dev	3.36

Gaus mean : 43.3918

Gaus Width: 3.1350



B1L001S, U24-ch112, adc3

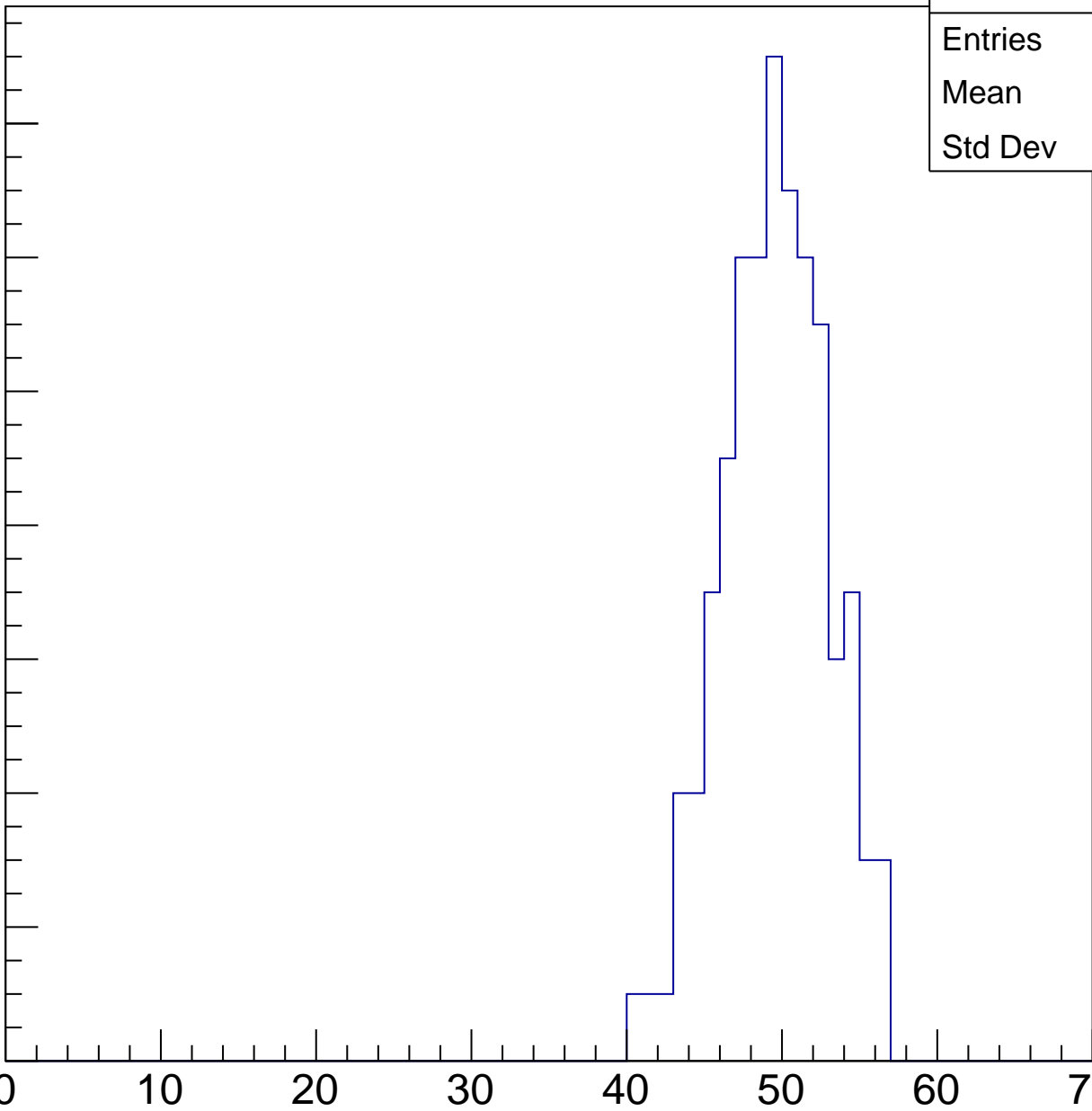
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	49.07
Std Dev	3.373

ampl



B1L001S, U24-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	85
Mean	54.12
Std Dev	3.352

Entry

10

8

6

4

2

0

0

10

20

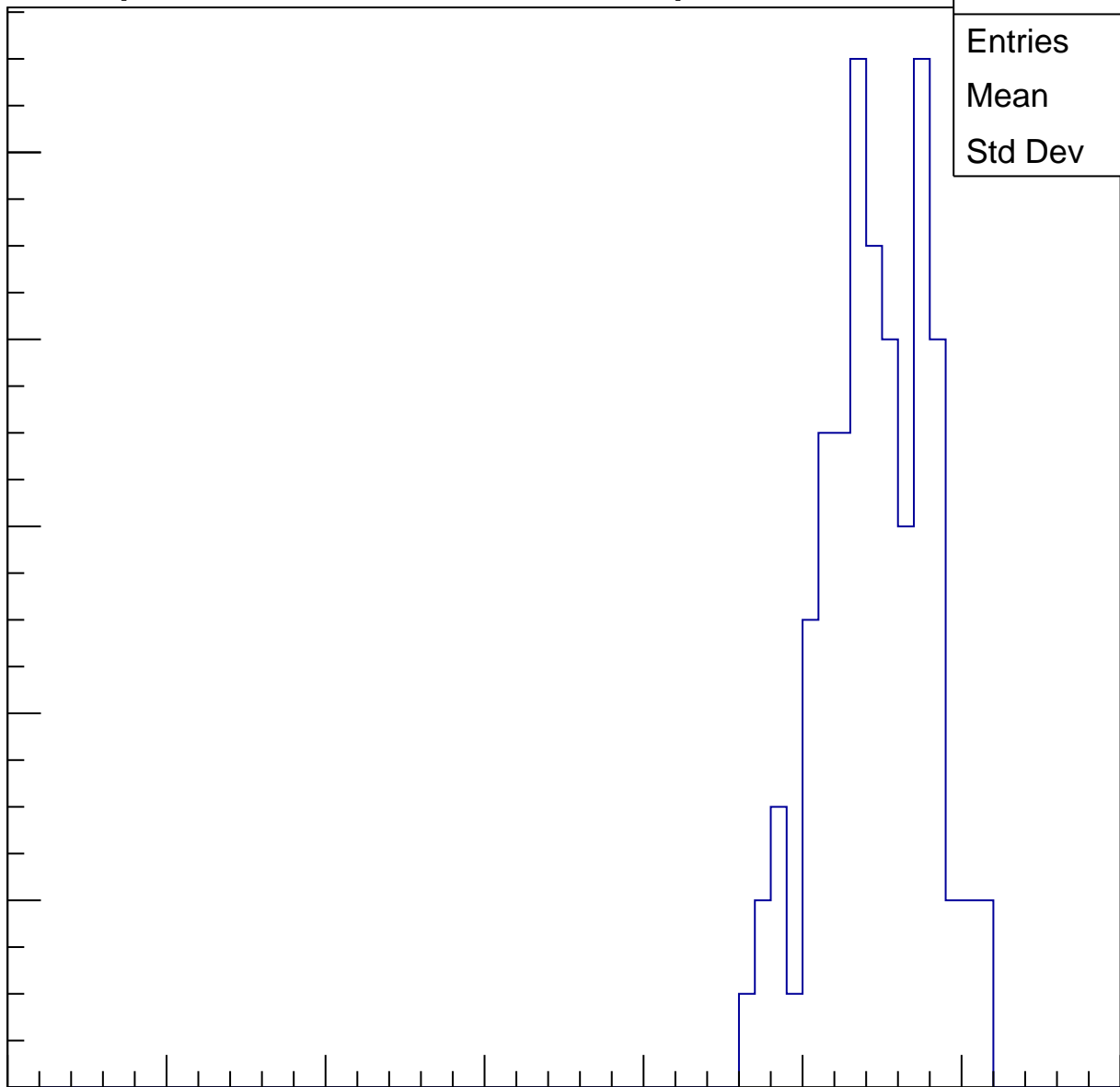
30

40

50

60

ampl



B1L001S, U24-ch112, adc5

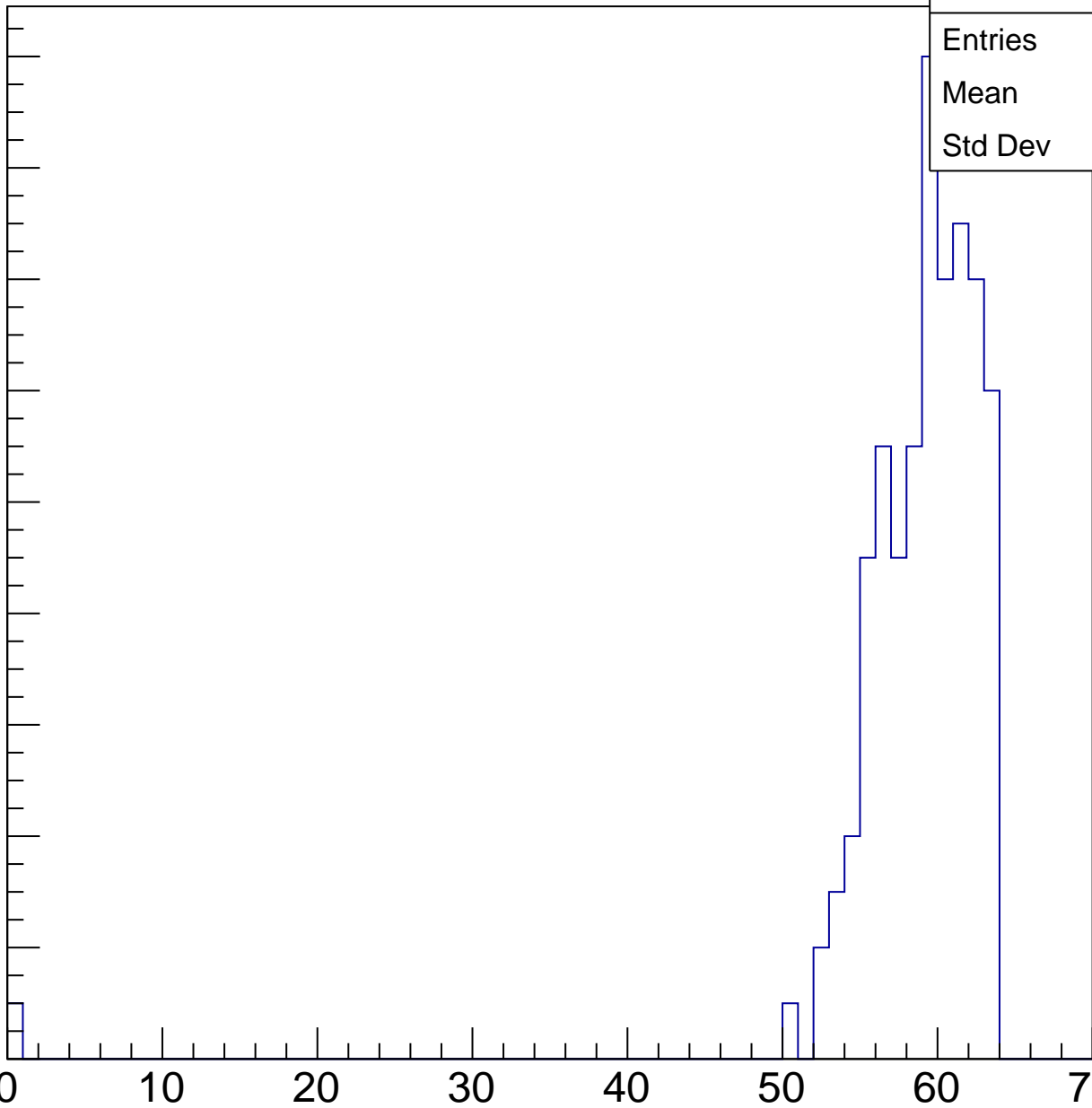
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.32
Std Dev	6.02

ampl

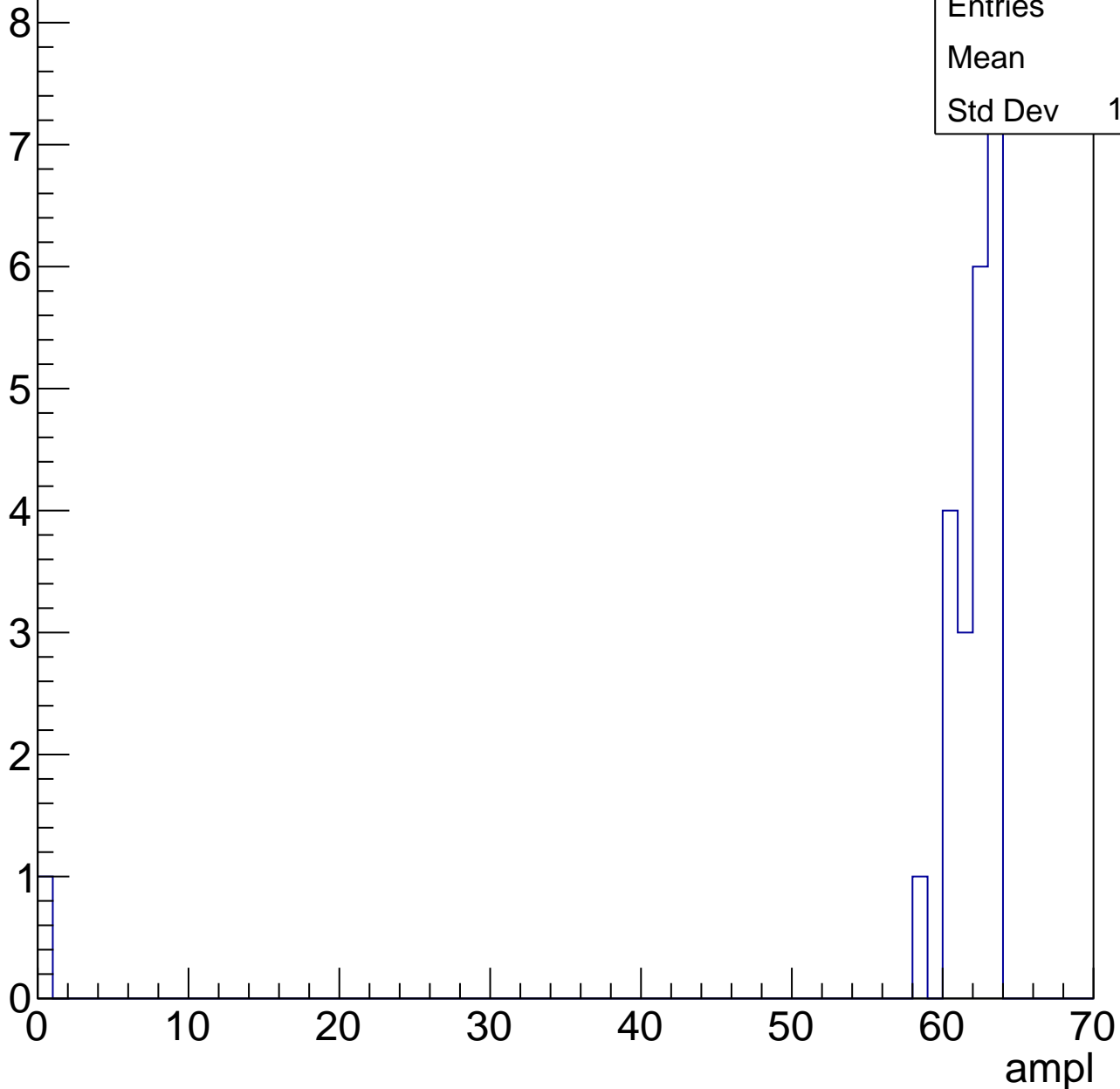


B1L001S, U24-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	59
Std Dev	12.65



B1L001S, U24-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.78
Std Dev	3.534

Gaus mean : 30.0433

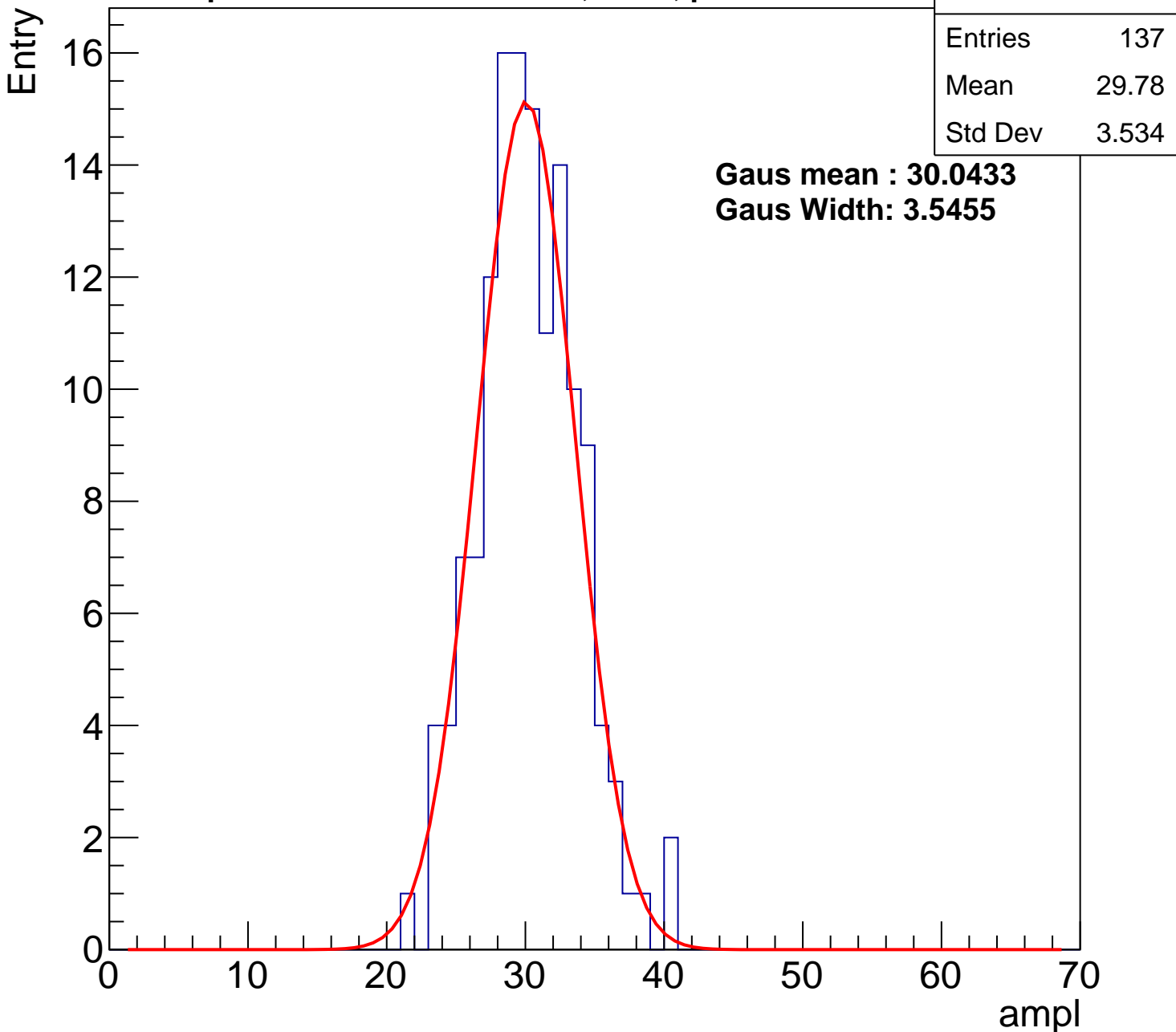
Gaus Width: 3.5455

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch113, adc1

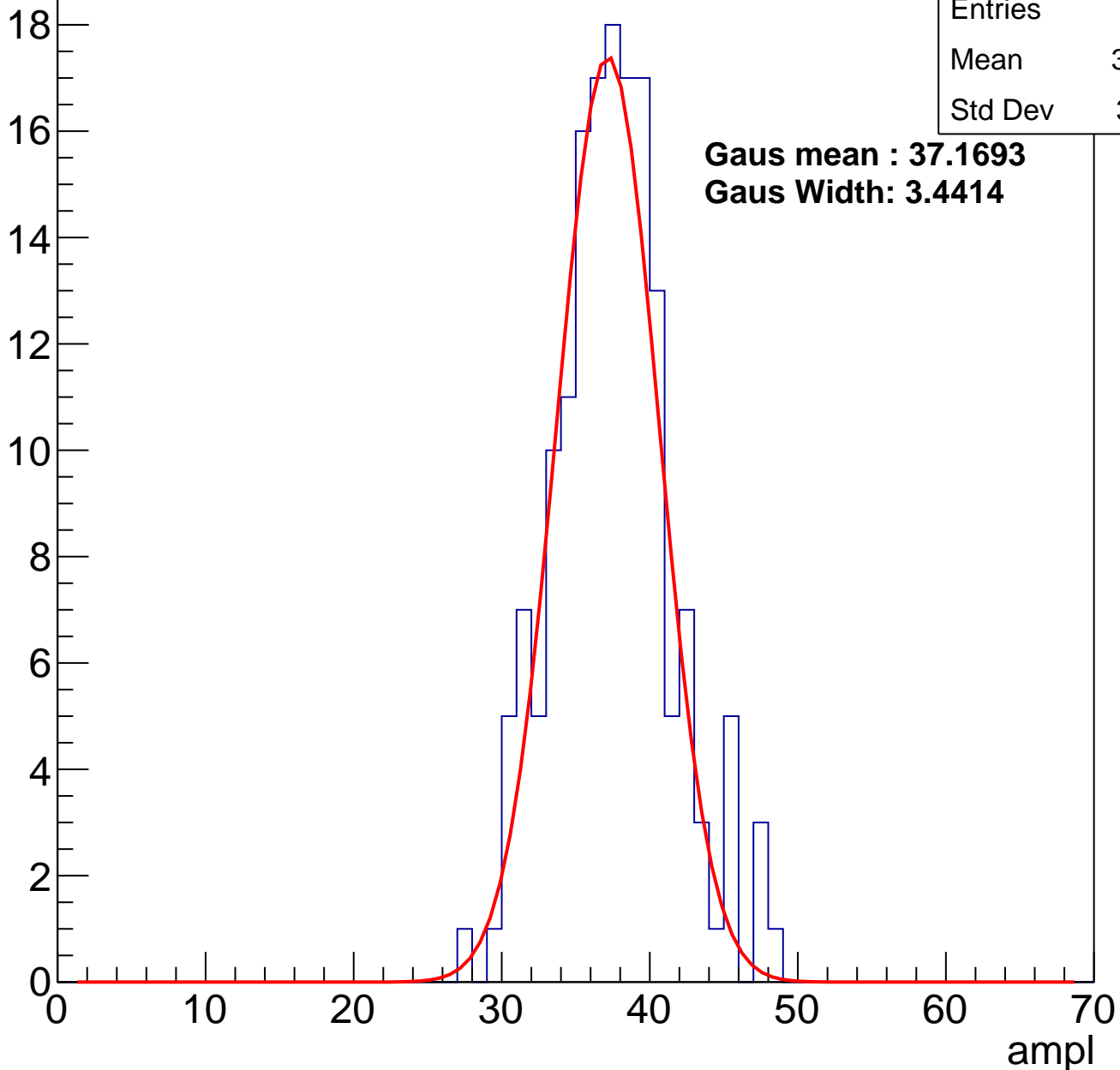
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	37.06
Std Dev	3.901

Gaus mean : 37.1693

Gaus Width: 3.4414

Entry



B1L001S, U24-ch113, adc2

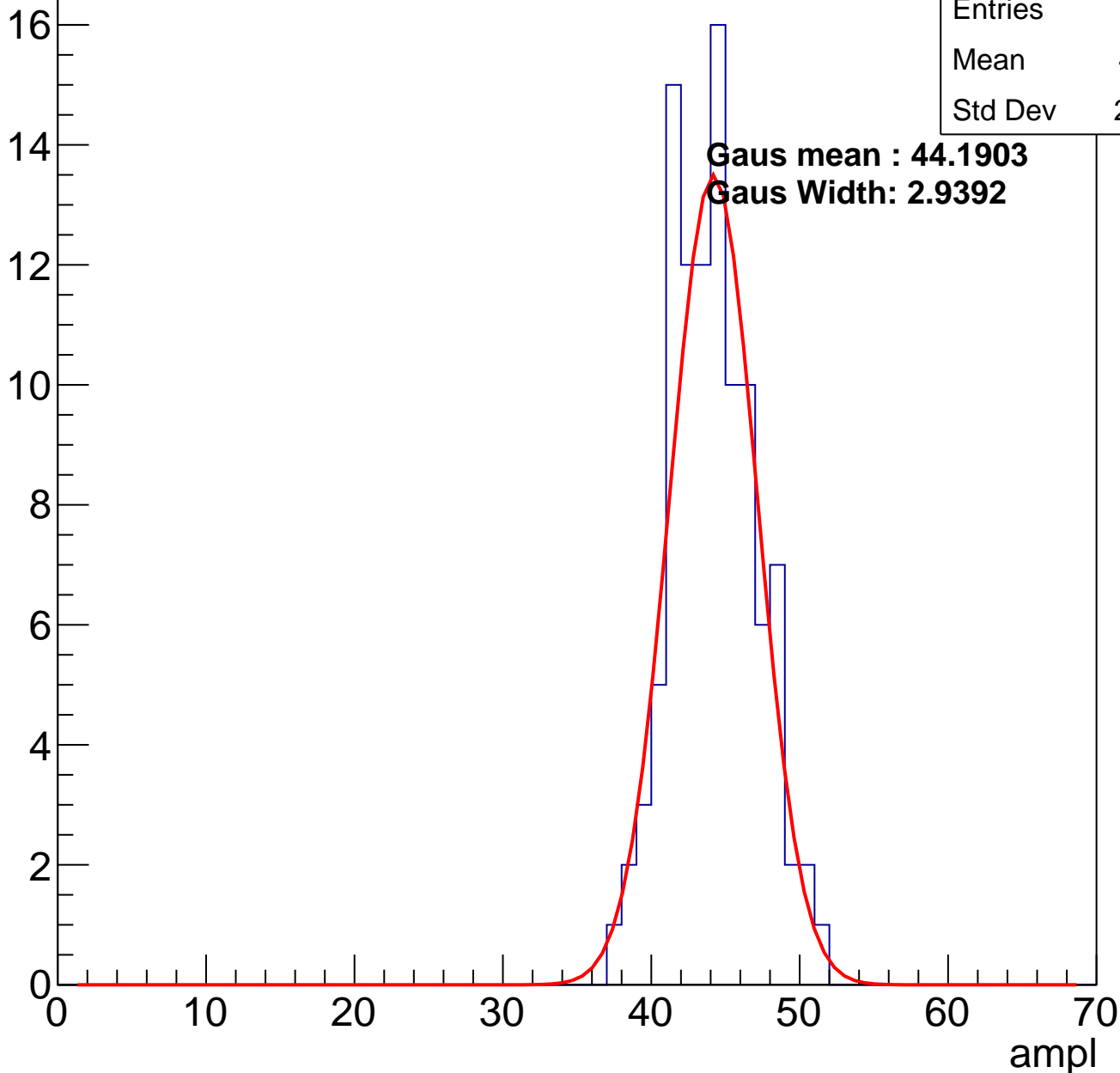
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	104
Mean	43.71
Std Dev	2.858

Gaus mean : 44.1903

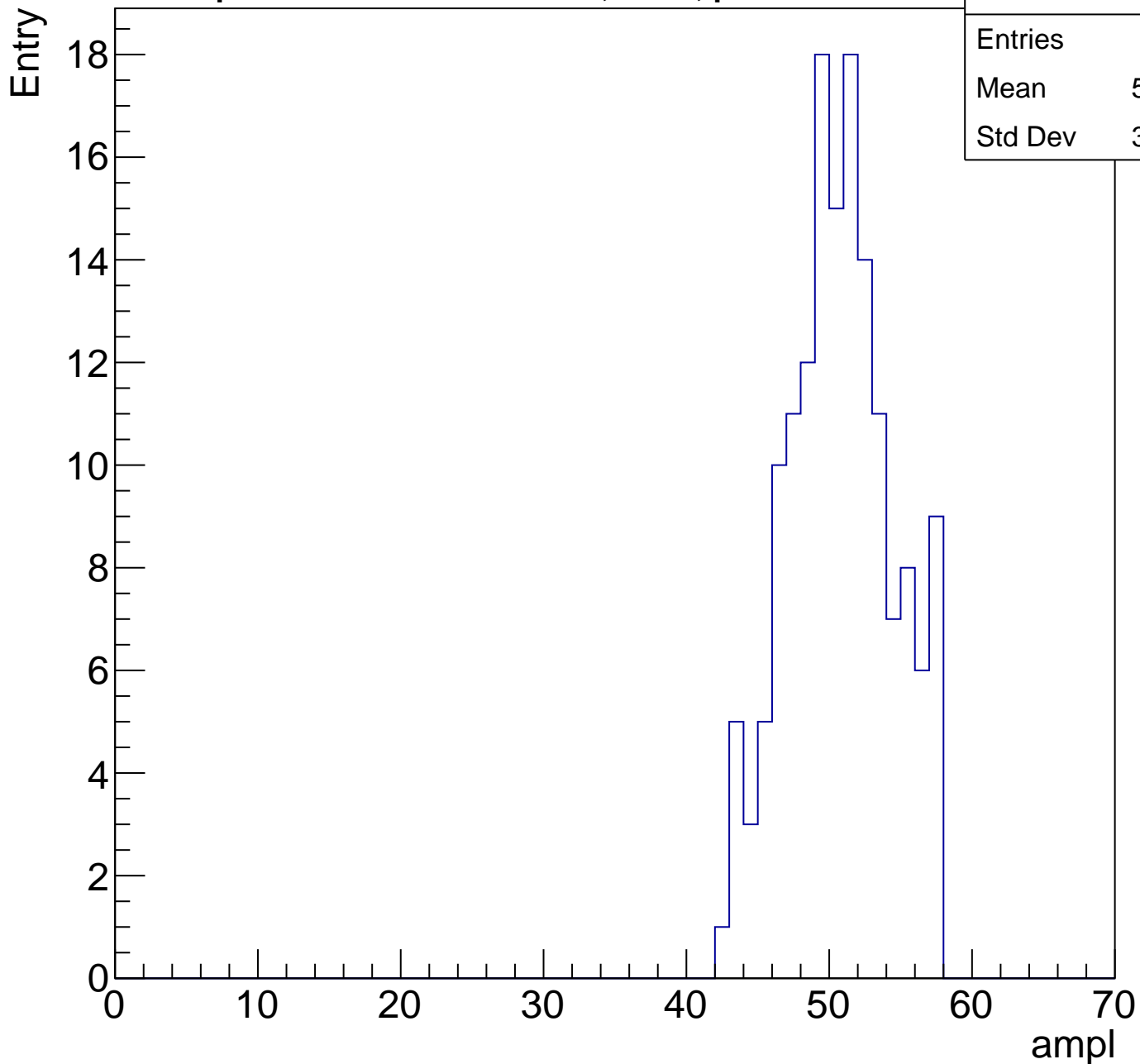
Gaus Width: 2.9392



B1L001S, U24-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	50.29
Std Dev	3.612



B1L001S, U24-ch113, adc4

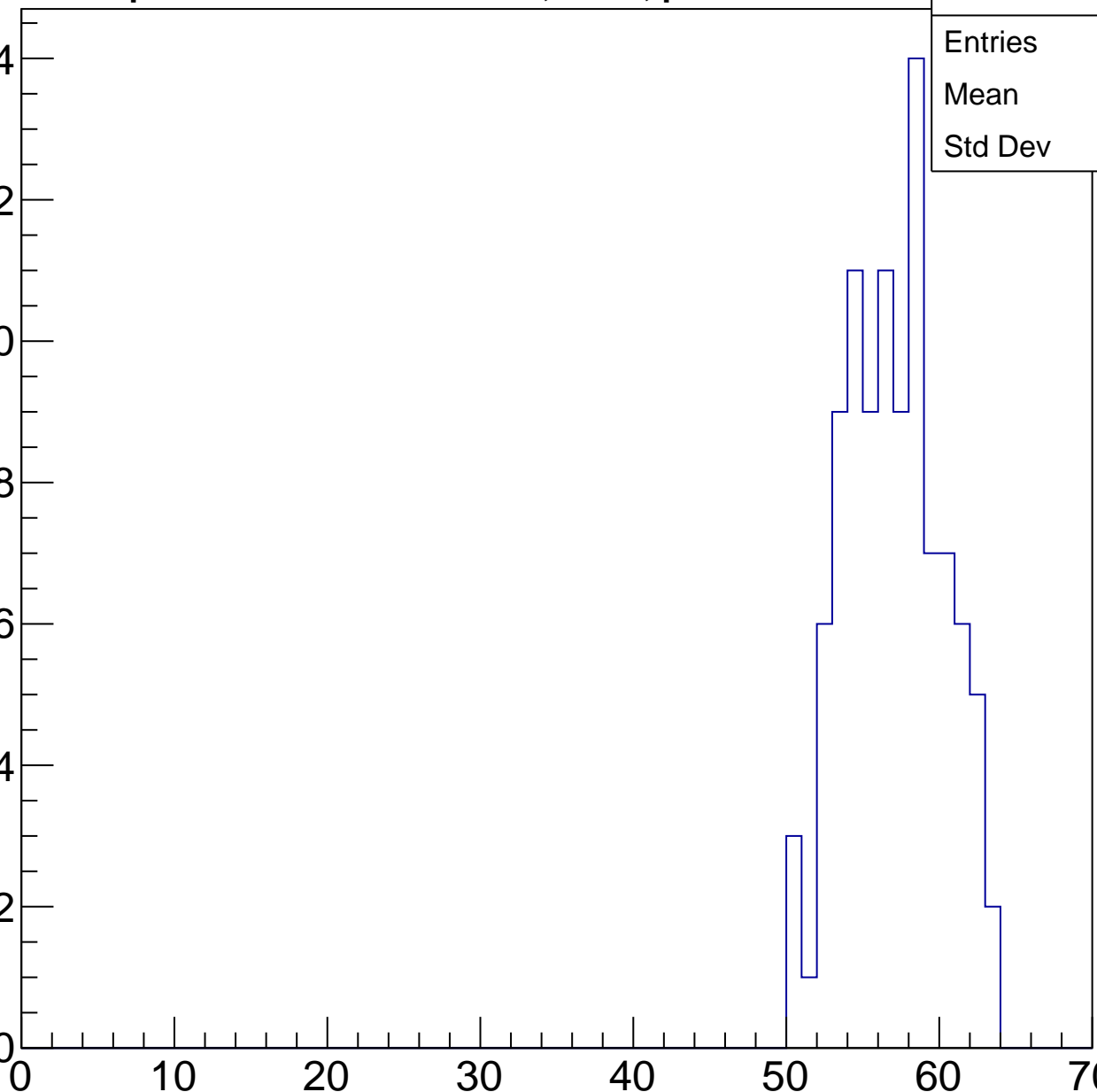
calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	56.55
Std Dev	3.163

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch113, adc5

calib_packv5_042523_0143.root, FC#2, port C2

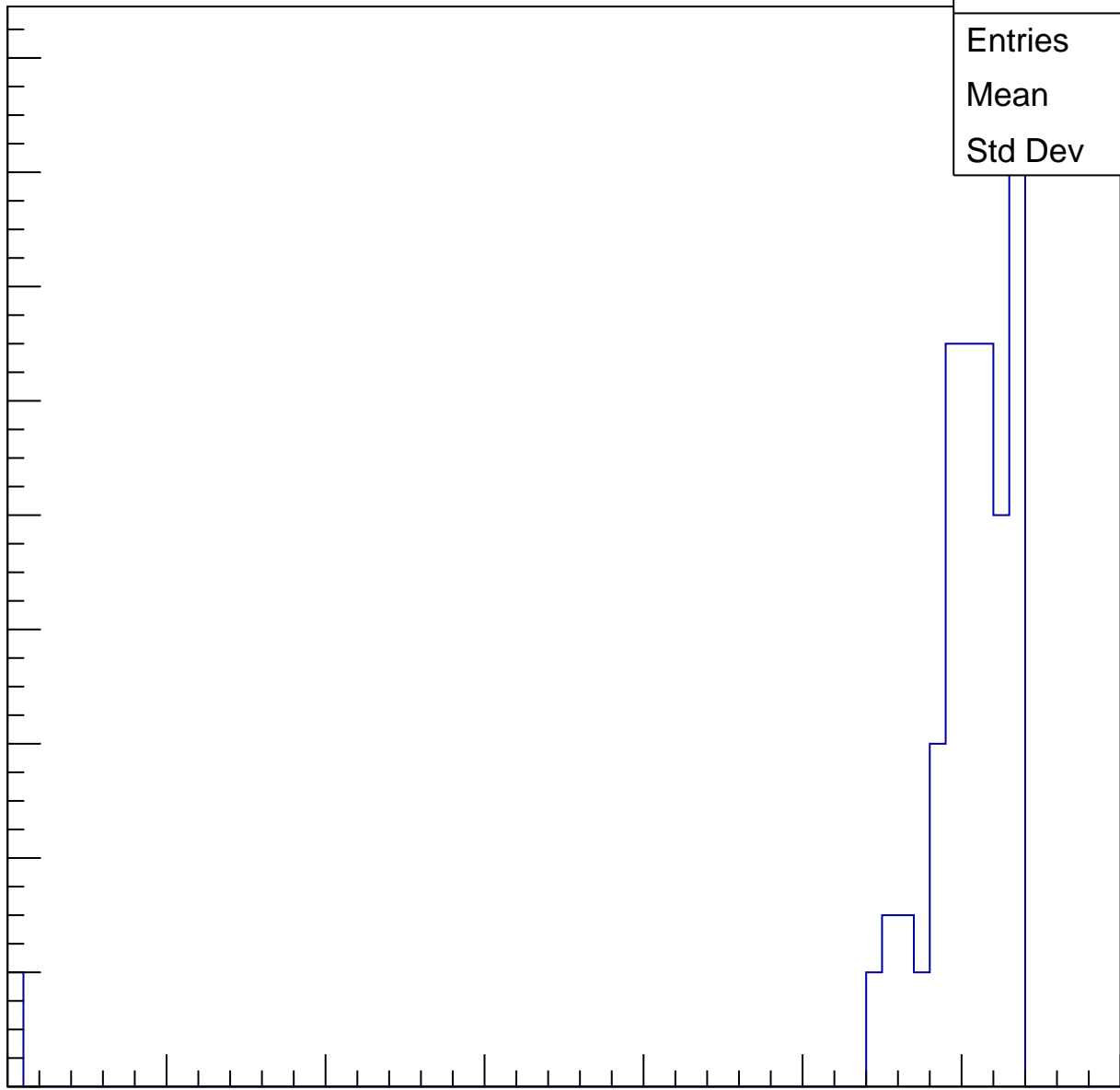
Entries	85
Mean	58.79
Std Dev	9.418

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

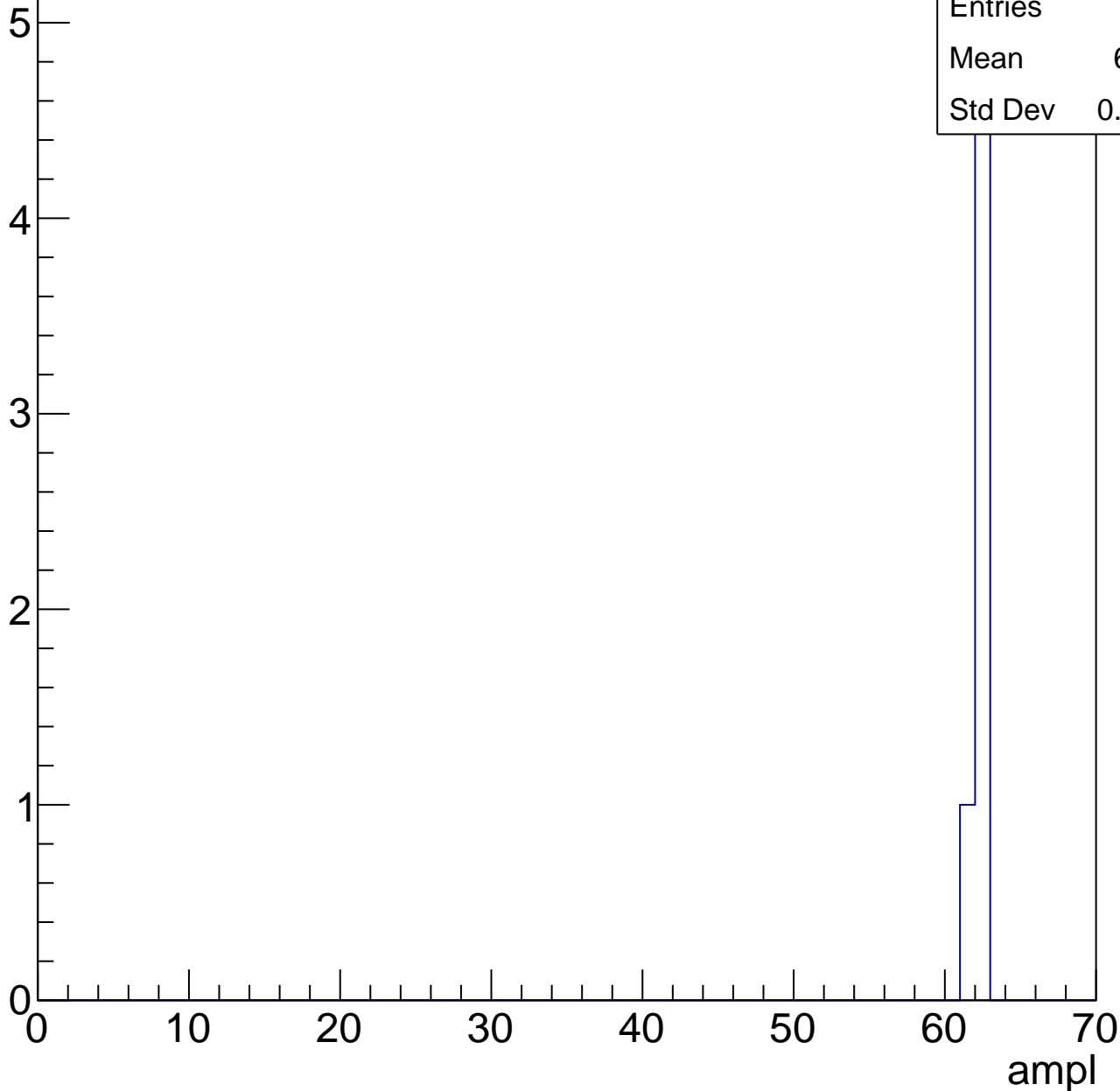


B1L001S, U24-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	6
Mean	61.83
Std Dev	0.3727



B1L001S, U24-ch113, adc7

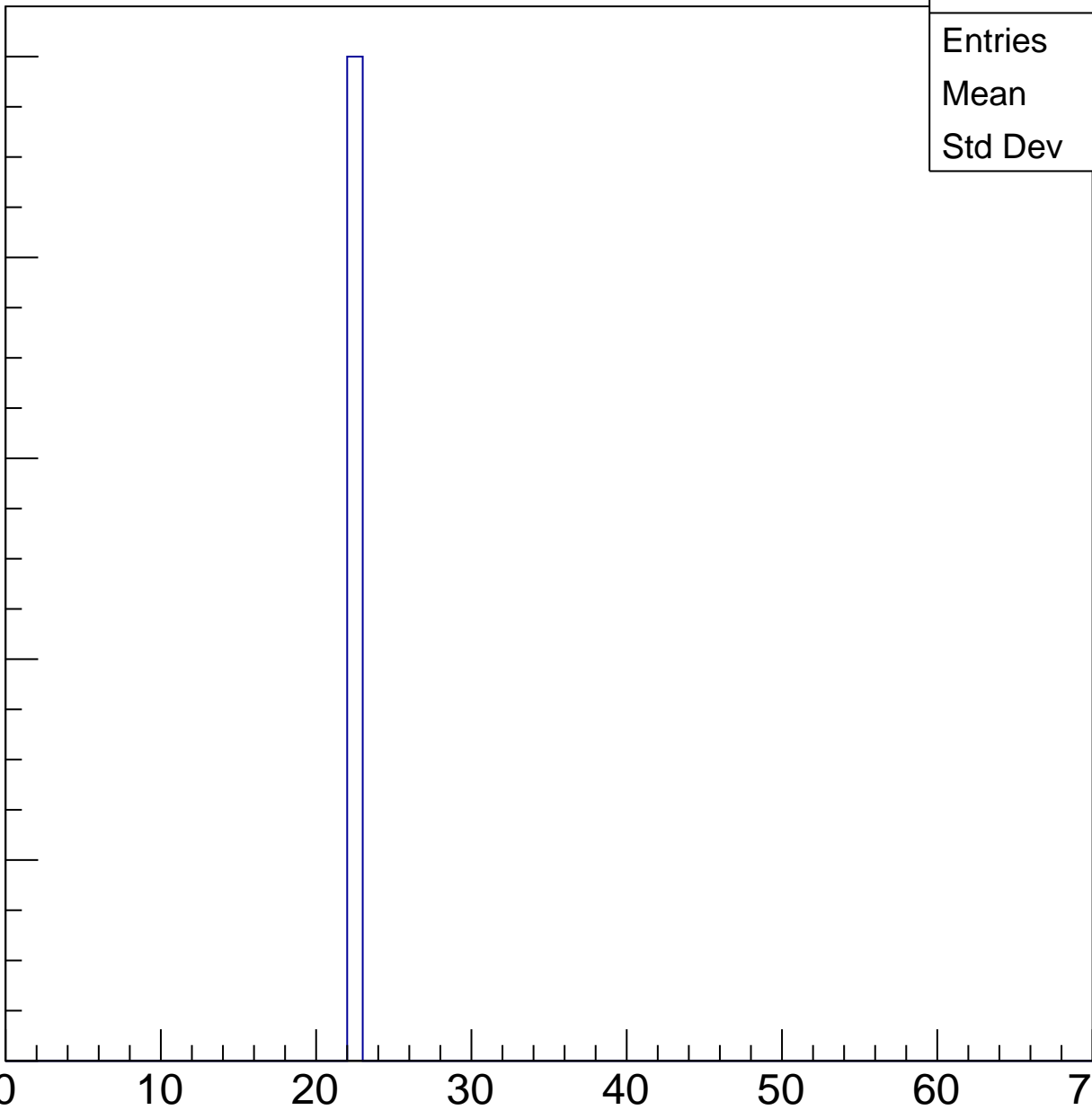
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl



B1L001S, U24-ch114, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.18
Std Dev	4.191

Gaus mean : 29.6421

Gaus Width: 3.4678

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

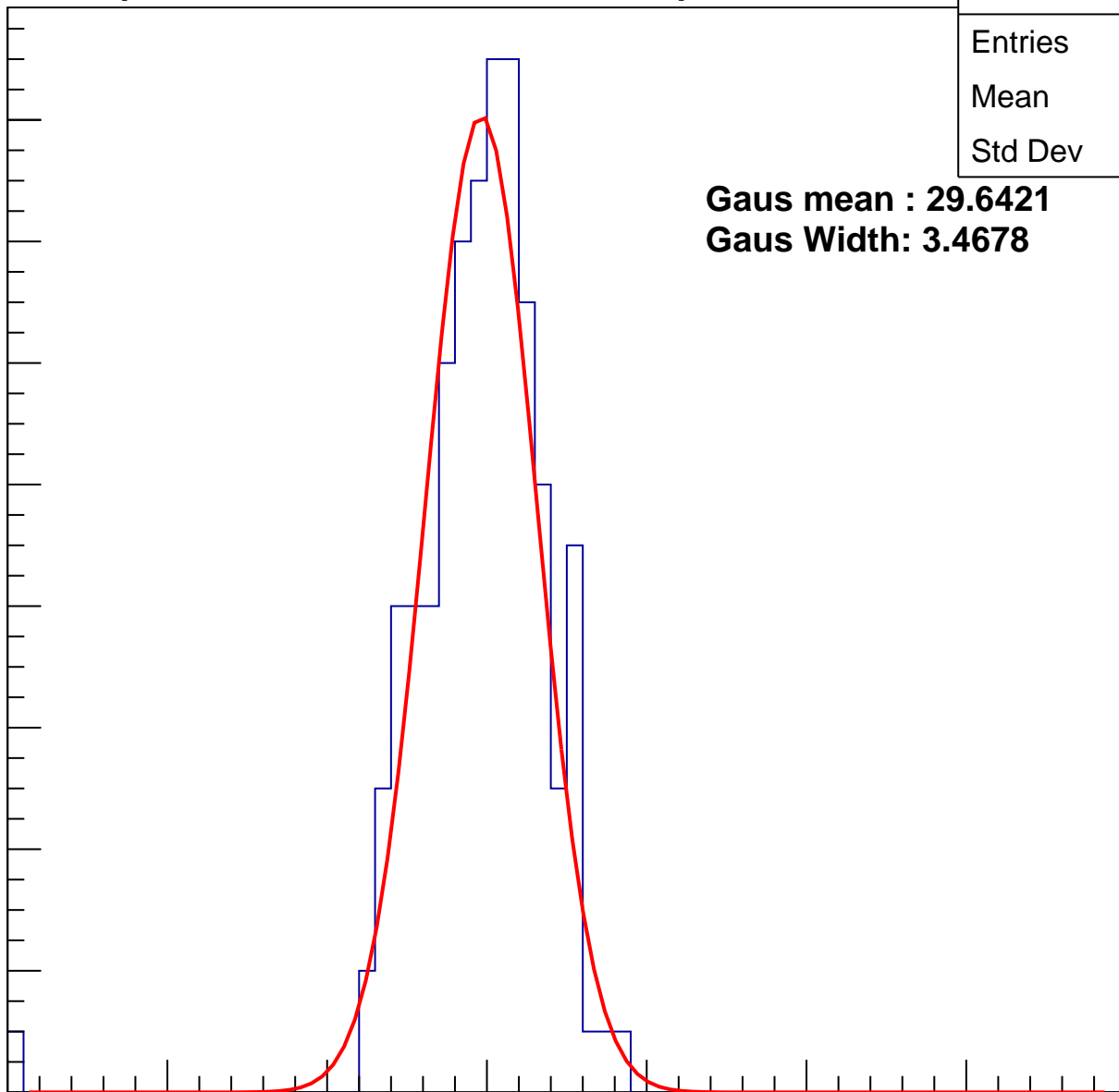
40

50

60

70

ampl



B1L001S, U24-ch114, adc1

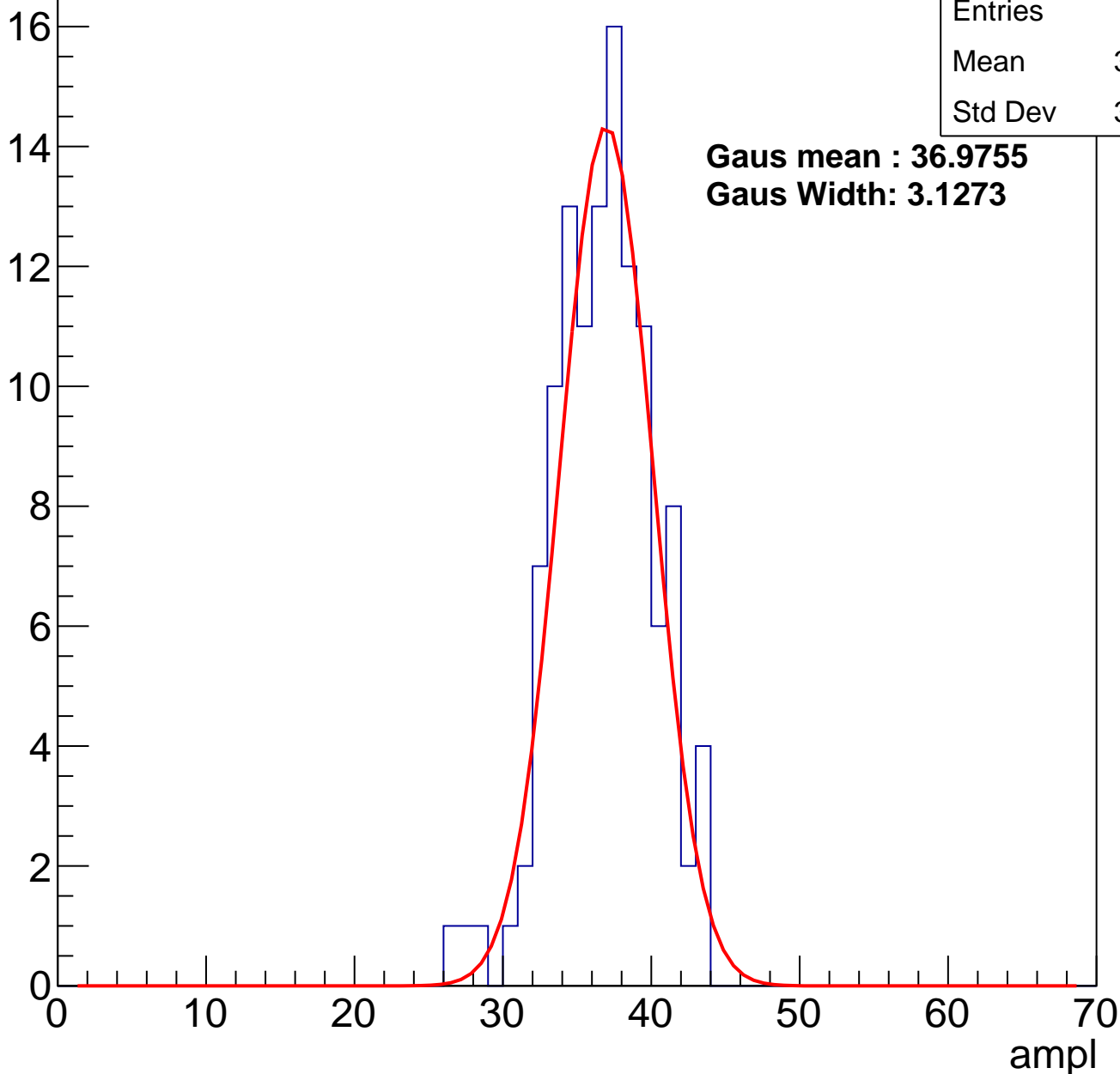
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	36.33
Std Dev	3.318

Gaus mean : 36.9755

Gaus Width: 3.1273

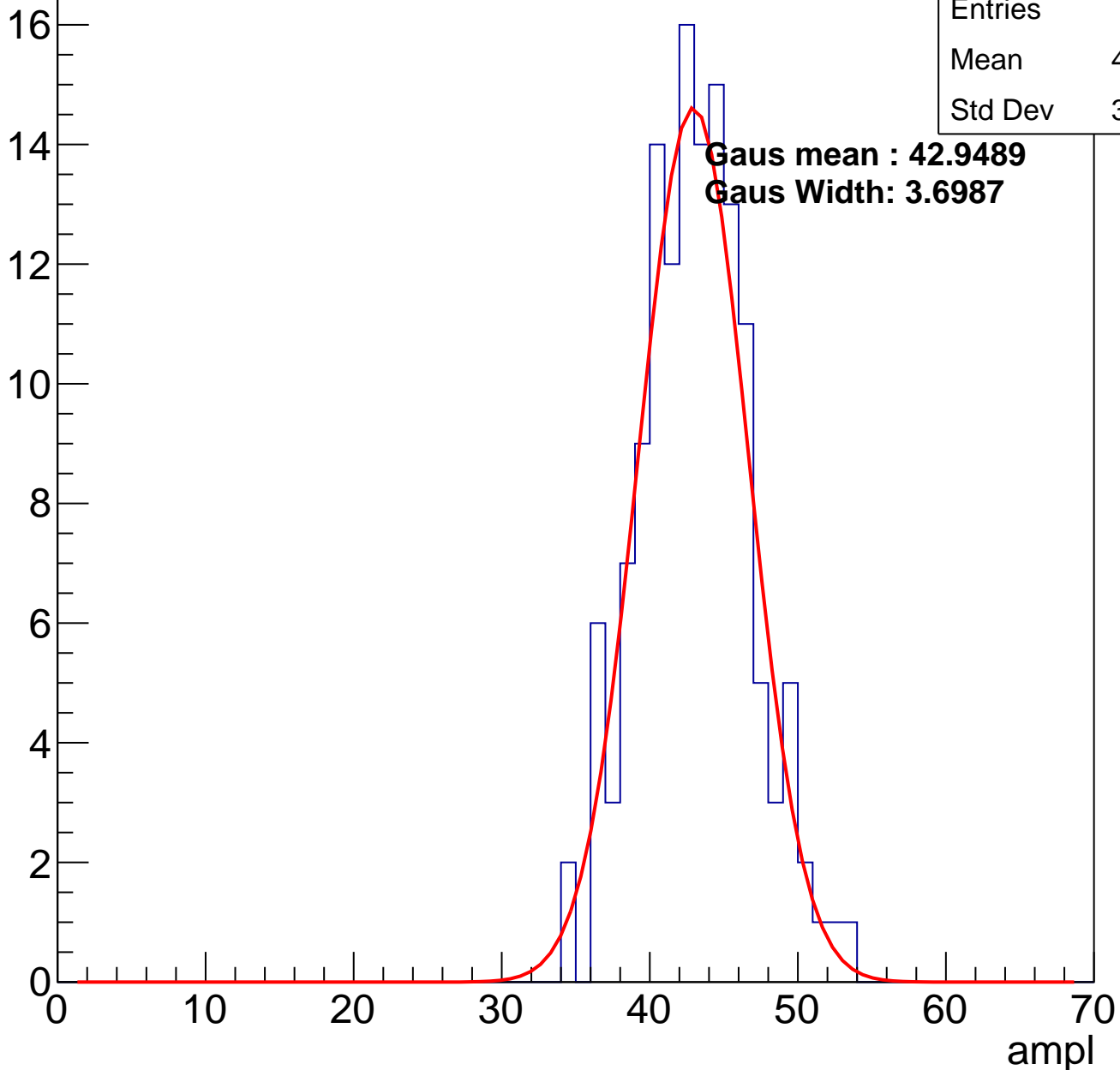
Entry



B1L001S, U24-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

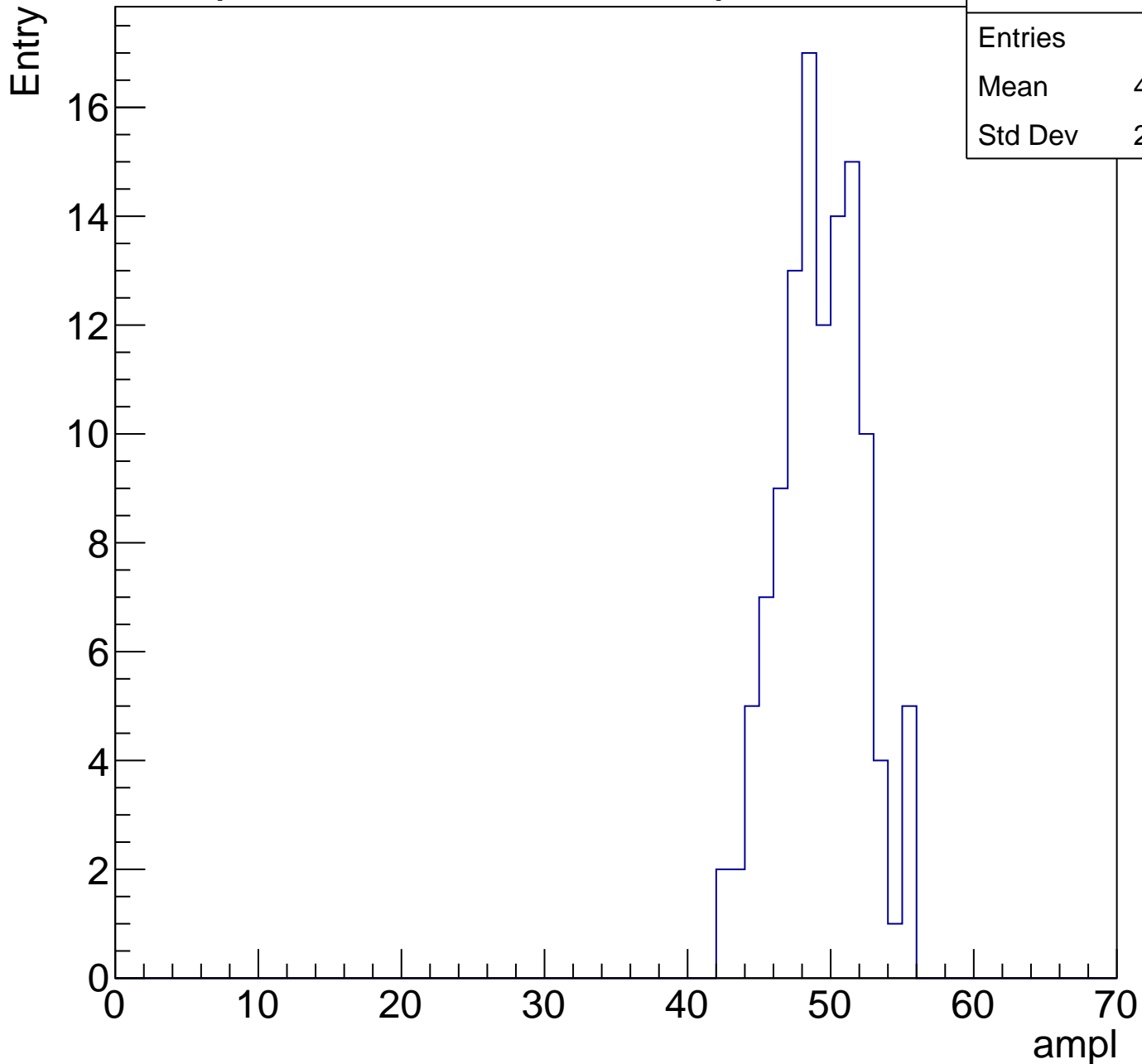
Entry



B1L001S, U24-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

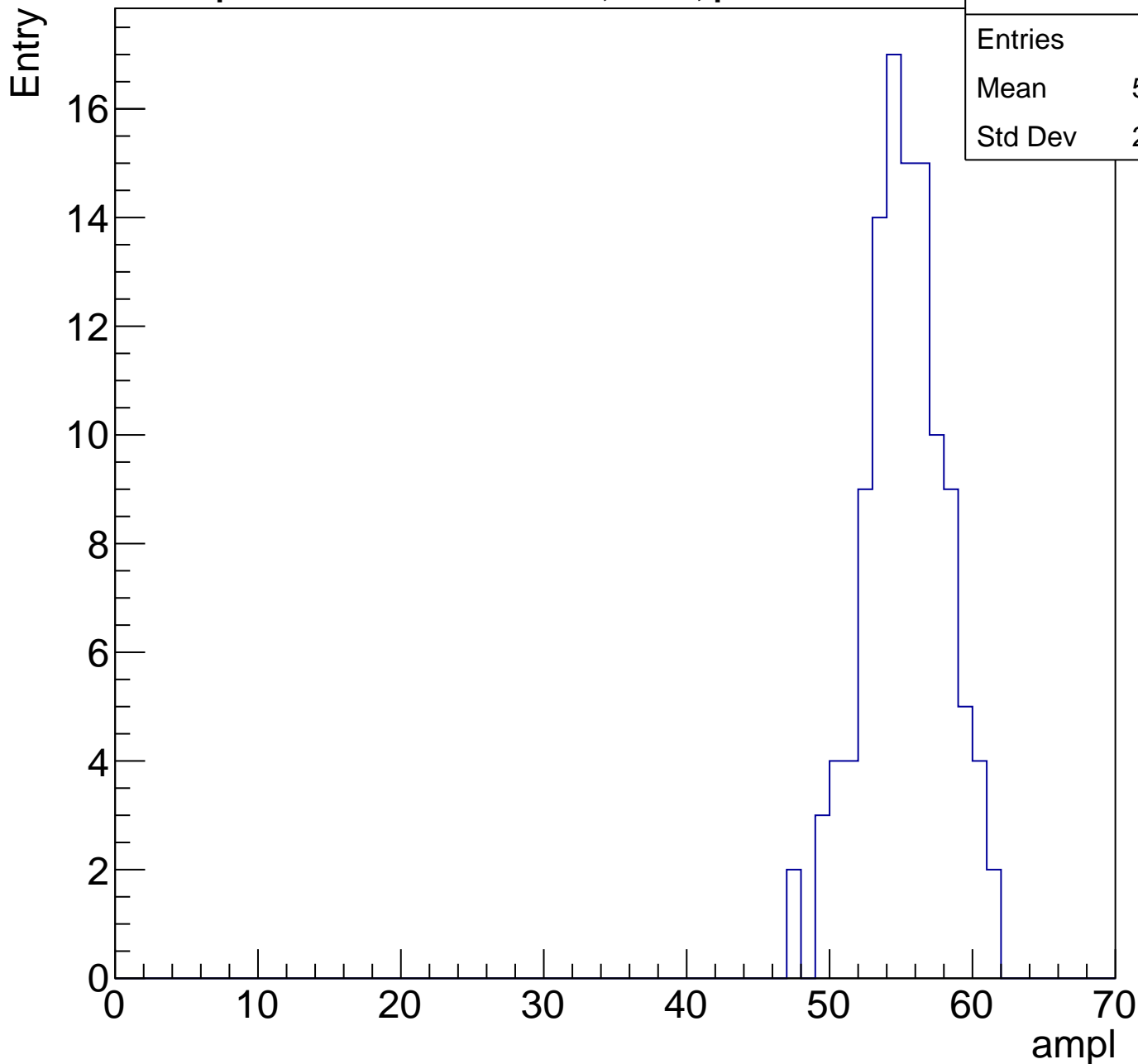
Entries	116
Mean	48.79
Std Dev	2.914



B1L001S, U24-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	54.75
Std Dev	2.877



B1L001S, U24-ch114, adc5

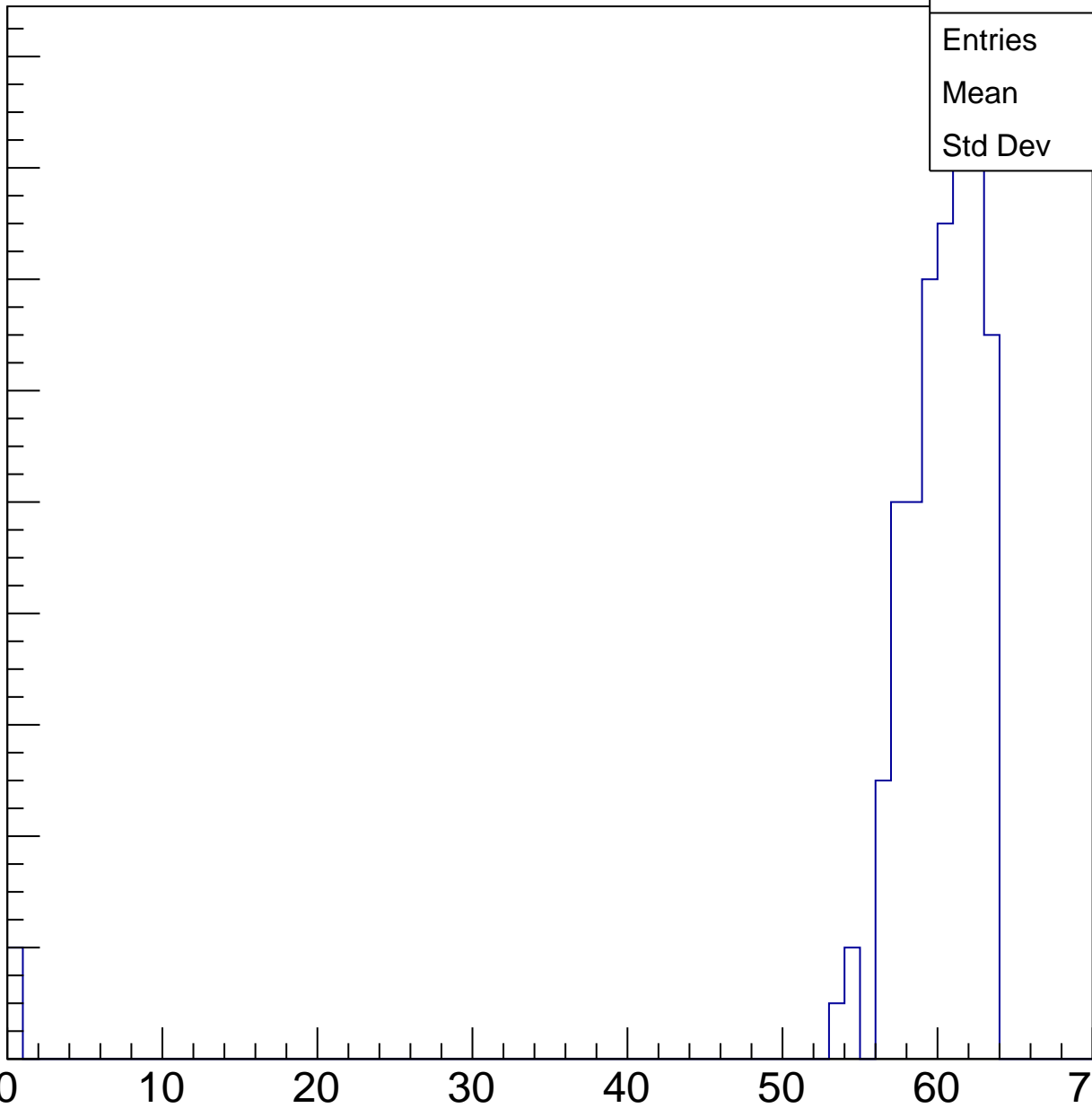
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.78
Std Dev	8.423

ampl

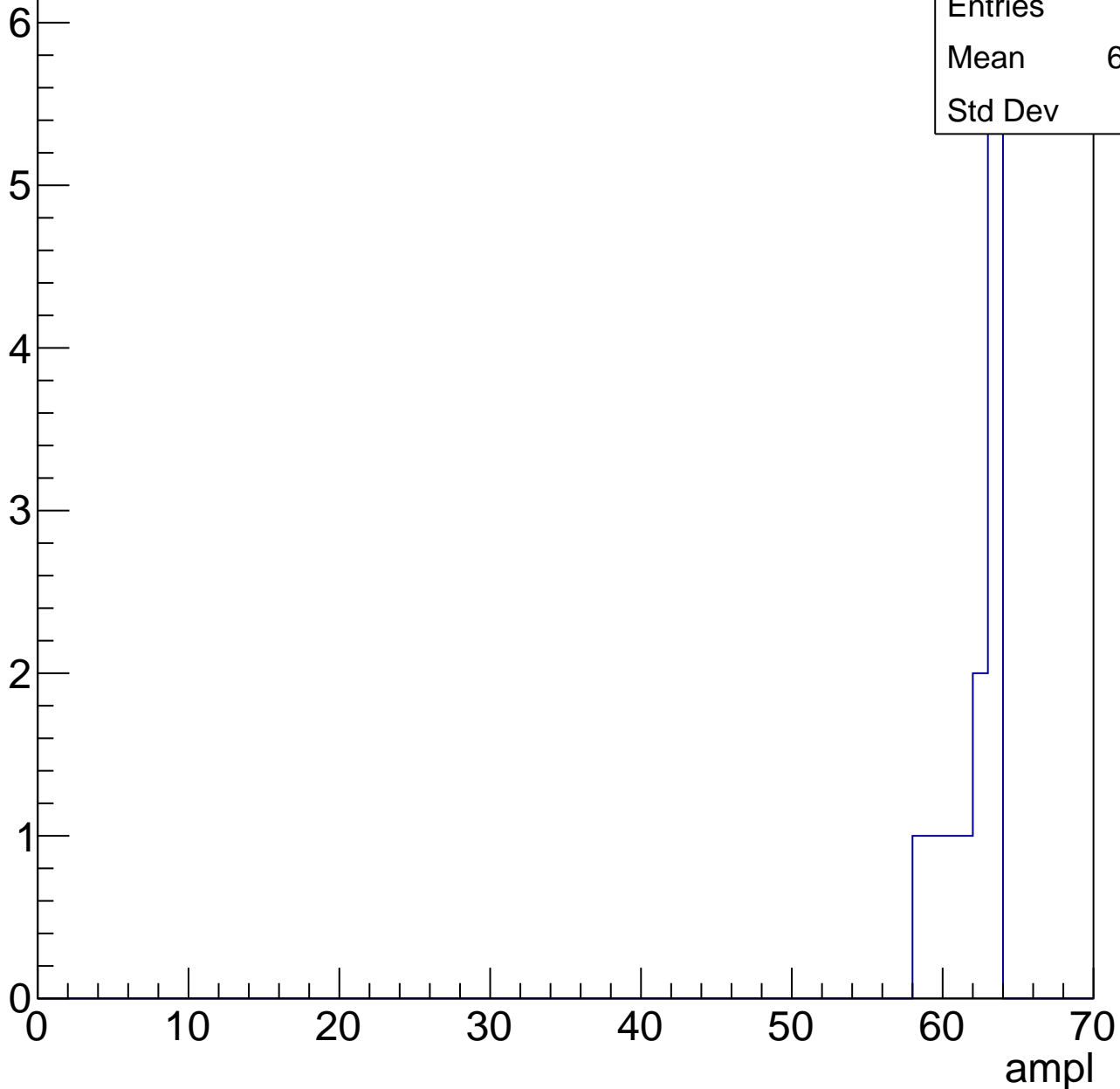


B1L001S, U24-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.67
Std Dev	1.7



B1L001S, U24-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L001S, U24-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	28.91
Std Dev	4.718

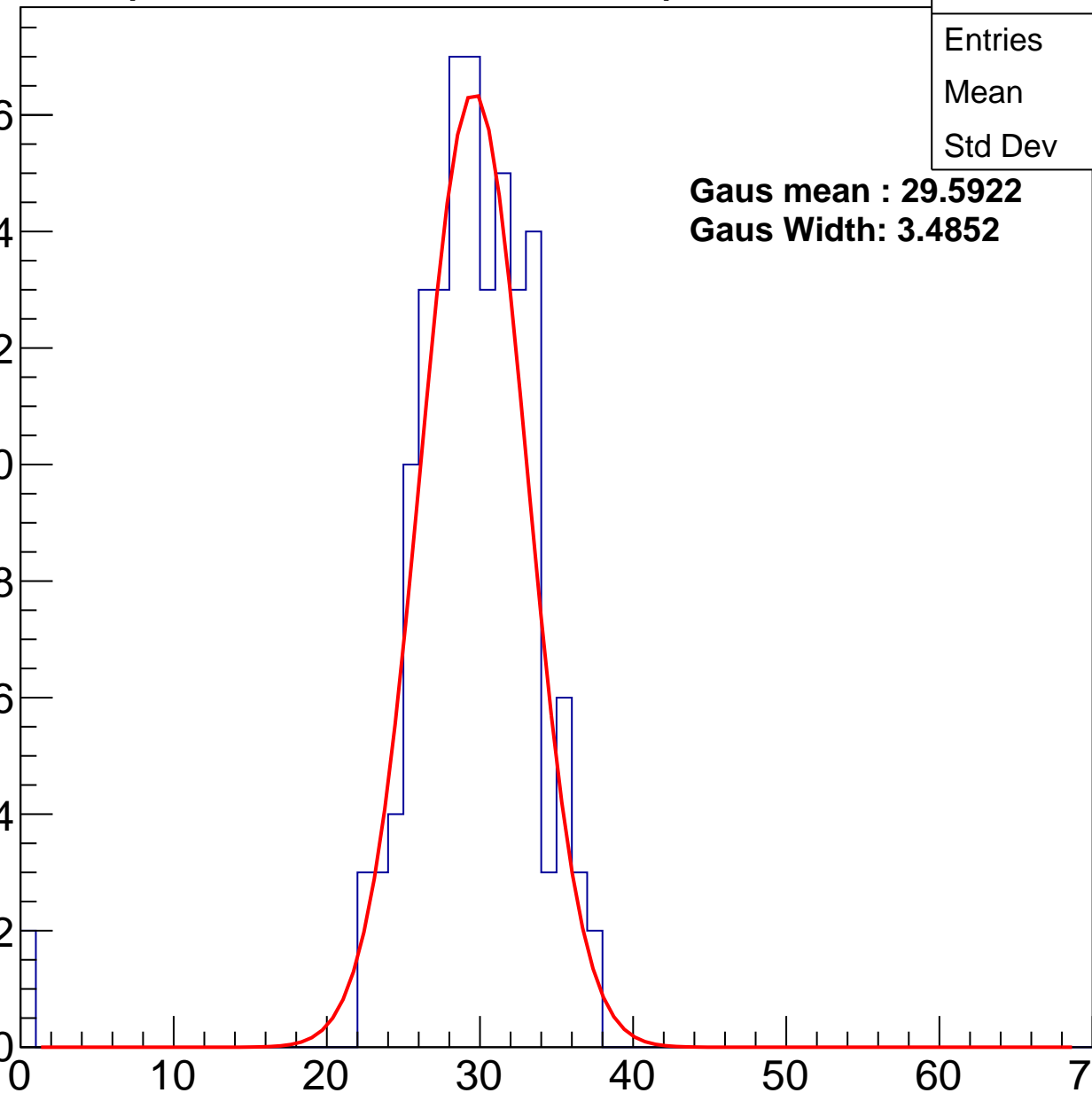
Gaus mean : 29.5922

Gaus Width: 3.4852

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch115, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.56
Std Dev	3.753

Gaus mean : 37.3620

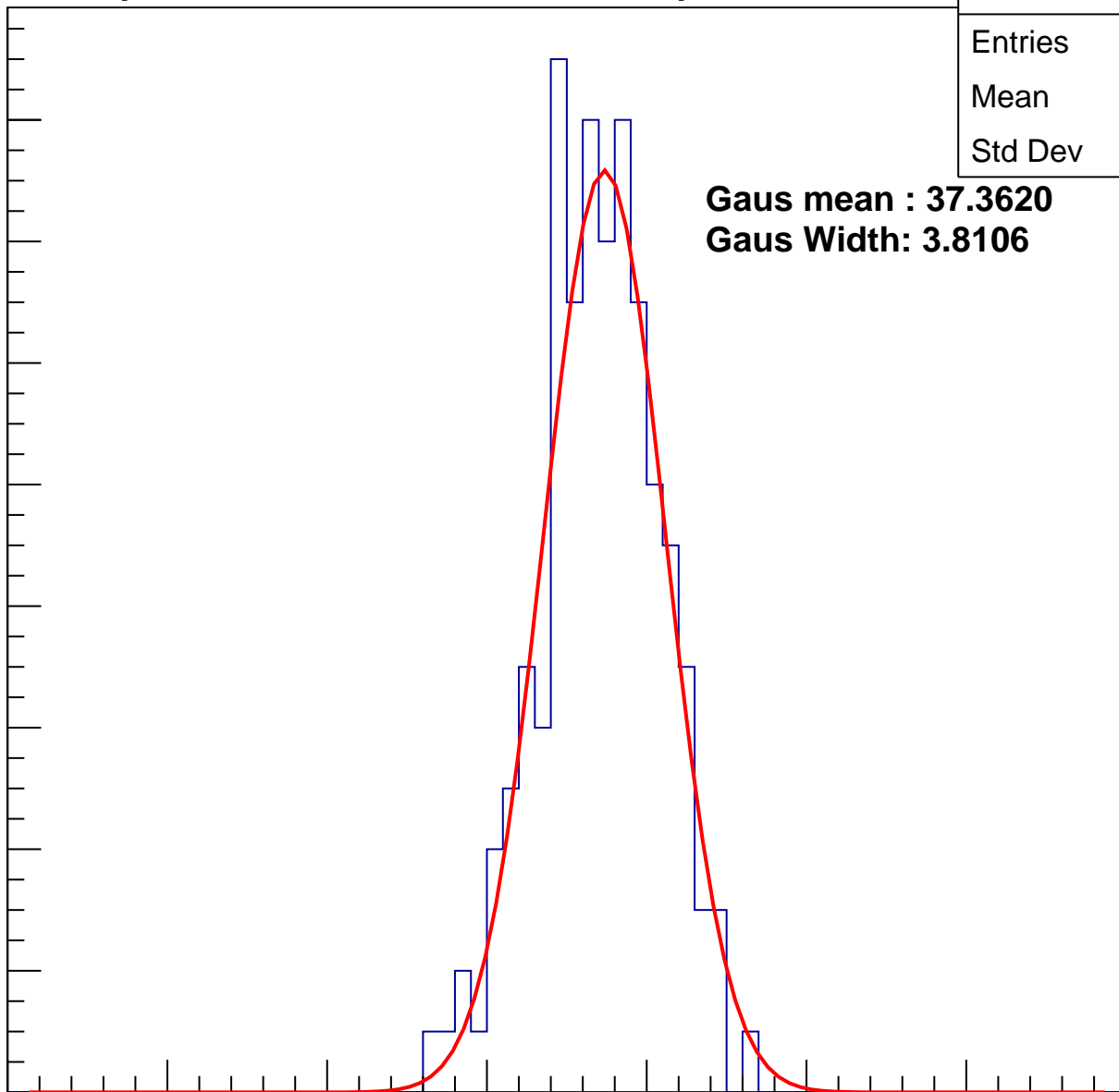
Gaus Width: 3.8106

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

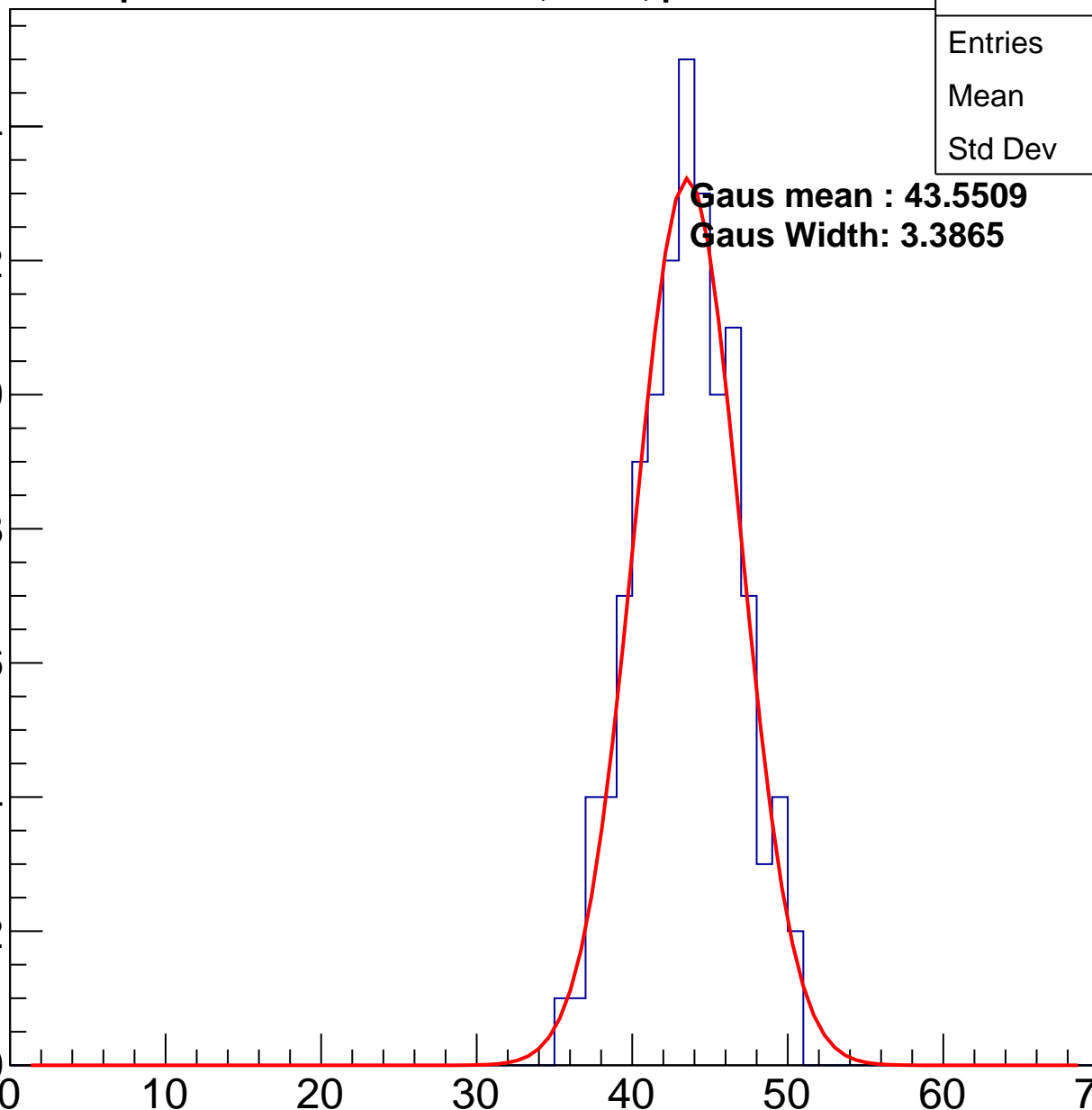
70

ampl

Entries	113
Mean	43.01
Std Dev	3.236

Gaus mean : 43.5509

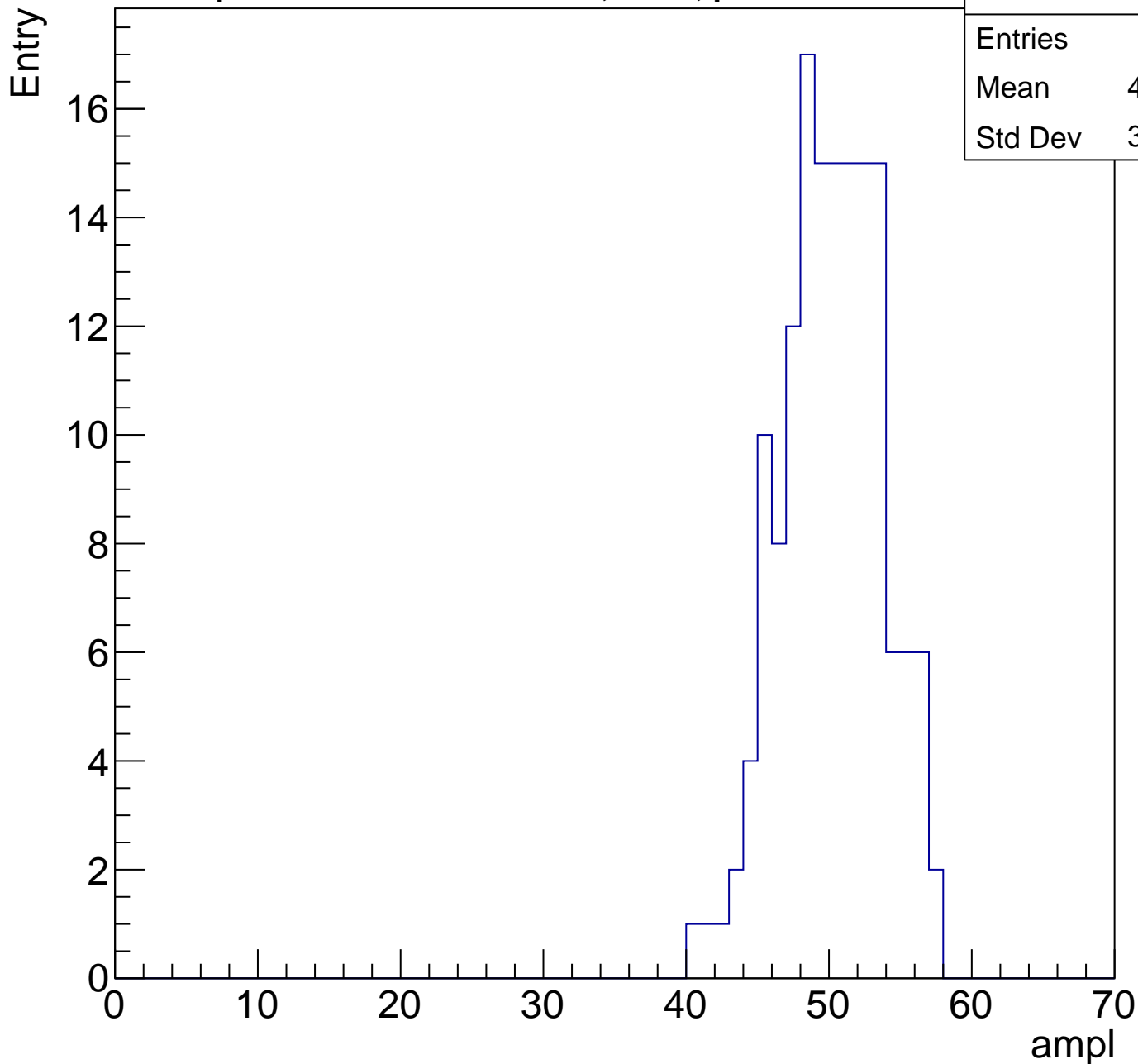
Gaus Width: 3.3865



B1L001S, U24-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

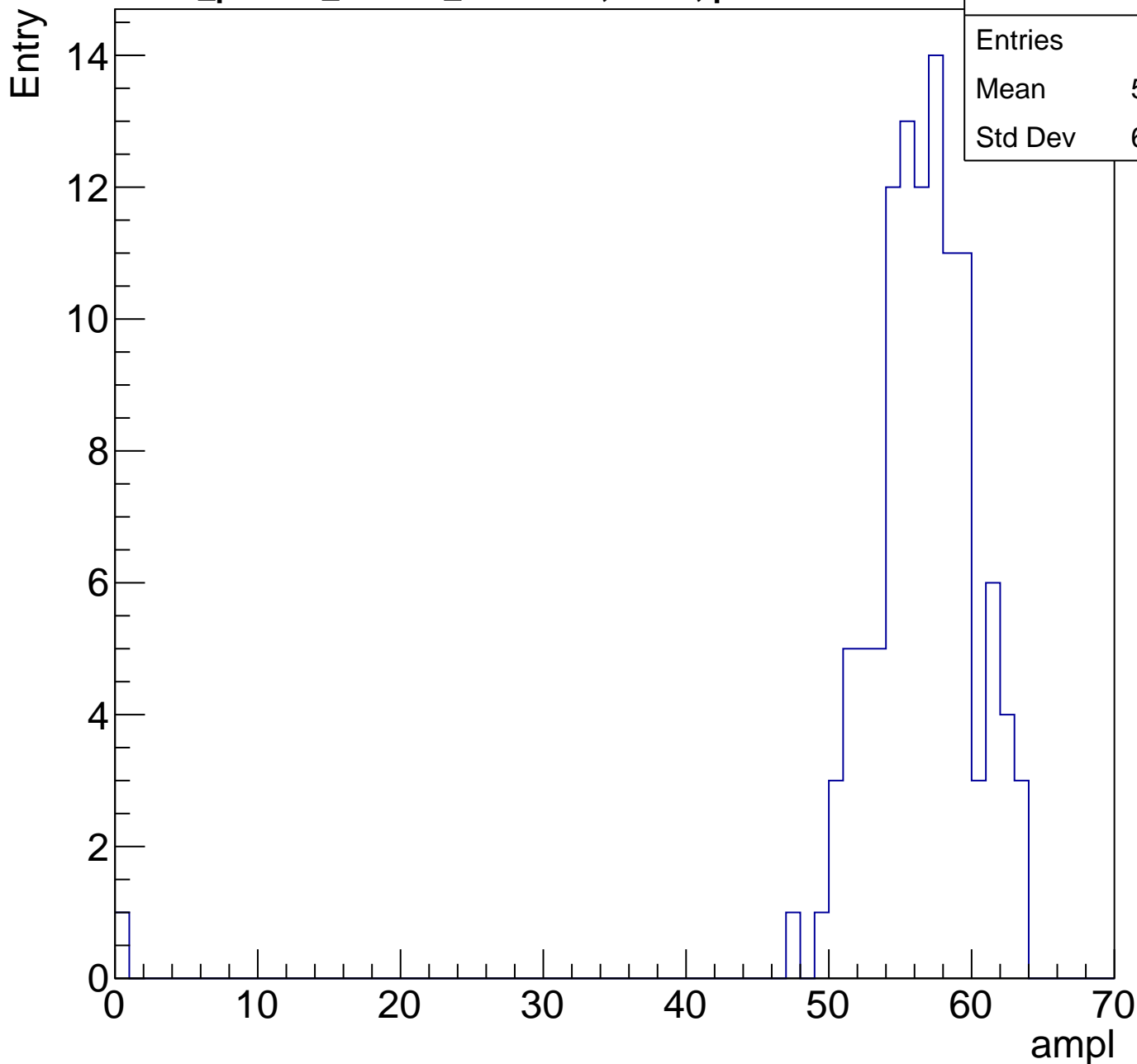
Entries	151
Mean	49.75
Std Dev	3.468



B1L001S, U24-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	55.72
Std Dev	6.268



B1L001S, U24-ch115, adc5

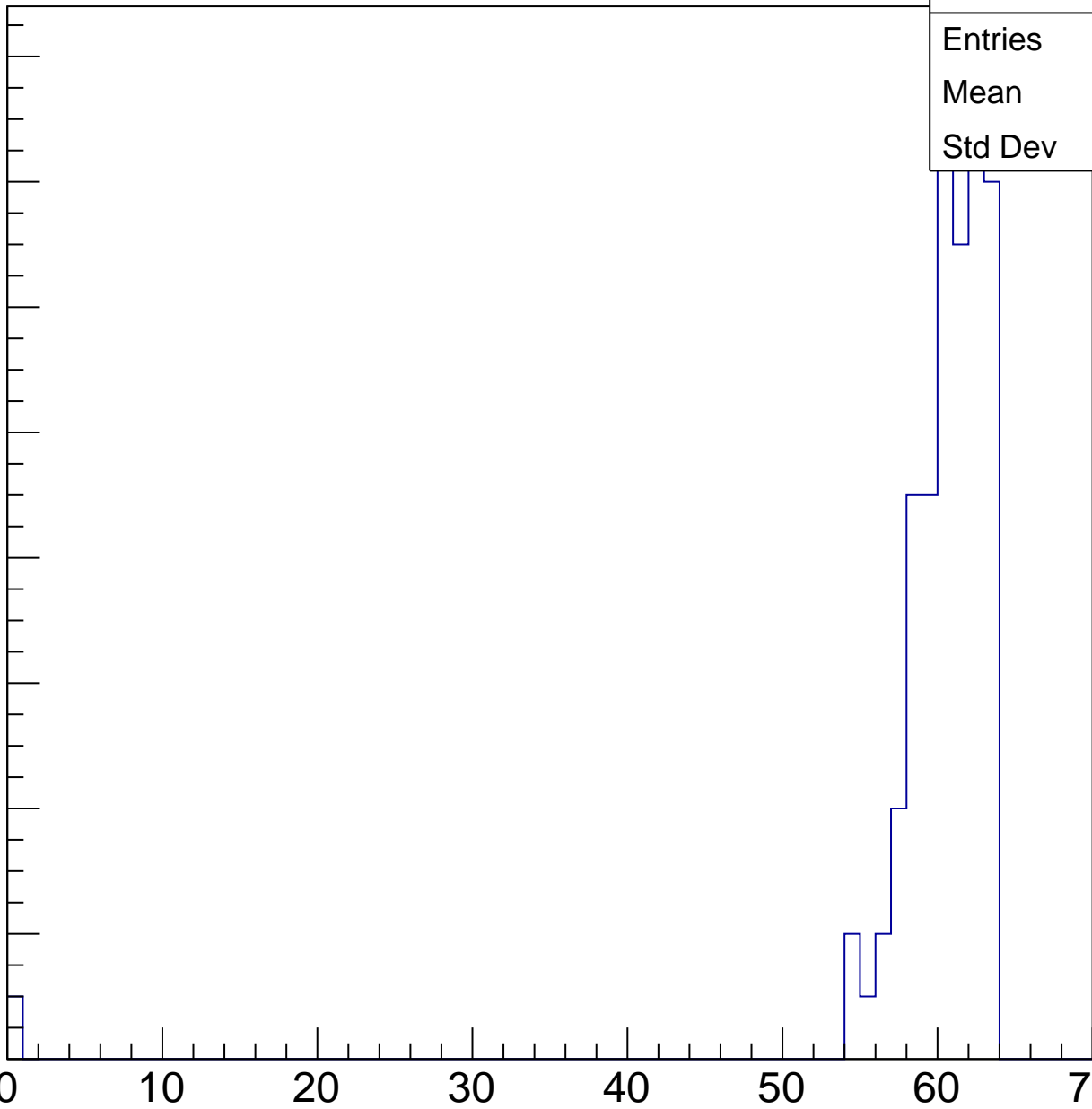
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.57
Std Dev	6.778

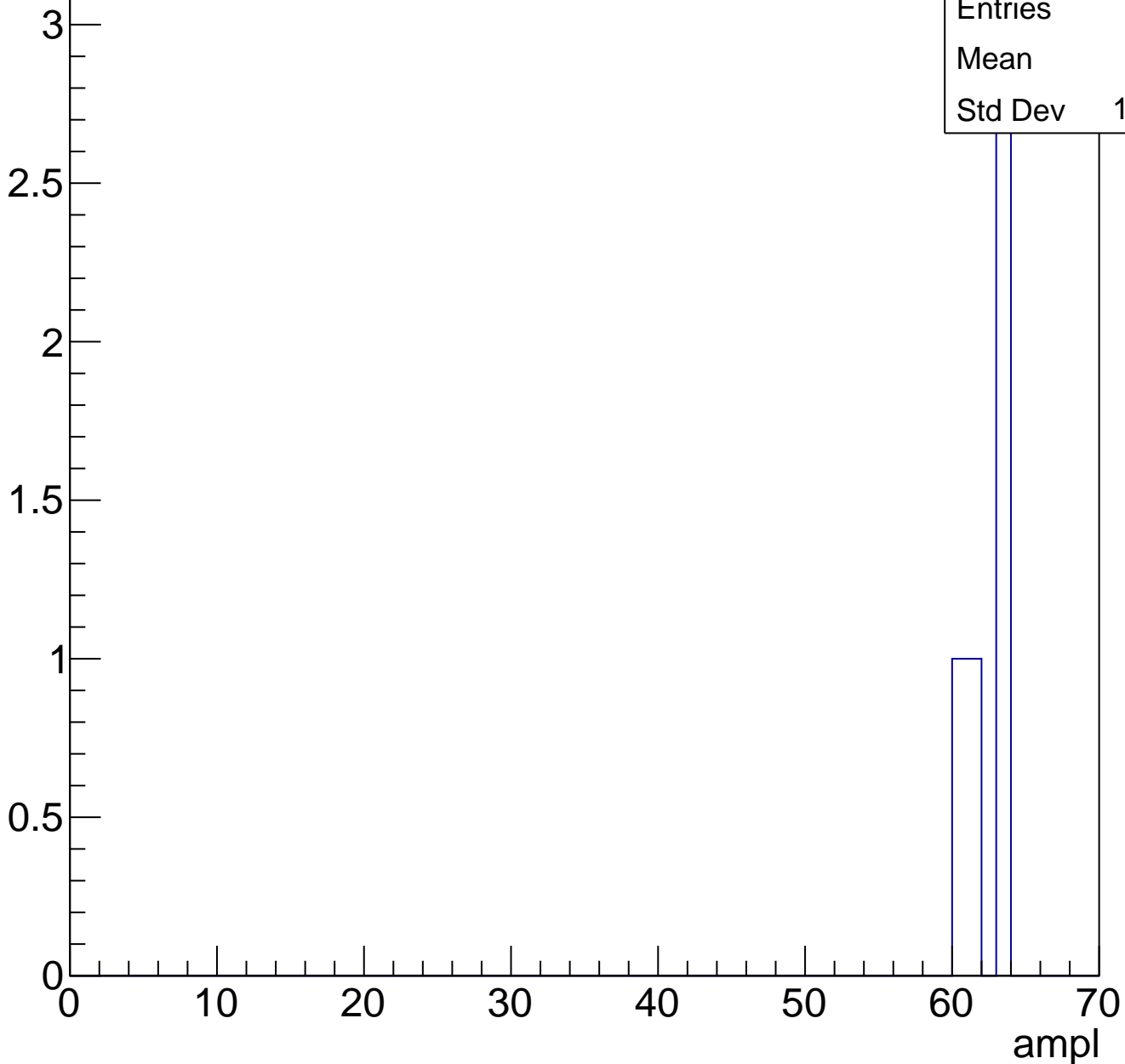
ampl



B1L001S, U24-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

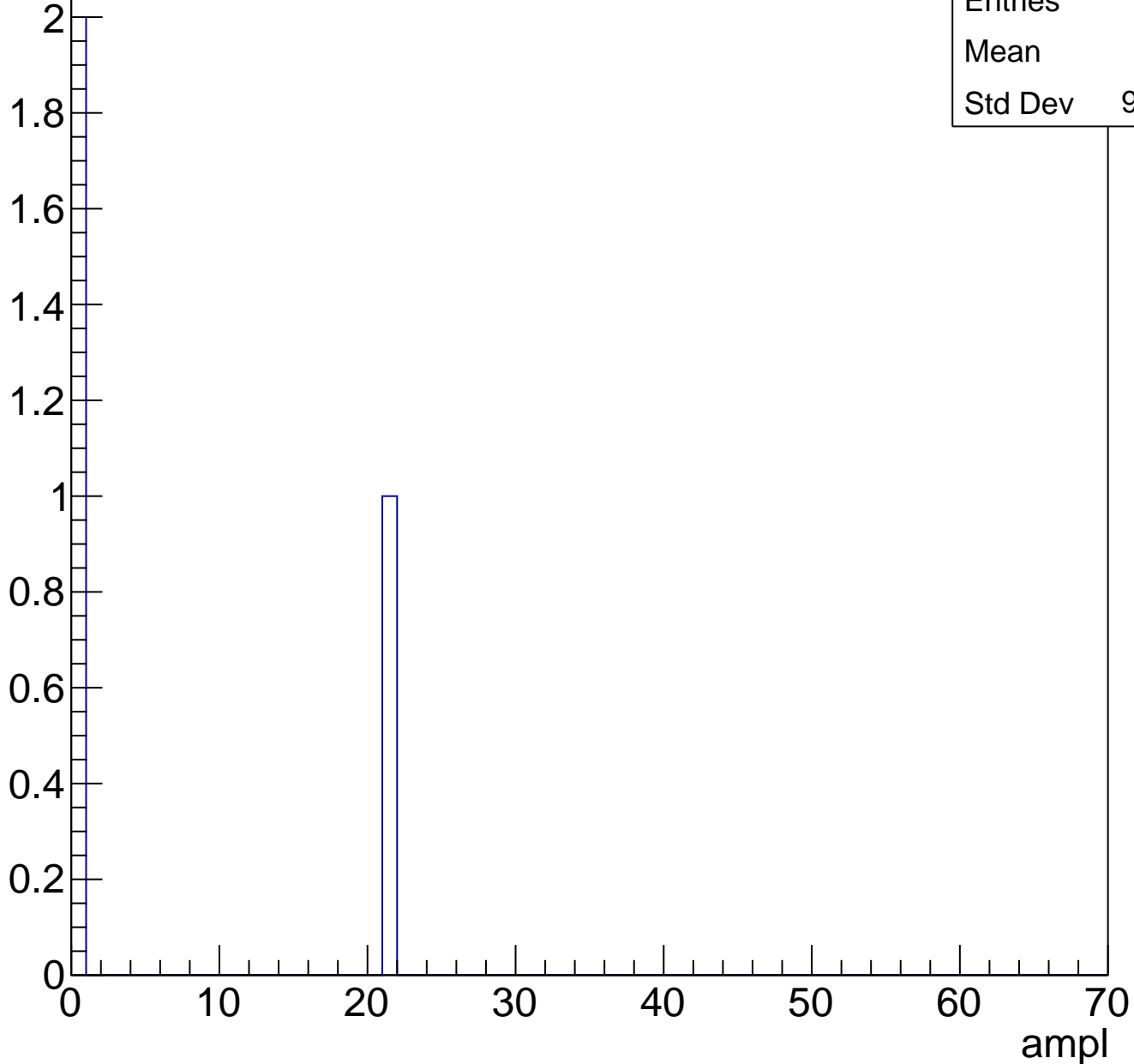
Entry



B1L001S, U24-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L001S, U24-ch116, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	27.74
Std Dev	3.01

Gaus mean : 28.1495

Gaus Width: 3.1919

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

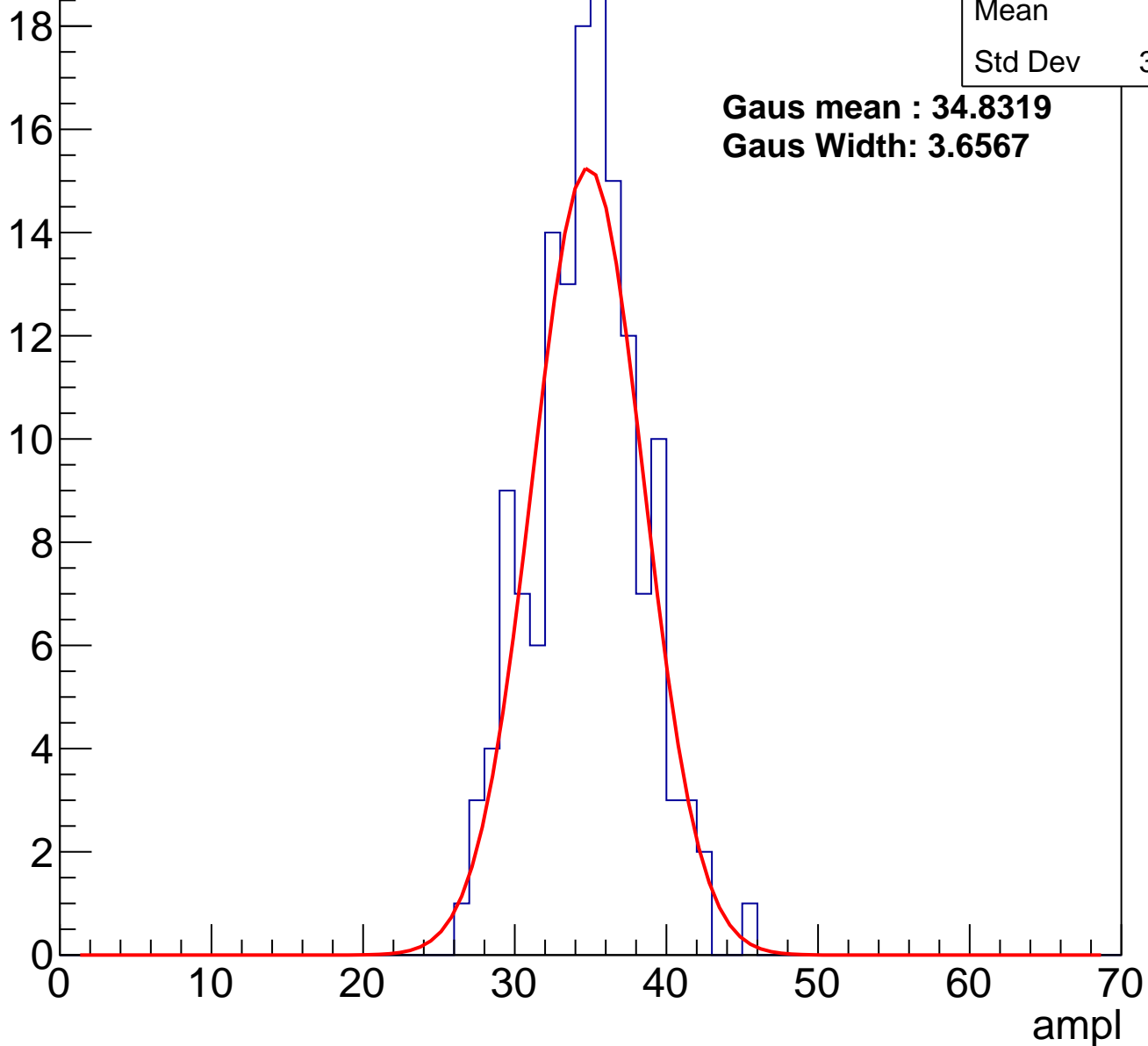
60

70

B1L001S, U24-ch116, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch116, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

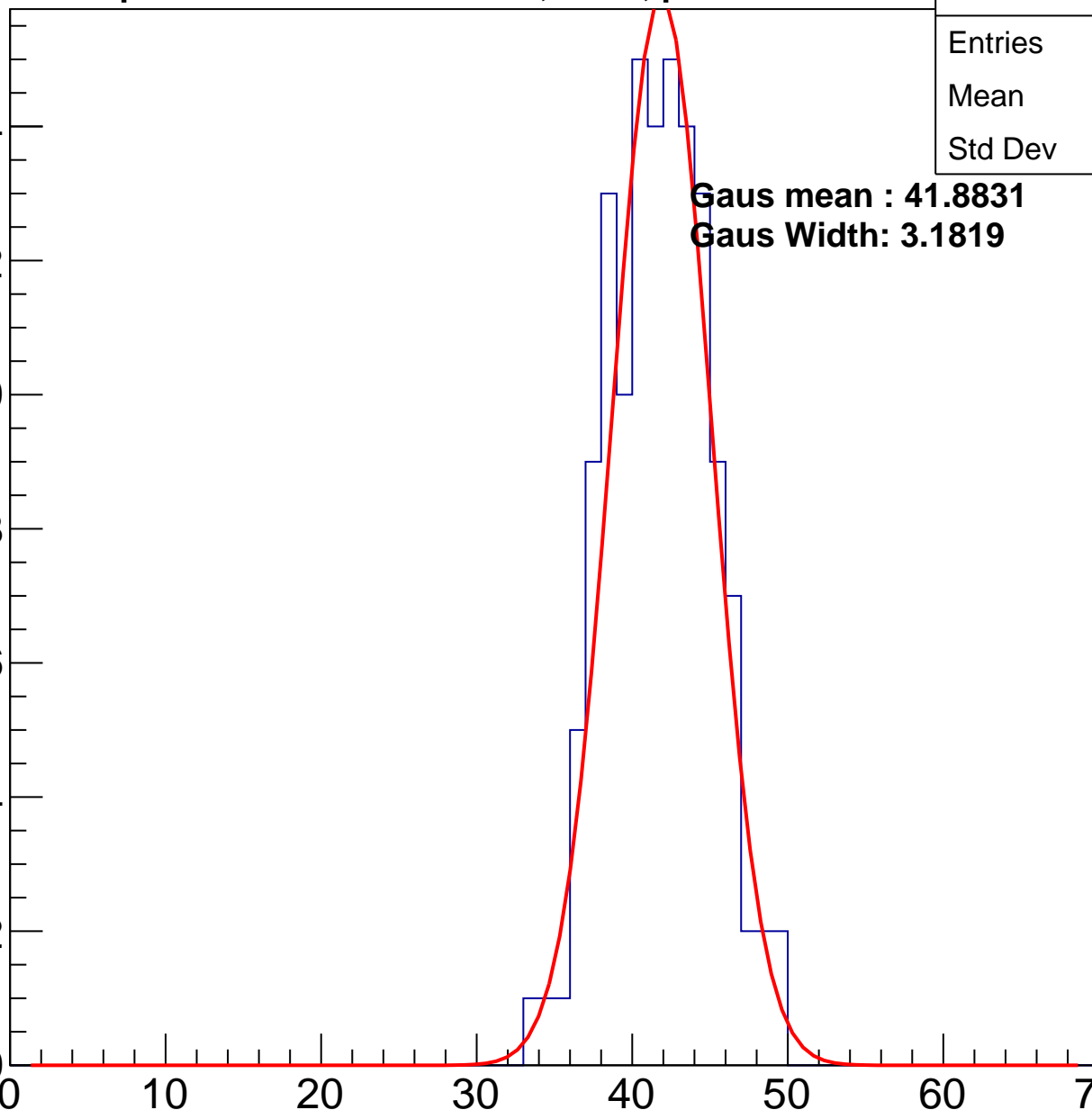
70

ampl

Entries	133
Mean	41.29
Std Dev	3.225

Gaus mean : 41.8831

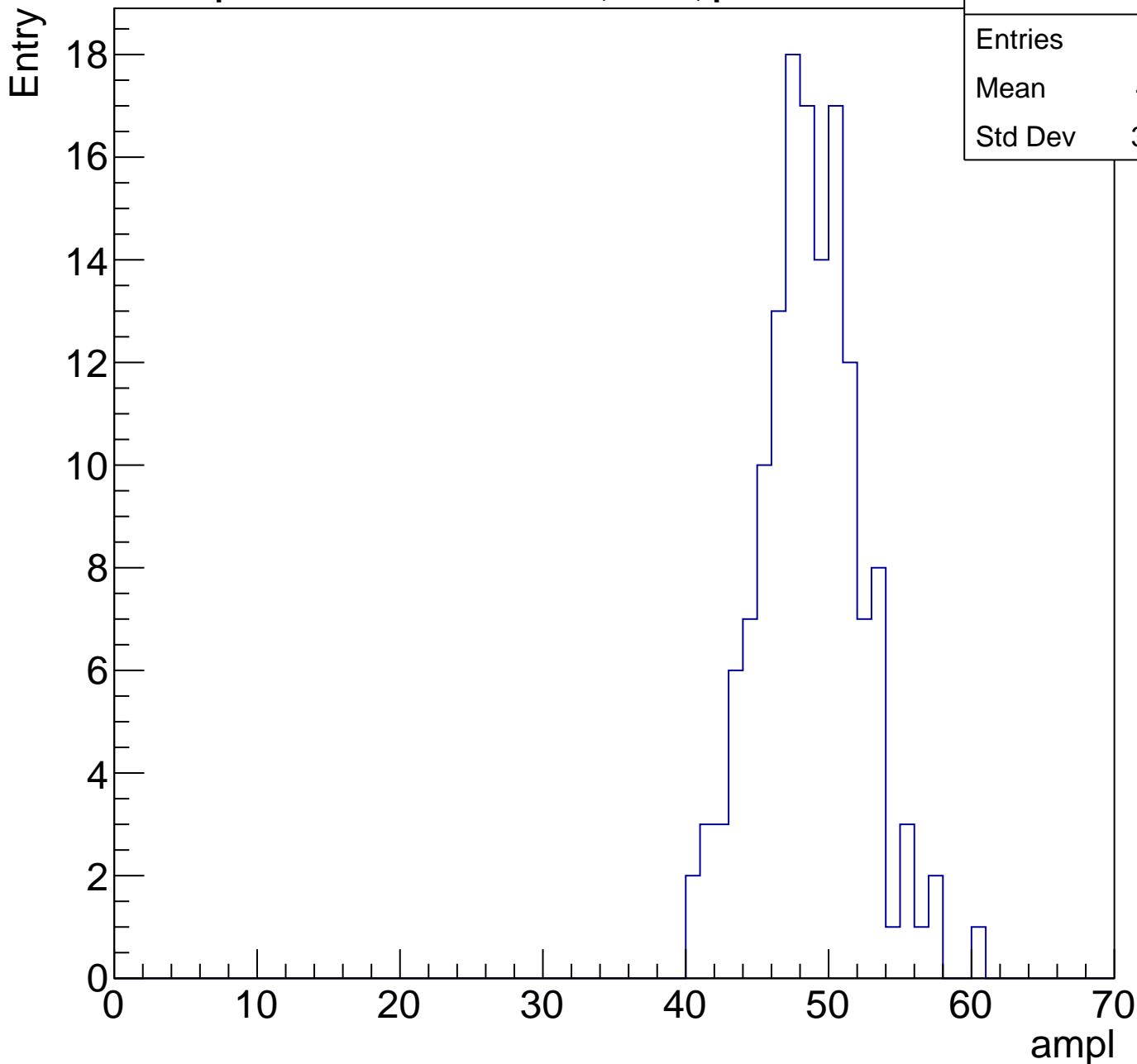
Gaus Width: 3.1819



B1L001S, U24-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

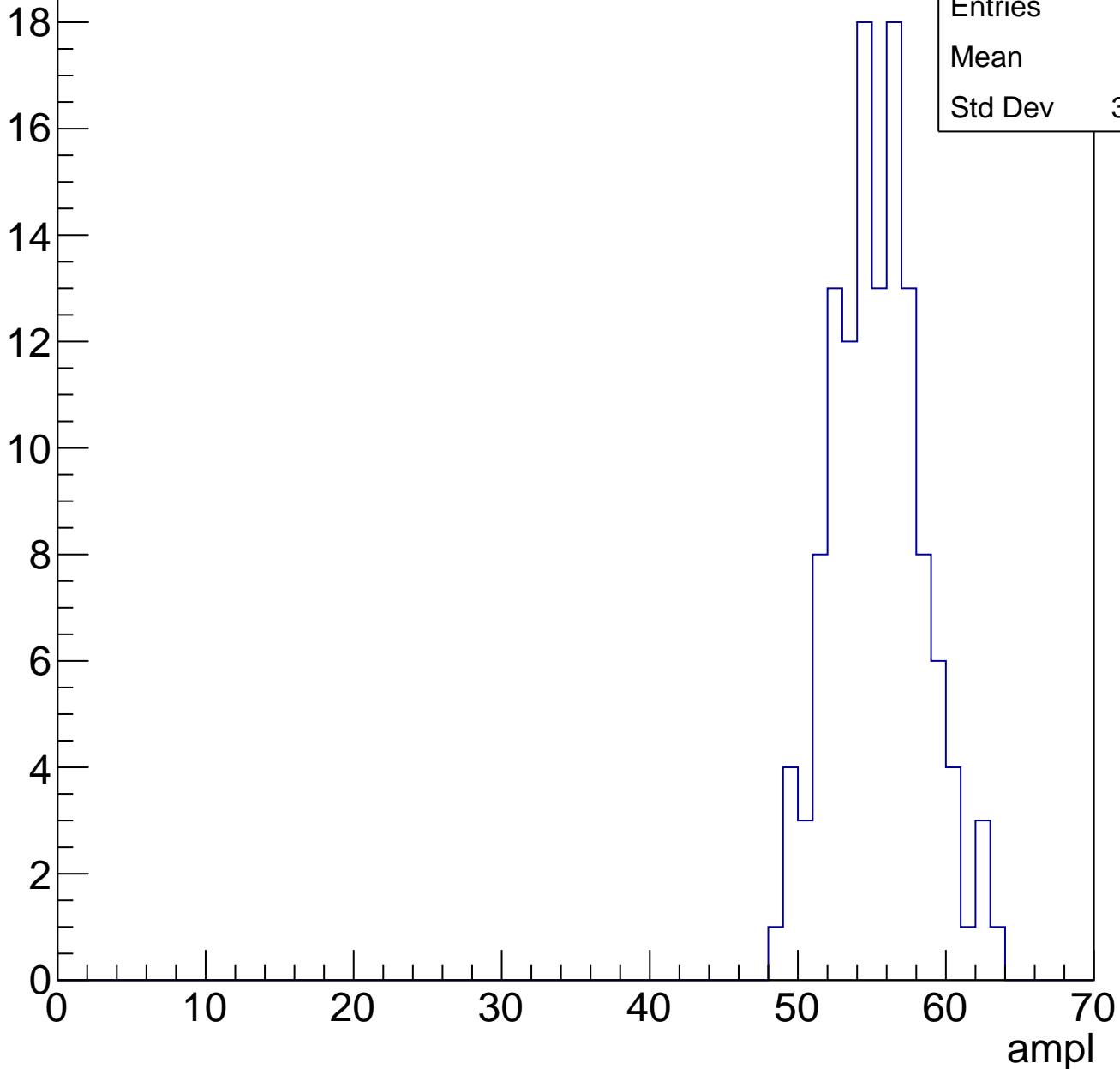
Entries	145
Mean	48.21
Std Dev	3.576



B1L001S, U24-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch116, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	73
Mean	59.79
Std Dev	2.239

Entry

12

10

8

6

4

2

0

0

10

20

30

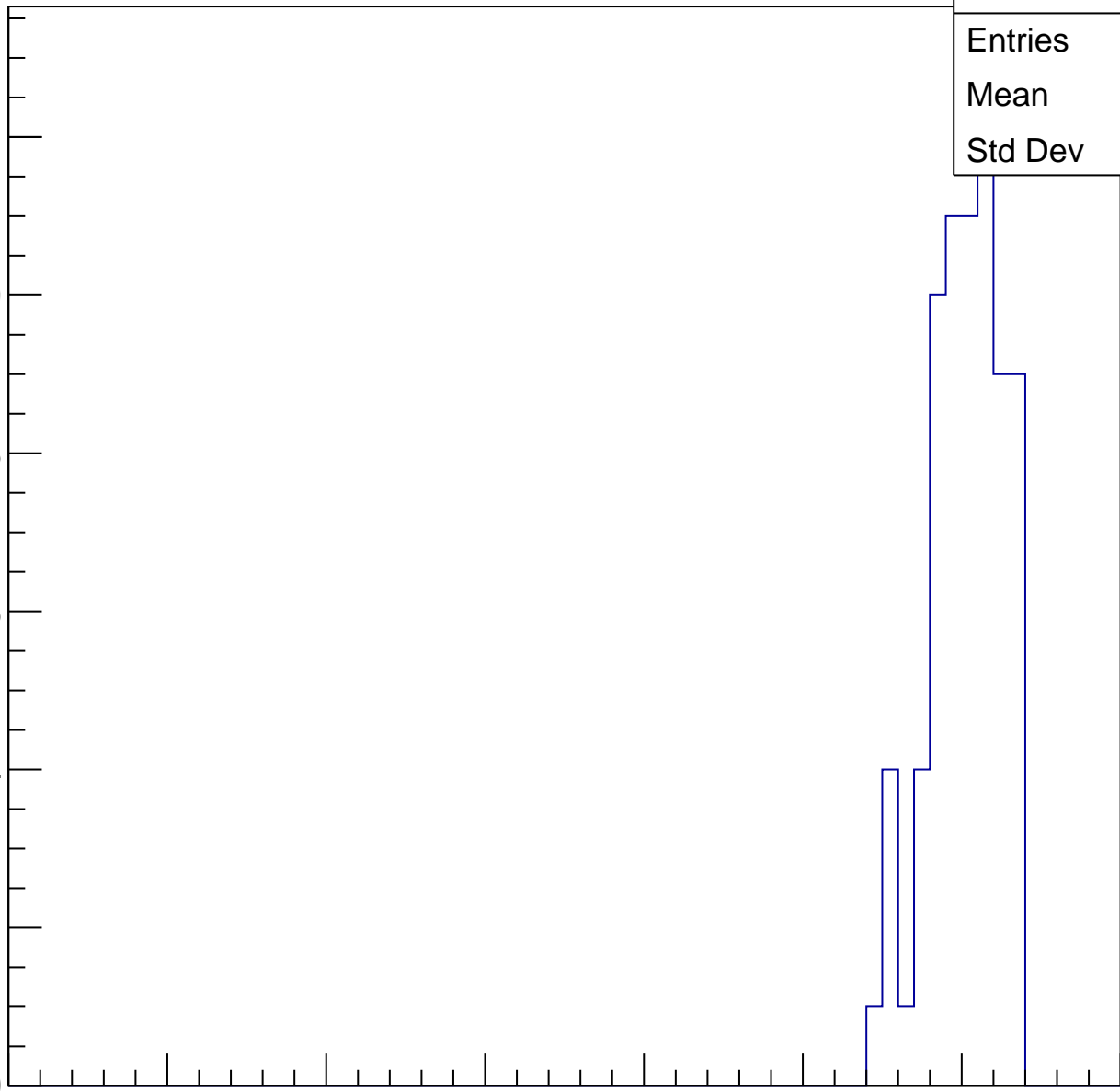
40

50

60

70

ampl



B1L001S, U24-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	36
Mean	57.83
Std Dev	14.12

Entry

10

8

6

4

2

0

0

10

20

30

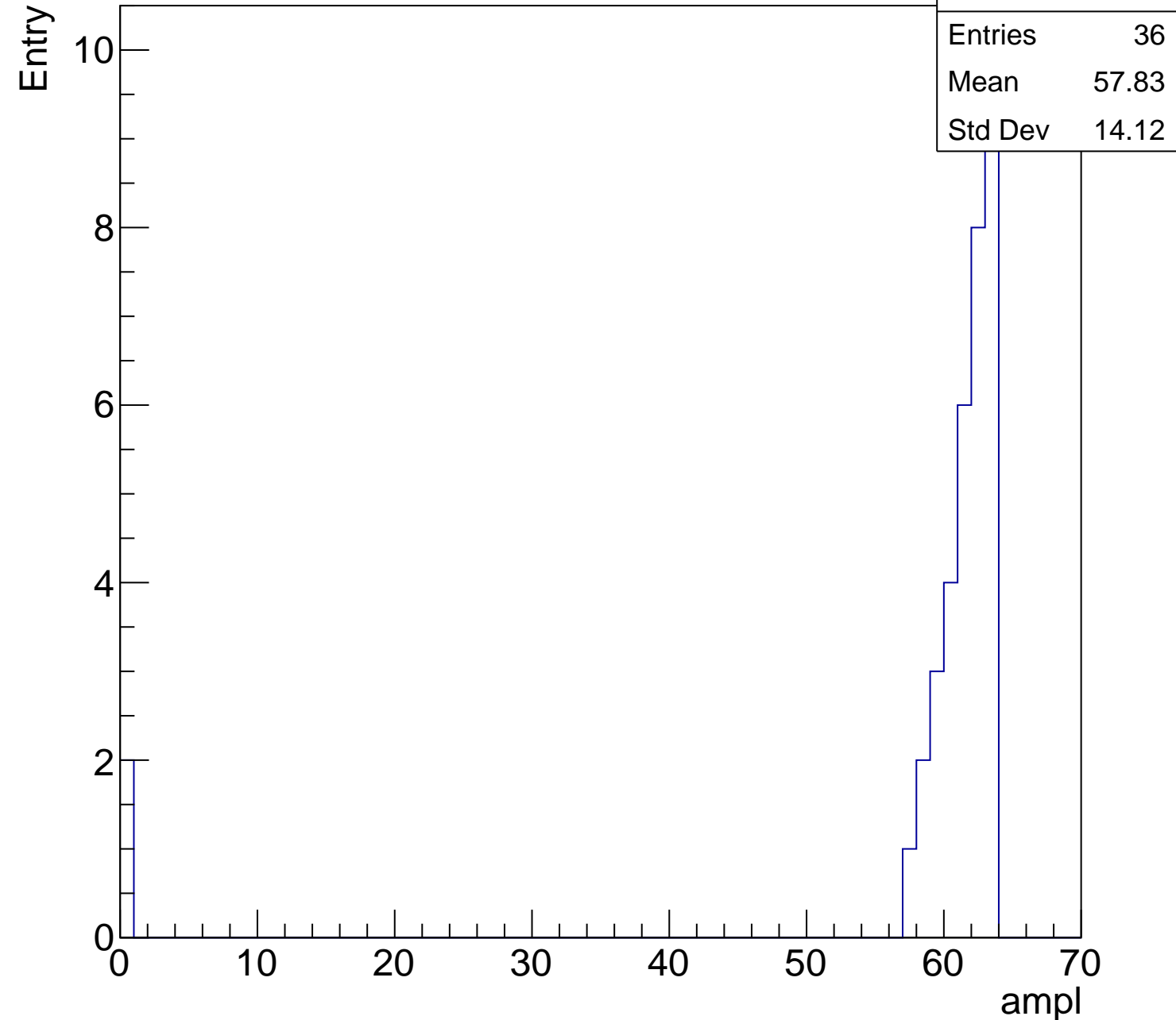
40

50

60

ampl

70



B1L001S, U24-ch116, adc7

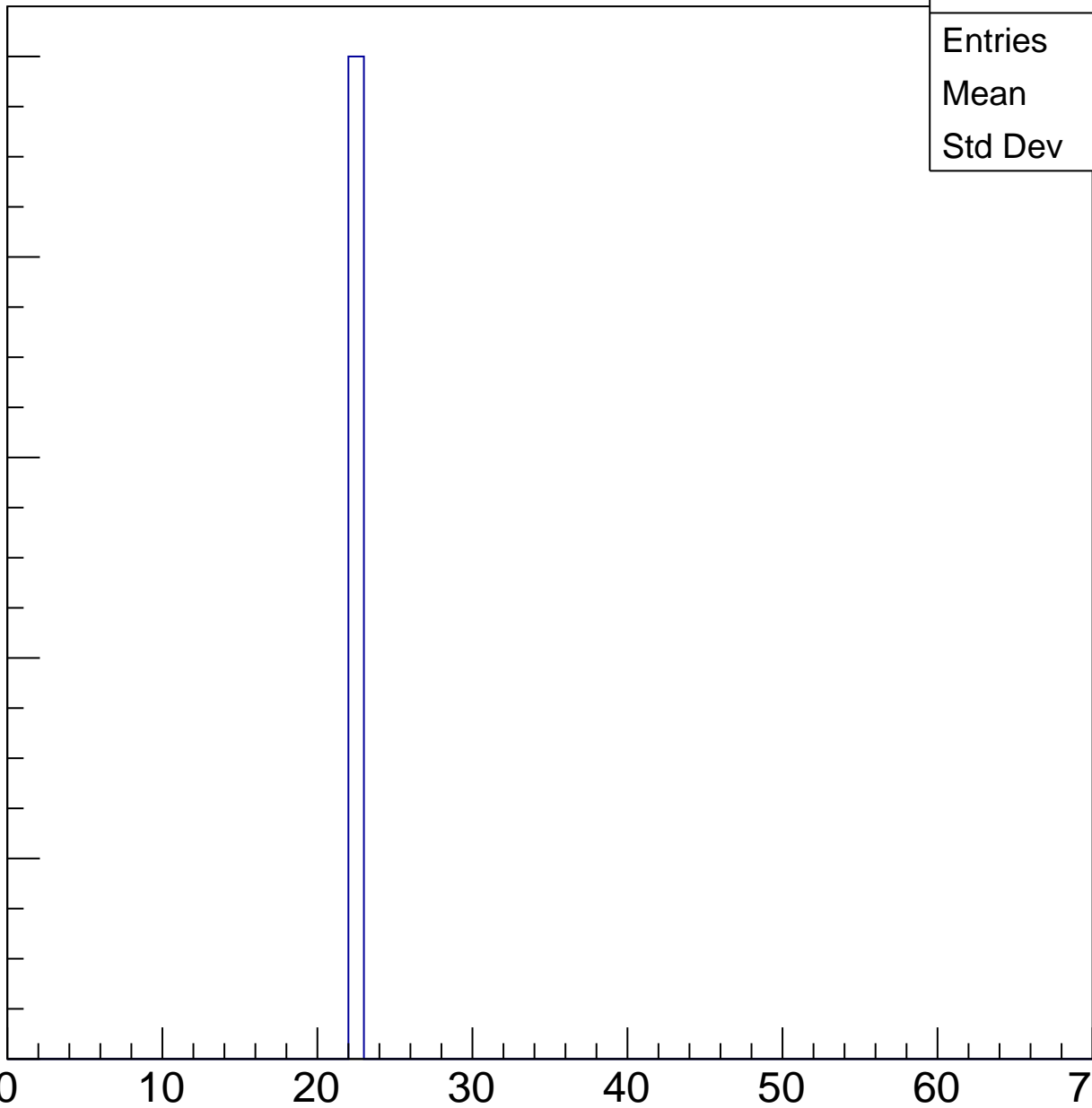
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl



B1L001S, U24-ch117, adc0

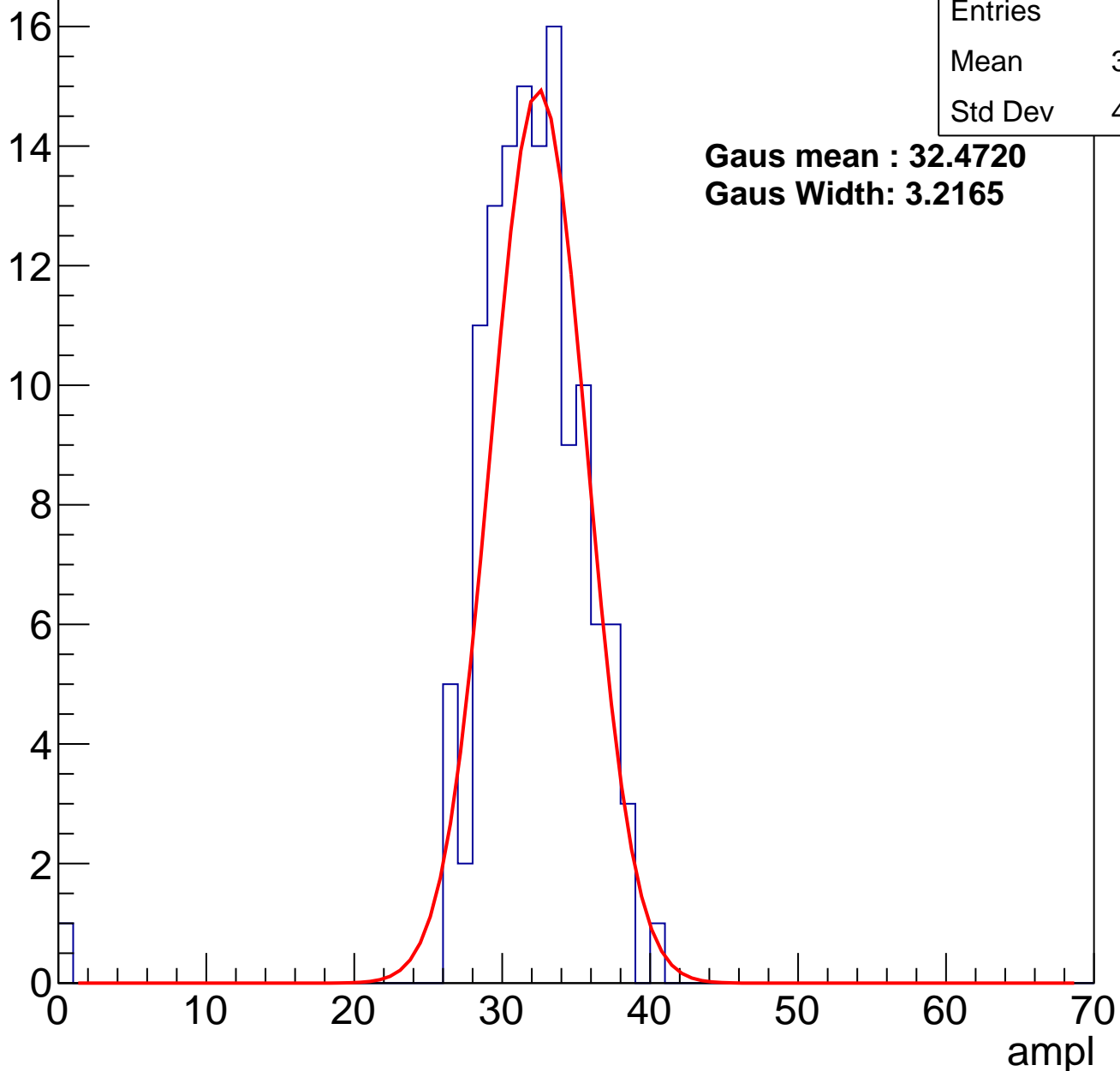
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	31.57
Std Dev	4.135

Gaus mean : 32.4720

Gaus Width: 3.2165

Entry



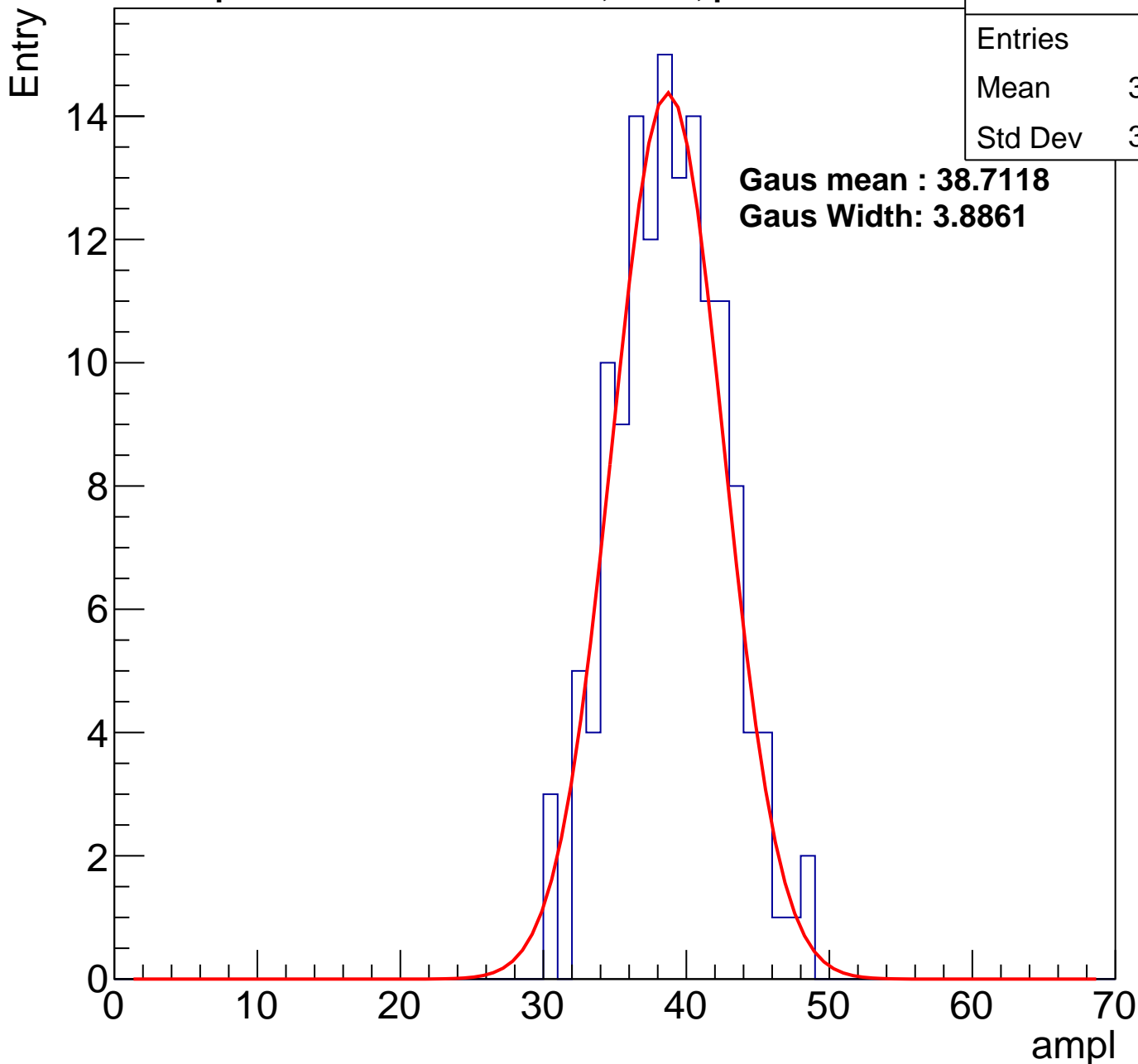
B1L001S, U24-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	38.47
Std Dev	3.746

Gaus mean : 38.7118

Gaus Width: 3.8861



B1L001S, U24-ch117, adc2

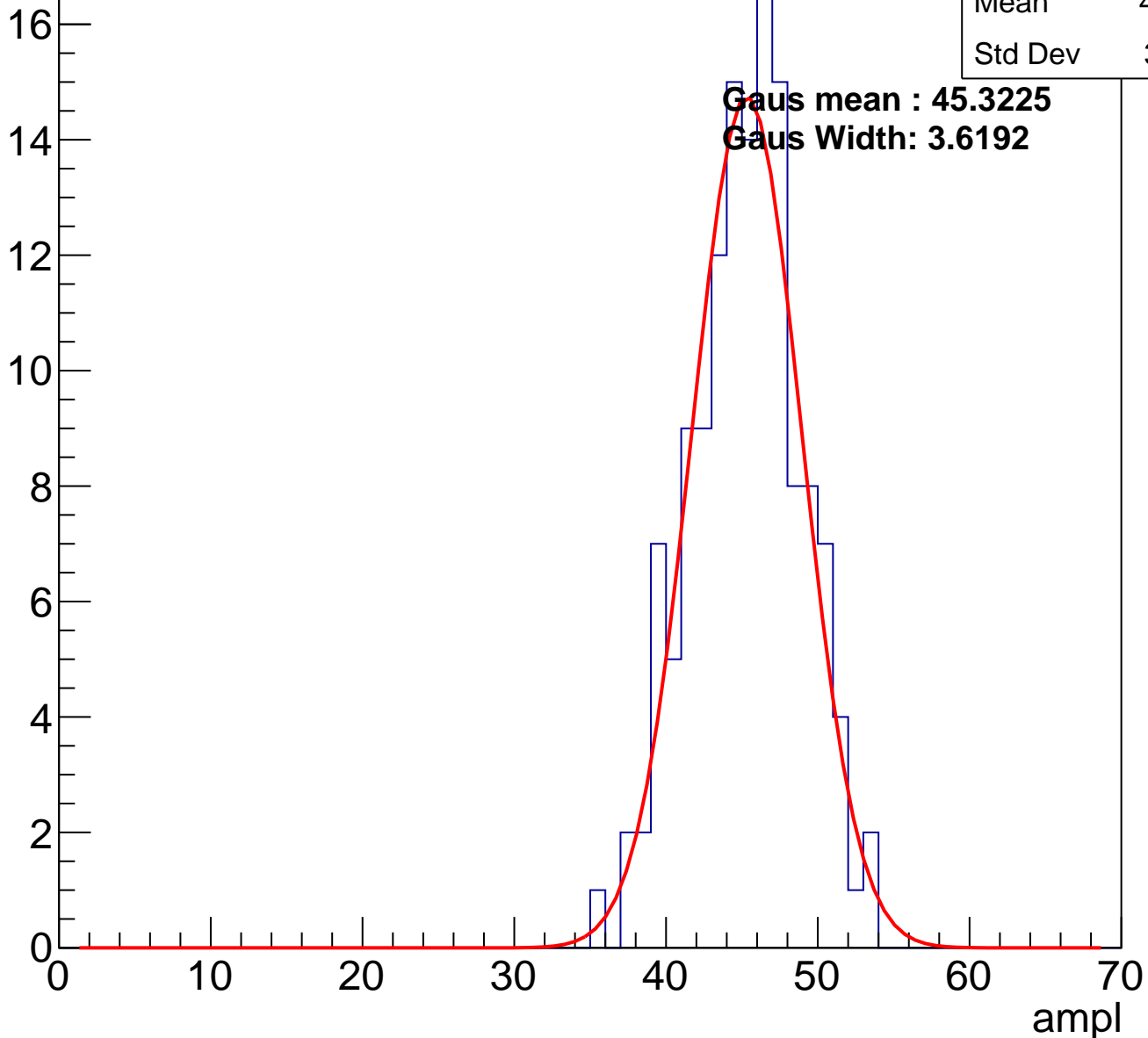
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	138
Mean	44.83
Std Dev	3.571

Gaus mean : 45.3225

Gaus Width: 3.6192



B1L001S, U24-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	51.54
Std Dev	3.138

Entry

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L001S, U24-ch117, adc4

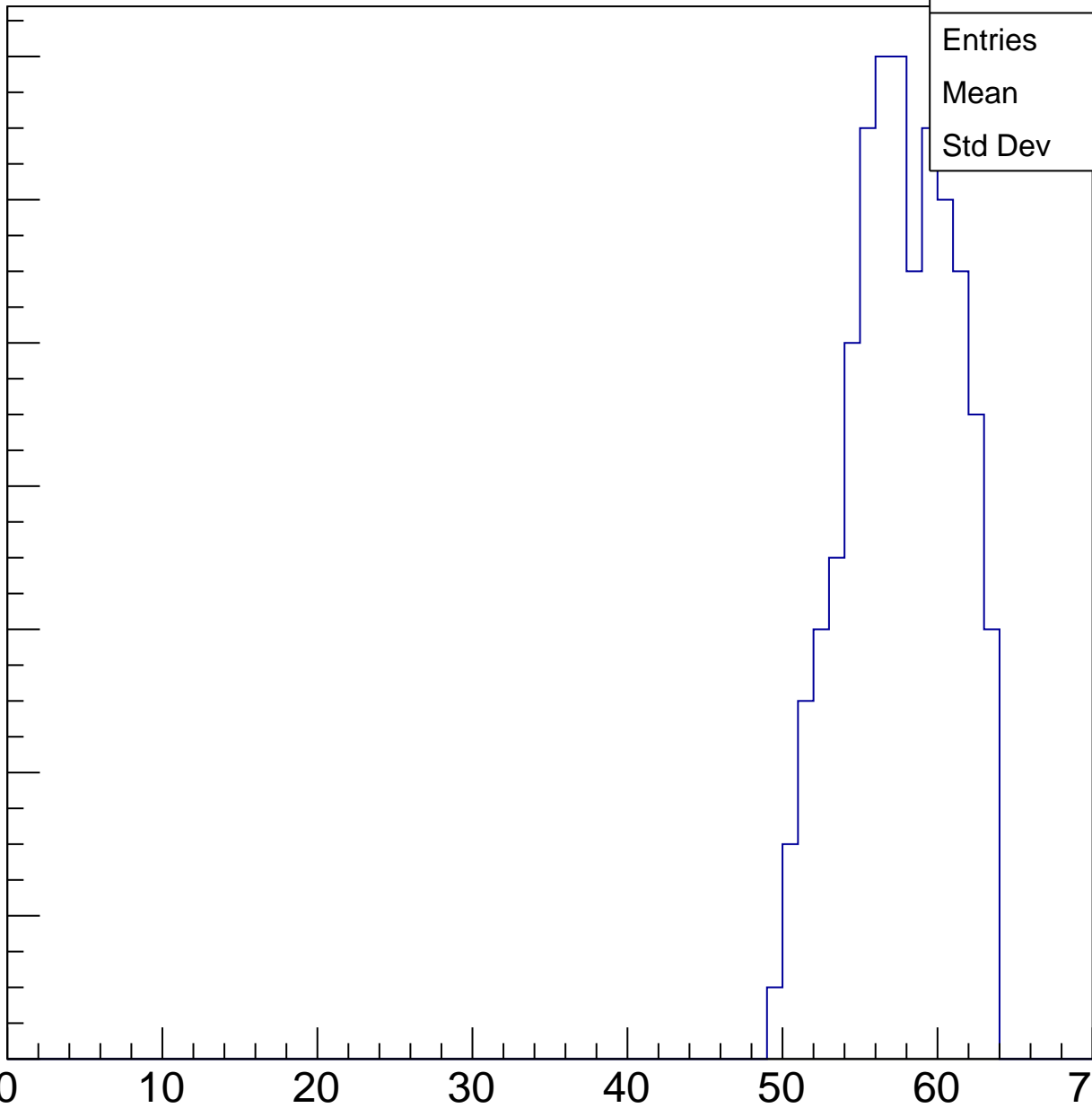
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	135
Mean	57.08
Std Dev	3.438

ampl



B1L001S, U24-ch117, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	58
Mean	58.59
Std Dev	11.23

Entry

12

10

8

6

4

2

0

0

10

20

30

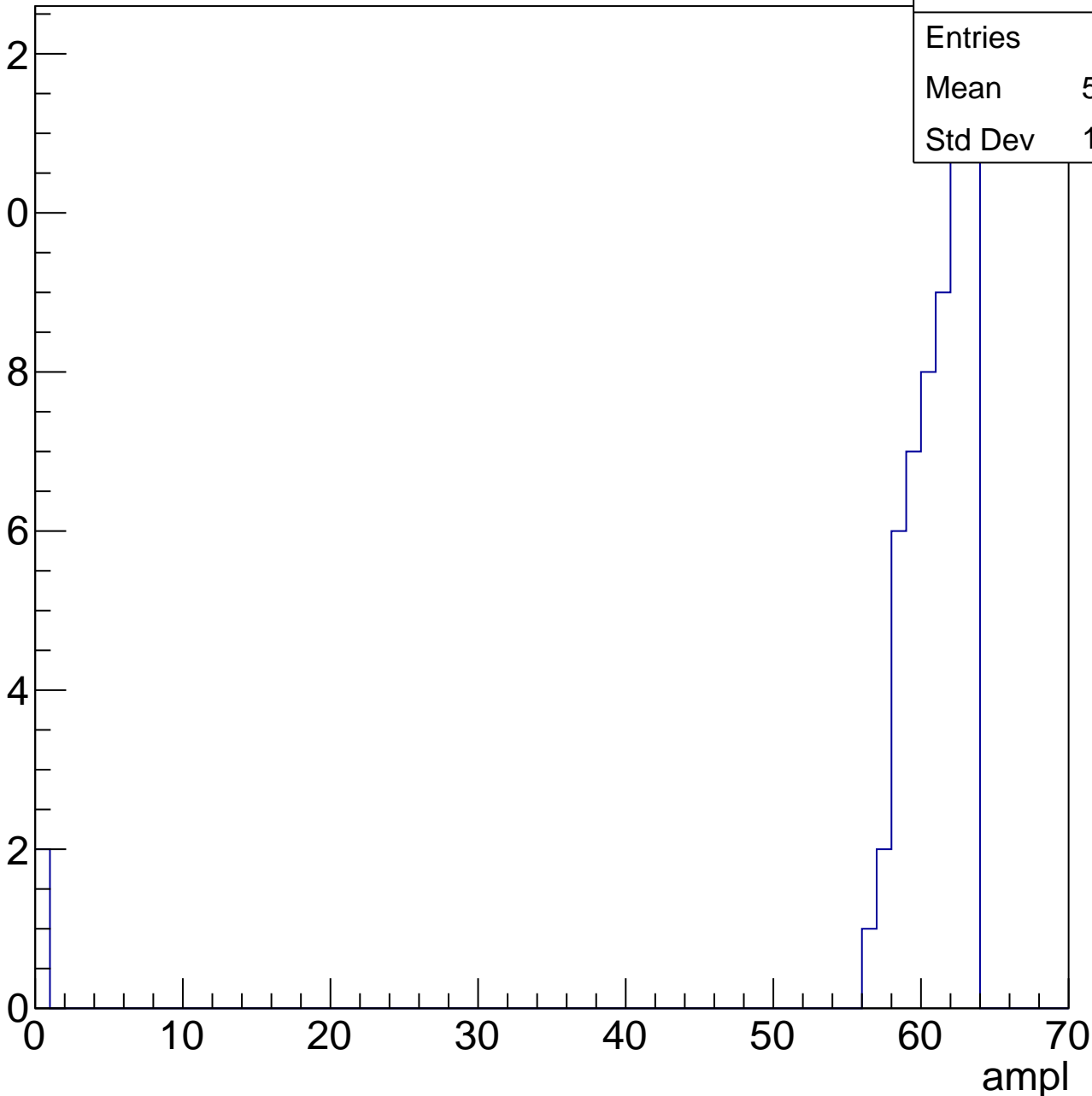
40

50

60

70

ampl



B1L001S, U24-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	63
Std Dev	0

B1L001S, U24-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U24-ch118, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.69
Std Dev	4.043

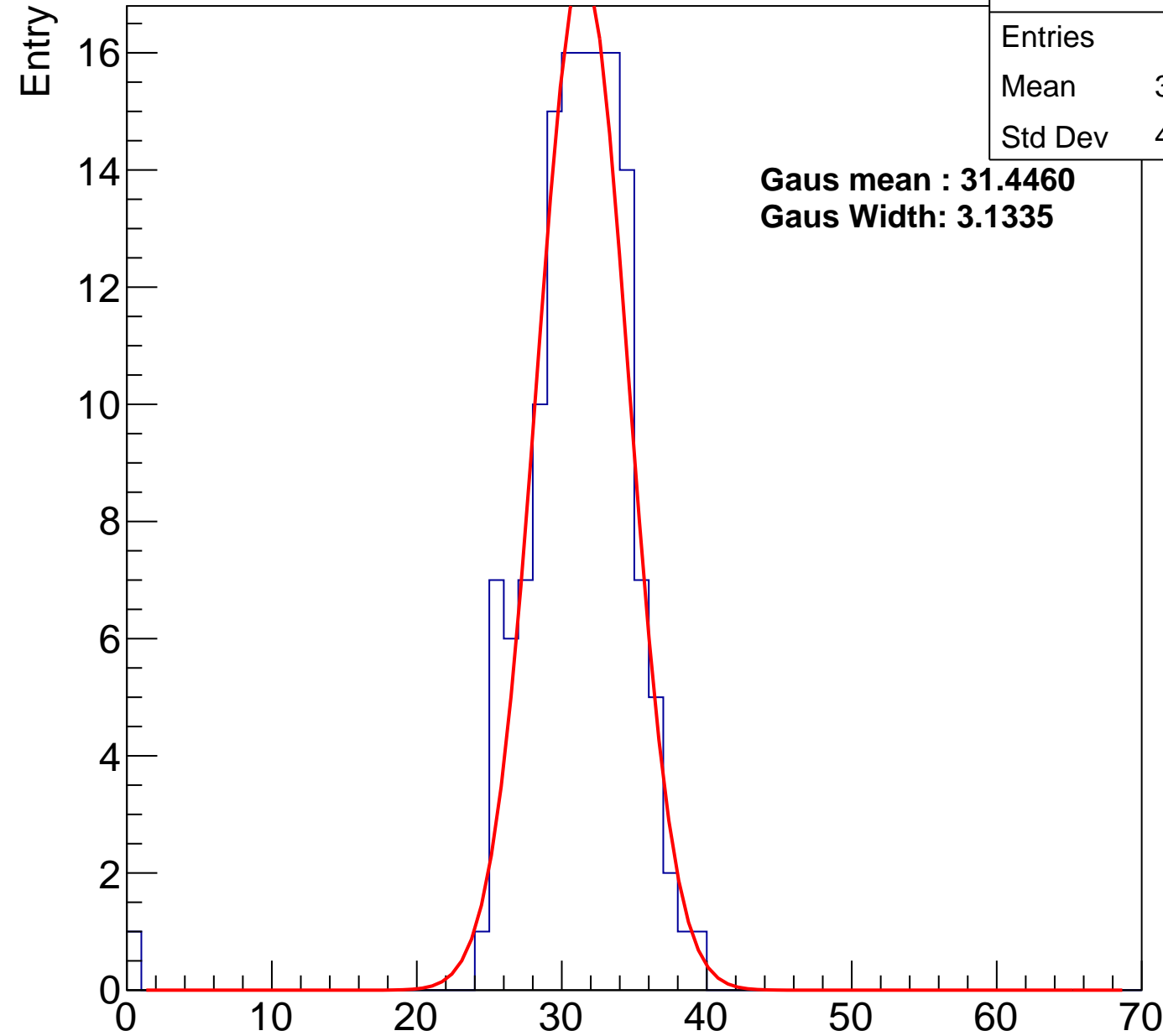
Gaus mean : 31.4460

Gaus Width: 3.1335

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch118, adc1

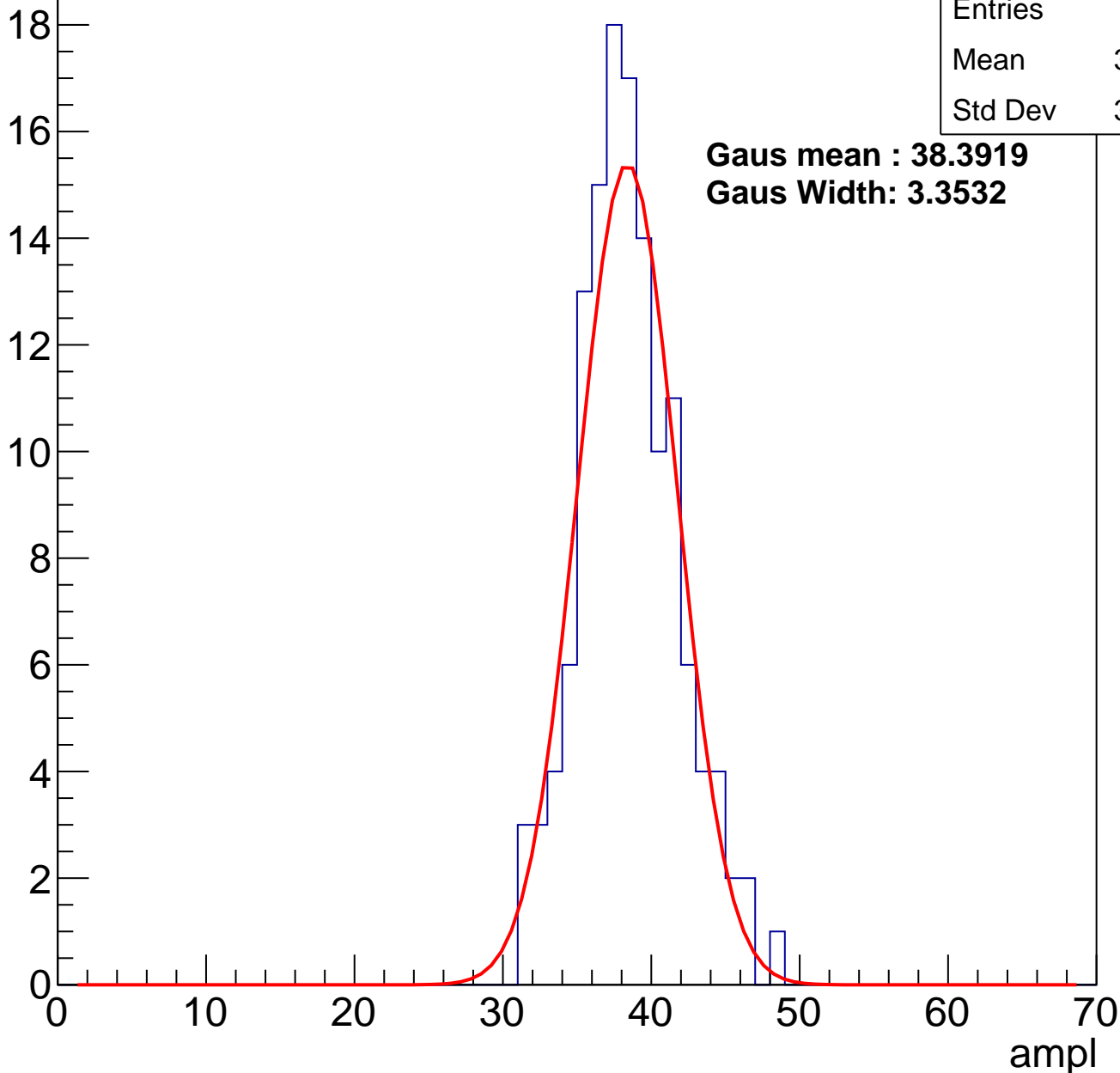
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	38.04
Std Dev	3.319

Gaus mean : 38.3919

Gaus Width: 3.3532

Entry



B1L001S, U24-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

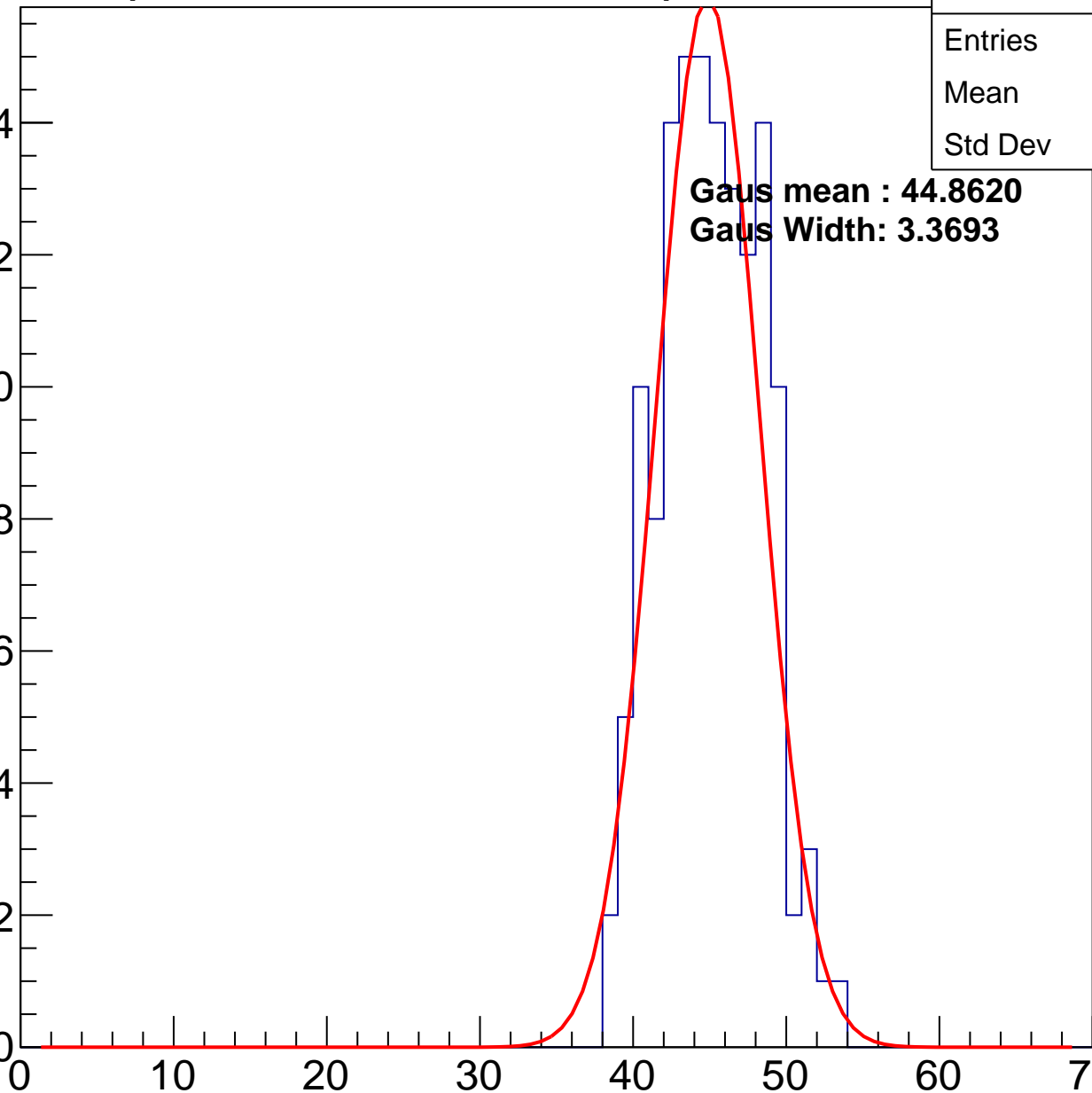
14
12
10
8
6
4
2
0

Entries	139
Mean	44.63
Std Dev	3.237

Gaus mean : 44.8620

Gaus Width: 3.3693

ampl

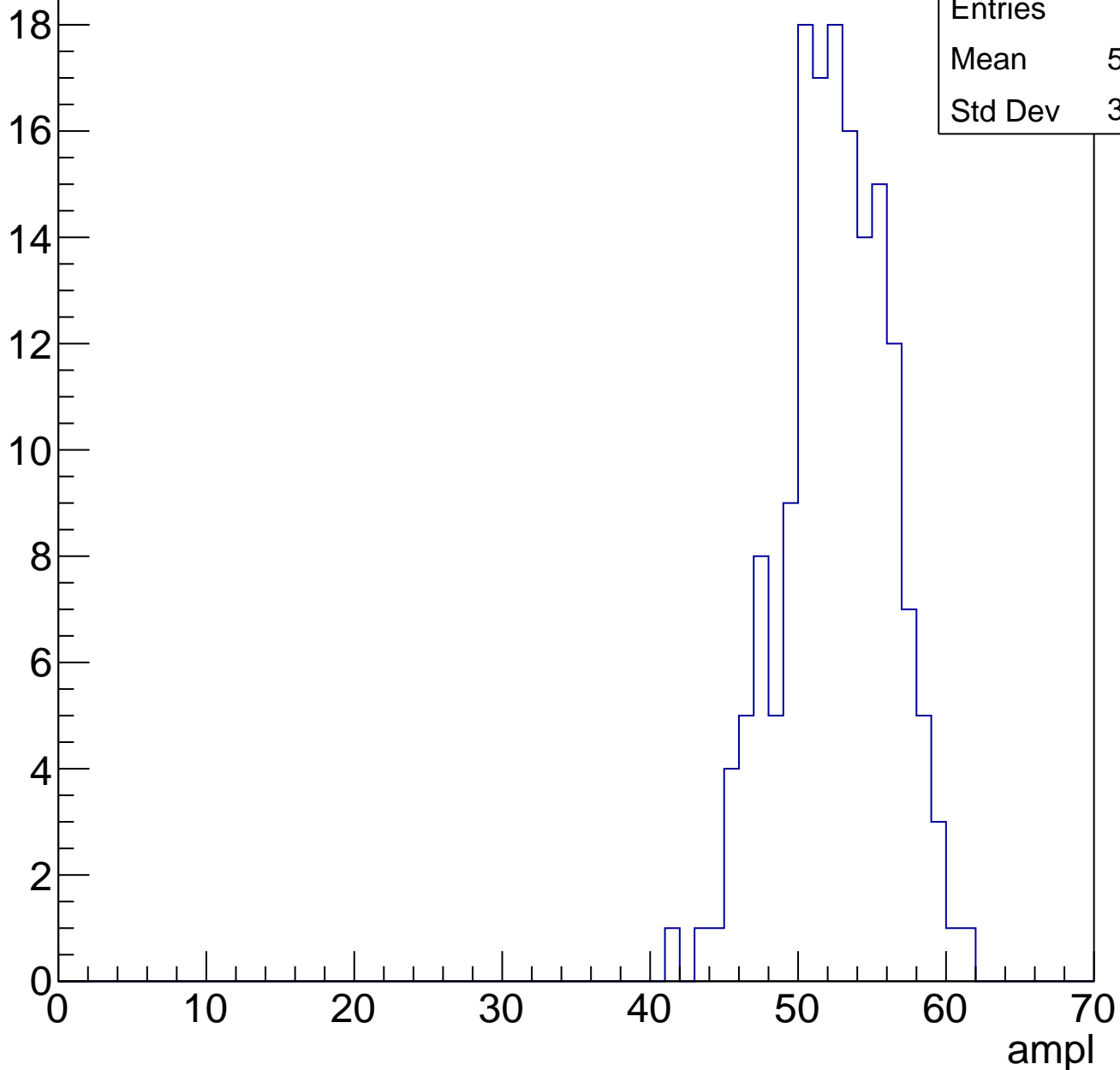


B1L001S, U24-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	52.09
Std Dev	3.643

Entry



B1L001S, U24-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	57.87
Std Dev	2.87

Entry

10

8

6

4

2

0

0

10

20

30

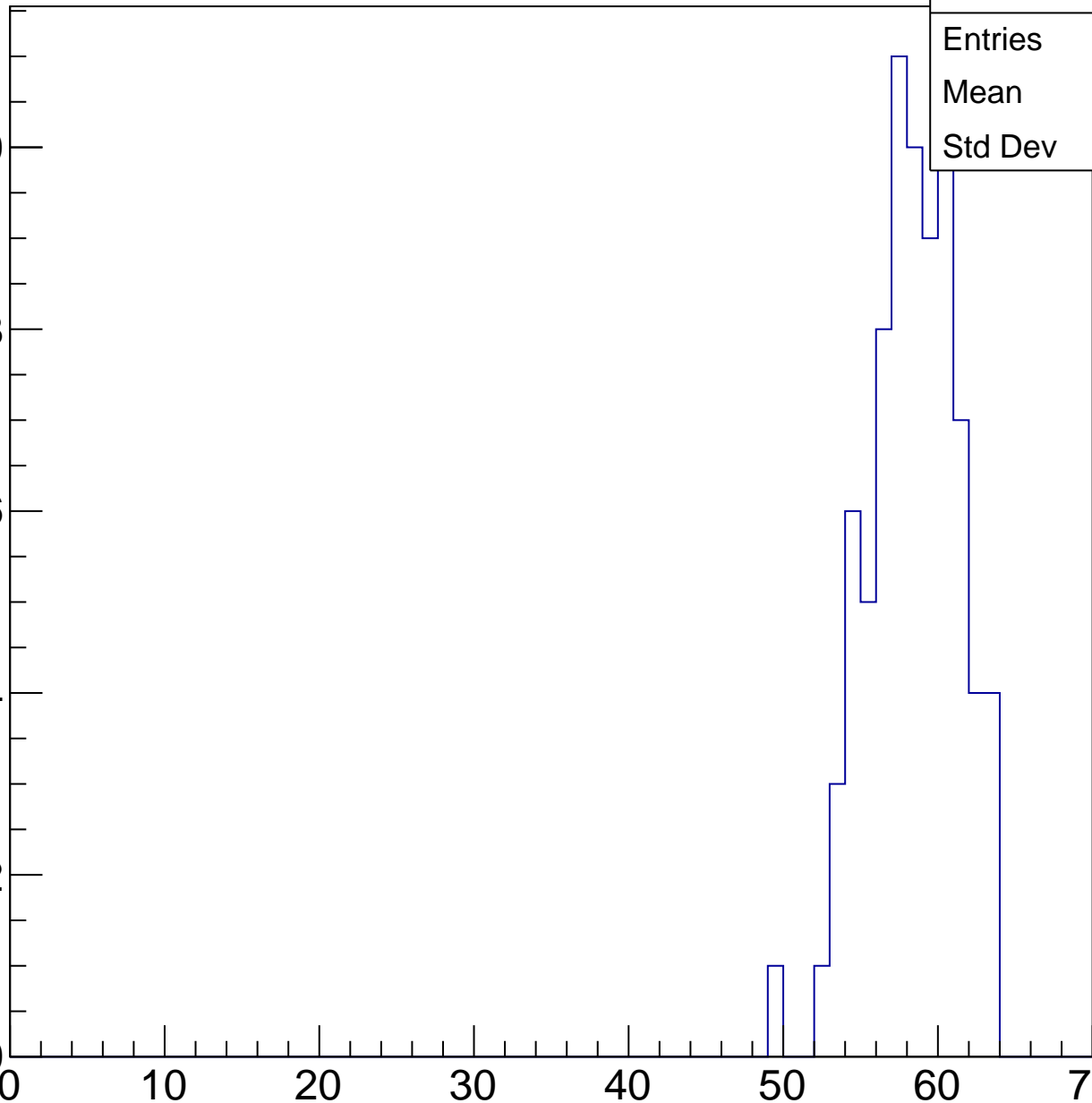
40

50

60

70

ampl



B1L001S, U24-ch118, adc5

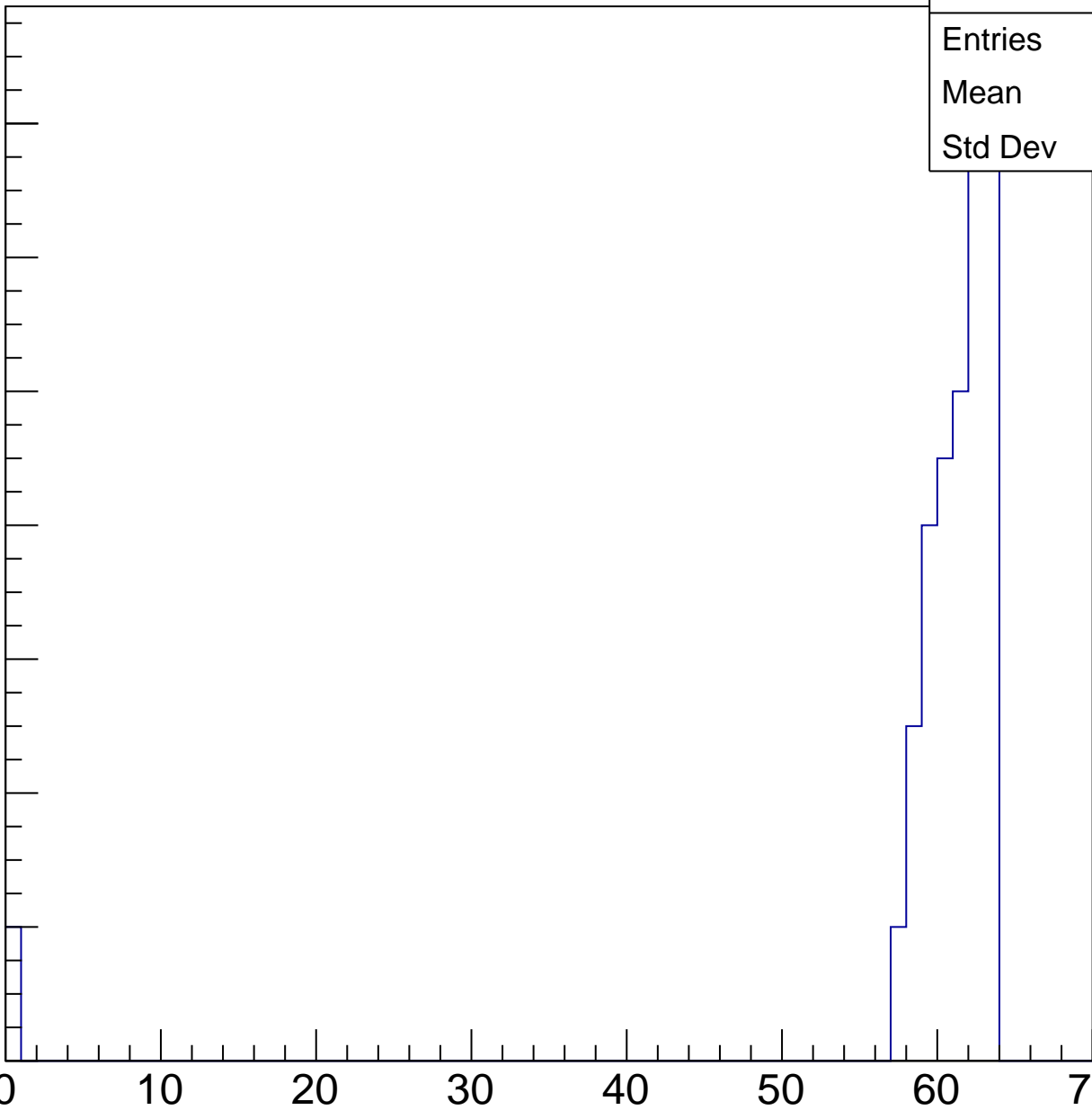
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.05
Std Dev	10.66

ampl



B1L001S, U24-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

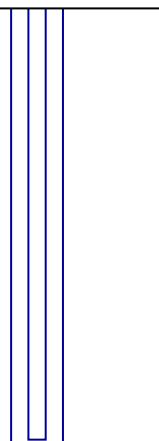
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

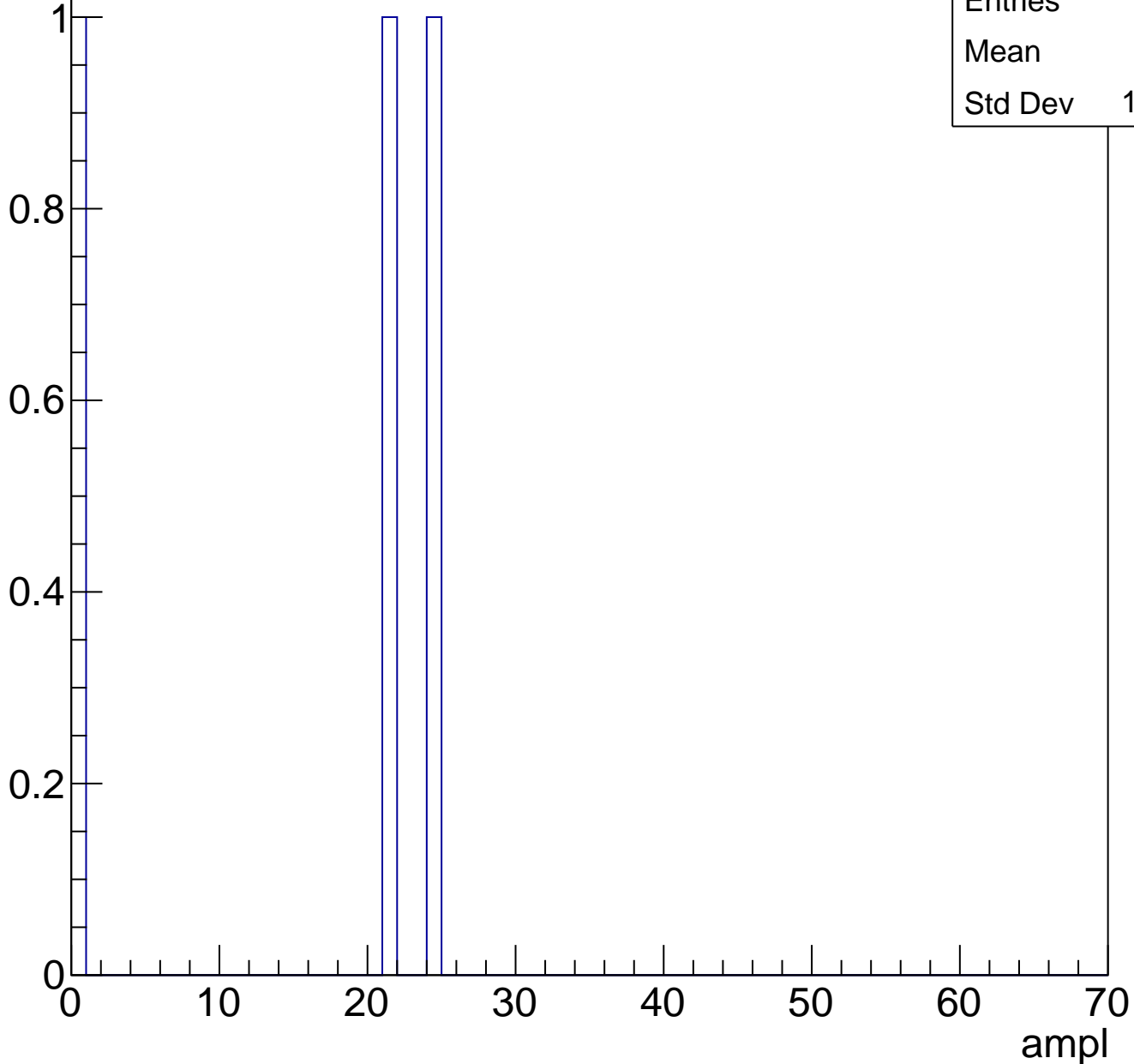
ampl



B1L001S, U24-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.04
Std Dev	3.499

Gaus mean : 30.6723

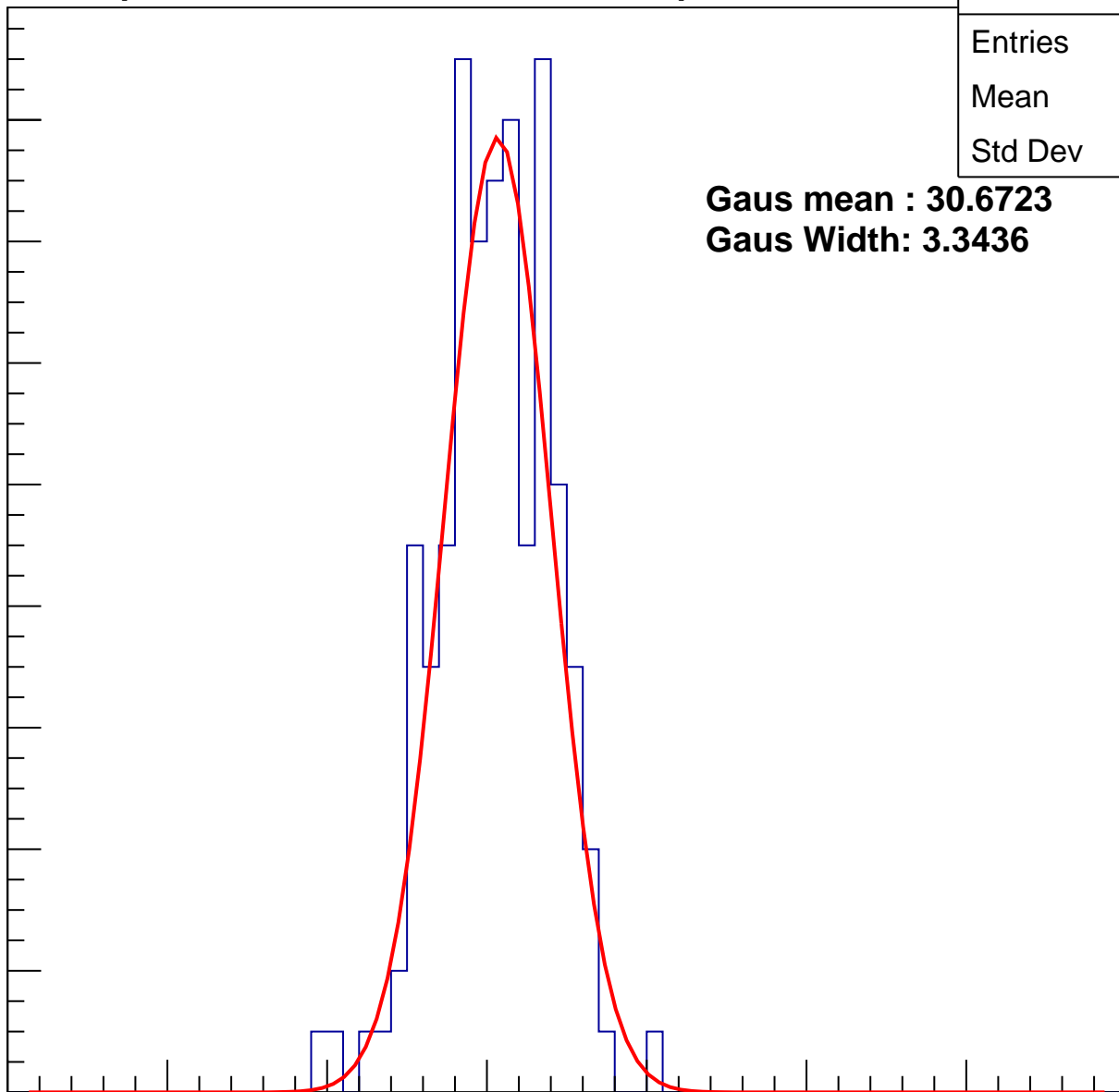
Gaus Width: 3.3436

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch119, adc1

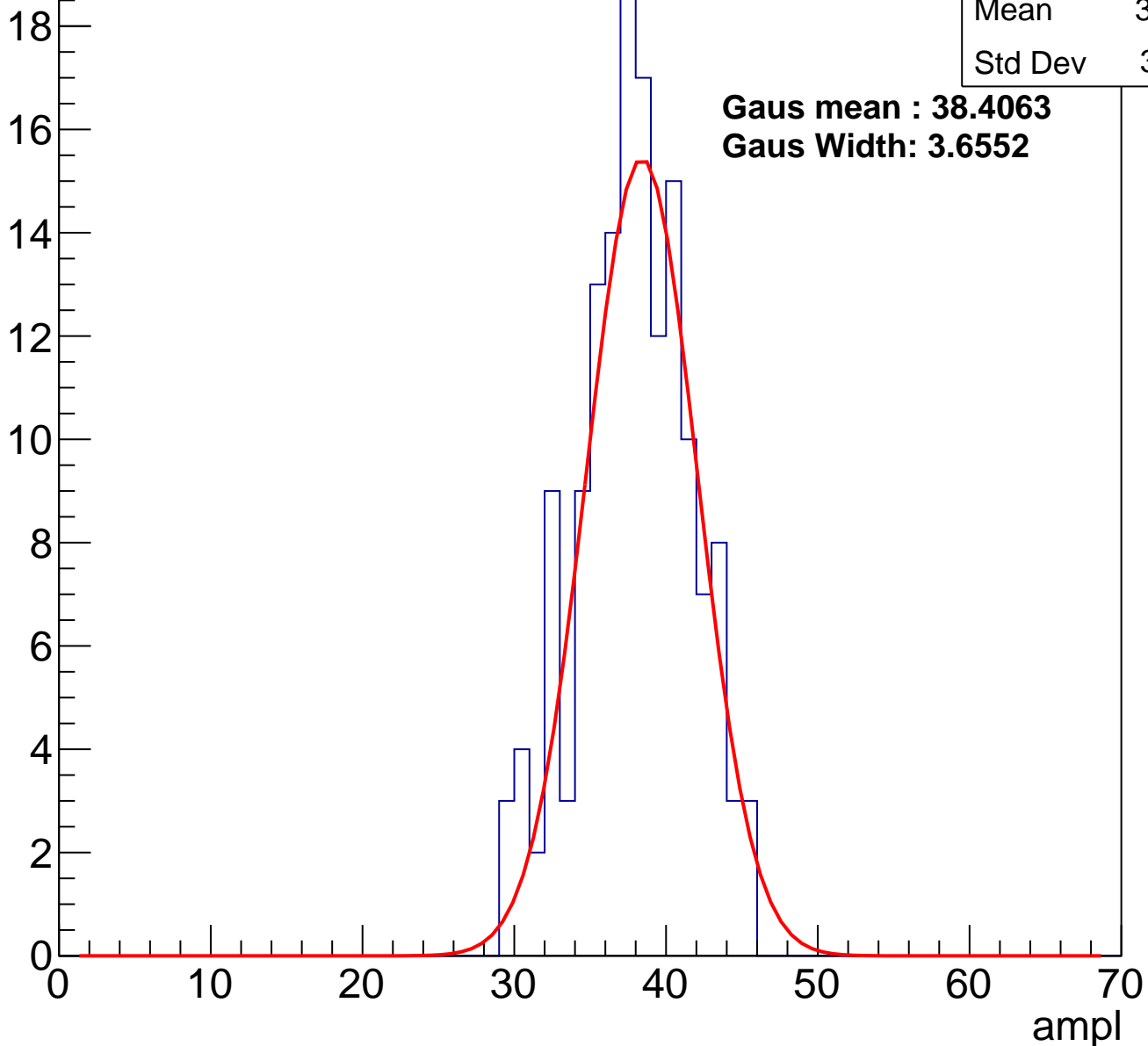
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	37.44
Std Dev	3.651

Gaus mean : 38.4063

Gaus Width: 3.6552

Entry



B1L001S, U24-ch119, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	44.6
Std Dev	3.684

Gaus mean : 45.1932

Gaus Width: 3.7503

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

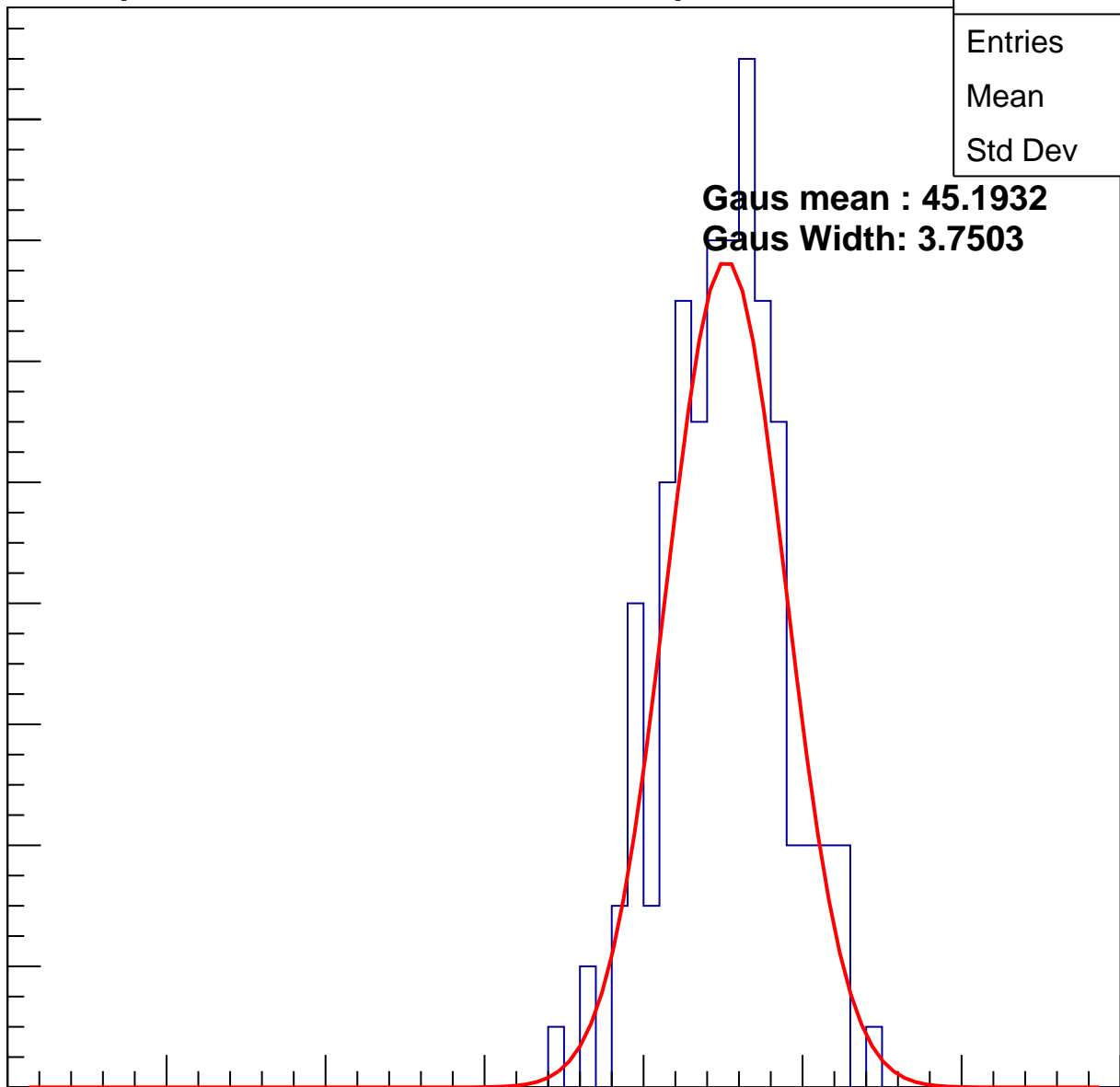
40

50

60

ampl

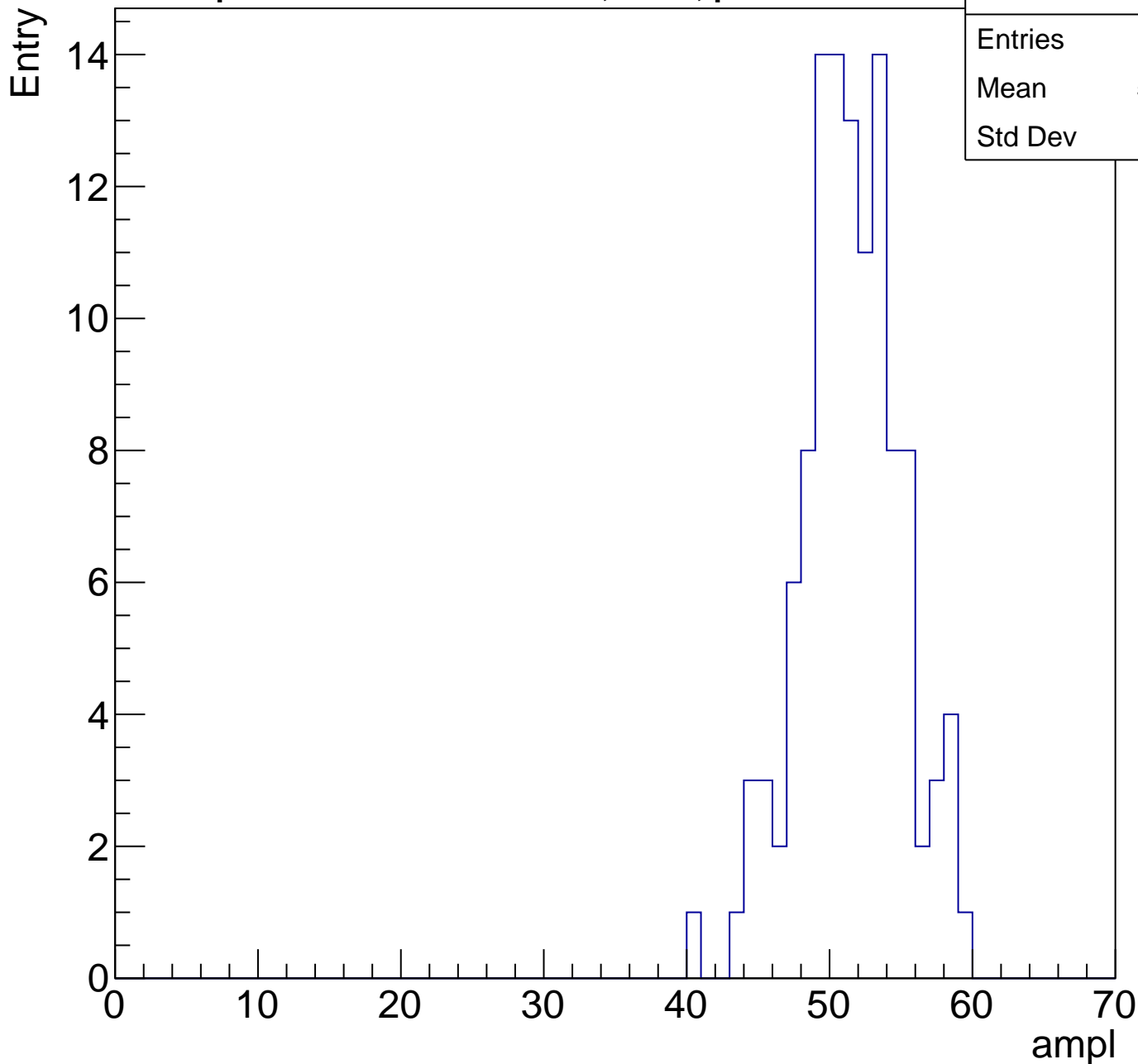
70



B1L001S, U24-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

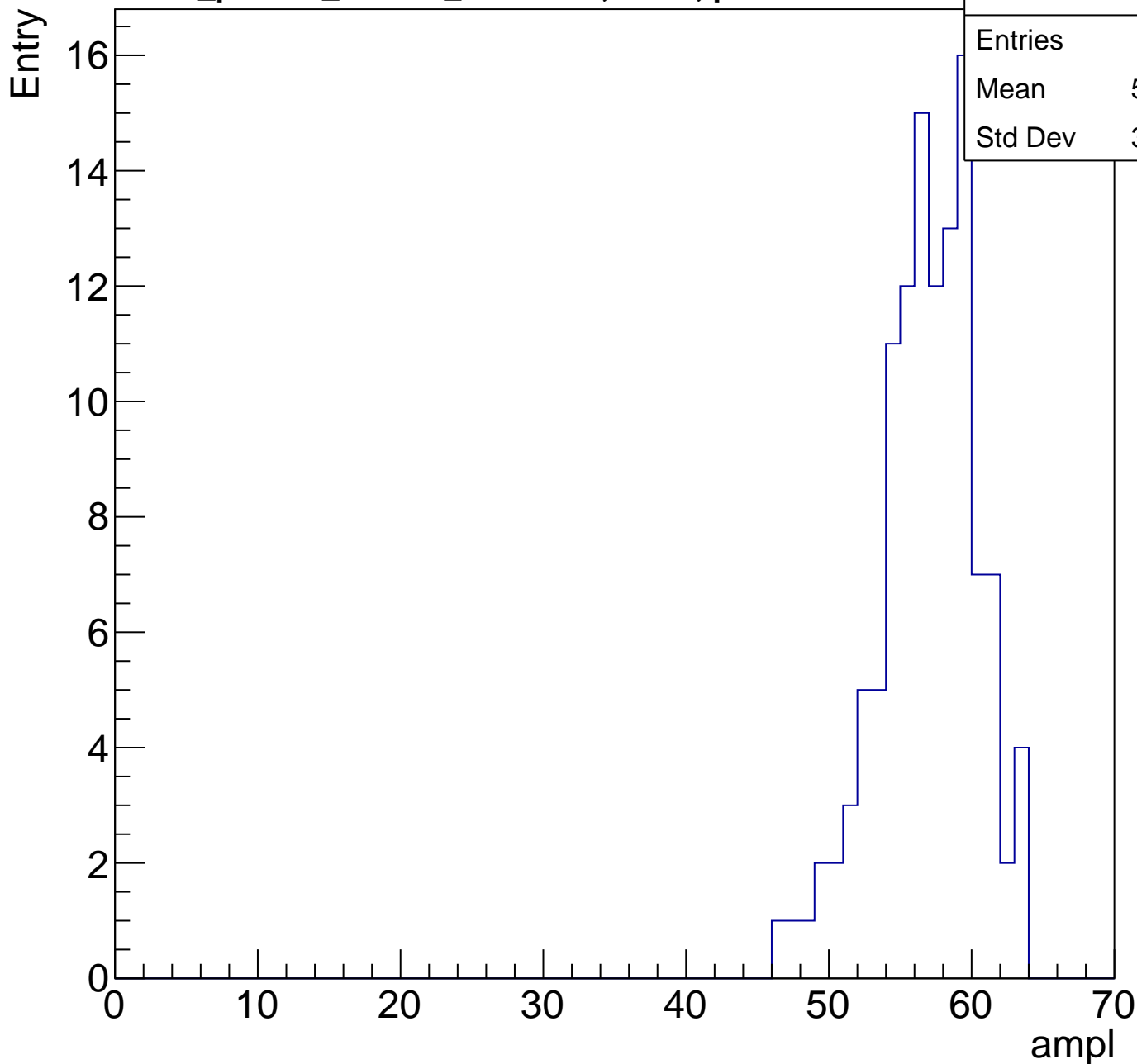
Entries	116
Mean	51.01
Std Dev	3.49



B1L001S, U24-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	56.44
Std Dev	3.444



B1L001S, U24-ch119, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

68

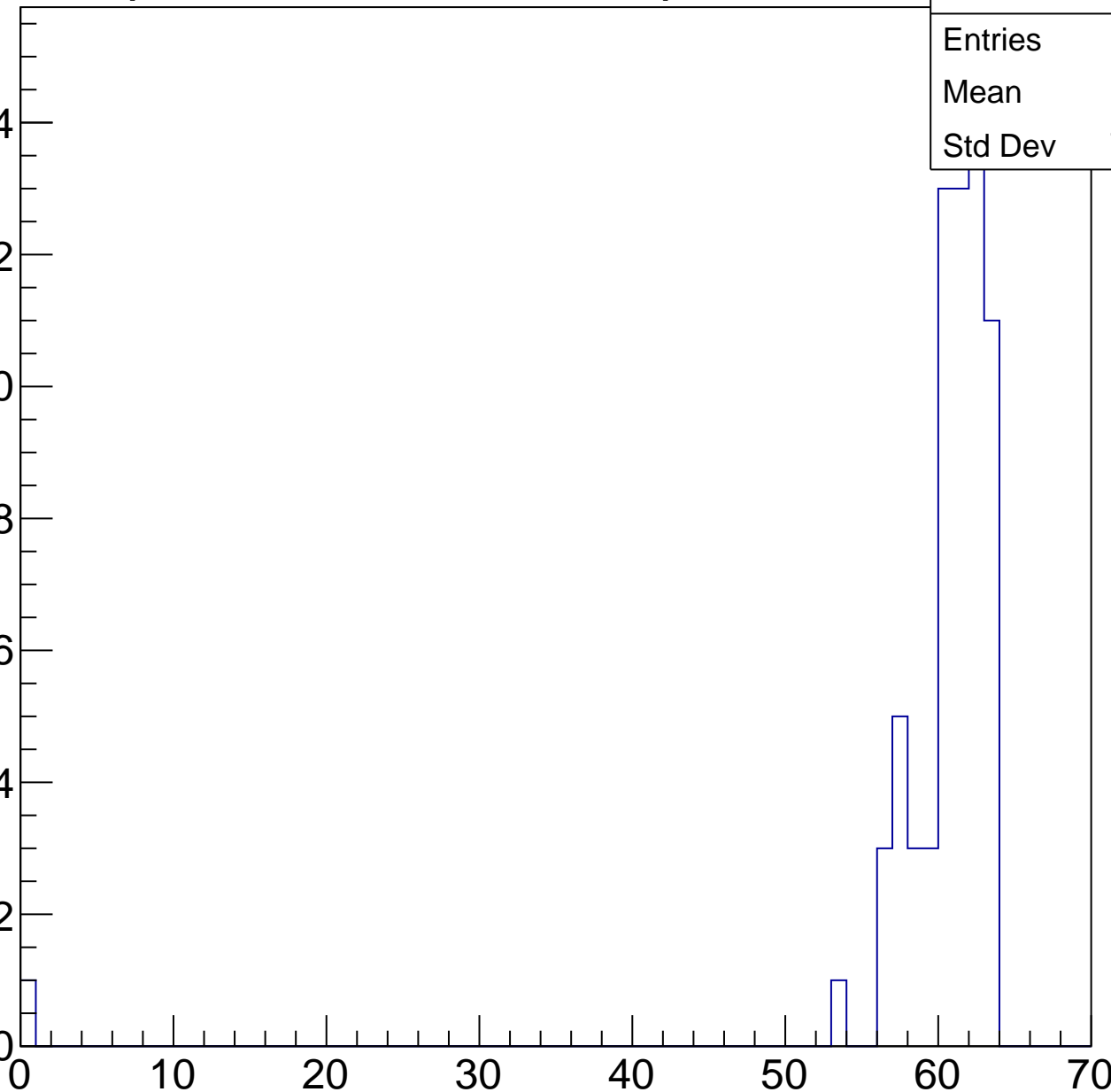
Mean

59.6

Std Dev

7.591

ampl

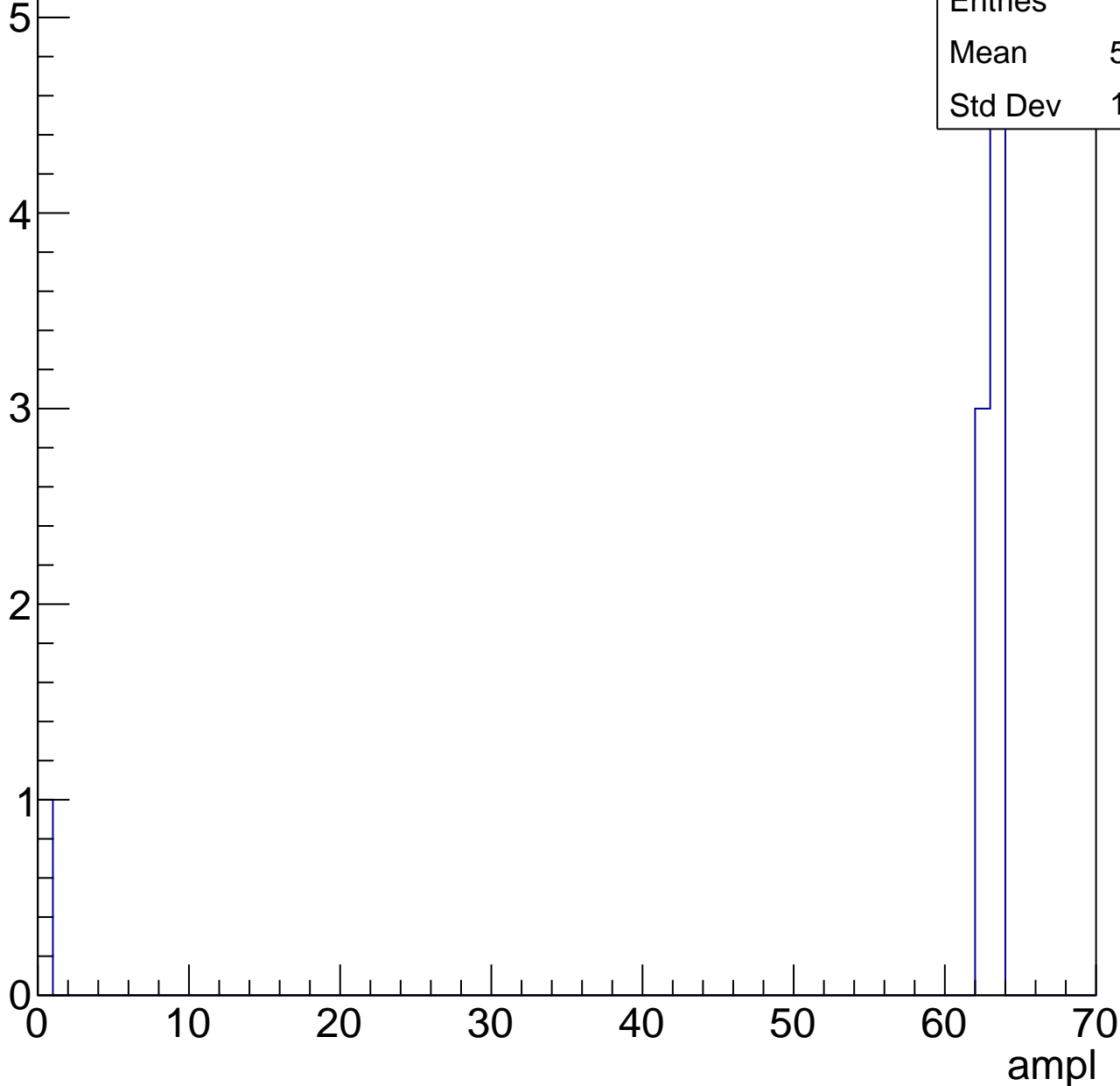


B1L001S, U24-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	55.67
Std Dev	19.69



B1L001S, U24-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U24-ch120, adc0

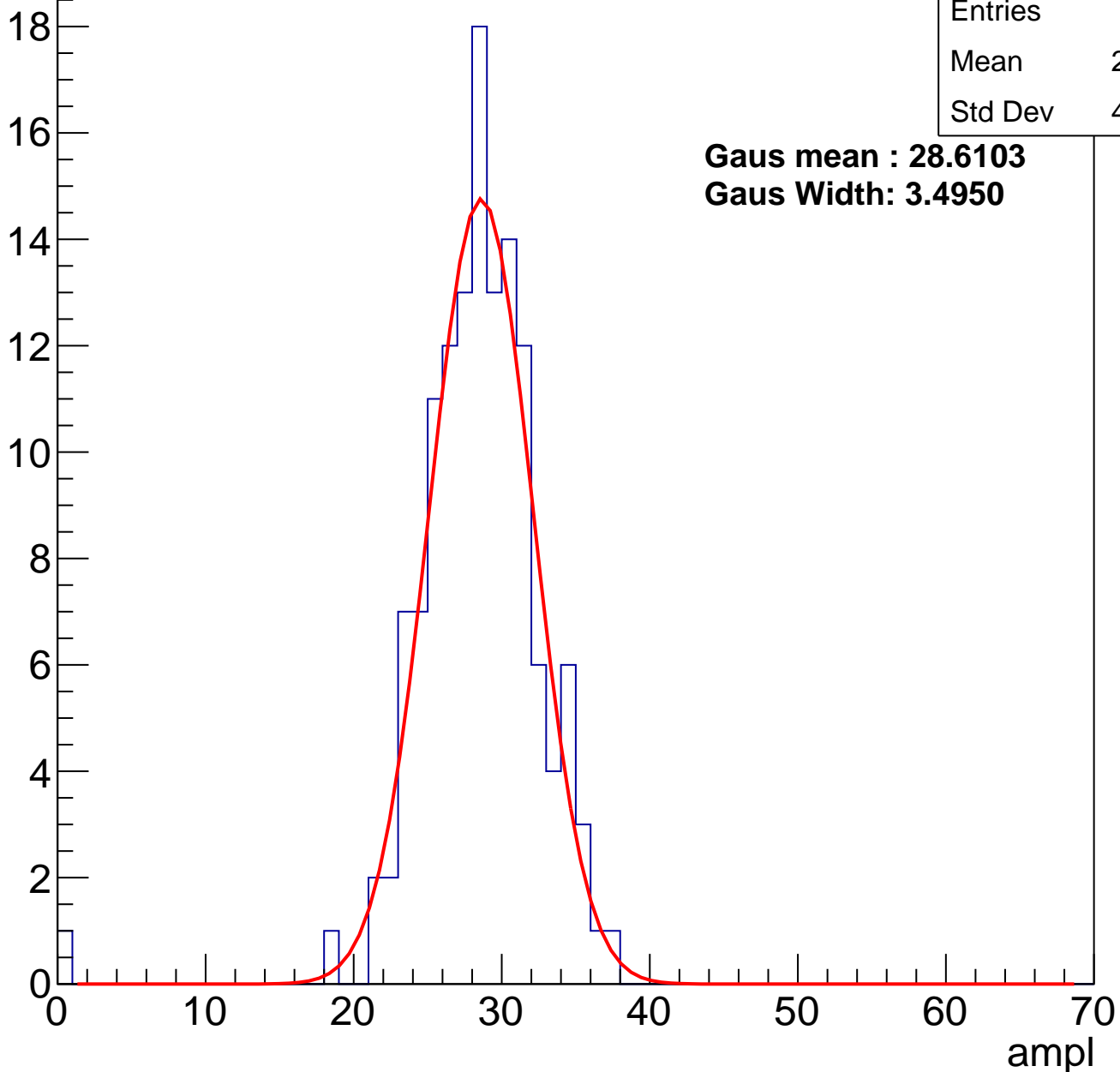
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	27.99
Std Dev	4.216

Gaus mean : 28.6103

Gaus Width: 3.4950

Entry



B1L001S, U24-ch120, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	34.92
Std Dev	3.586

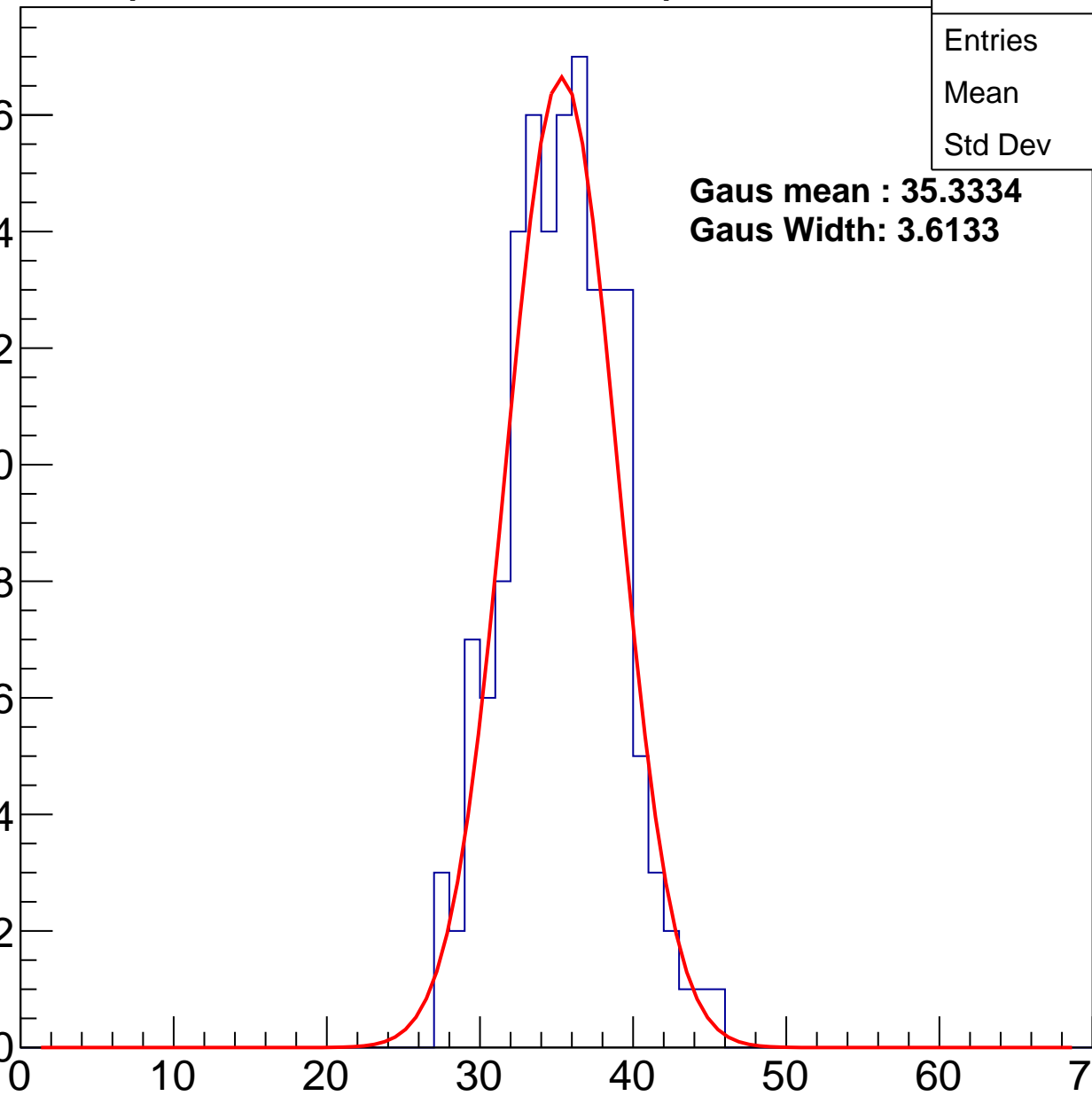
Gaus mean : 35.3334

Gaus Width: 3.6133

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch120, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.18
Std Dev	3.12

Gaus mean : 42.7969

Gaus Width: 3.3634

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

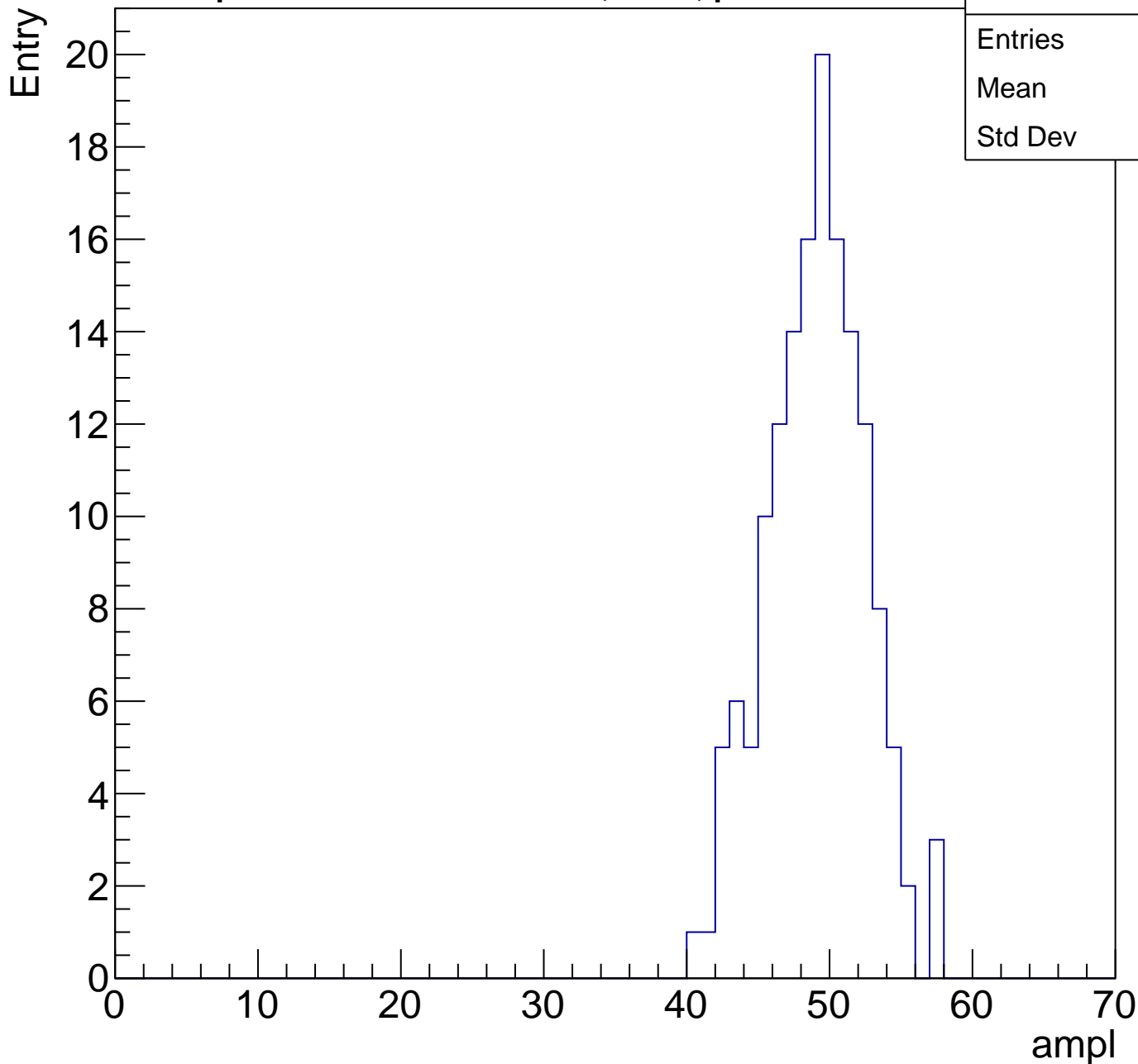
60

70

B1L001S, U24-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

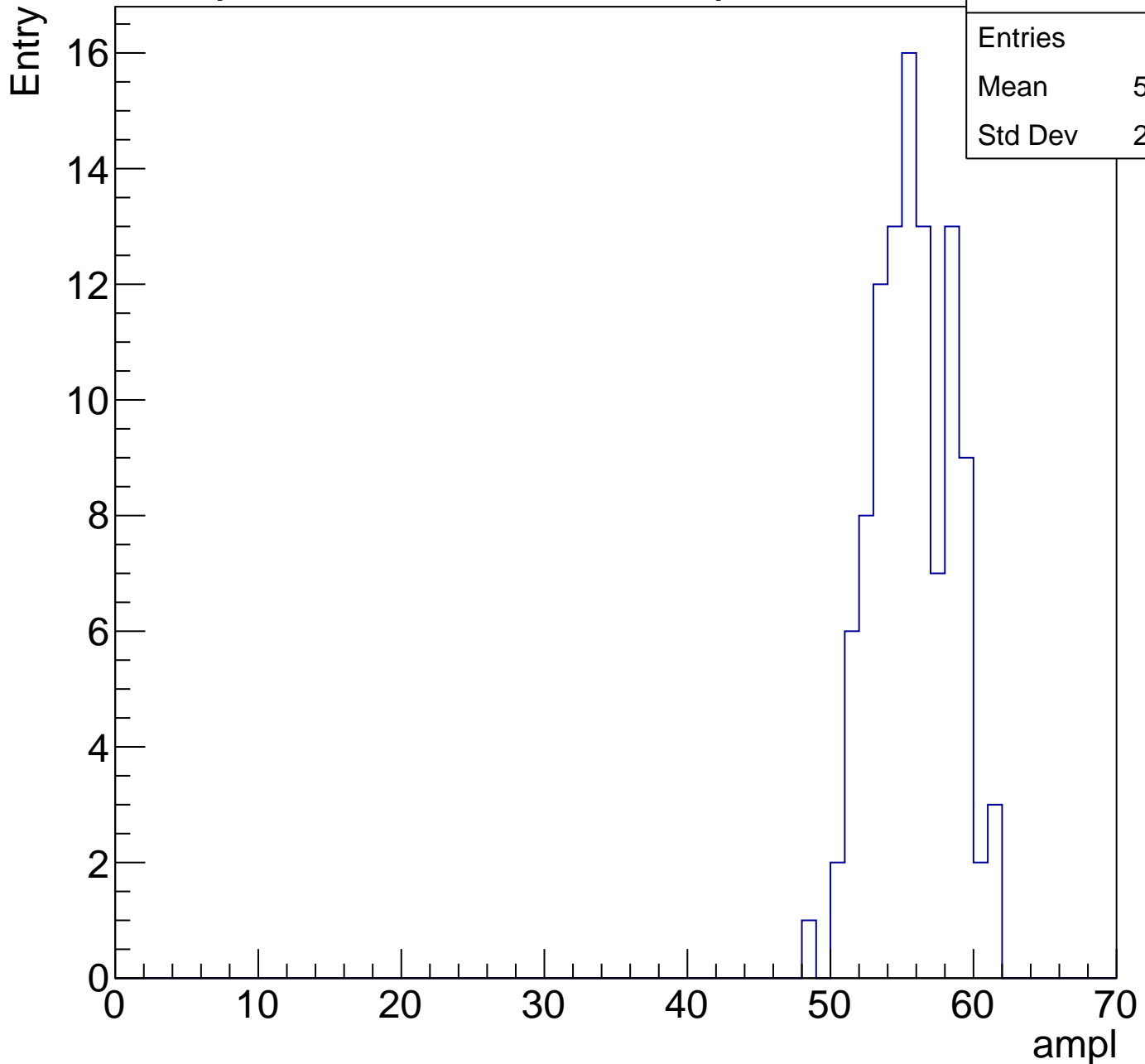
Entries	150
Mean	48.6
Std Dev	3.41



B1L001S, U24-ch120, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	55.27
Std Dev	2.726



B1L001S, U24-ch120, adc5

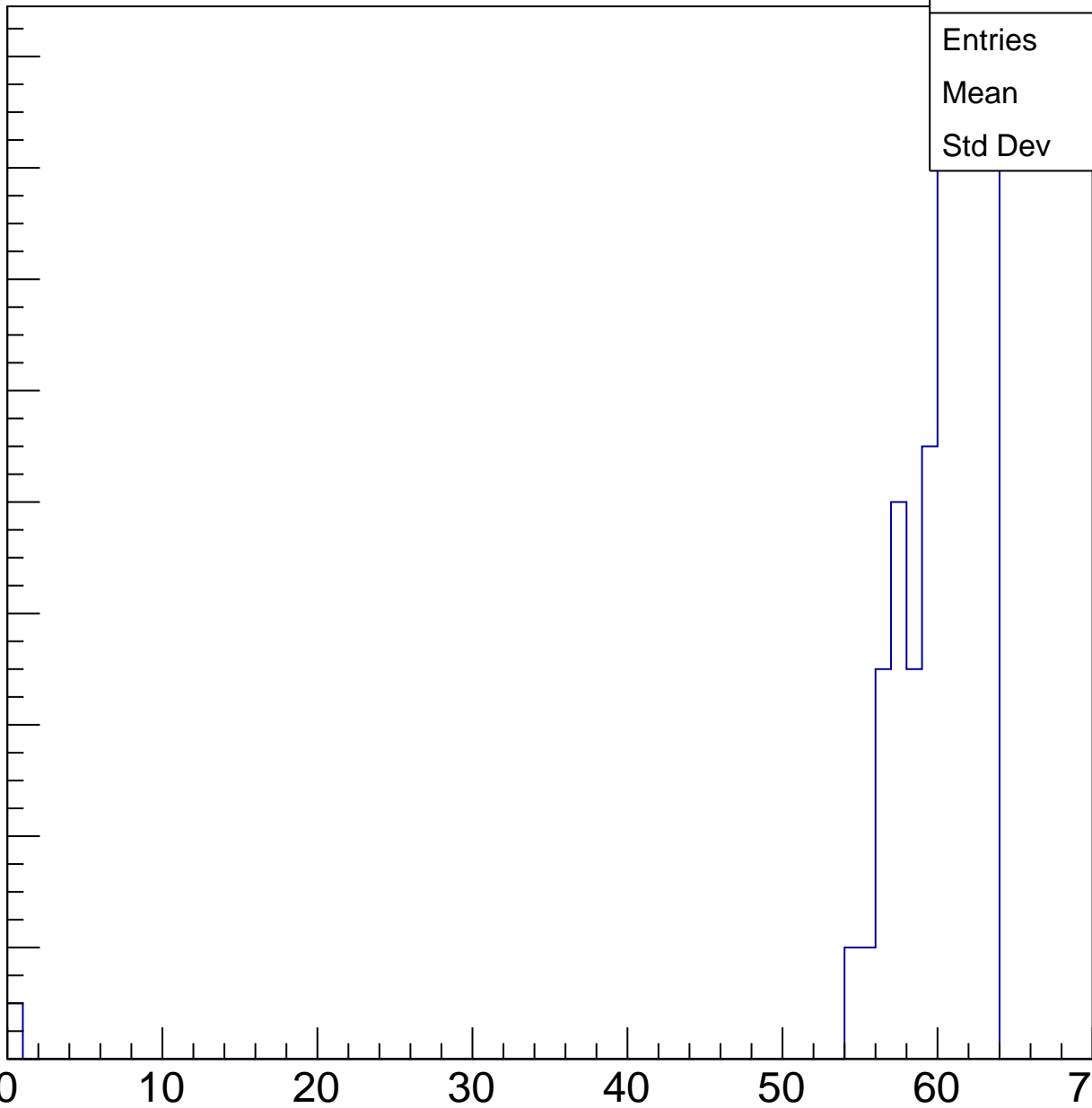
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	108
Mean	59.41
Std Dev	6.205

ampl



B1L001S, U24-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch121, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

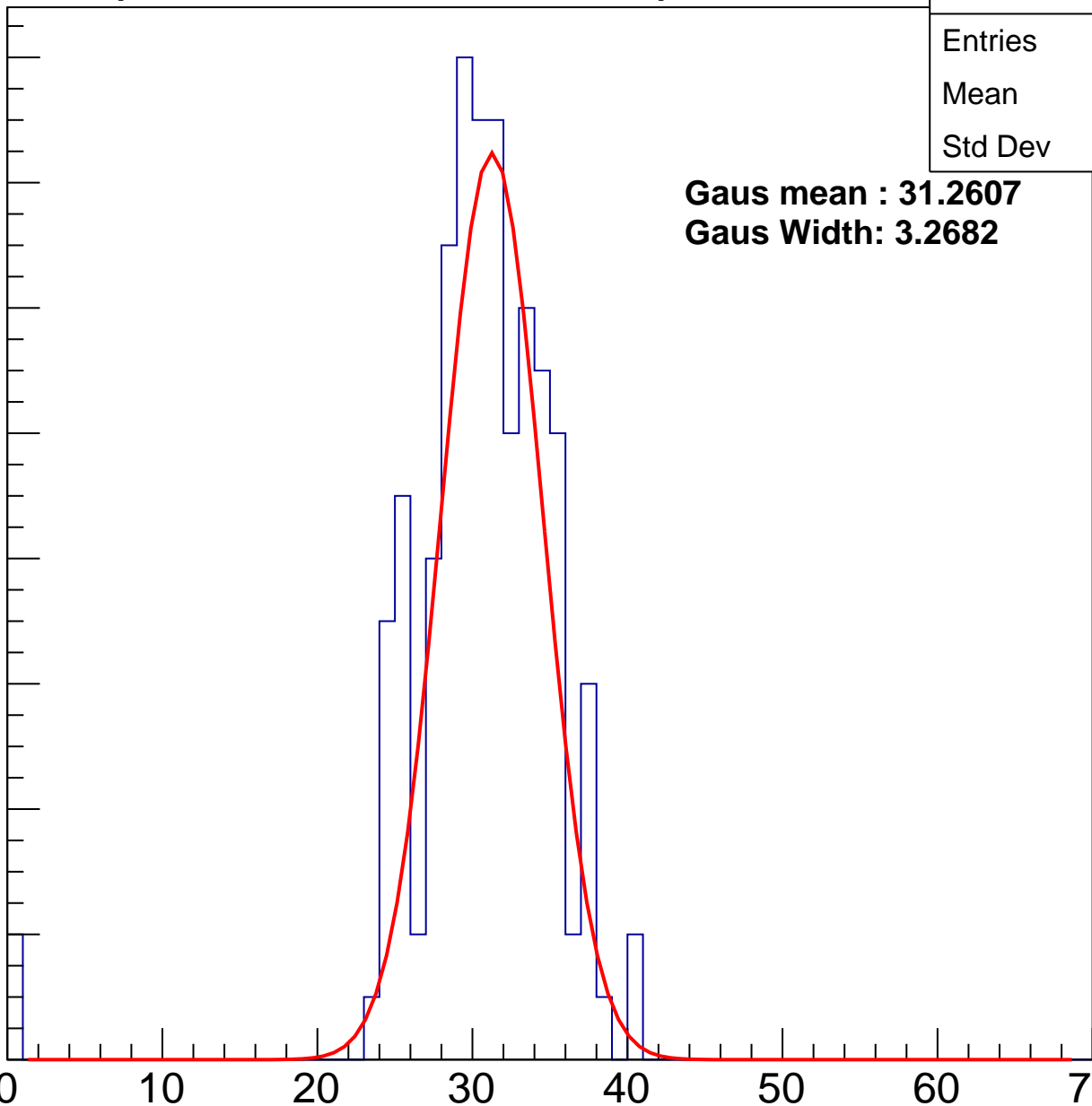
16
14
12
10
8
6
4
2
0

Entries	142
Mean	30.13
Std Dev	5.115

Gaus mean : 31.2607

Gaus Width: 3.2682

0 10 20 30 40 50 60 70
ampl



B1L001S, U24-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

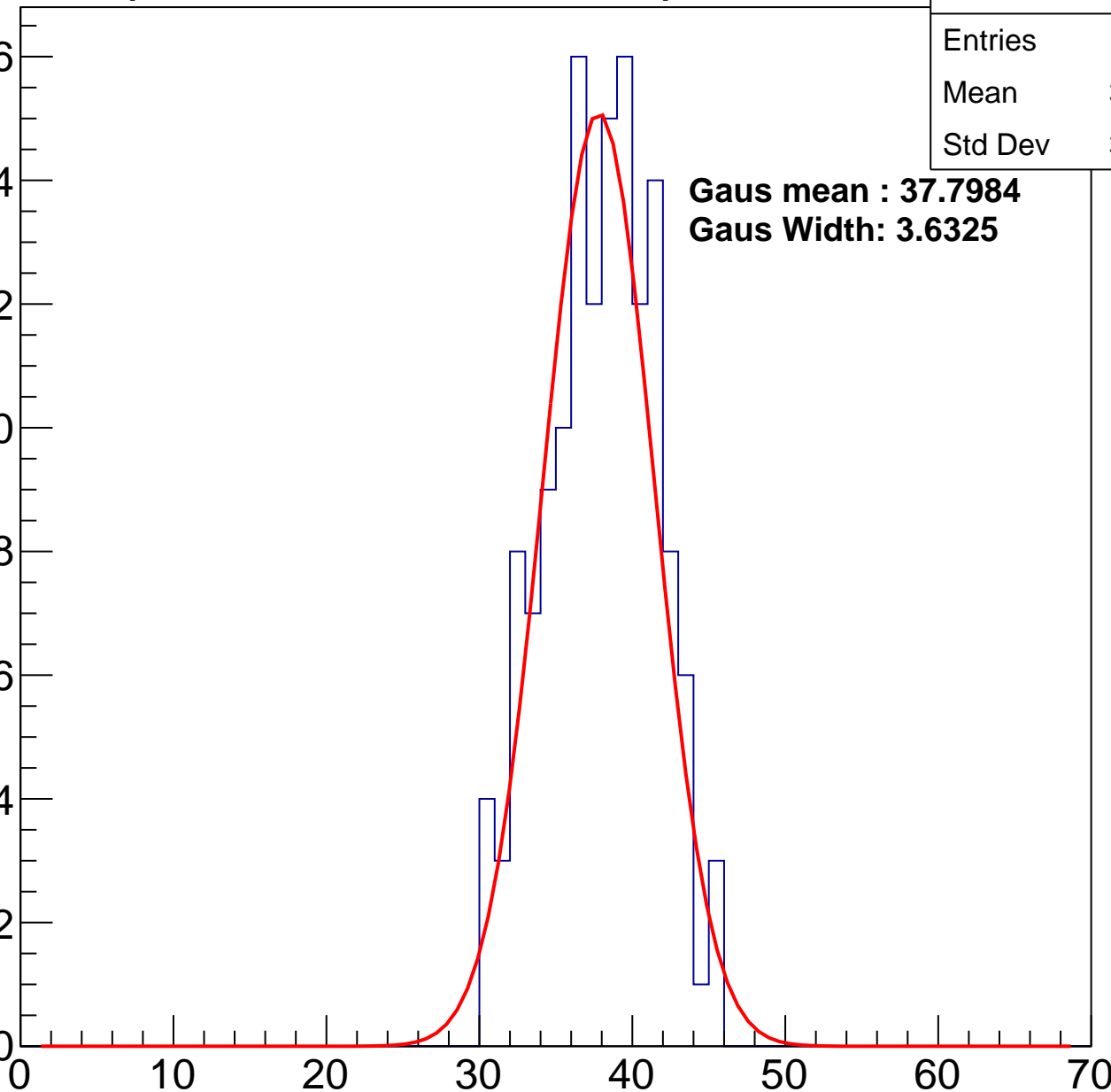
Entry

16
14
12
10
8
6
4
2
0

Entries	144
Mean	37.48
Std Dev	3.524

Gaus mean : 37.7984
Gaus Width: 3.6325

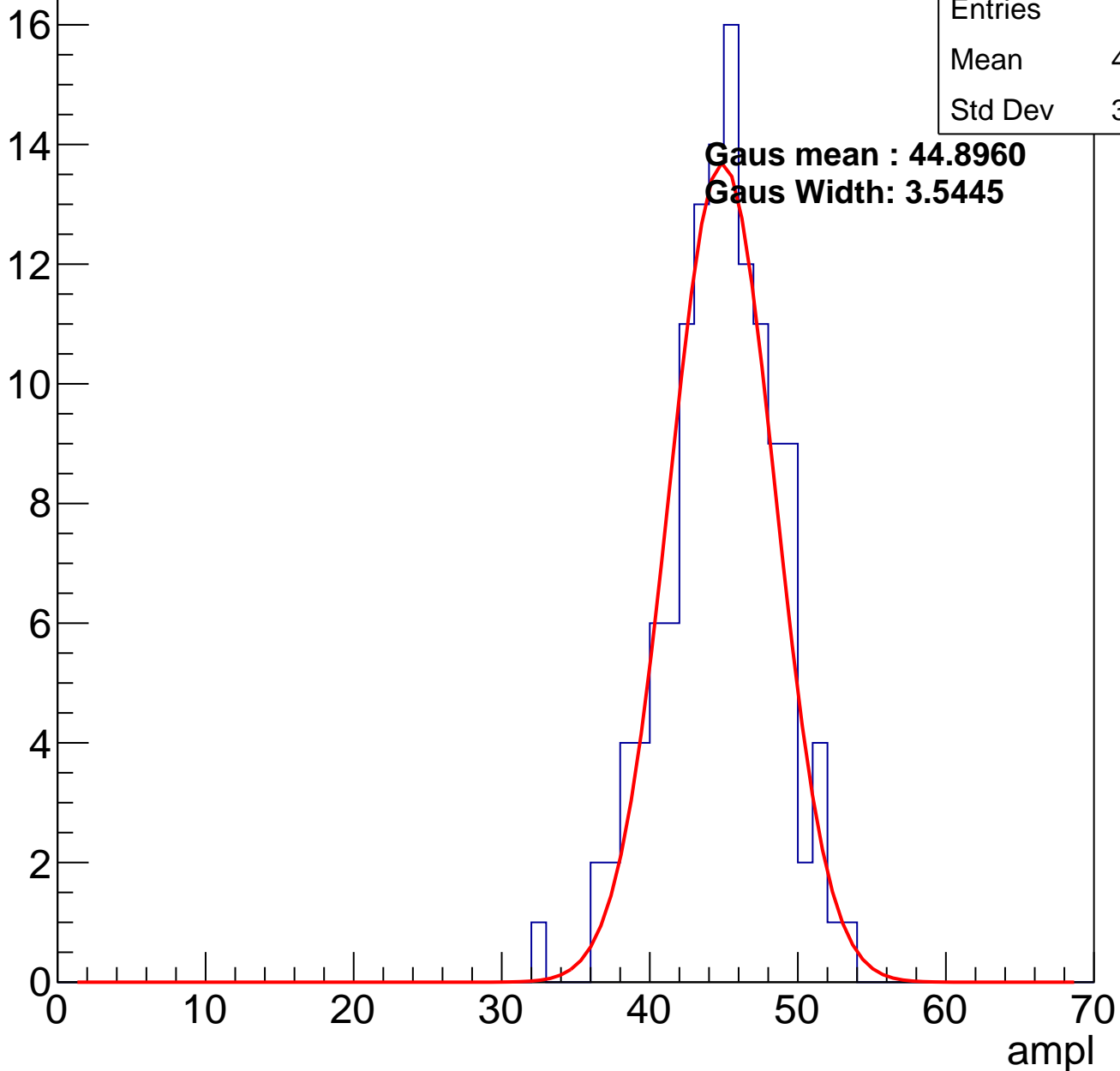
ampl



B1L001S, U24-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

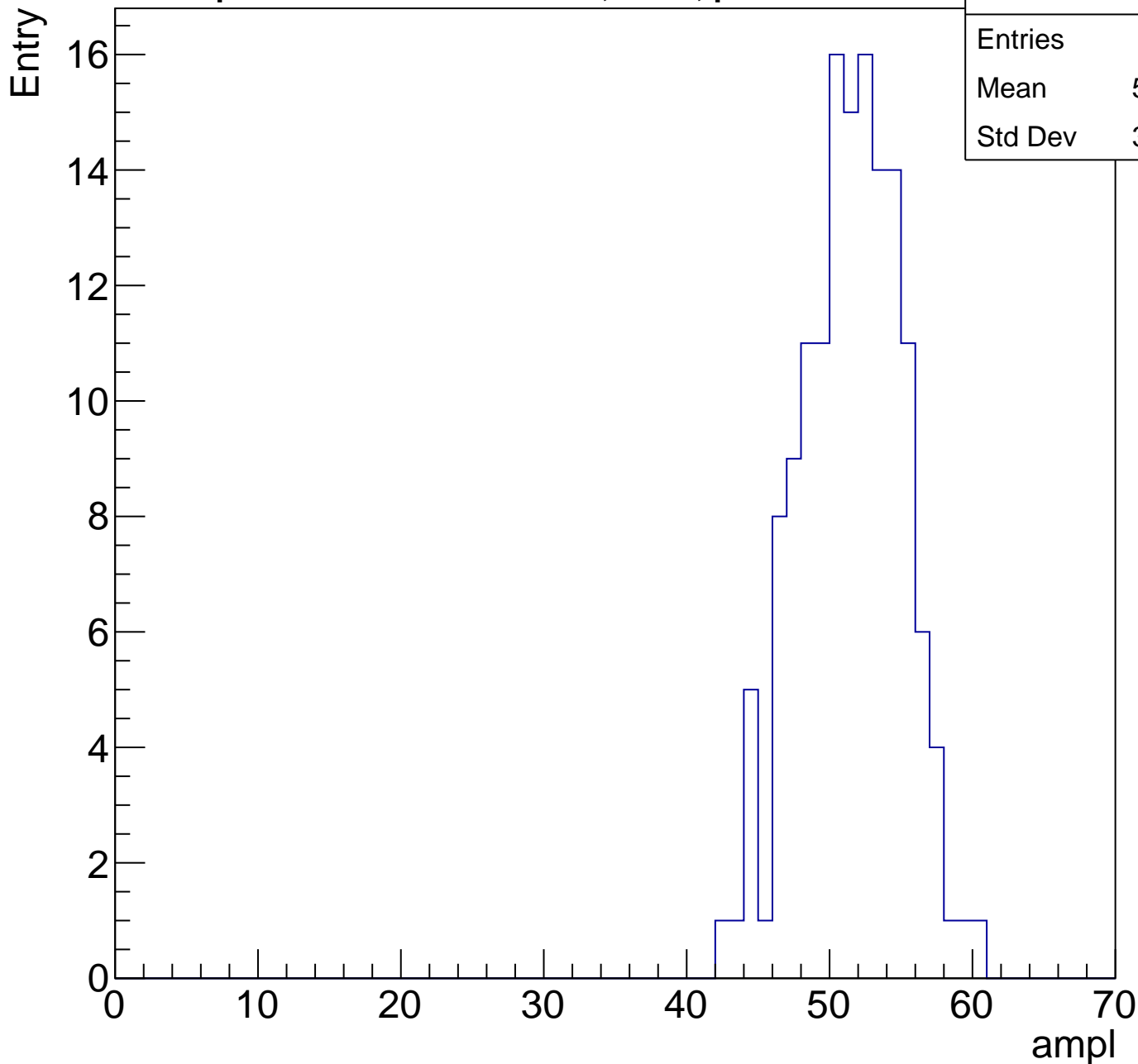
Entry



B1L001S, U24-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	51.02
Std Dev	3.502



B1L001S, U24-ch121, adc4

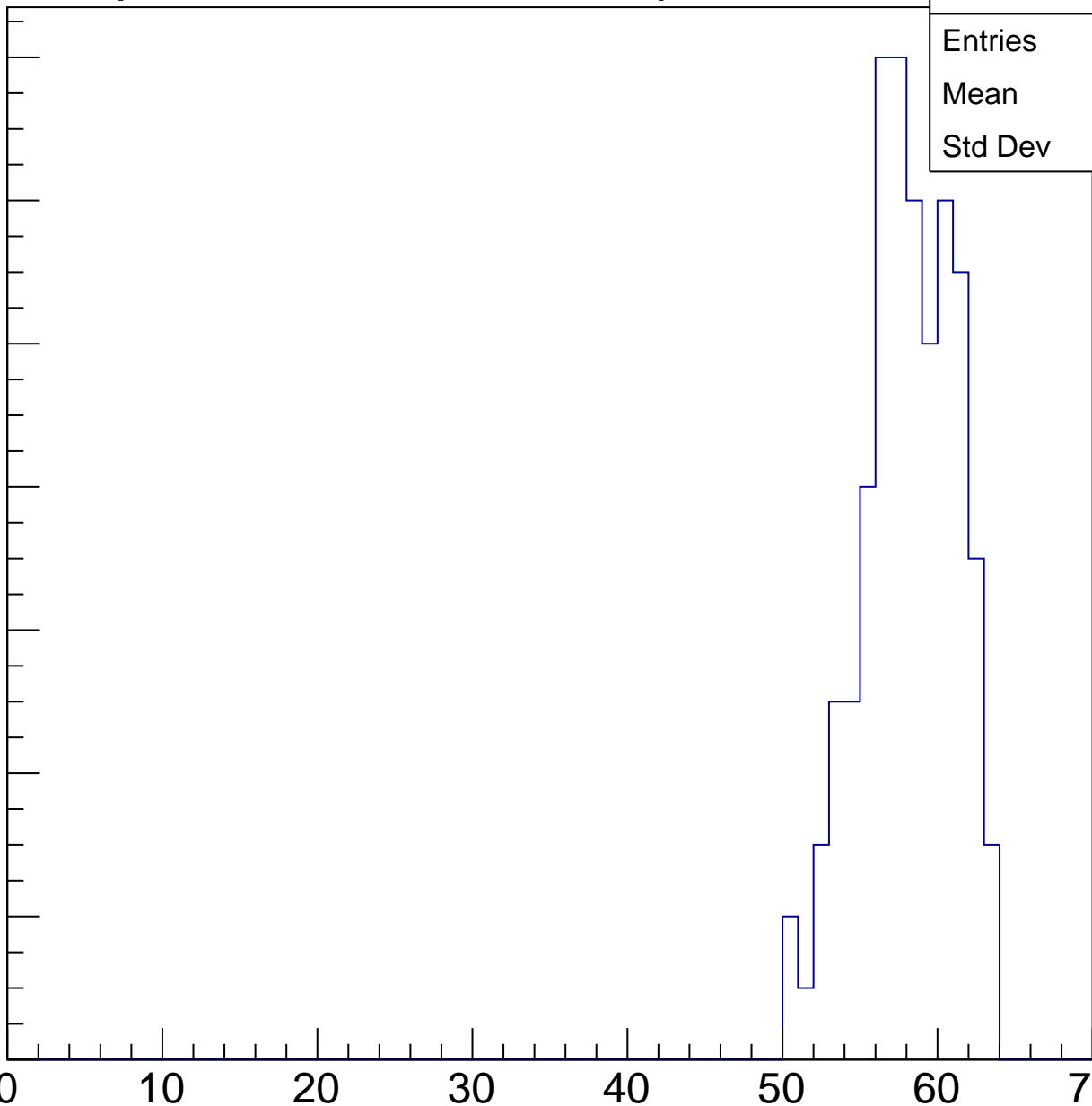
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	57.61
Std Dev	2.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch121, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

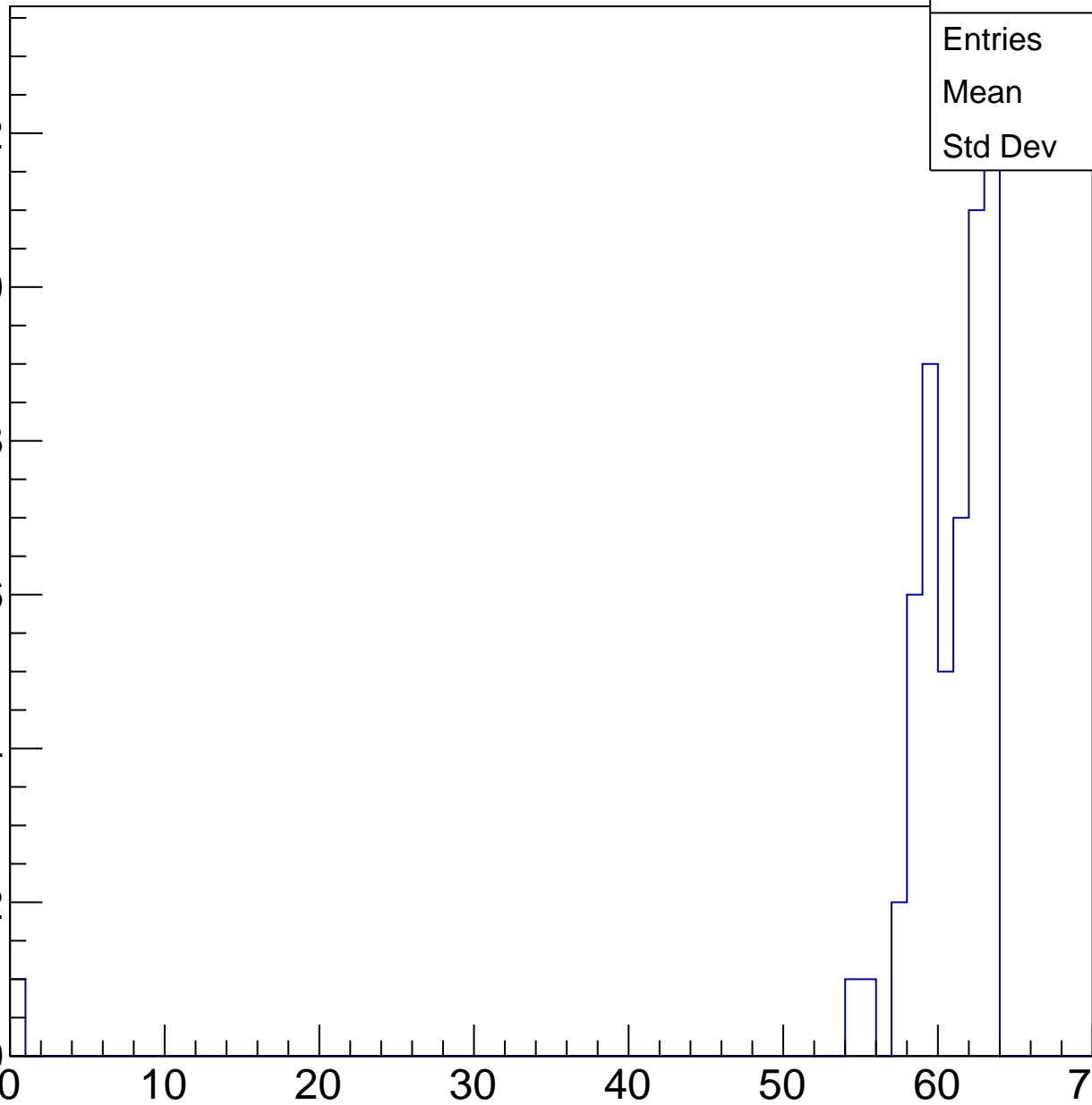
4

2

0

Entries	56
Mean	59.46
Std Dev	8.307

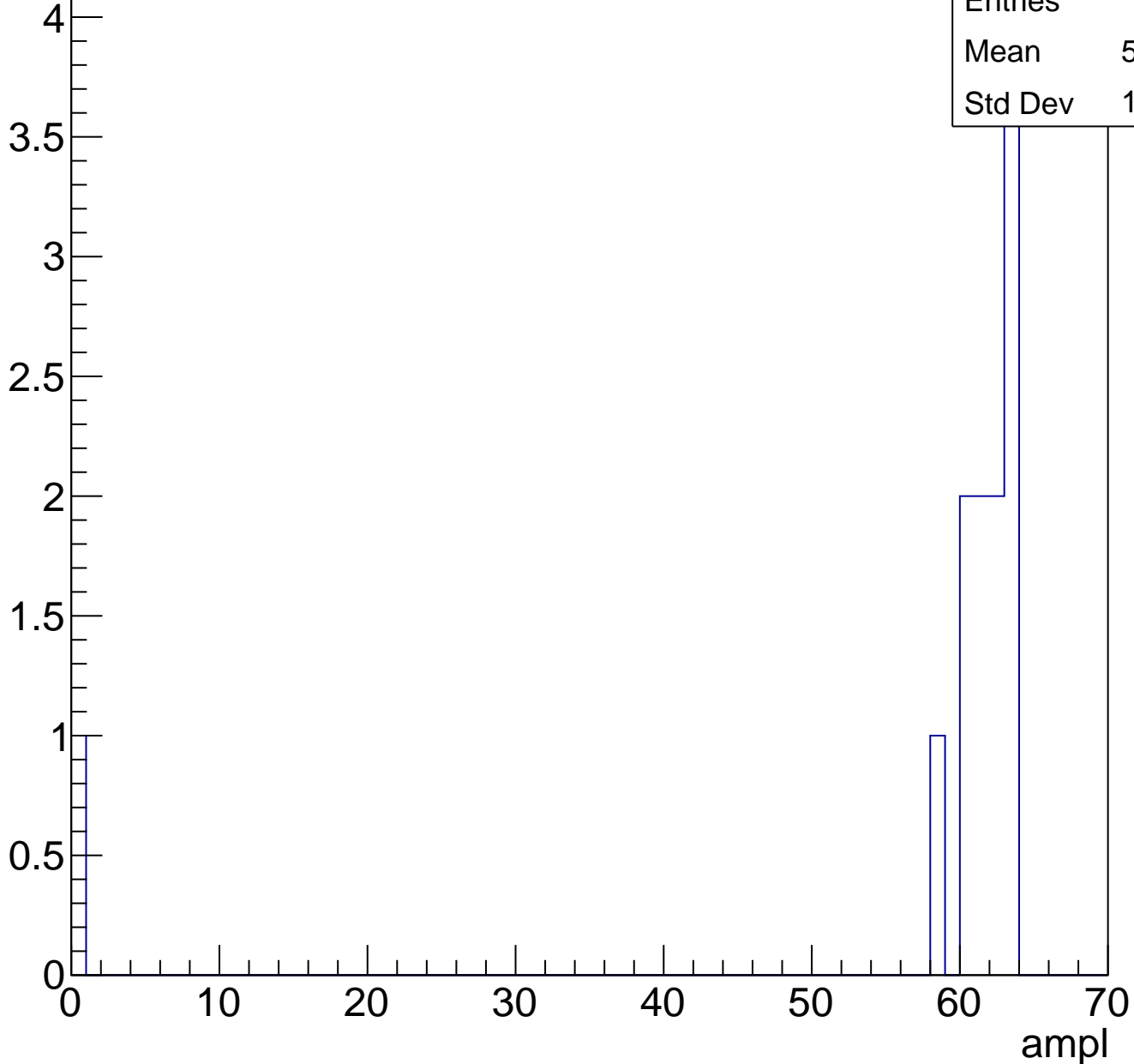
ampl



B1L001S, U24-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

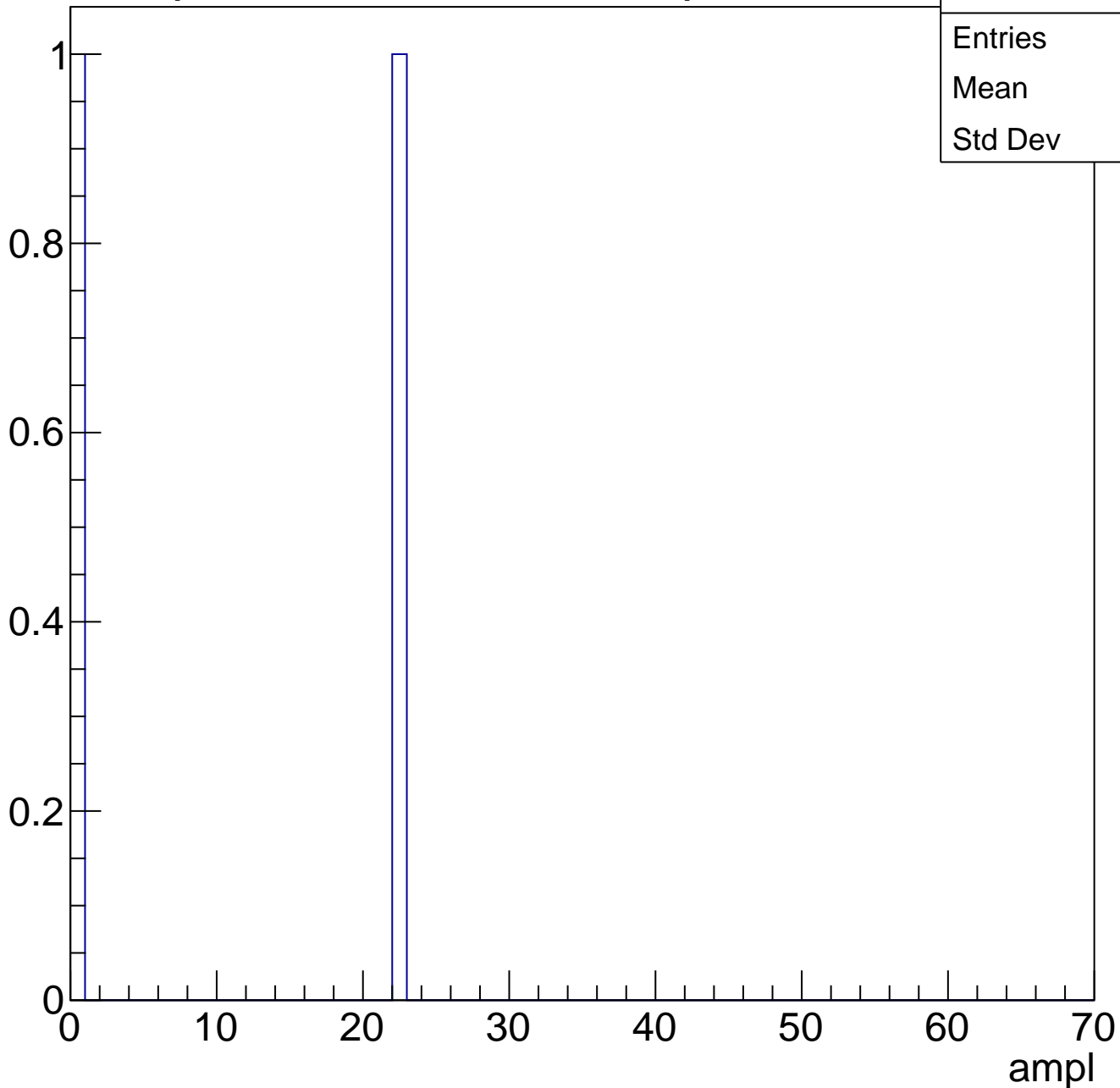
Entry



B1L001S, U24-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U24-ch122, adc0

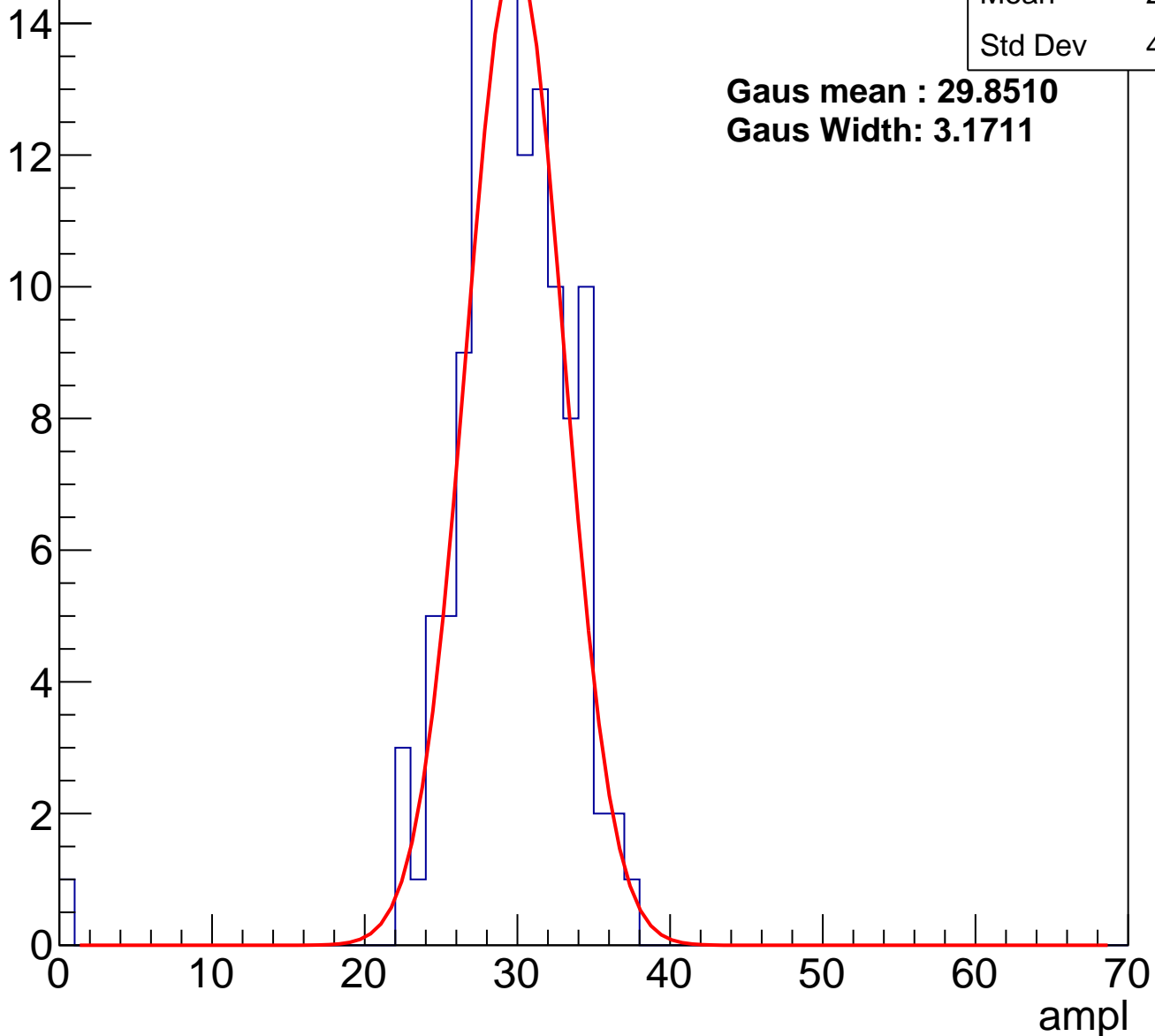
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	29.09
Std Dev	4.115

Gaus mean : 29.8510

Gaus Width: 3.1711

Entry



B1L001S, U24-ch122, adc1

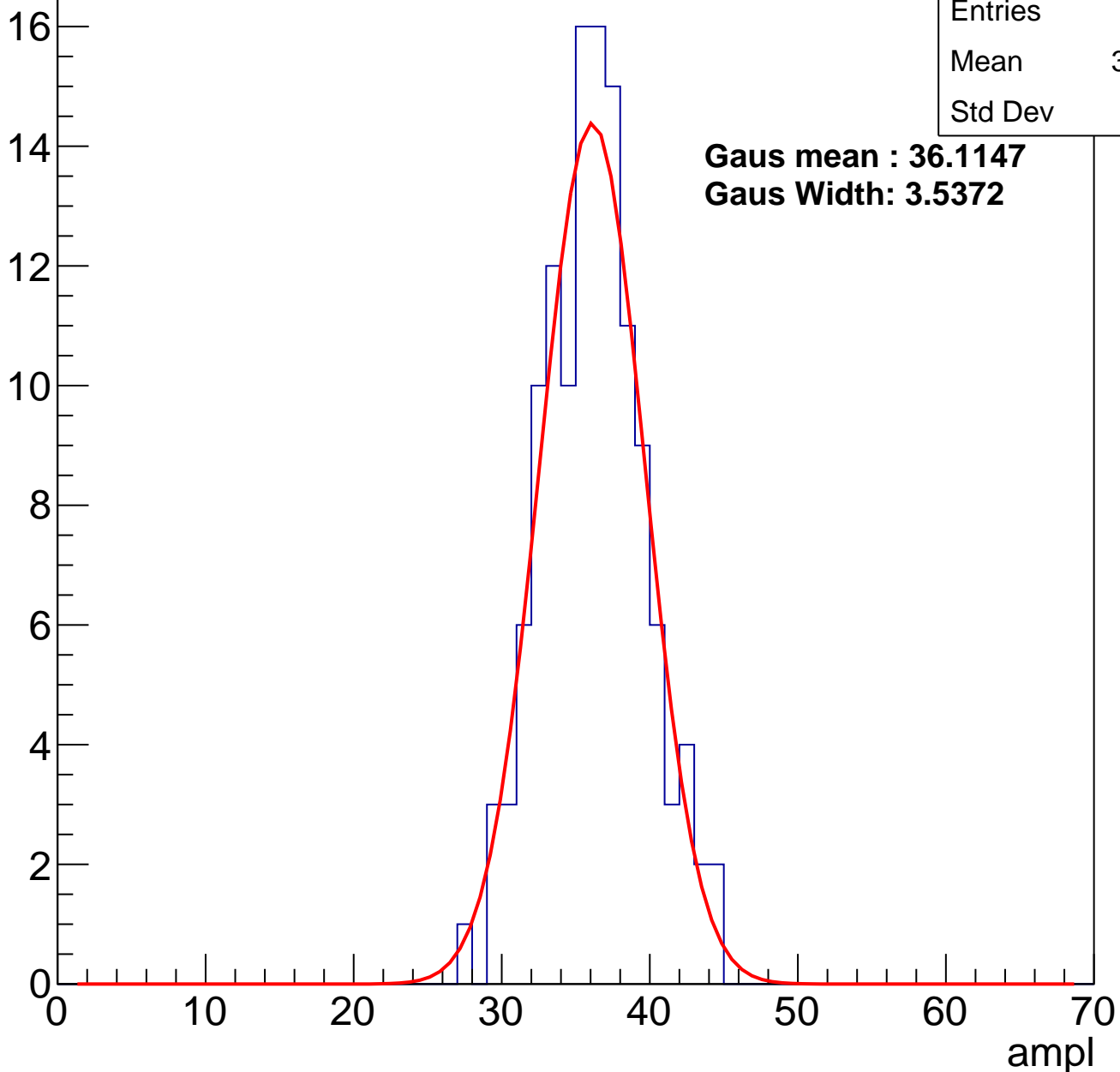
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.74
Std Dev	3.39

Gaus mean : 36.1147

Gaus Width: 3.5372

Entry



B1L001S, U24-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

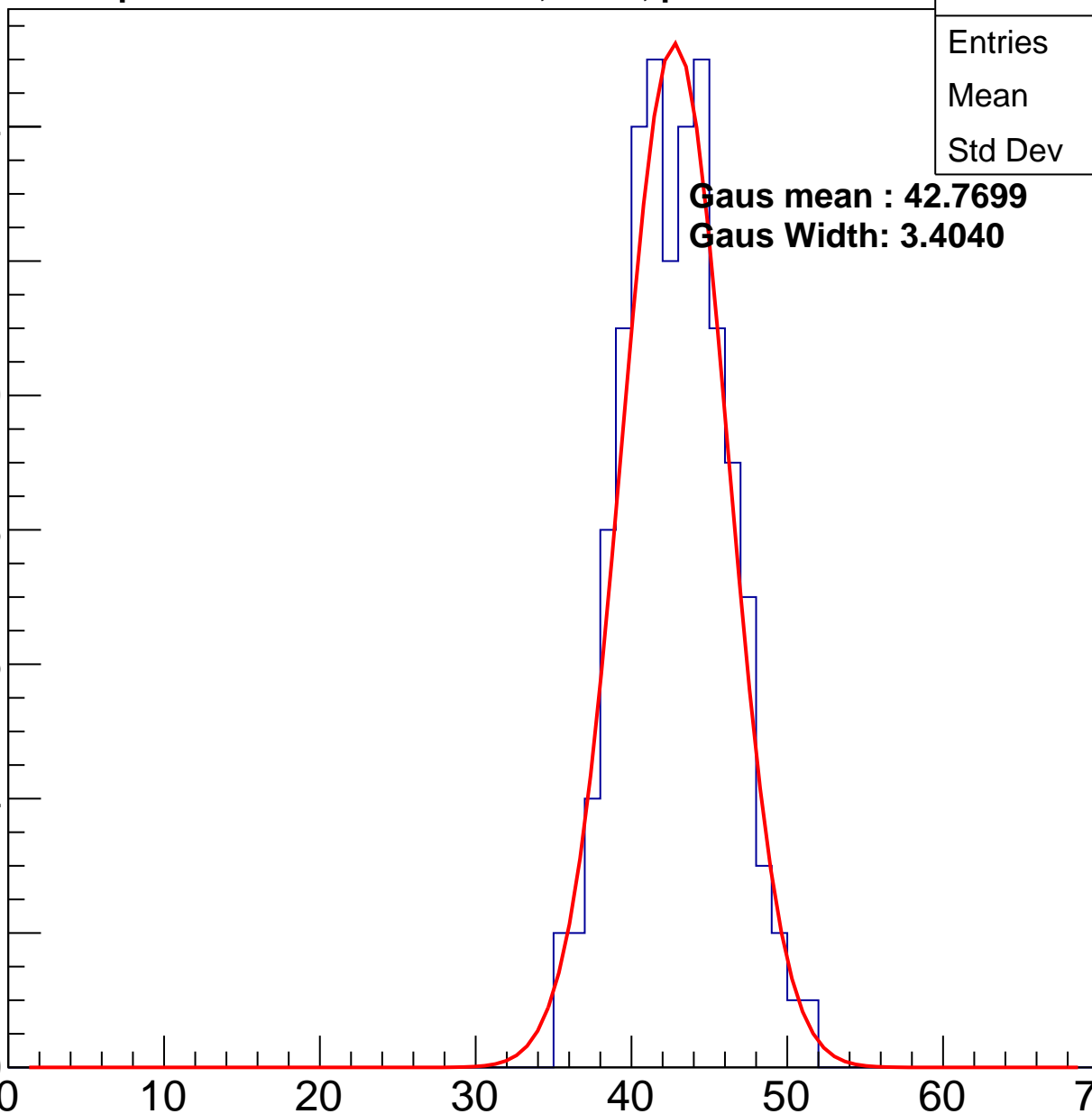
70

ampl

Entries	131
Mean	42.33
Std Dev	3.265

Gaus mean : 42.7699

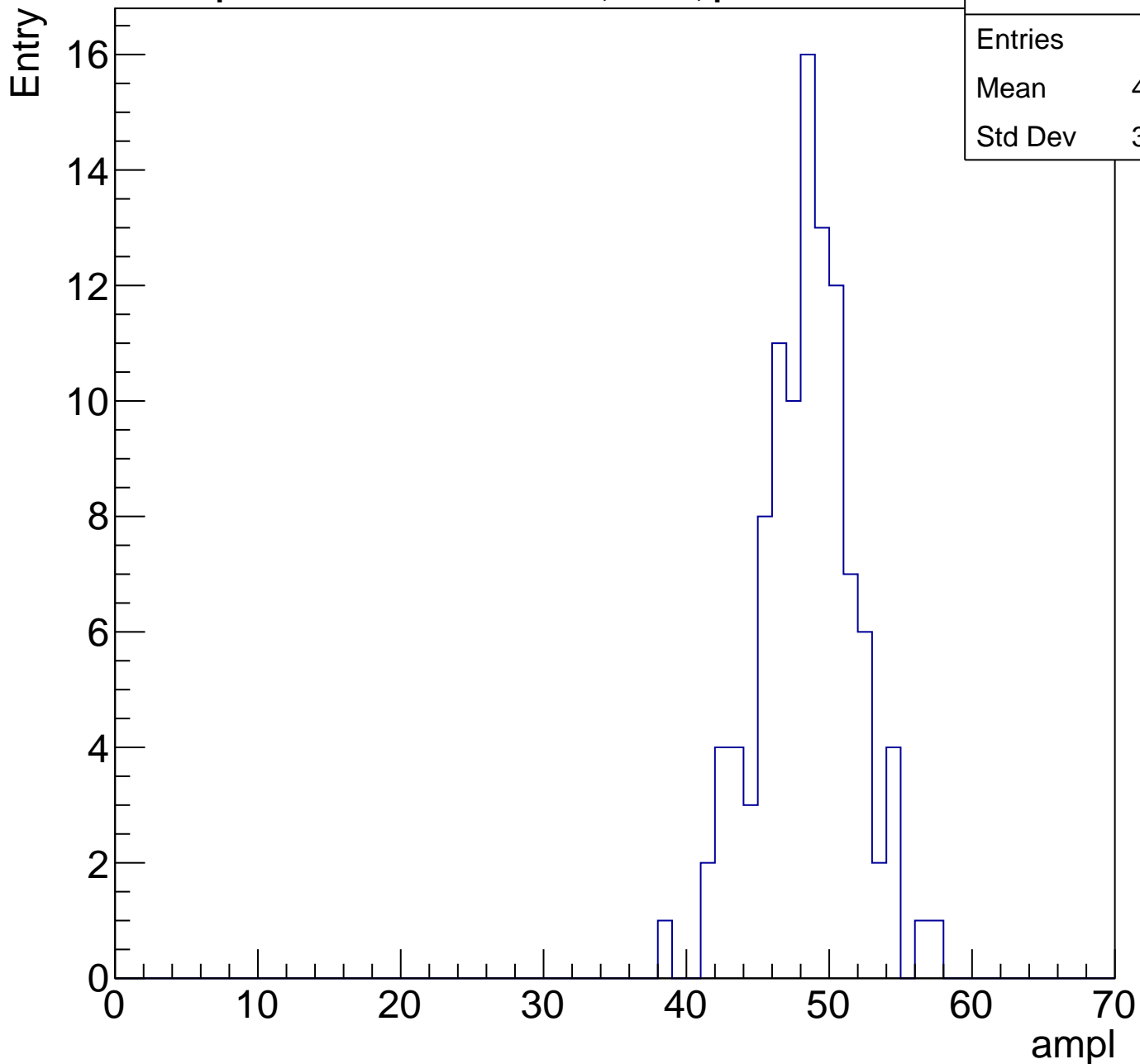
Gaus Width: 3.4040



B1L001S, U24-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

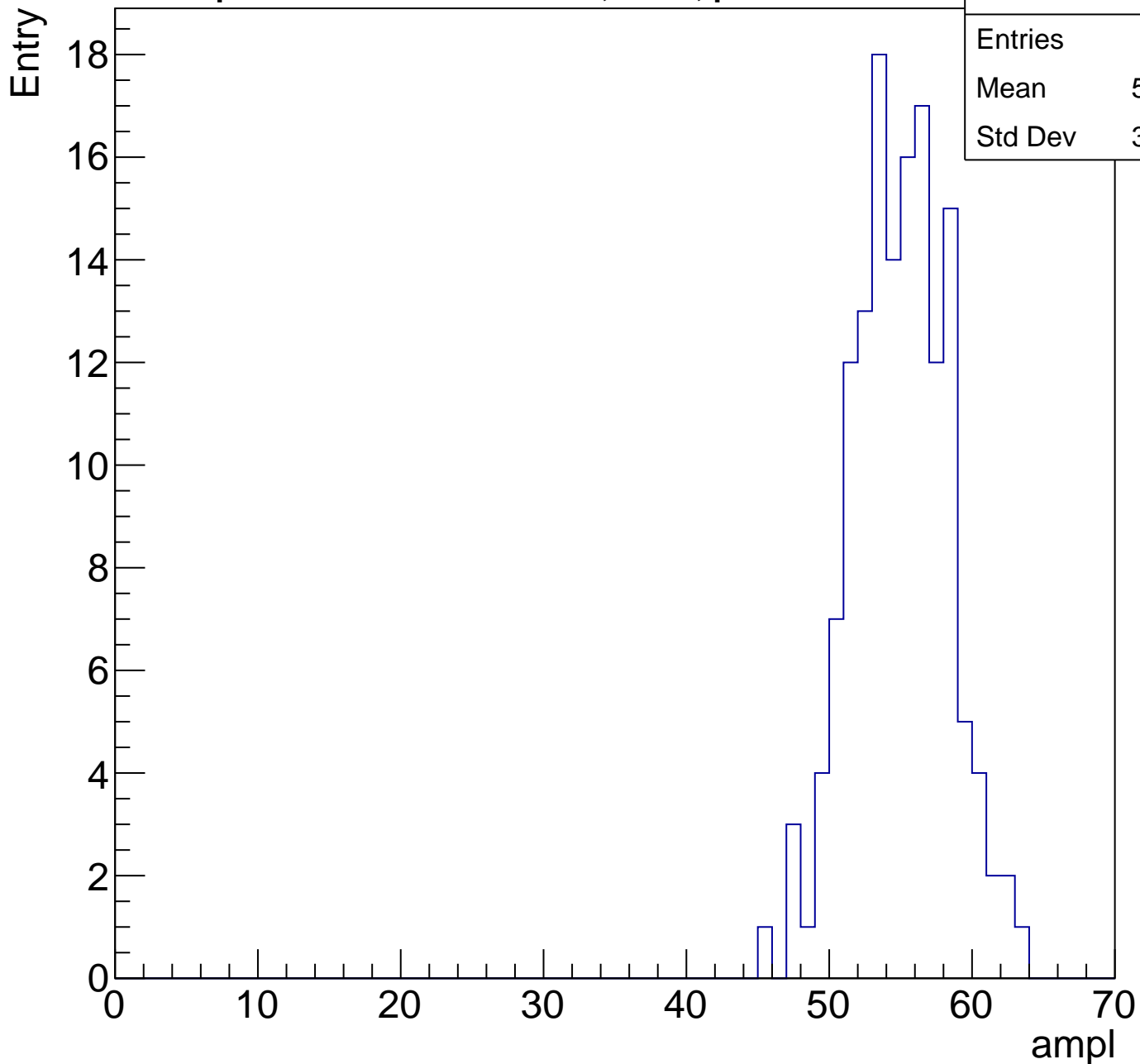
Entries	105
Mean	47.97
Std Dev	3.345



B1L001S, U24-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	54.48
Std Dev	3.329



B1L001S, U24-ch122, adc5

calib_packv5_042523_0143.root, FC#2, port C2

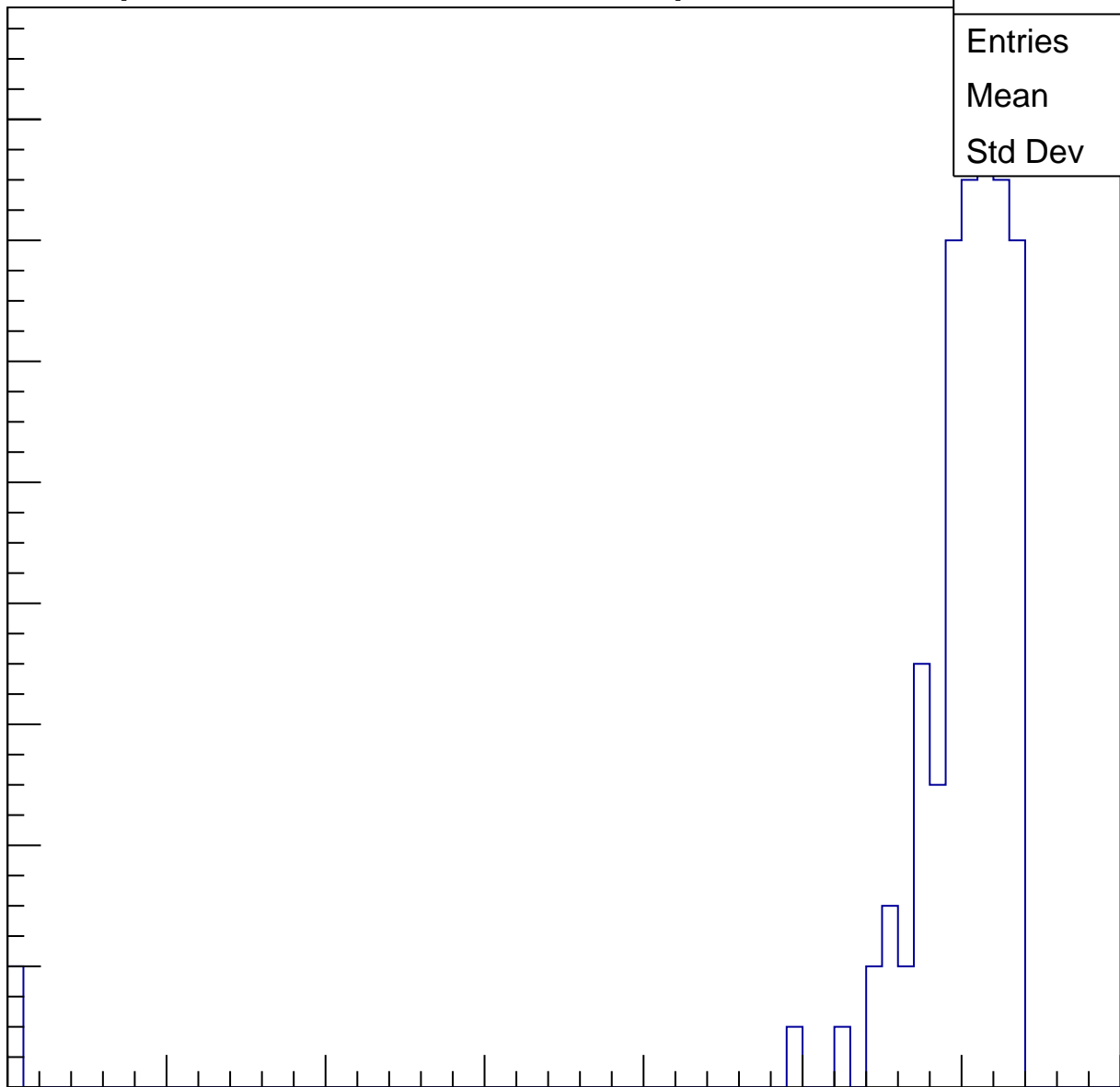
Entries	98
Mean	58.67
Std Dev	8.859

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

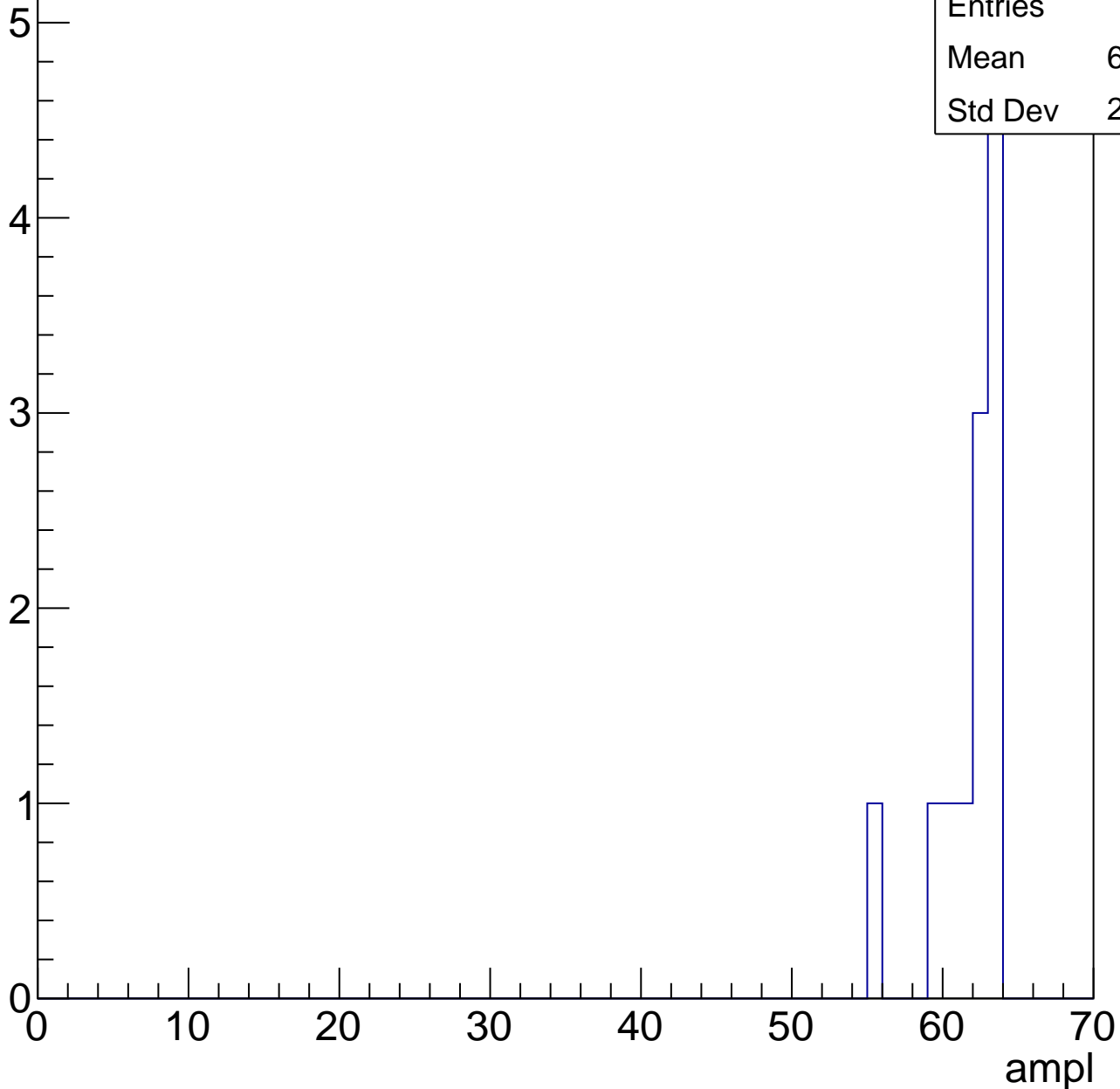


B1L001S, U24-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.33
Std Dev	2.285



B1L001S, U24-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

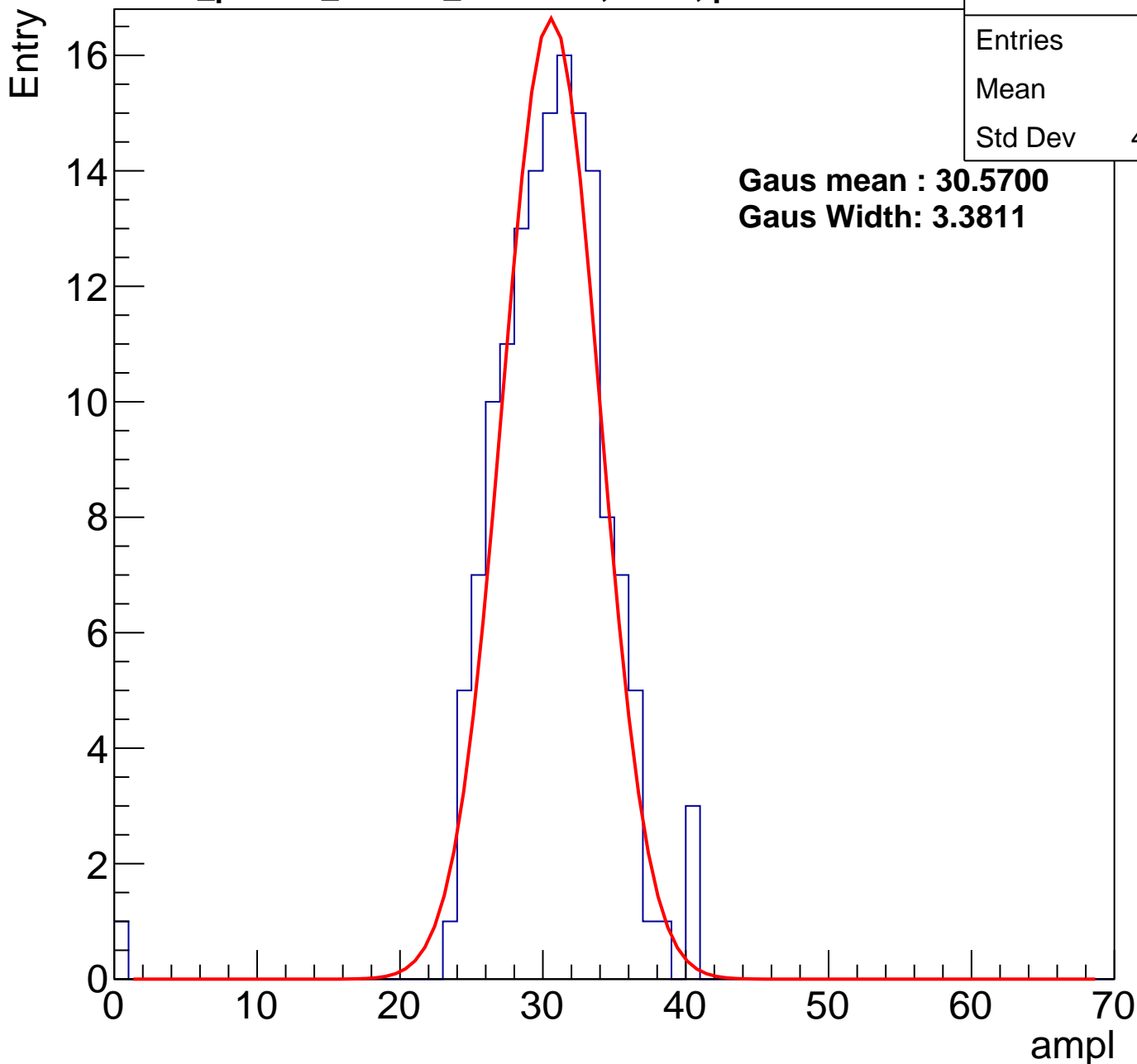
B1L001S, U24-ch123, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	30.1
Std Dev	4.306

Gaus mean : 30.5700

Gaus Width: 3.3811



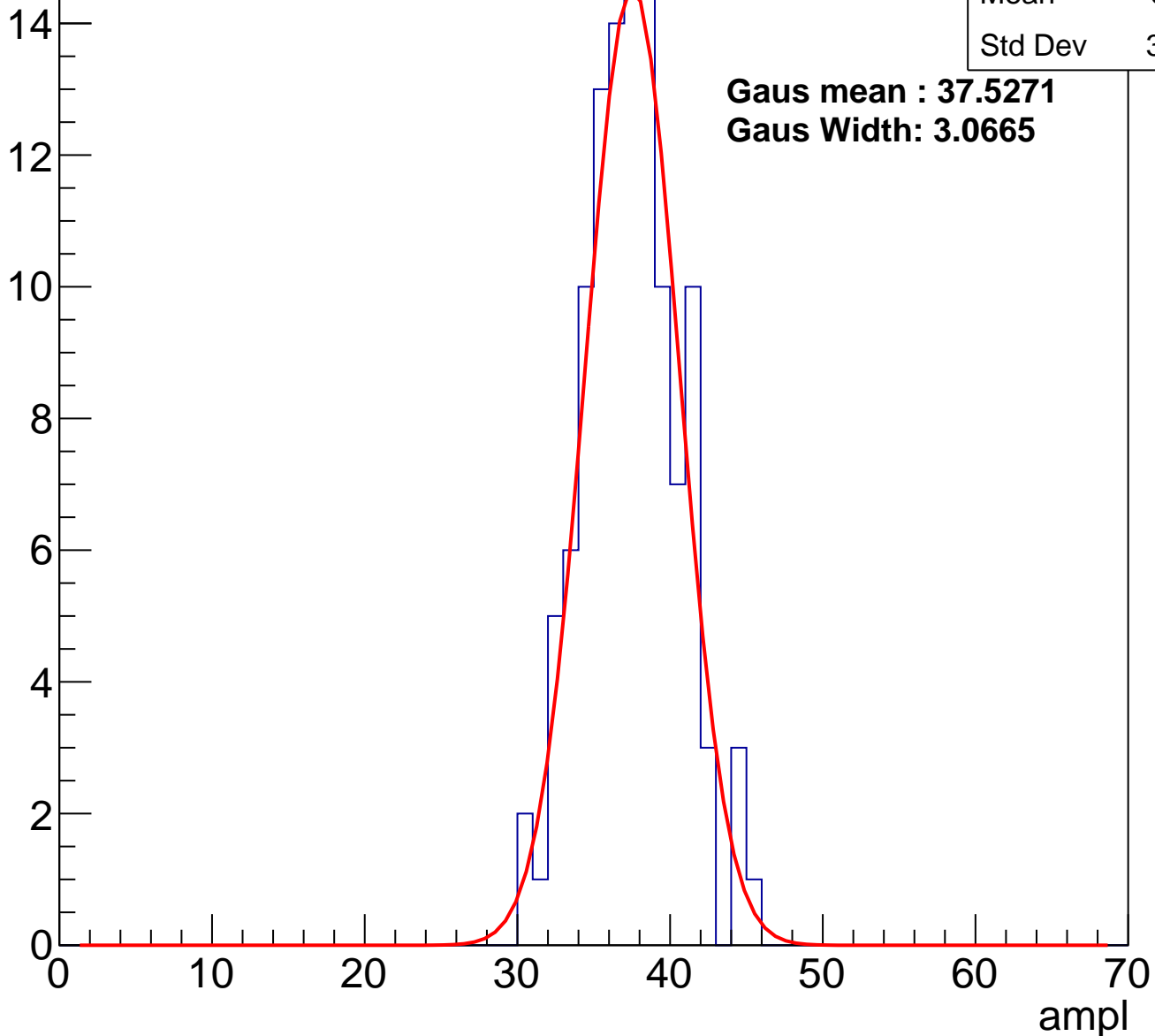
B1L001S, U24-ch123, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	37.01
Std Dev	3.057

Gaus mean : 37.5271
Gaus Width: 3.0665

Entry



B1L001S, U24-ch123, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

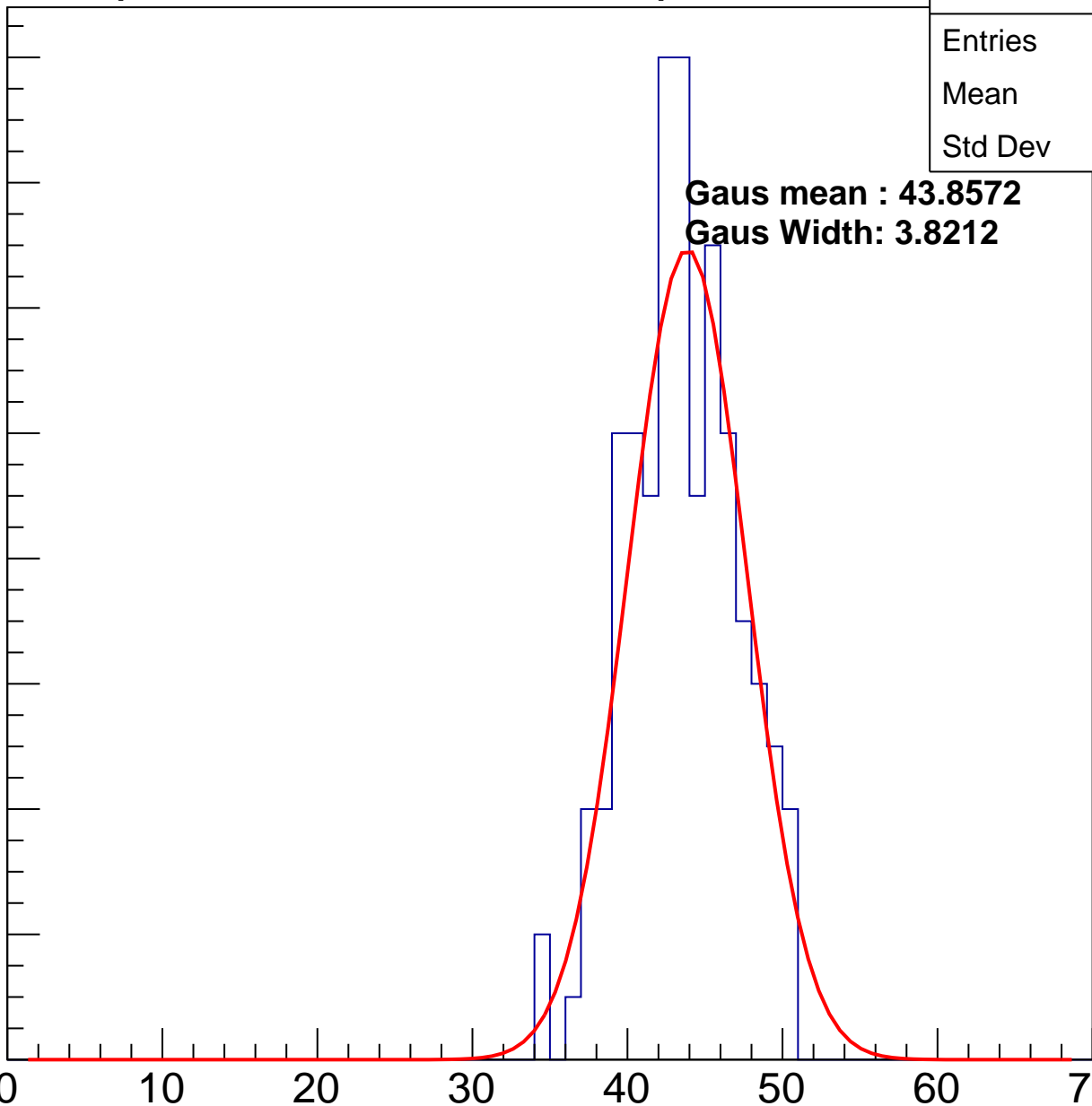
16
14
12
10
8
6
4
2
0

Entries	126
Mean	43.06
Std Dev	3.502

Gaus mean : 43.8572

Gaus Width: 3.8212

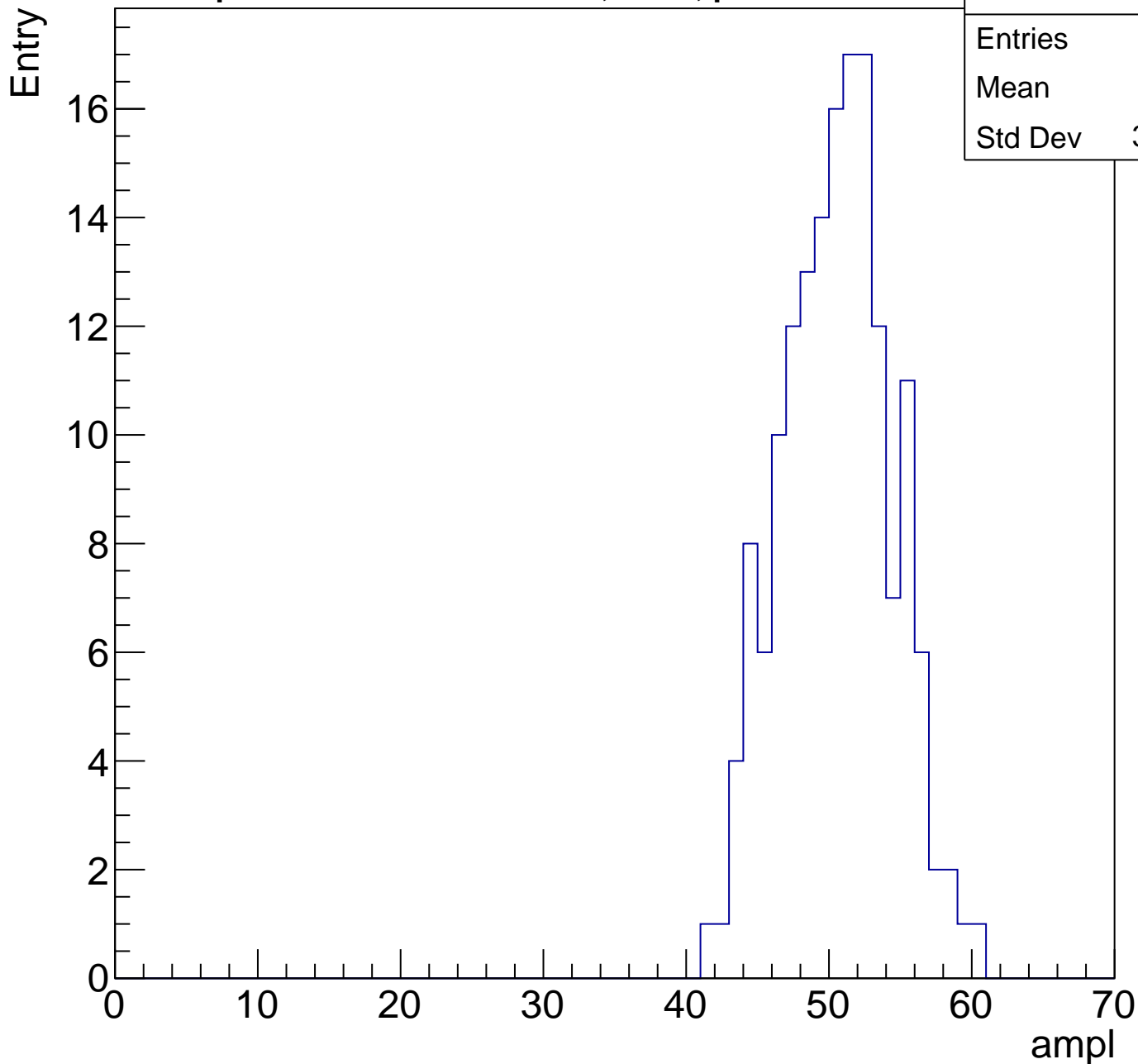
0 10 20 30 40 50 60 70
ampl



B1L001S, U24-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	50.1
Std Dev	3.791



B1L001S, U24-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

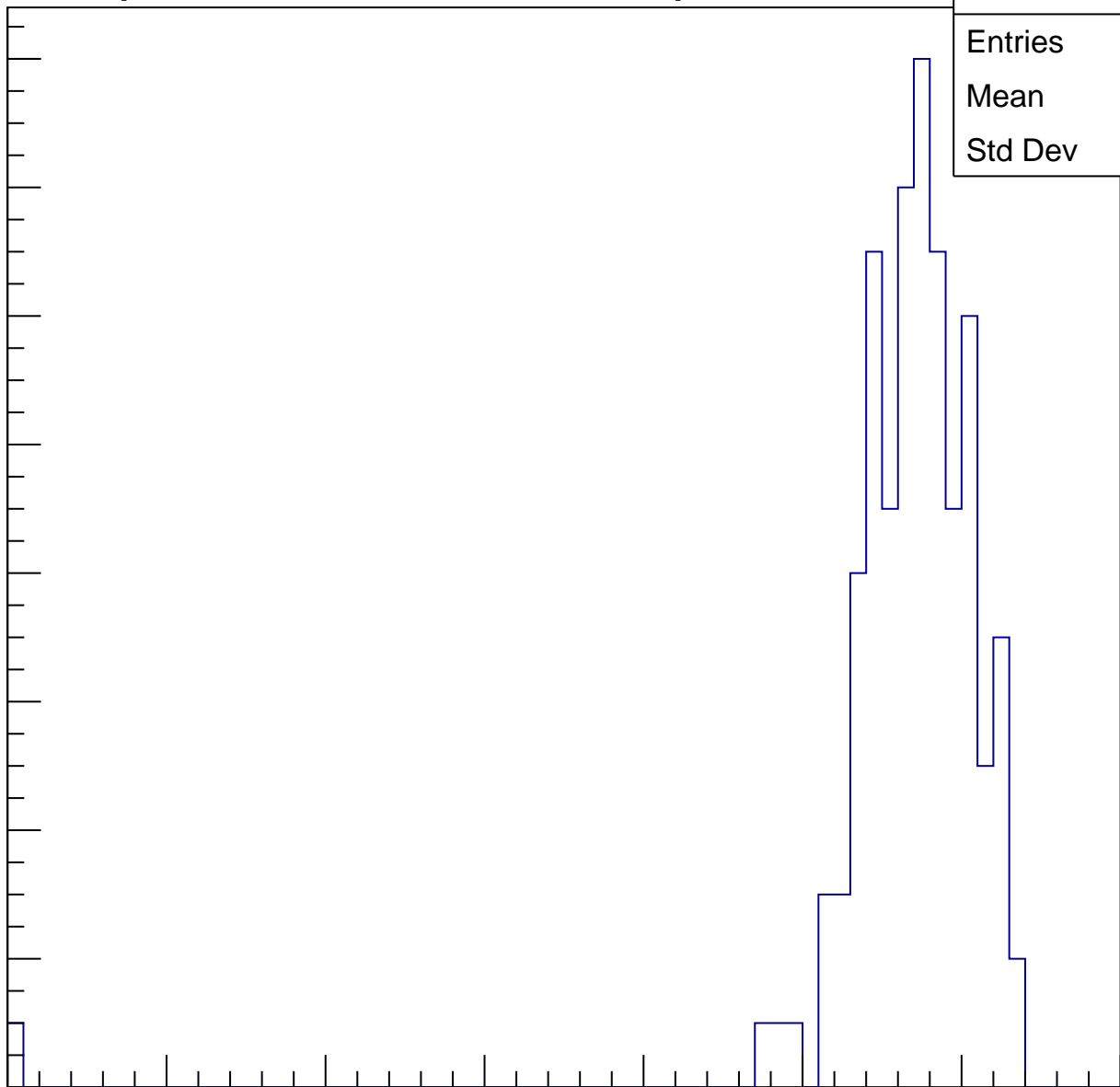
Entries	118
Mean	56.27
Std Dev	6.102

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch123, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	66
Mean	59.97
Std Dev	7.638

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

40

50

60

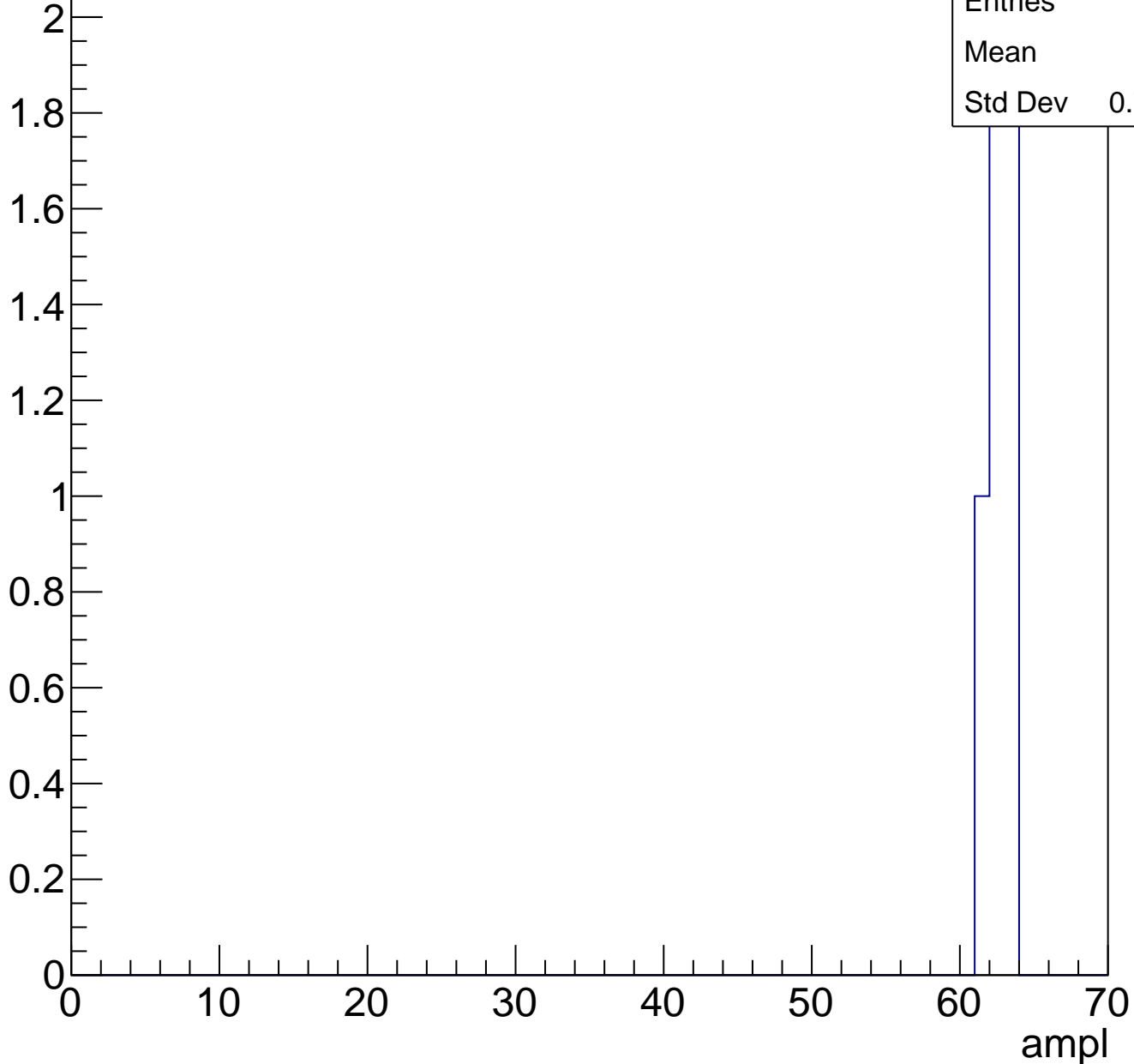
ampl

70

B1L001S, U24-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U24-ch124, adc0

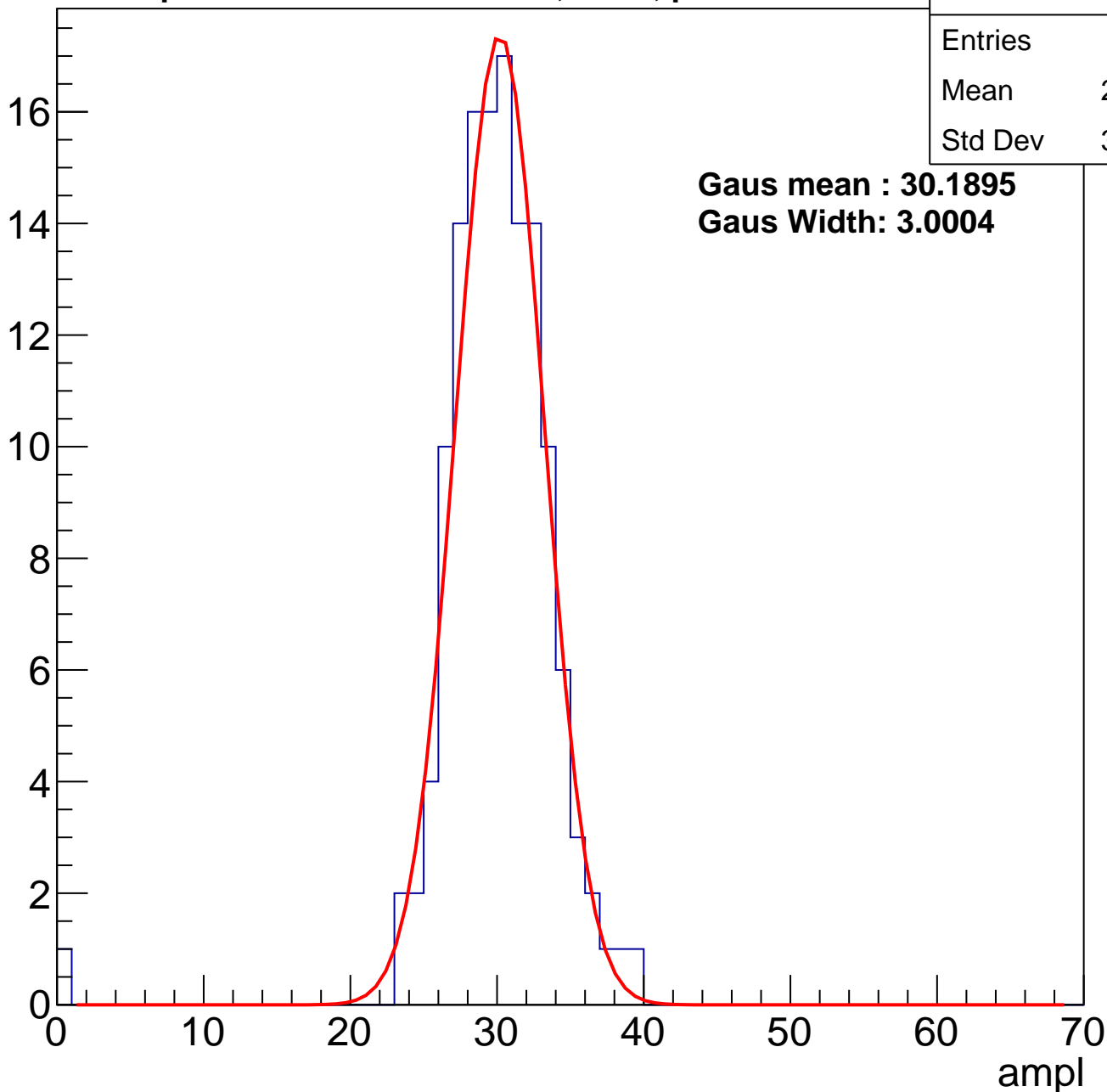
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.56
Std Dev	3.965

Gaus mean : 30.1895

Gaus Width: 3.0004

Entry



B1L001S, U24-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.86
Std Dev	3.339

Gaus mean : 37.4032

Gaus Width: 3.4916

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

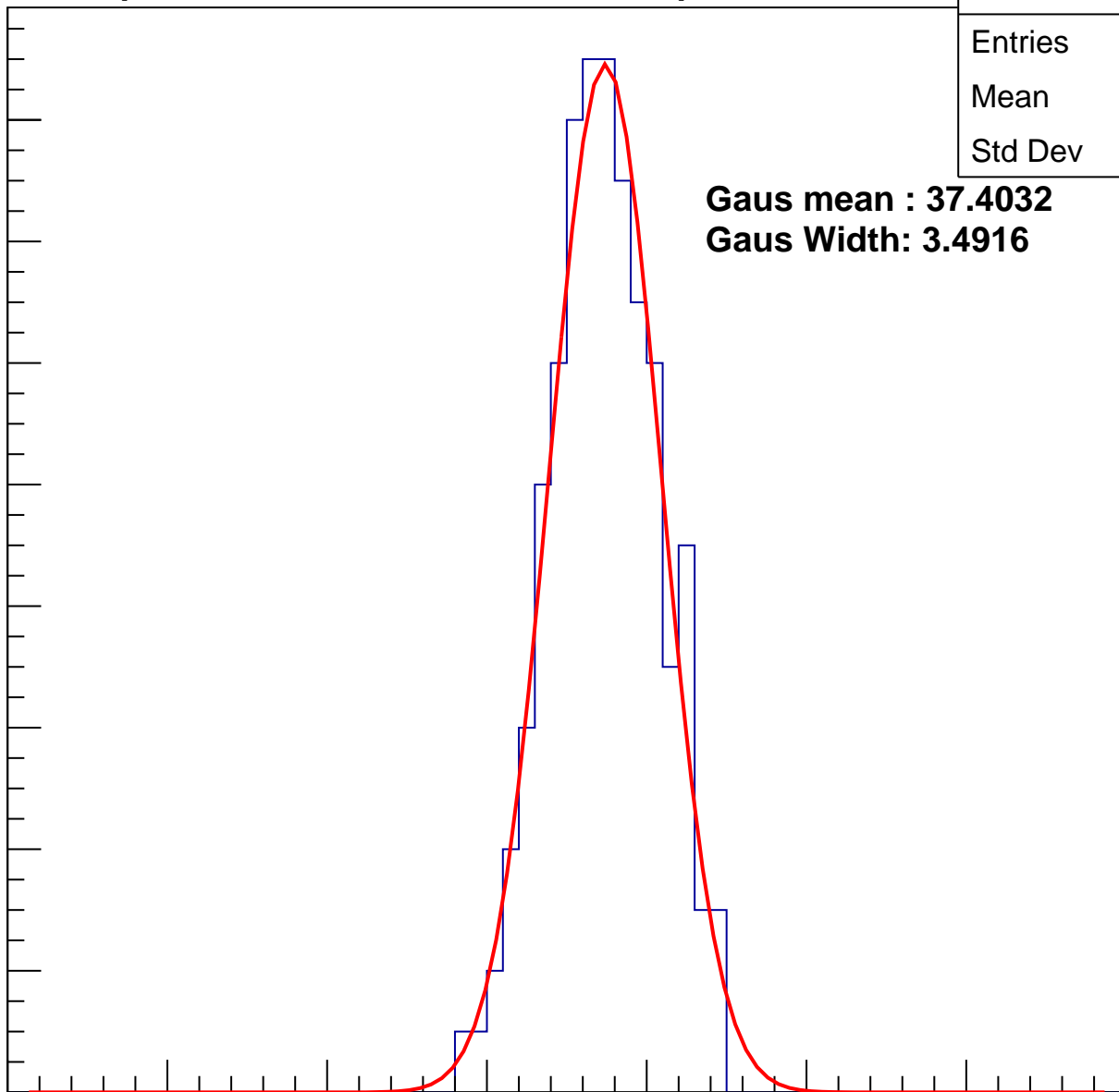
40

50

60

70

ampl



B1L001S, U24-ch124, adc2

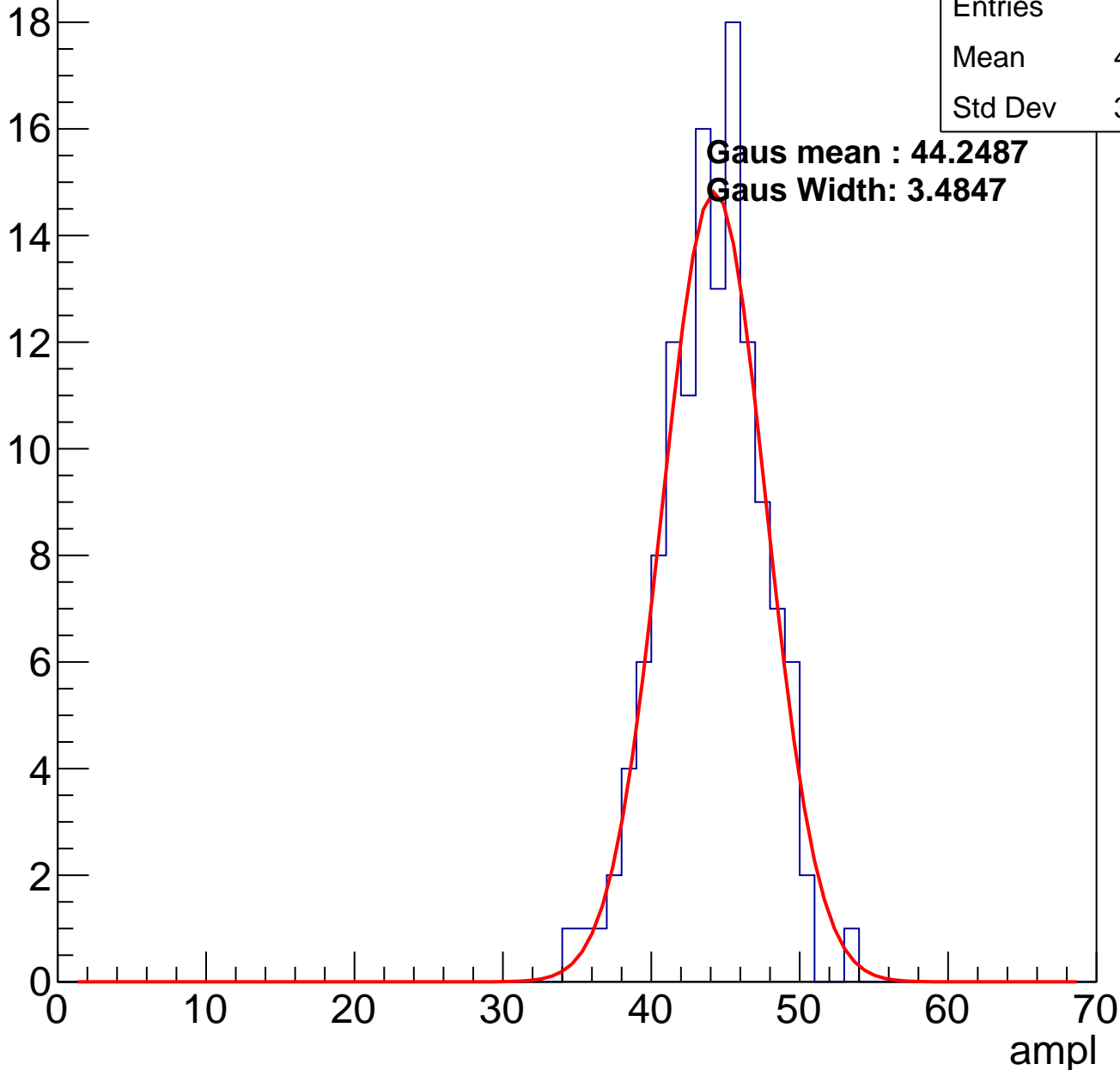
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	130
Mean	43.59
Std Dev	3.367

Gaus mean : 44.2487

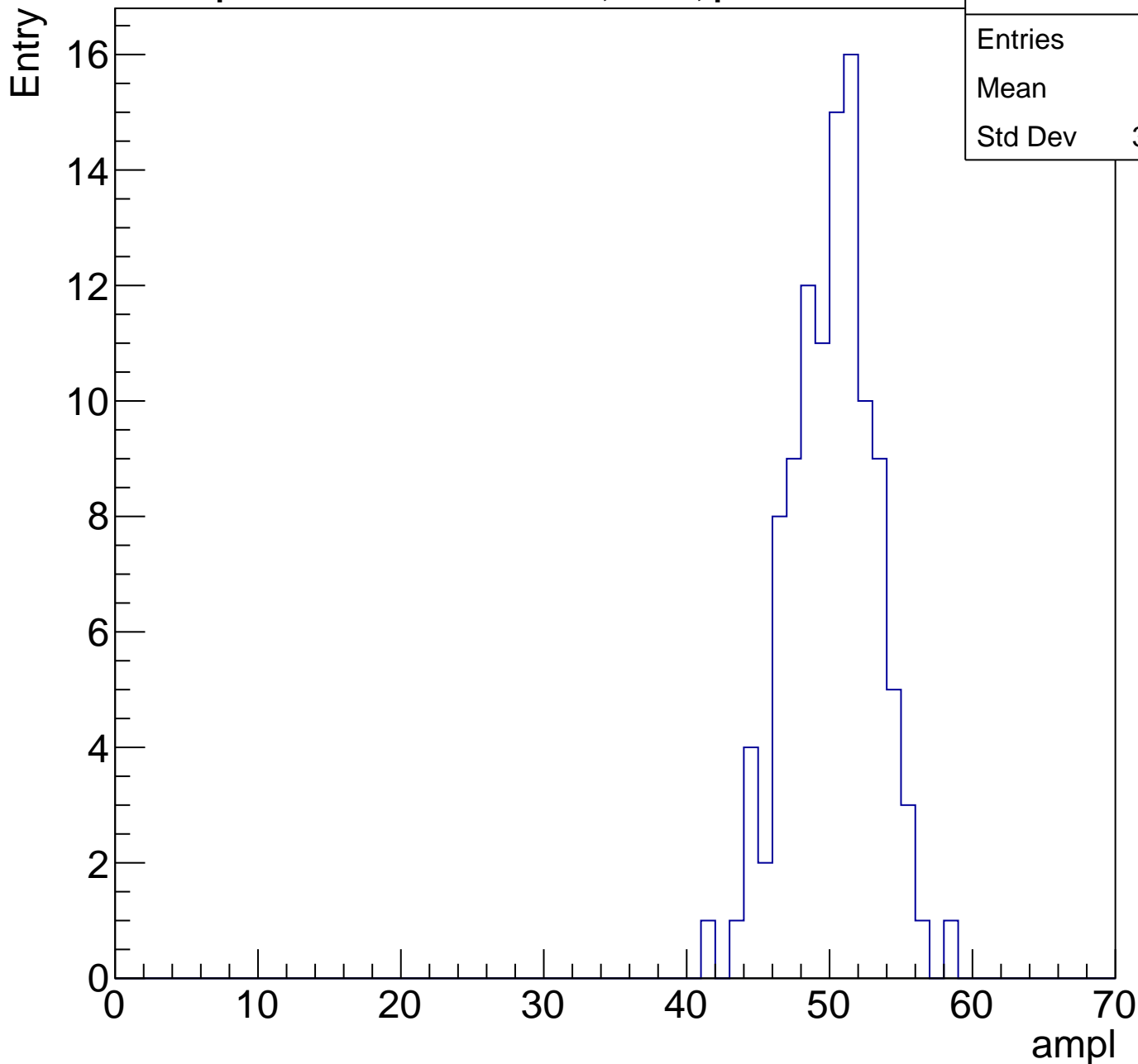
Gaus Width: 3.4847



B1L001S, U24-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

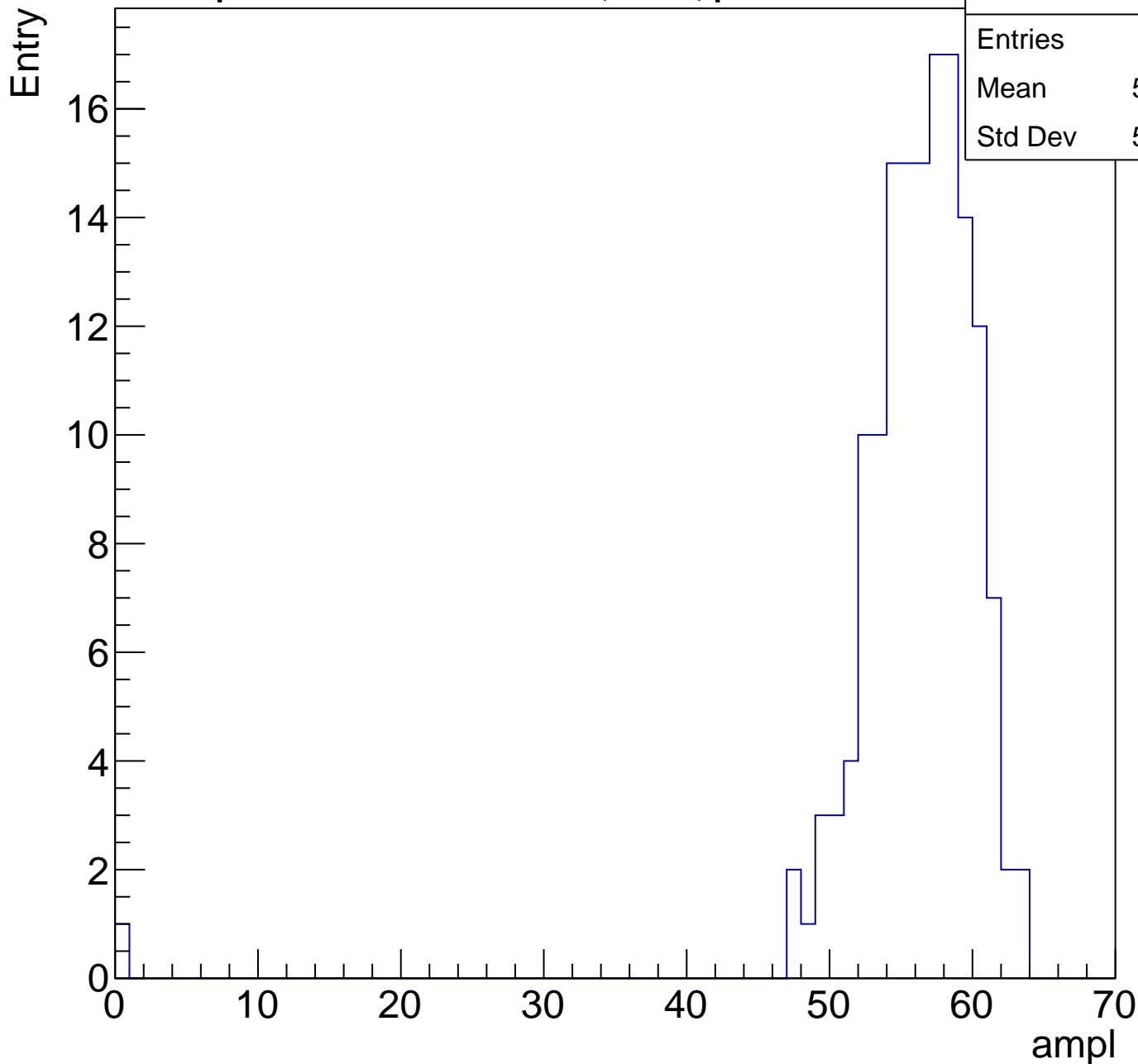
Entries	108
Mean	49.7
Std Dev	3.004



B1L001S, U24-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	55.64
Std Dev	5.639



B1L001S, U24-ch124, adc5

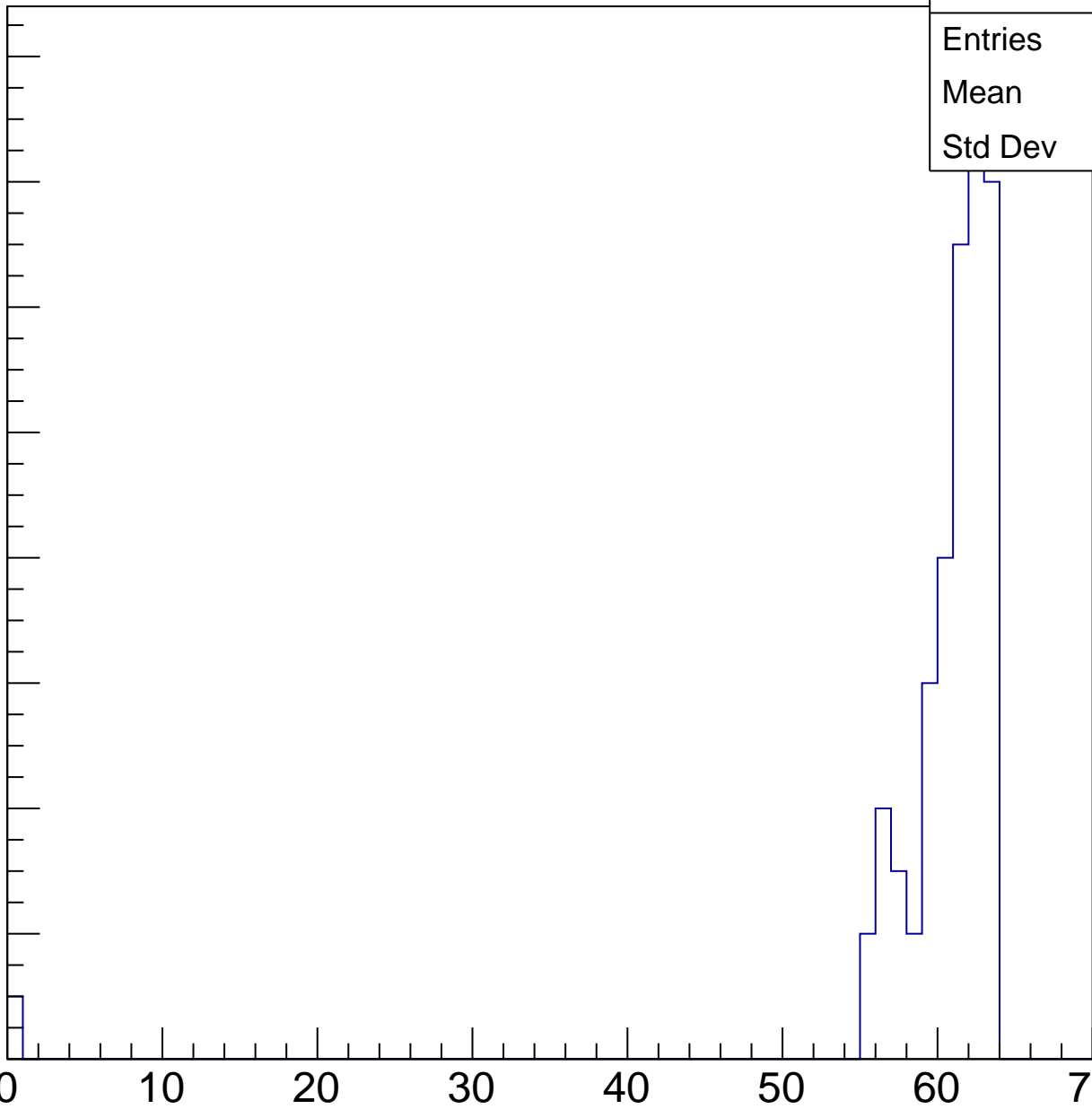
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	59.74
Std Dev	7.569

ampl



B1L001S, U24-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L001S, U24-ch125, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	29.5
Std Dev	3.403

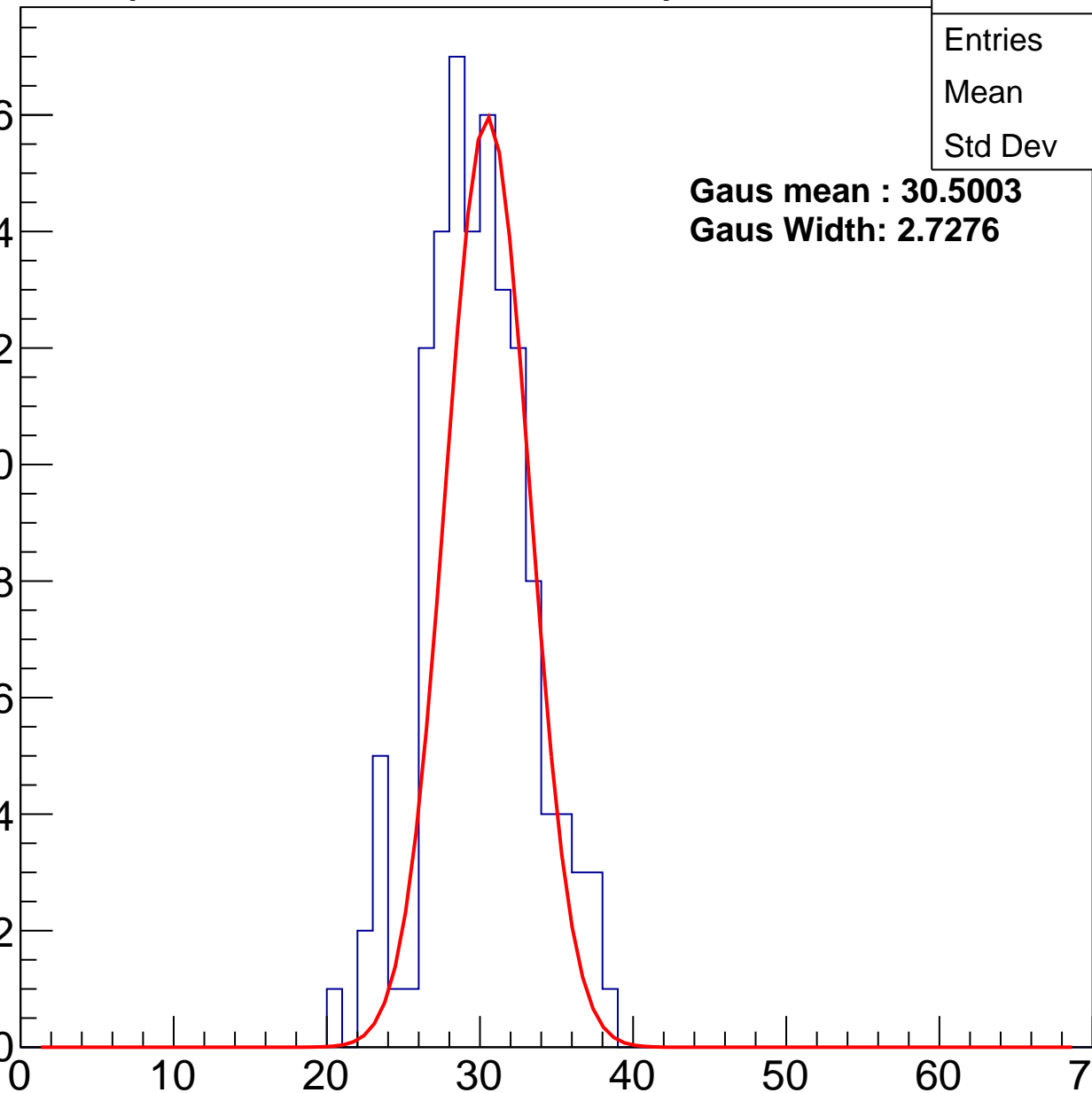
Gaus mean : 30.5003

Gaus Width: 2.7276

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U24-ch125, adc1

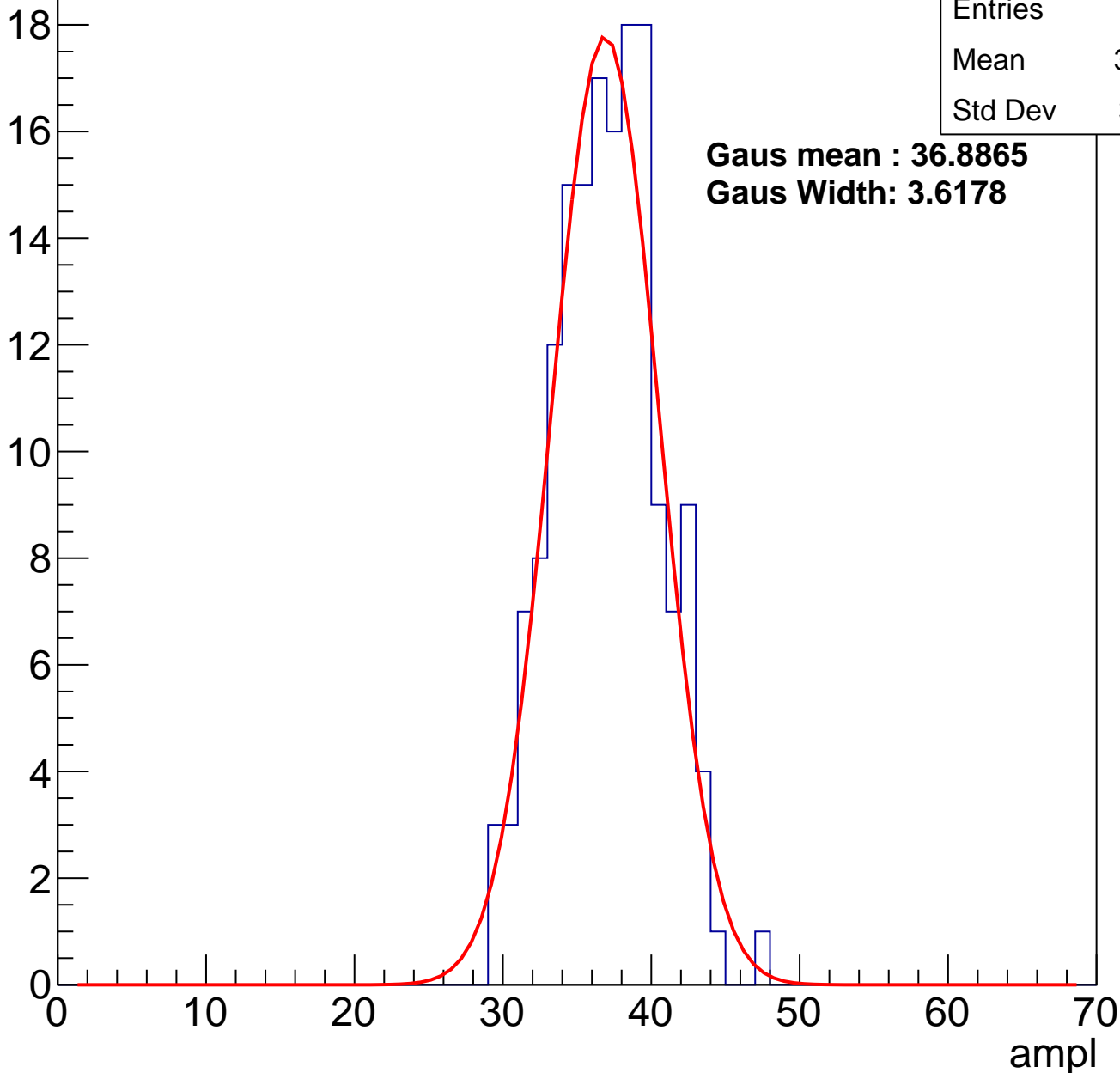
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	36.56
Std Dev	3.471

Gaus mean : 36.8865

Gaus Width: 3.6178

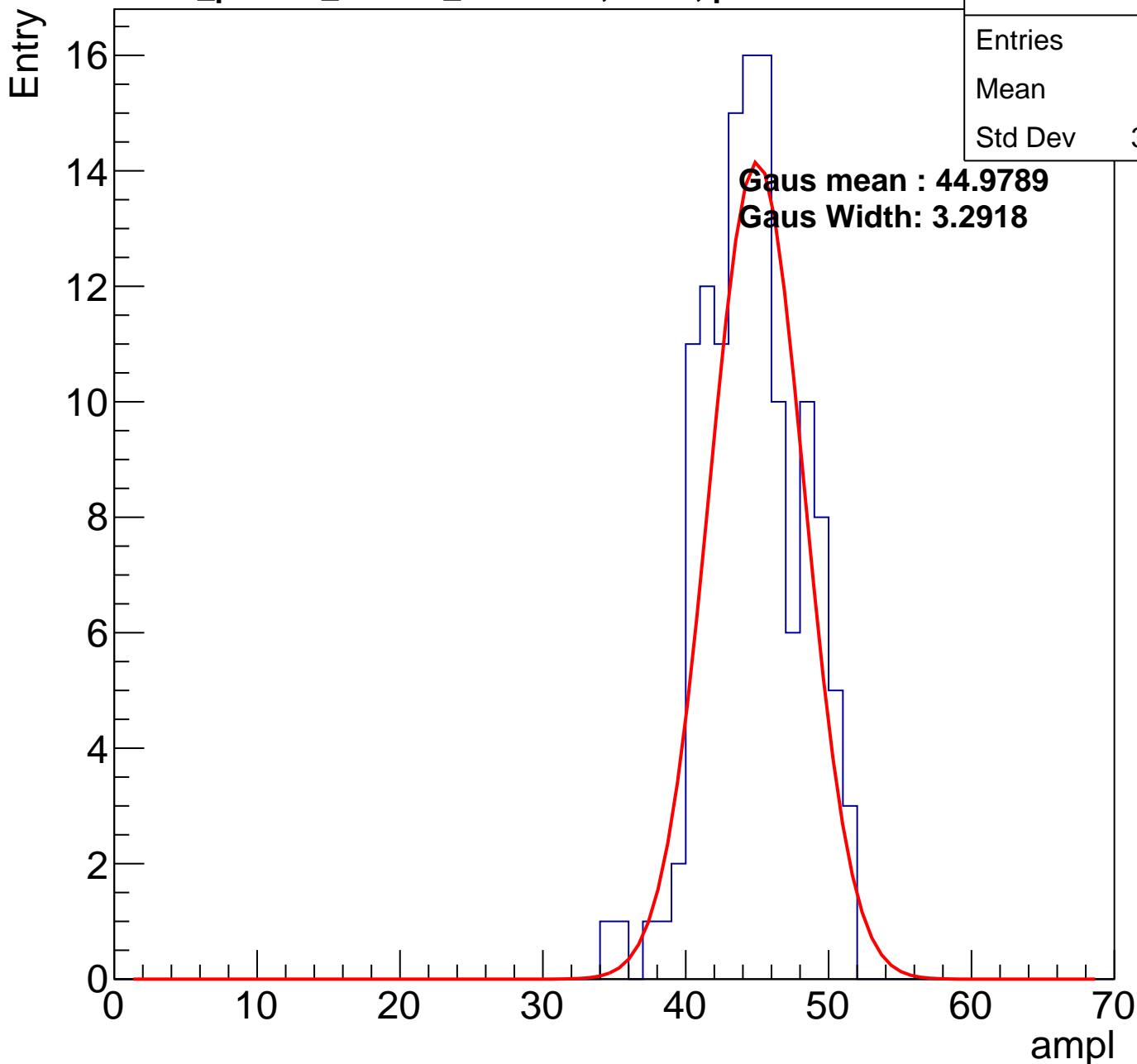
Entry



B1L001S, U24-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	44.2
Std Dev	3.354

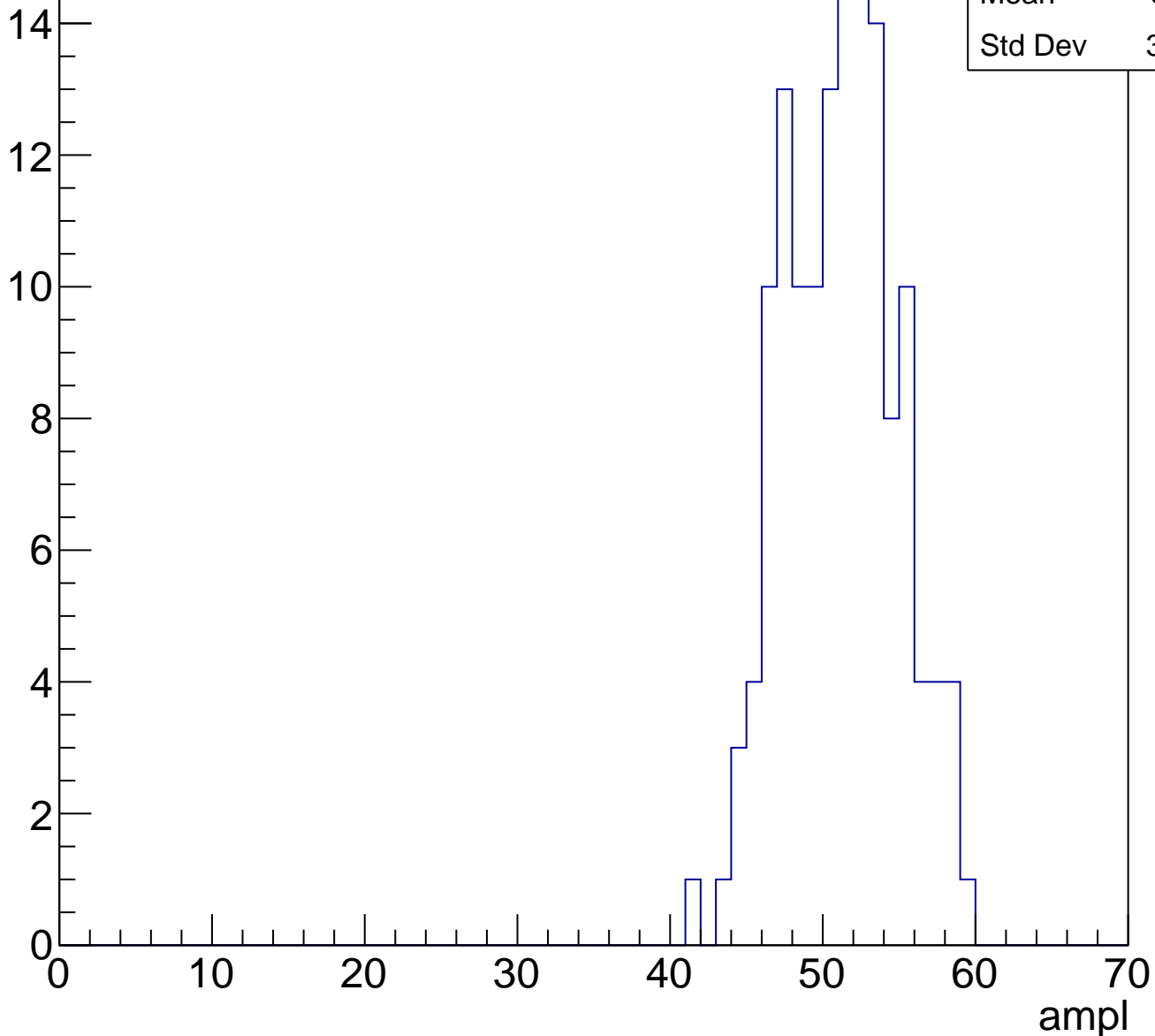


B1L001S, U24-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	50.71
Std Dev	3.636

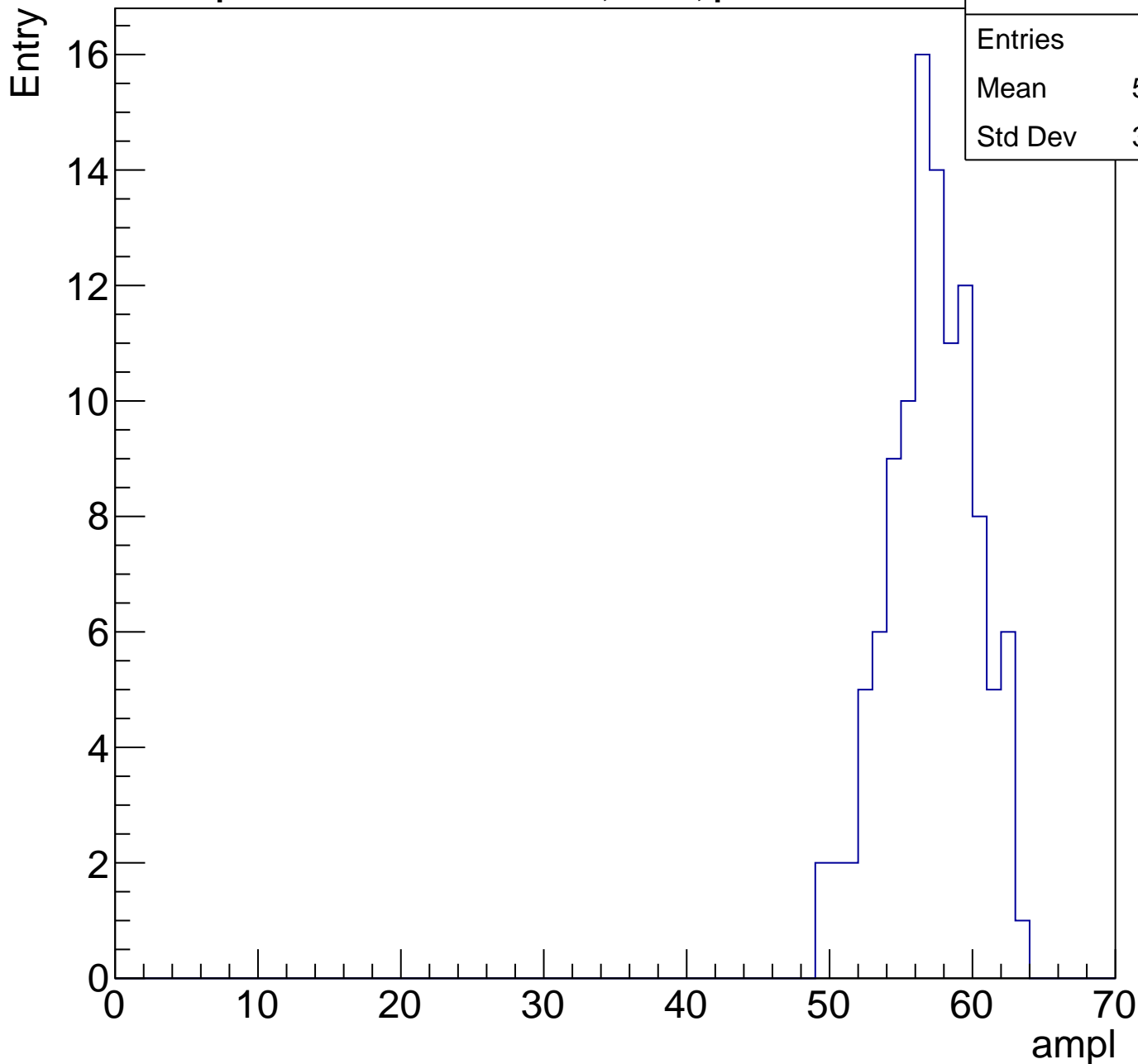
Entry



B1L001S, U24-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	56.64
Std Dev	3.087



B1L001S, U24-ch125, adc5

calib_packv5_042523_0143.root, FC#2, port C2

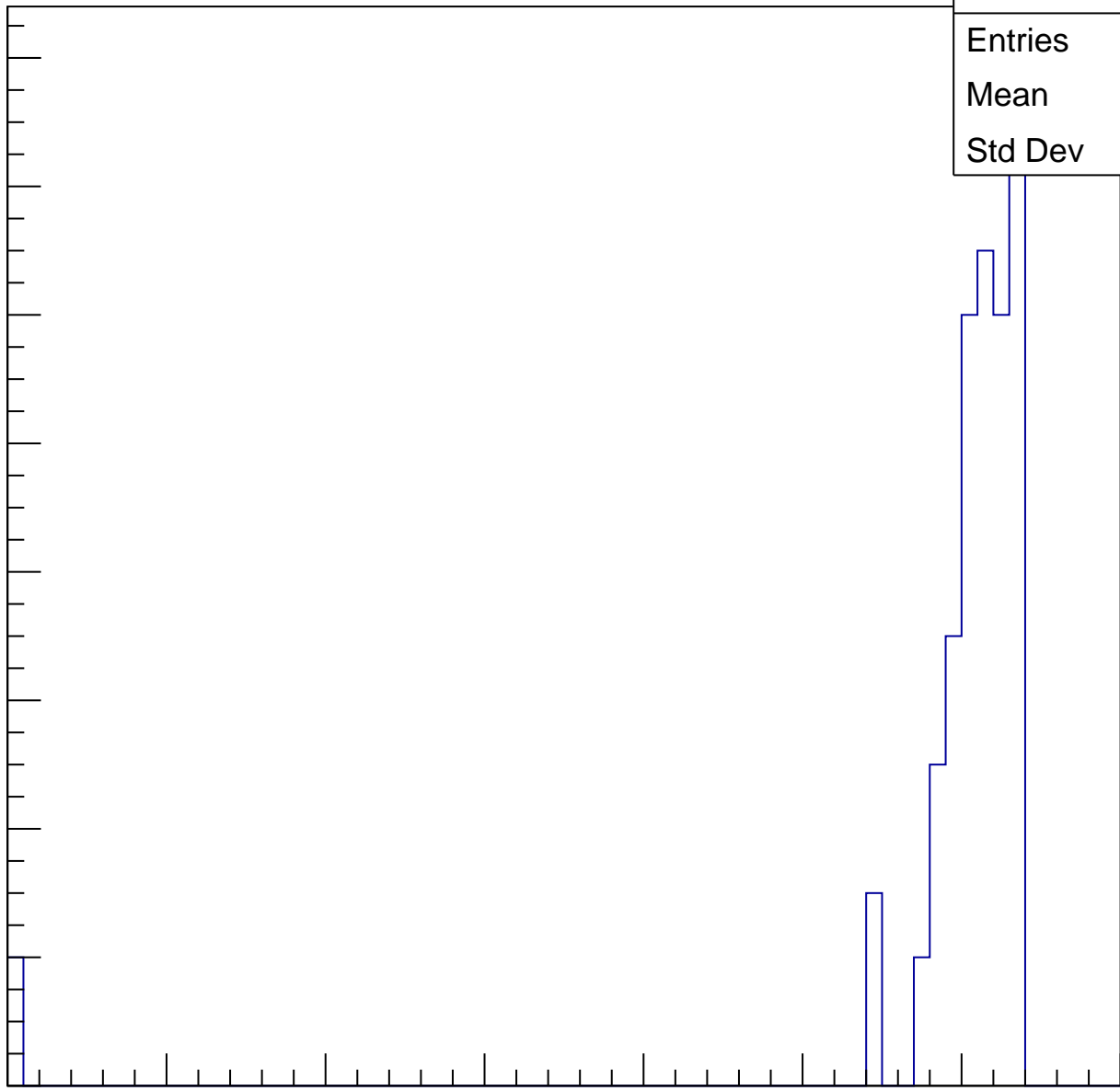
Entries	72
Mean	58.94
Std Dev	10.19

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U24-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	28.35
Std Dev	4.256

Gaus mean : 28.8710

Gaus Width: 3.4752

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

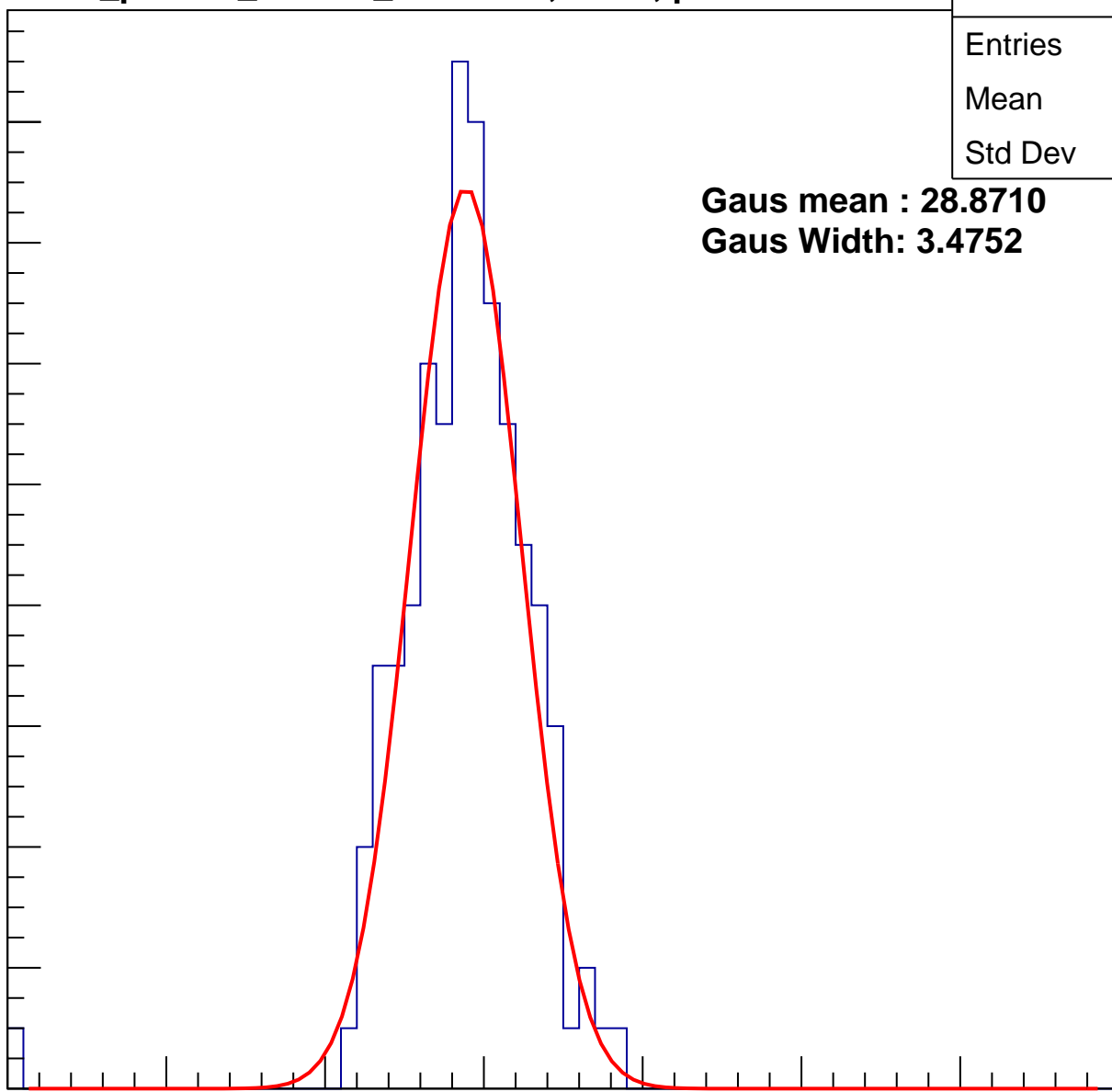
40

50

60

70

ampl



B1L001S, U24-ch126, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	35.12
Std Dev	3.374

Gaus mean : 35.4437

Gaus Width: 3.2979

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

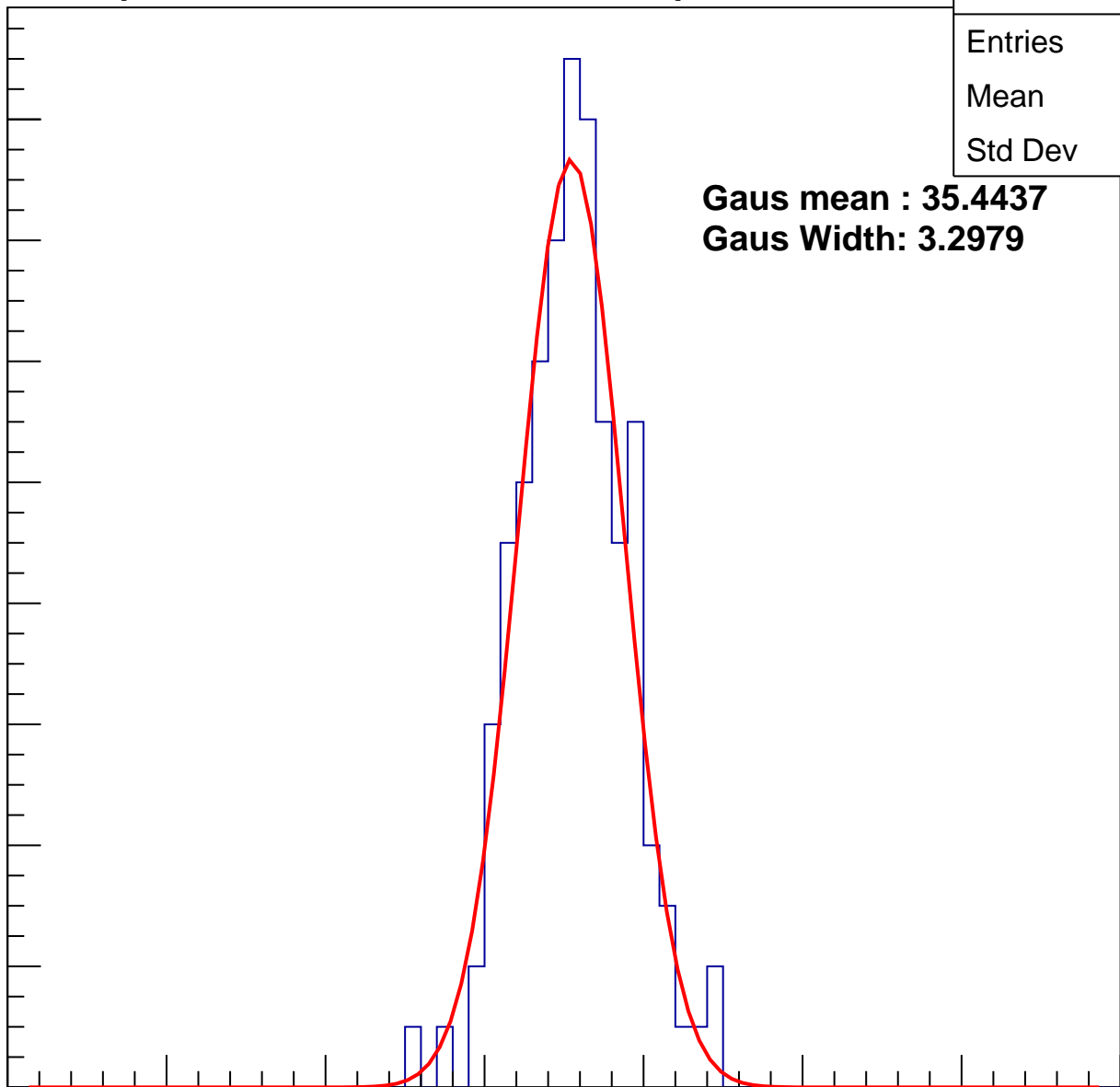
40

50

60

70

ampl



B1L001S, U24-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

142

Mean

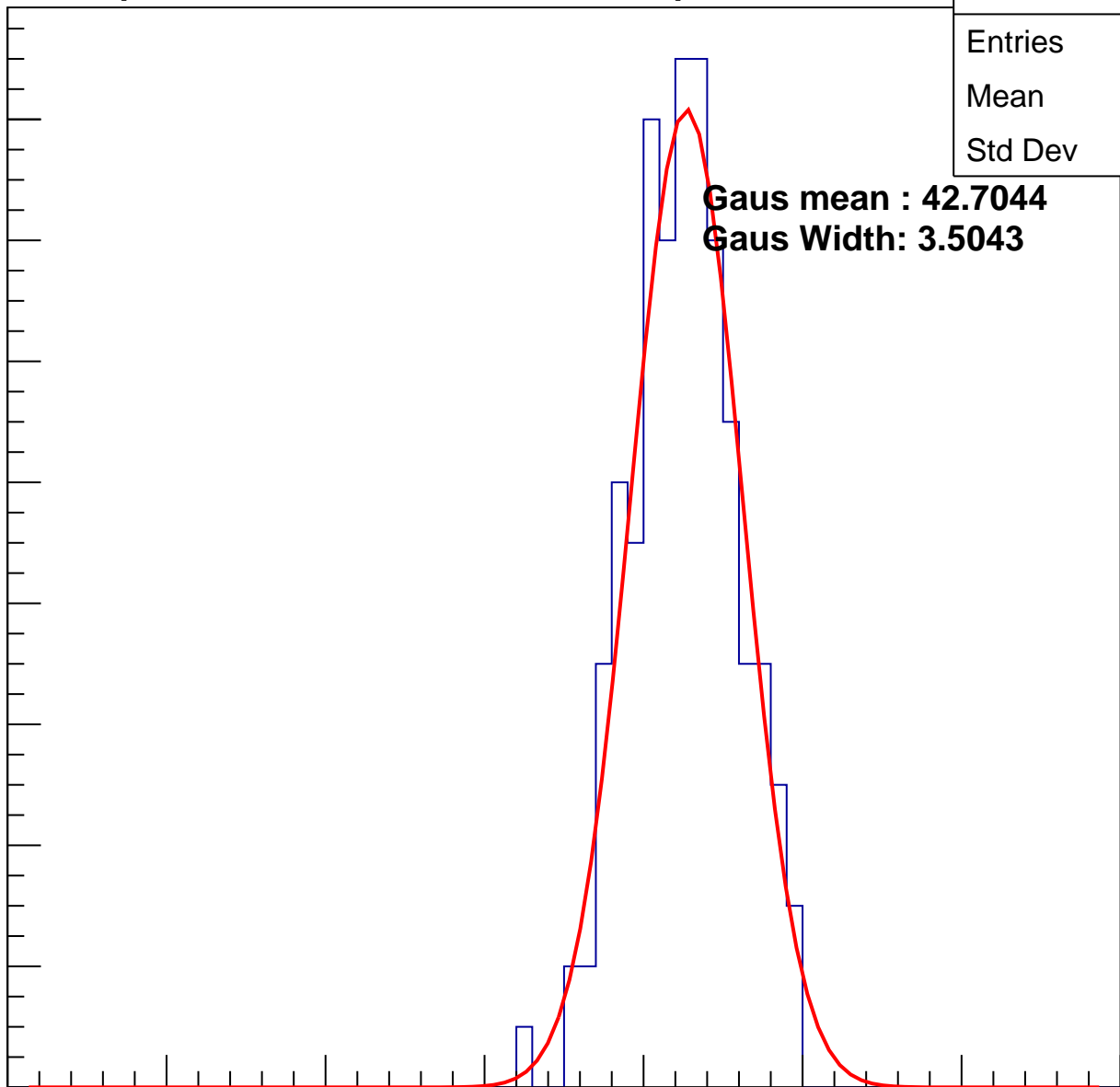
42.06

Std Dev

3.303

Gaus mean : 42.7044

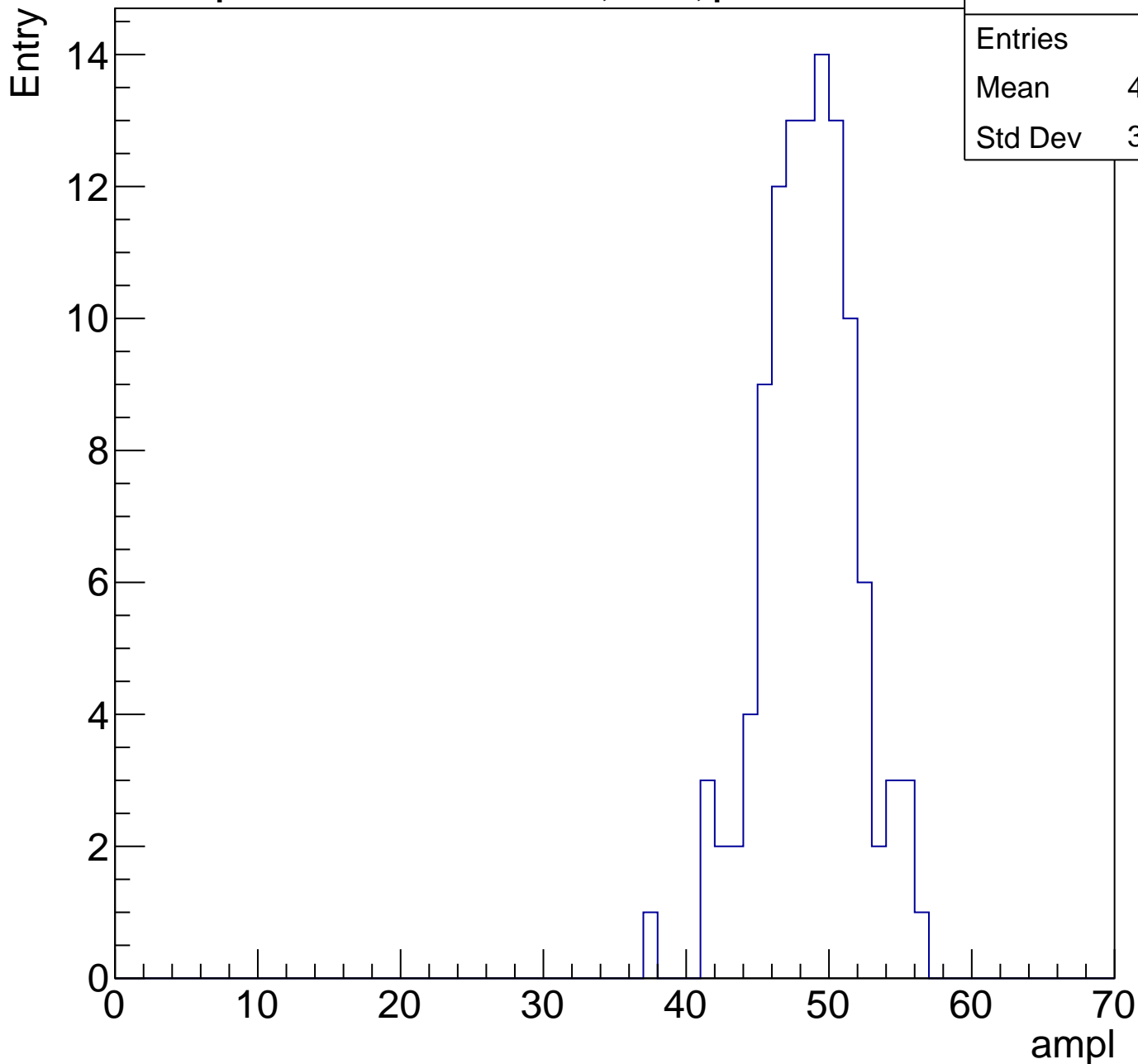
Gaus Width: 3.5043



B1L001S, U24-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

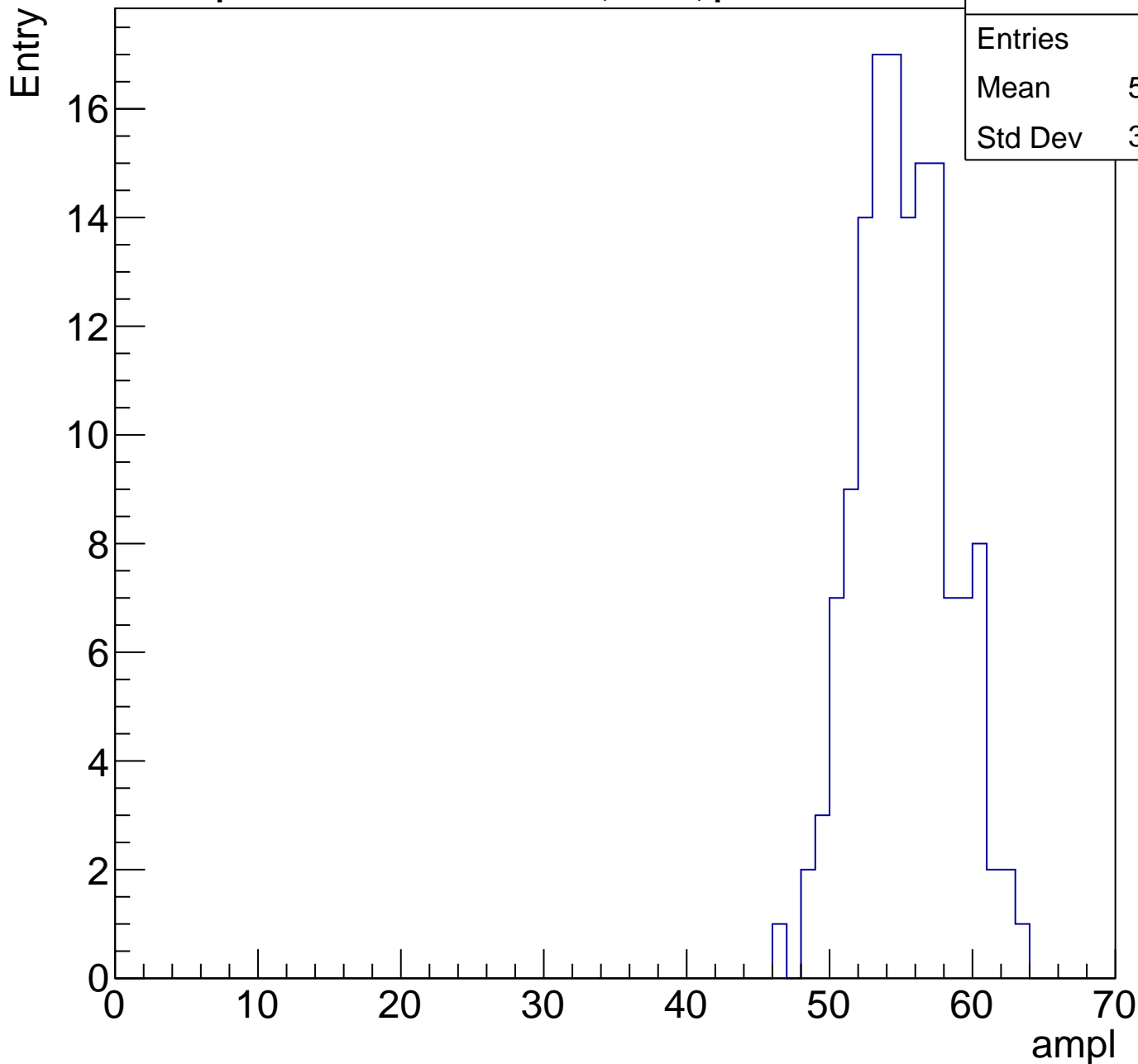
Entries	111
Mean	48.15
Std Dev	3.308



B1L001S, U24-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	54.74
Std Dev	3.252



B1L001S, U24-ch126, adc5

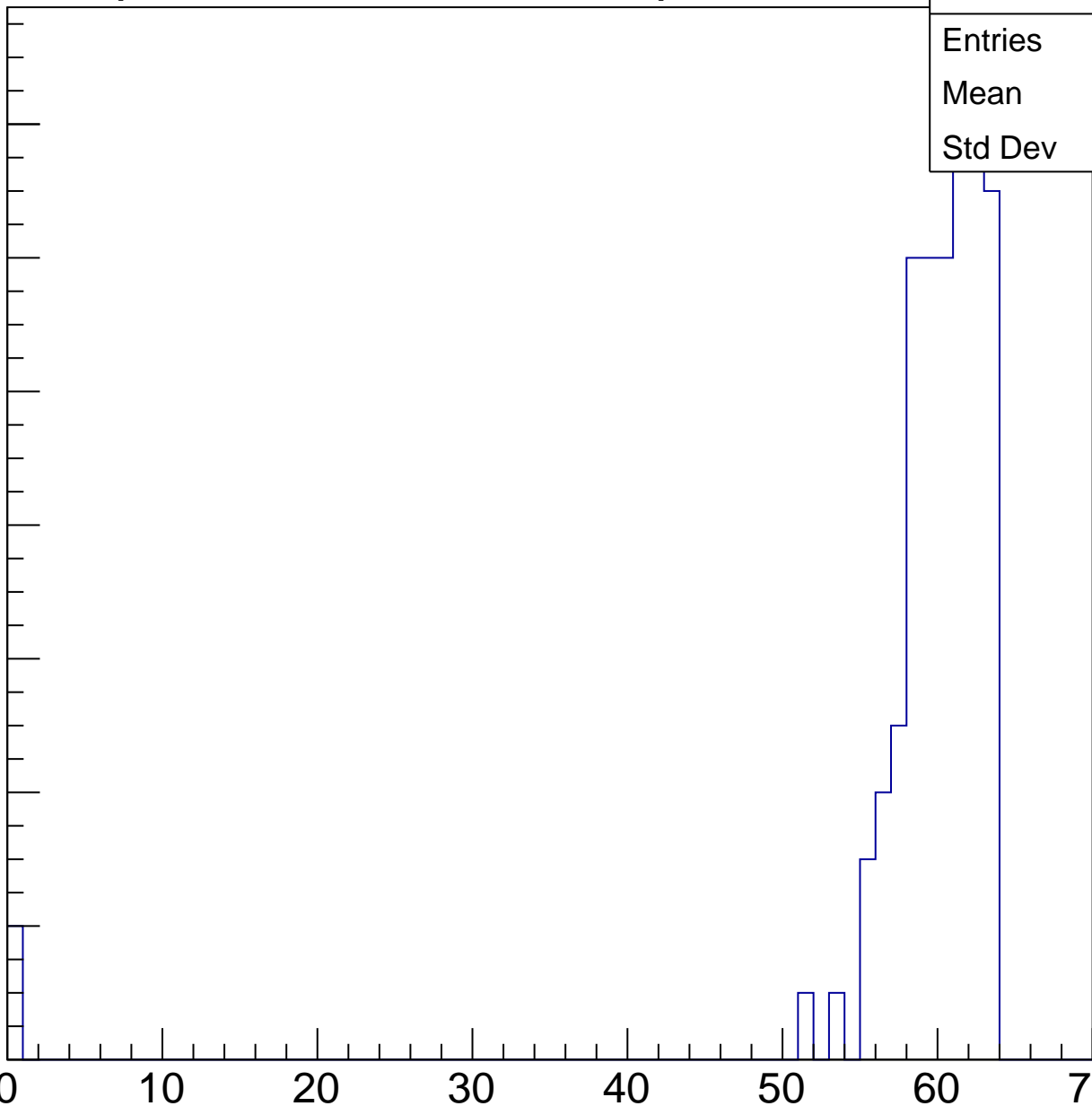
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.59
Std Dev	8.931

ampl

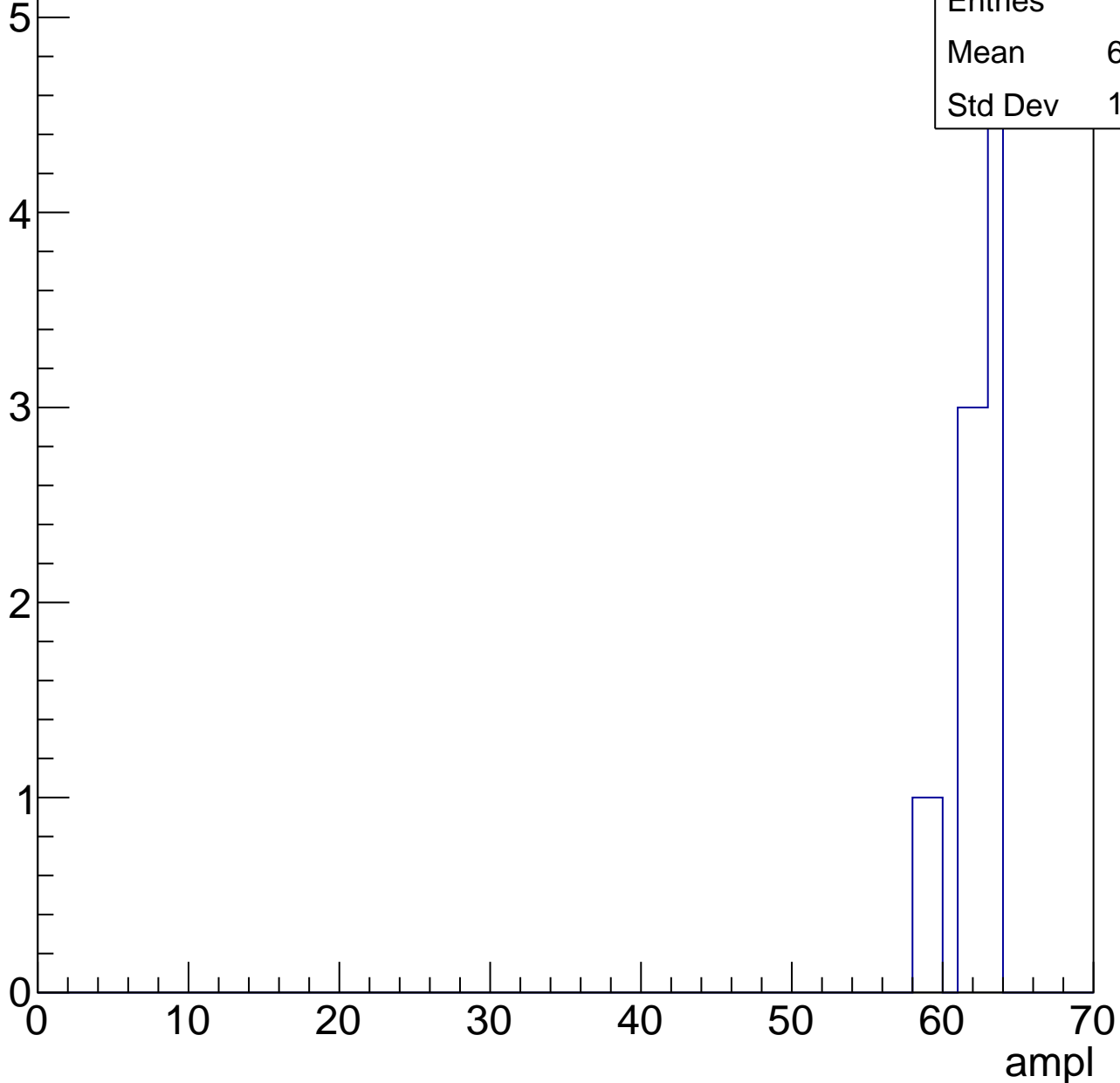


B1L001S, U24-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

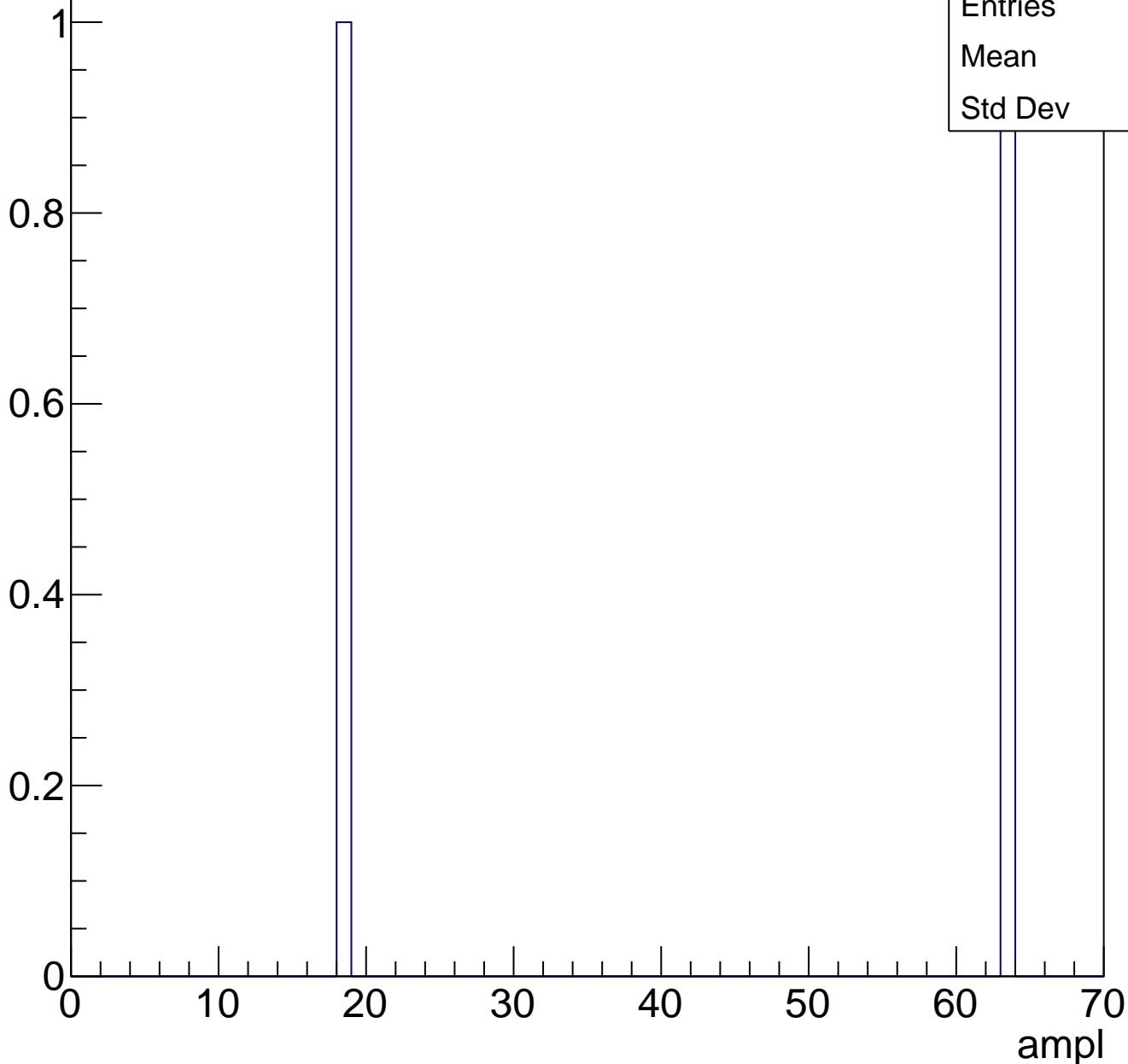
Entries	13
Mean	61.62
Std Dev	1.546



B1L001S, U24-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch127, adc0

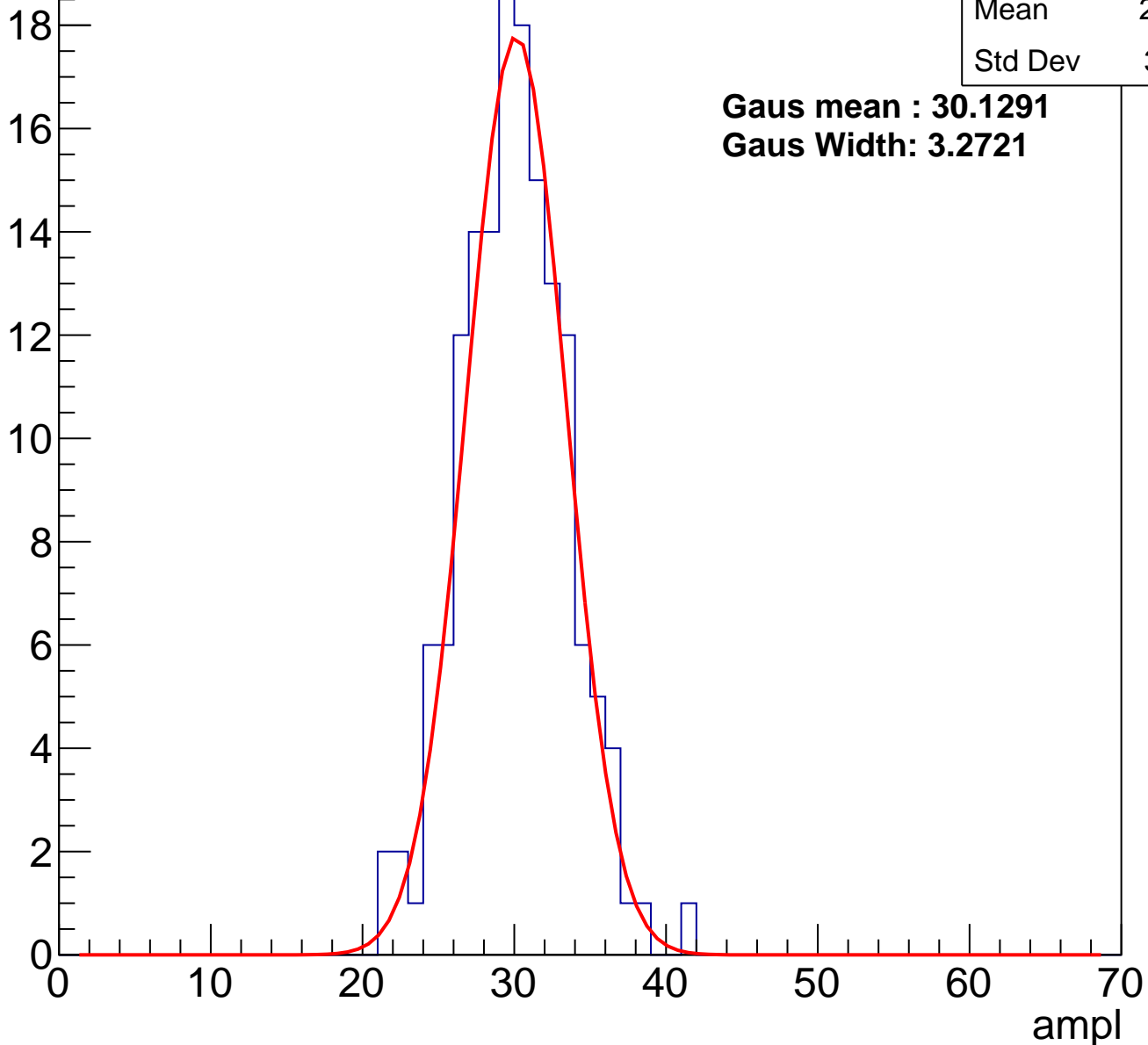
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.55
Std Dev	3.481

Gaus mean : 30.1291

Gaus Width: 3.2721

Entry



B1L001S, U24-ch127, adc1

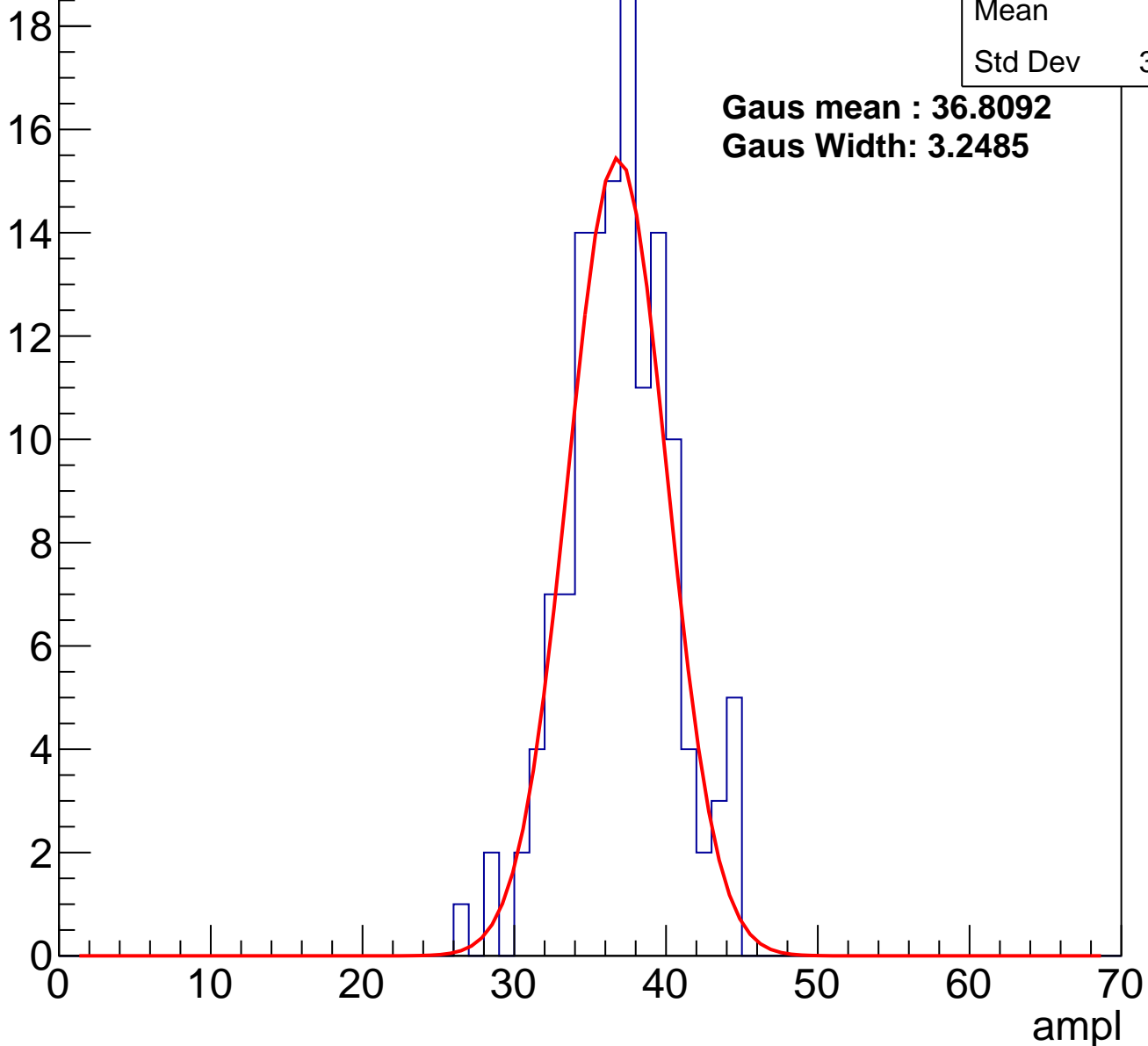
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	36.5
Std Dev	3.455

Gaus mean : 36.8092

Gaus Width: 3.2485

Entry



B1L001S, U24-ch127, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

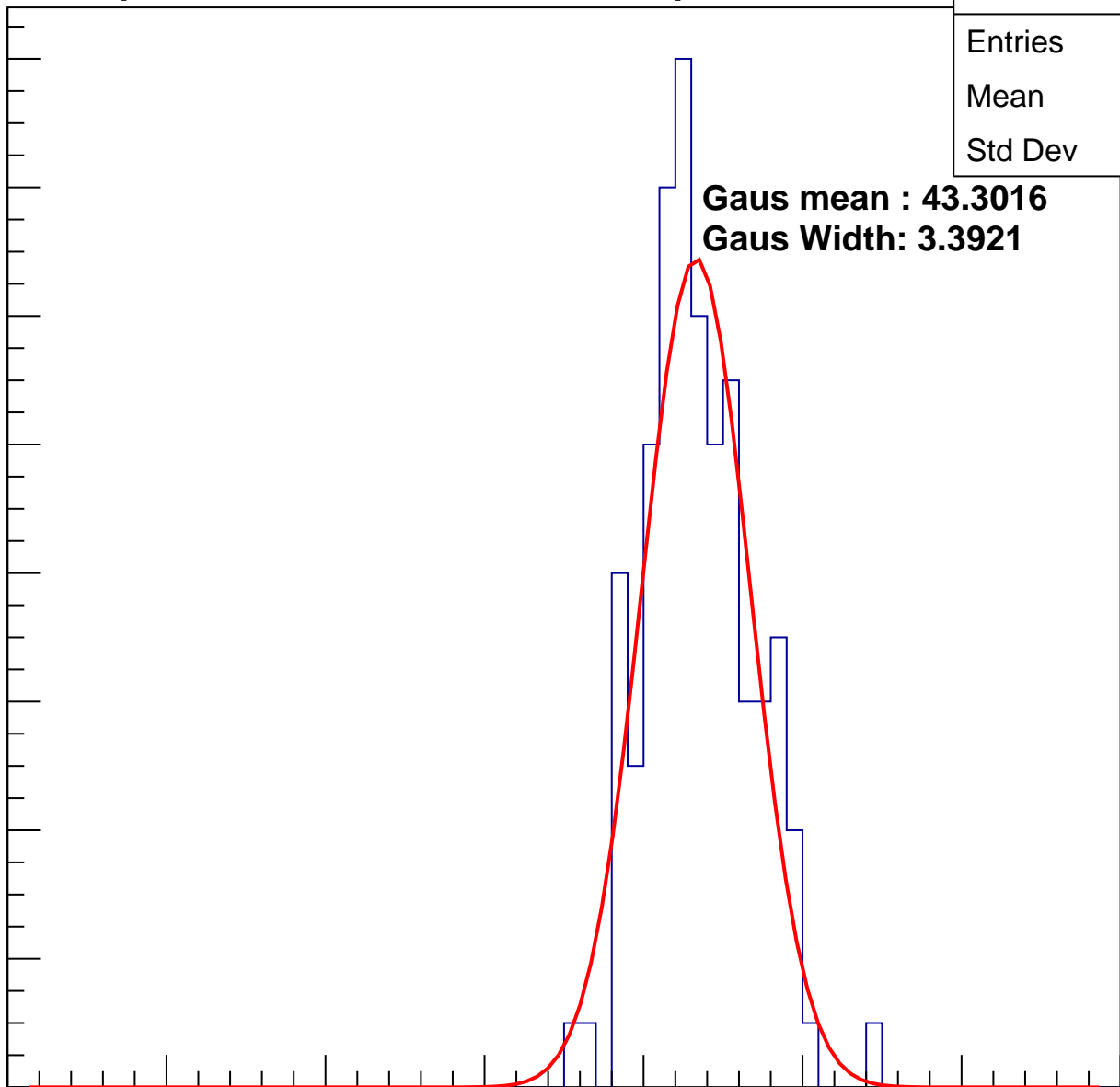
Entries	113
Mean	43.02
Std Dev	3.323

Gaus mean : 43.3016

Gaus Width: 3.3921

0 10 20 30 40 50 60 70

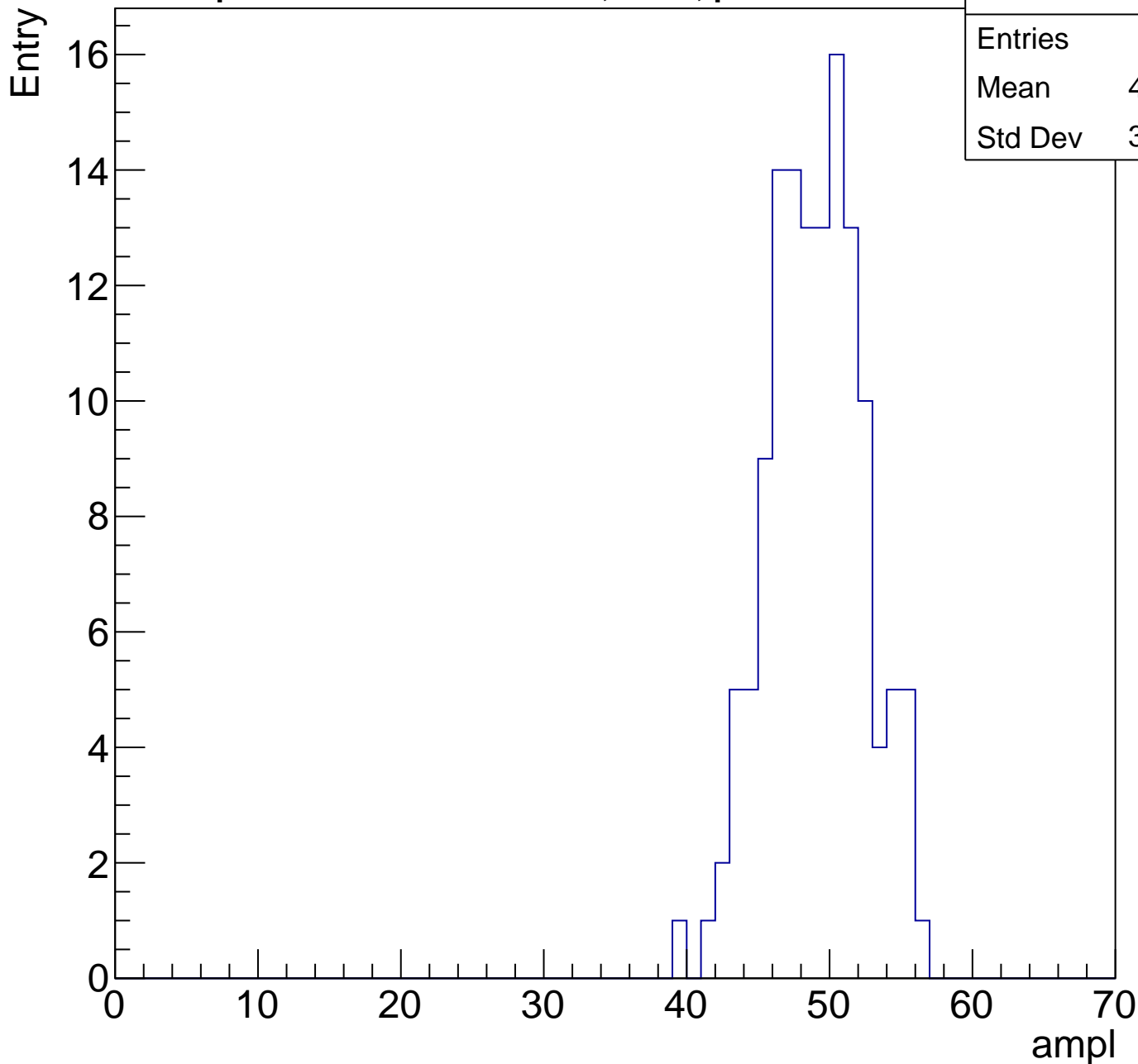
ampl



B1L001S, U24-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	48.57
Std Dev	3.336



B1L001S, U24-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

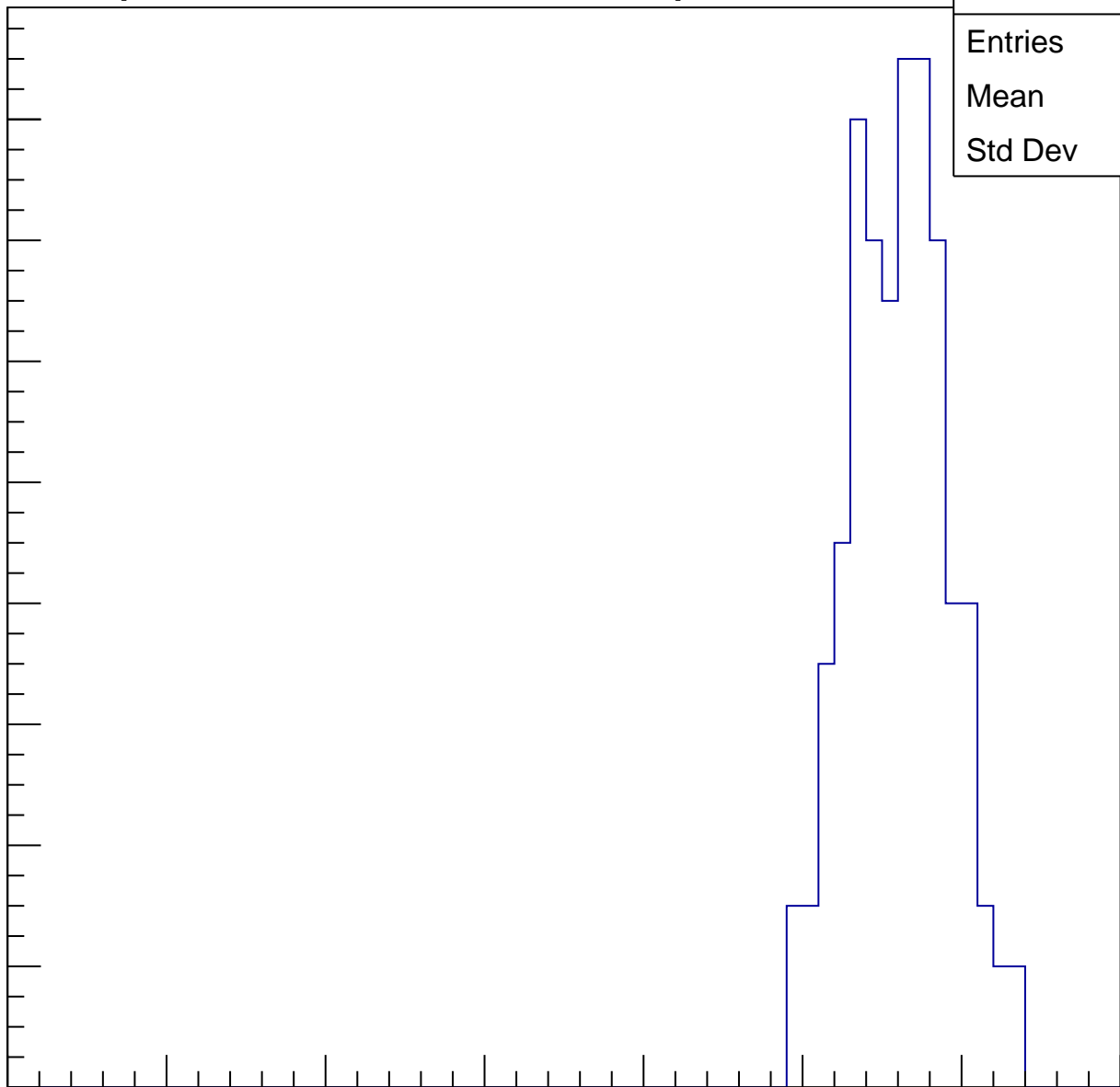
Entries	136
Mean	55.58
Std Dev	3.069

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U24-ch127, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	69
Mean	59.32
Std Dev	7.552

Entry

12

10

8

6

4

2

0

0

10

20

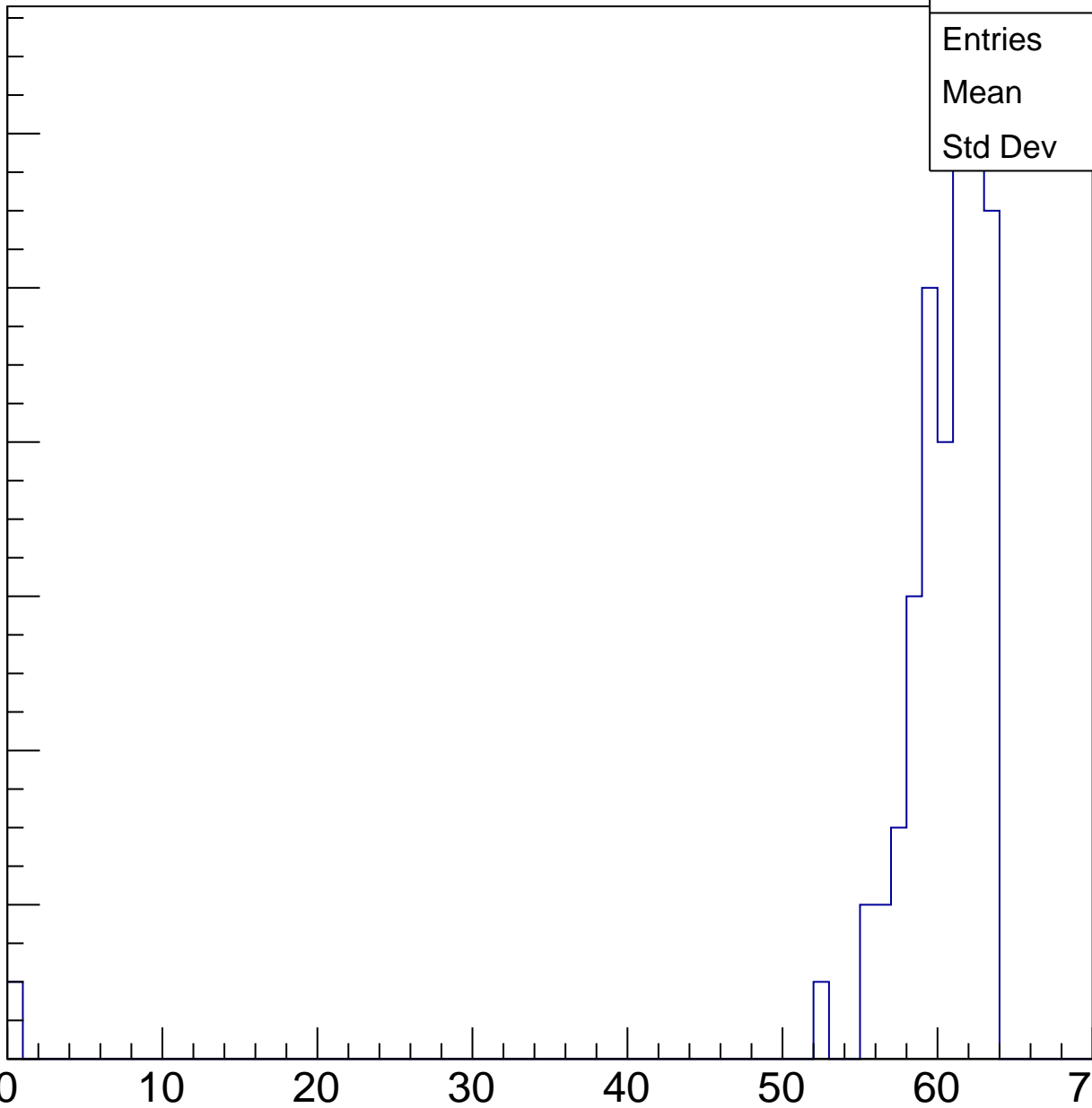
30

40

50

60

ampl

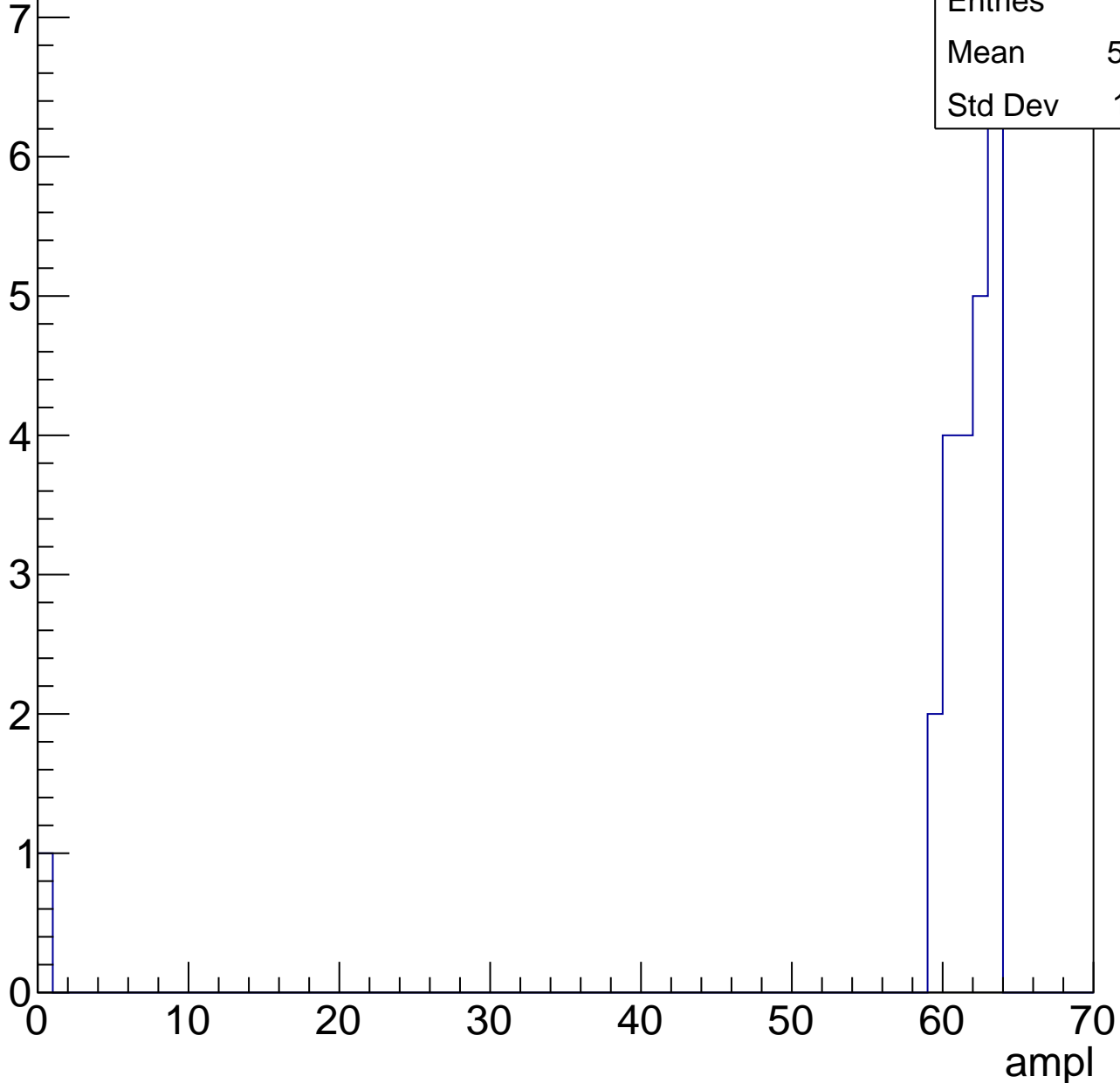


B1L001S, U24-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

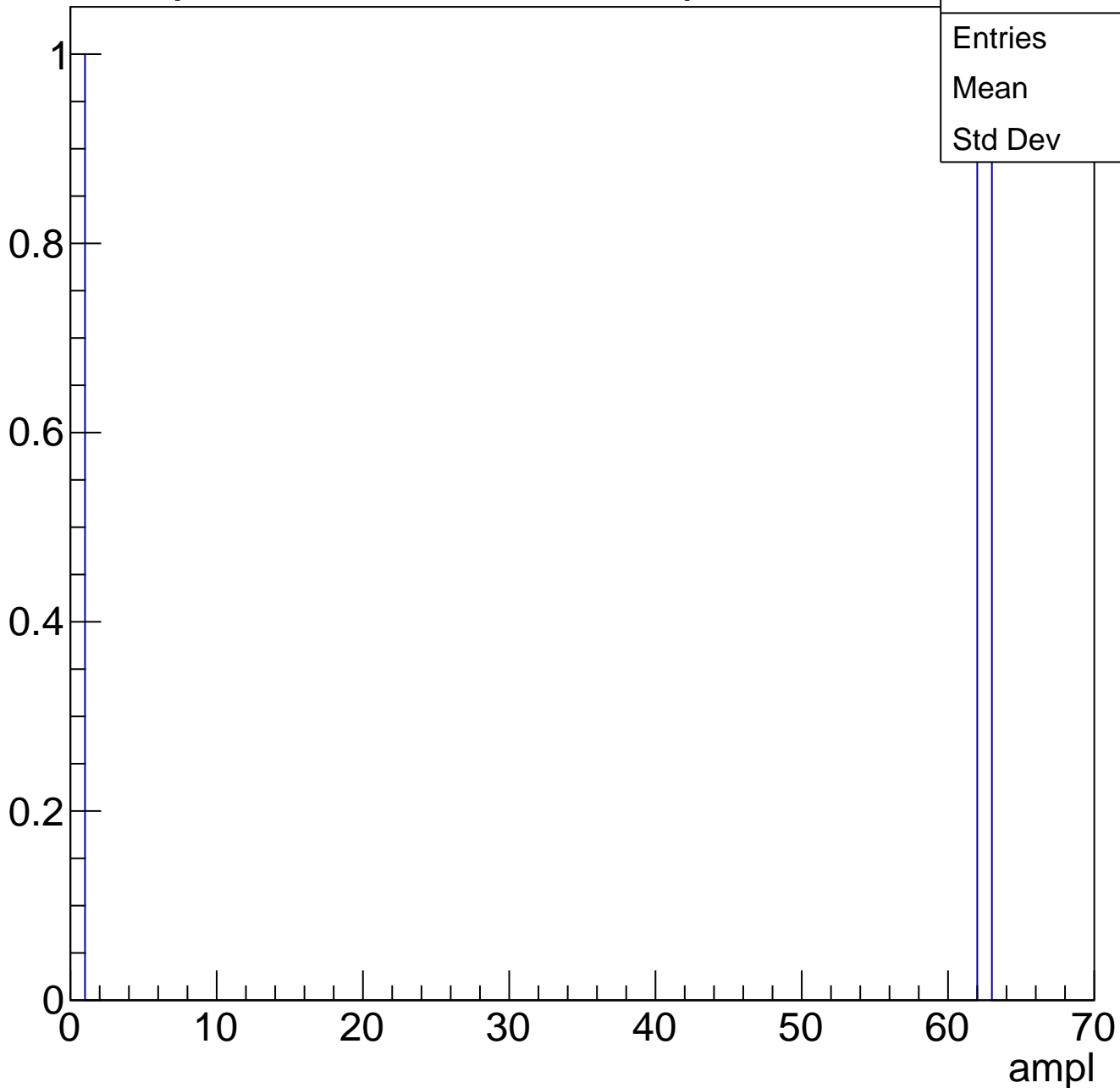
Entries	23
Mean	58.83
Std Dev	12.61



B1L001S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

