

B1L103S, U16-ch0

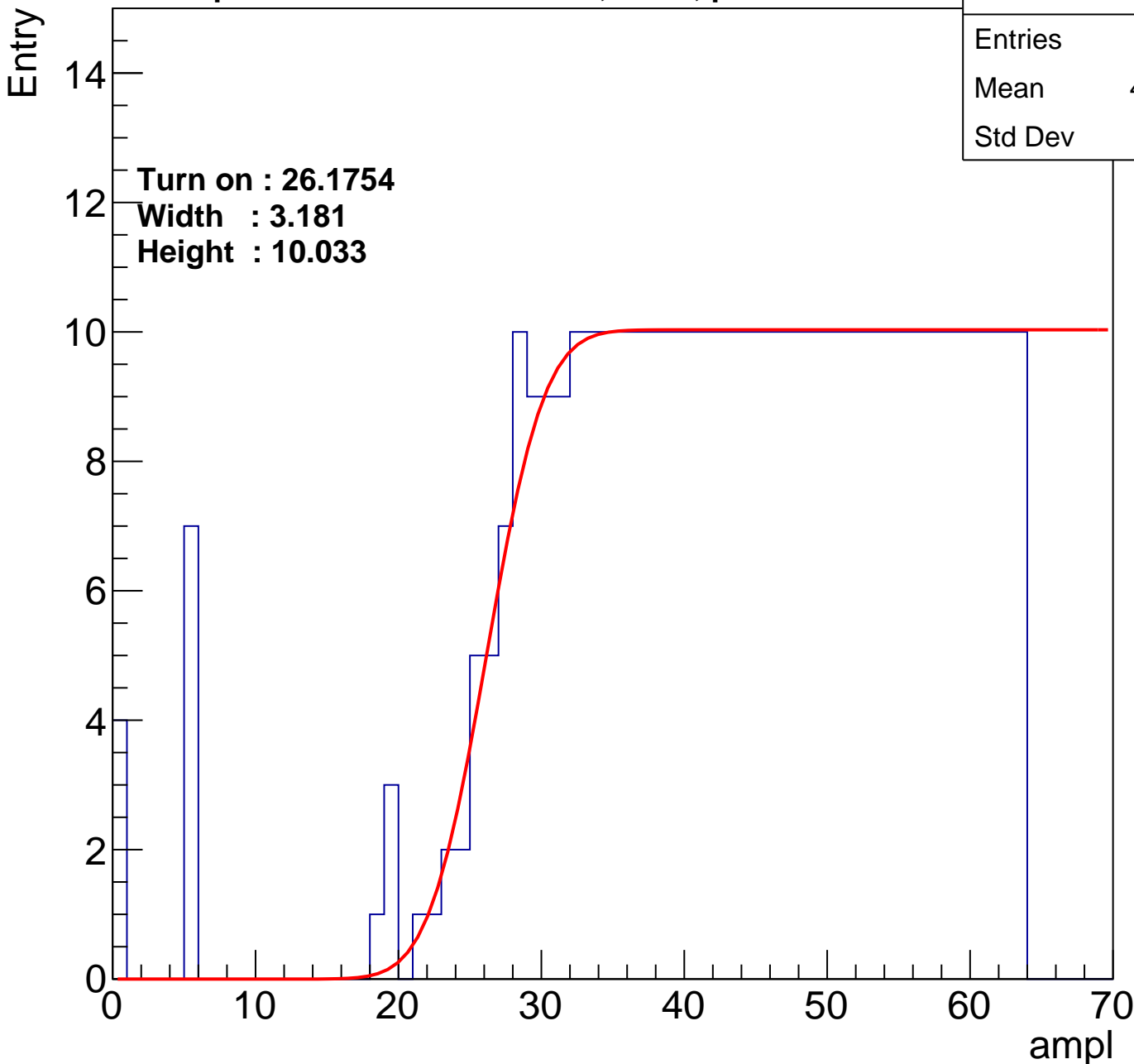
calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	42.99
Std Dev	13.1

Turn on : 26.1754

Width : 3.181

Height : 10.033



B1L103S, U16-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.6
Std Dev	11.4

Turn on : 27.5424

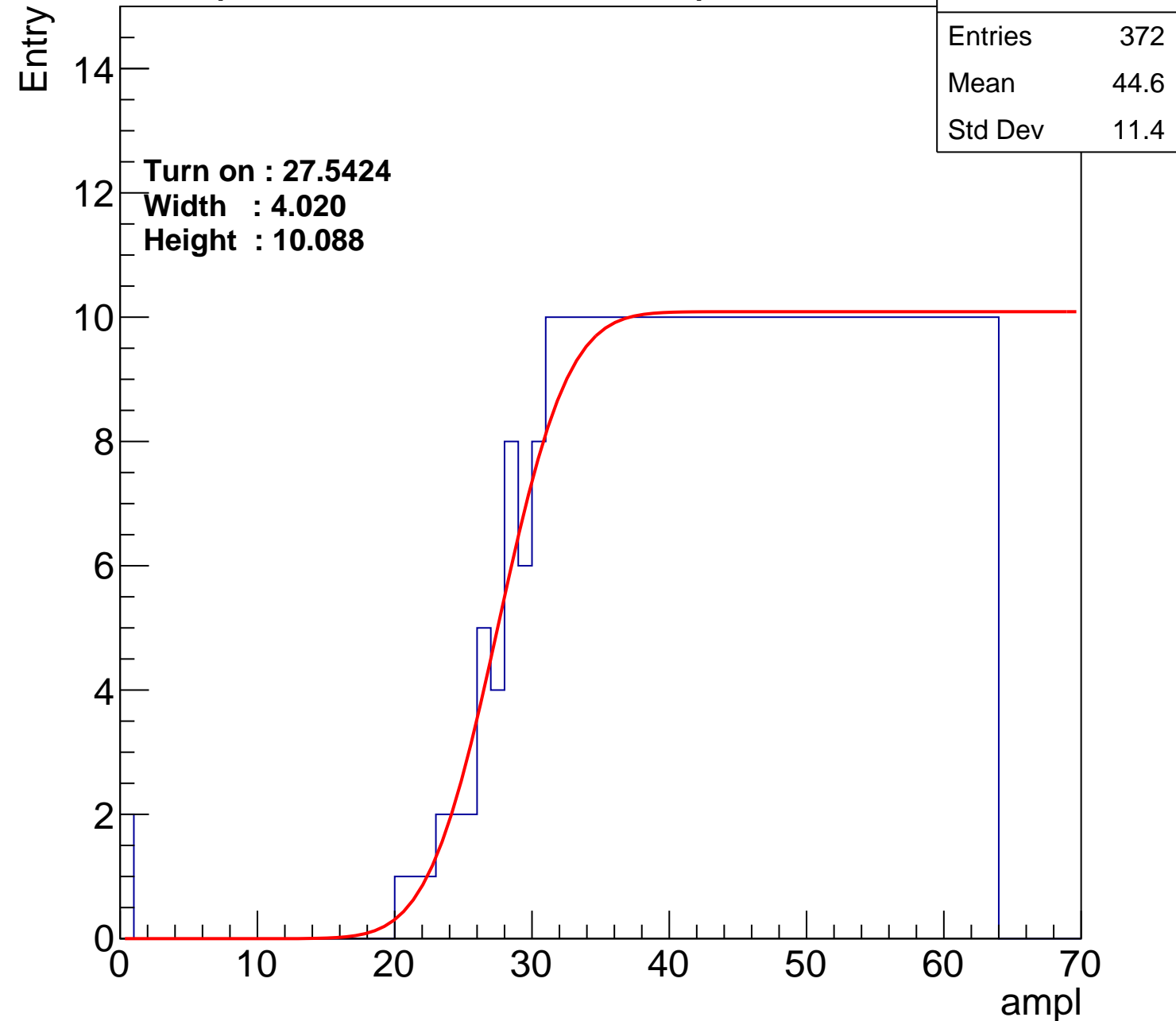
Width : 4.020

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.39
Std Dev	11.55

Turn on : 27.0370

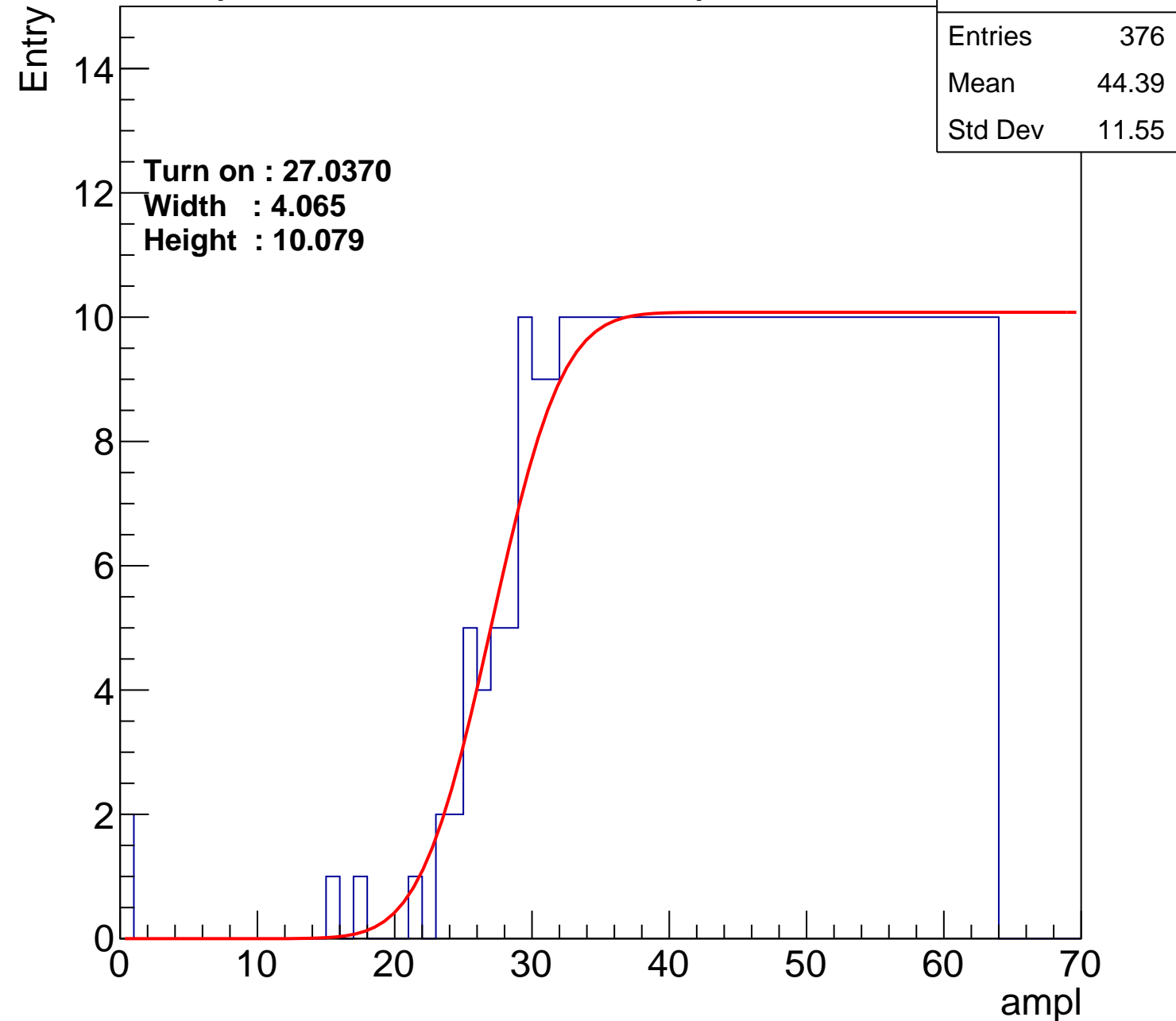
Width : 4.065

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.38
Std Dev	11

Turn on : 28.6213

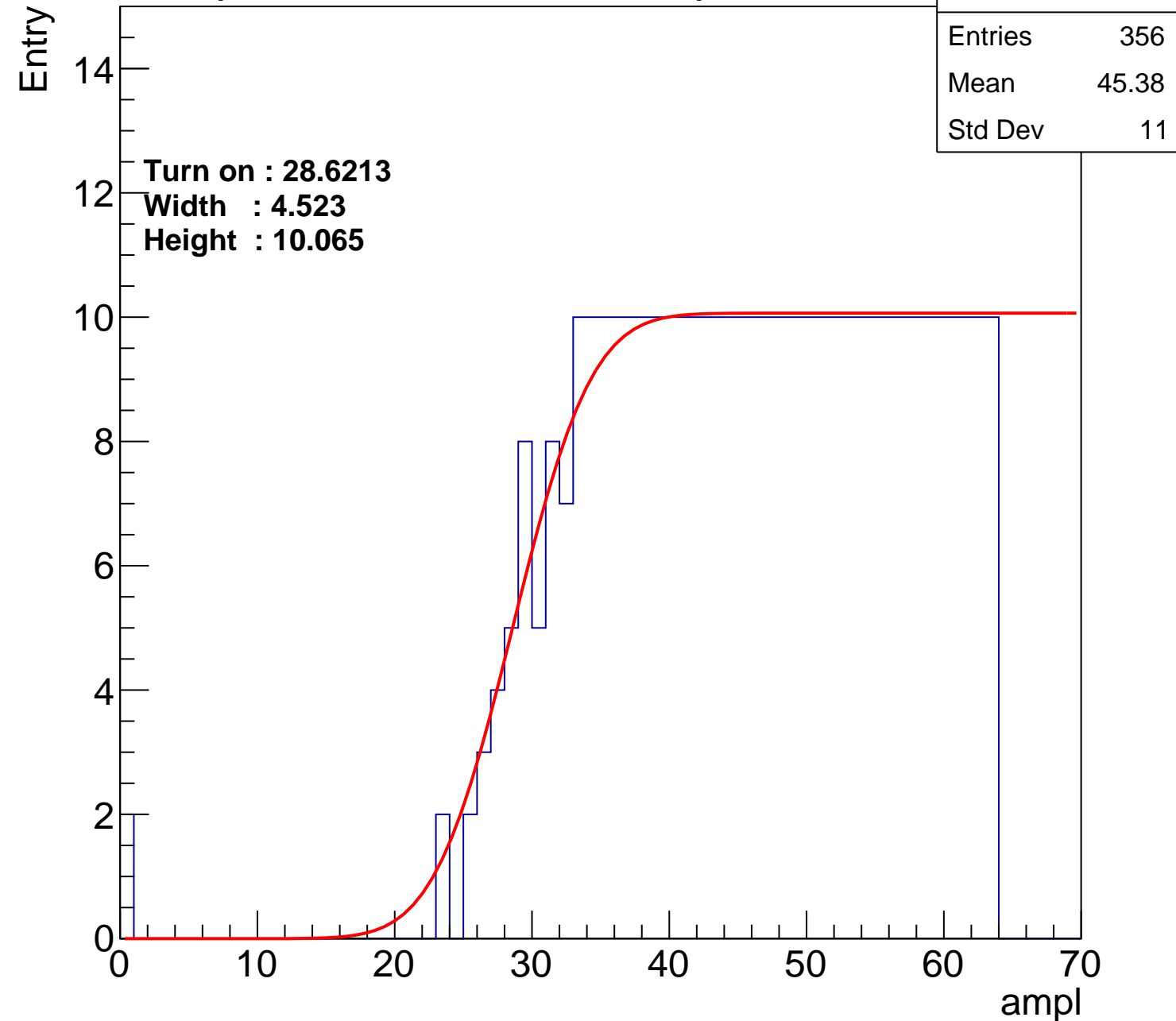
Width : 4.523

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.62
Std Dev	11.42

Turn on : 27.4018

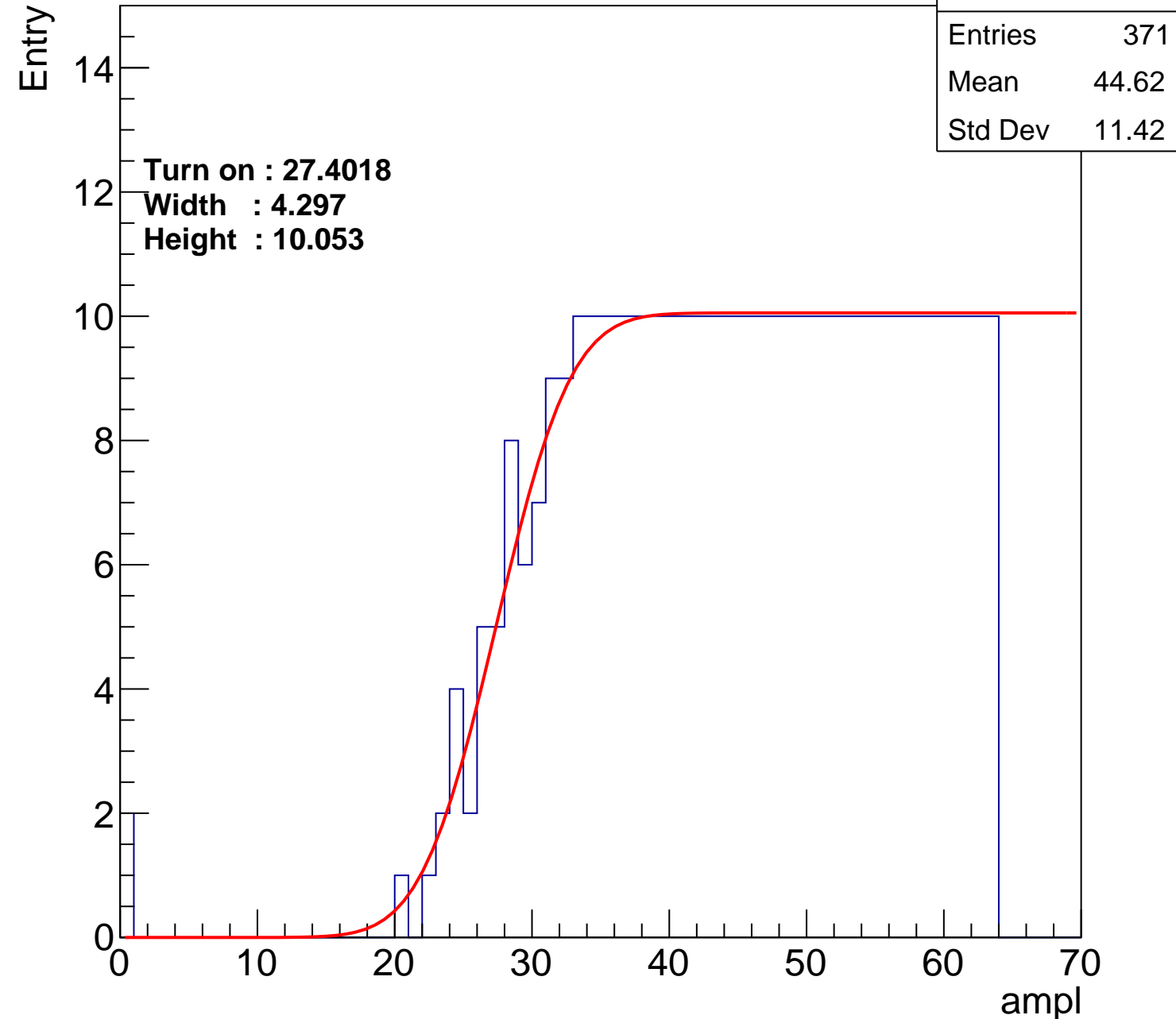
Width : 4.297

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.52
Std Dev	10.68

Turn on : 28.6538

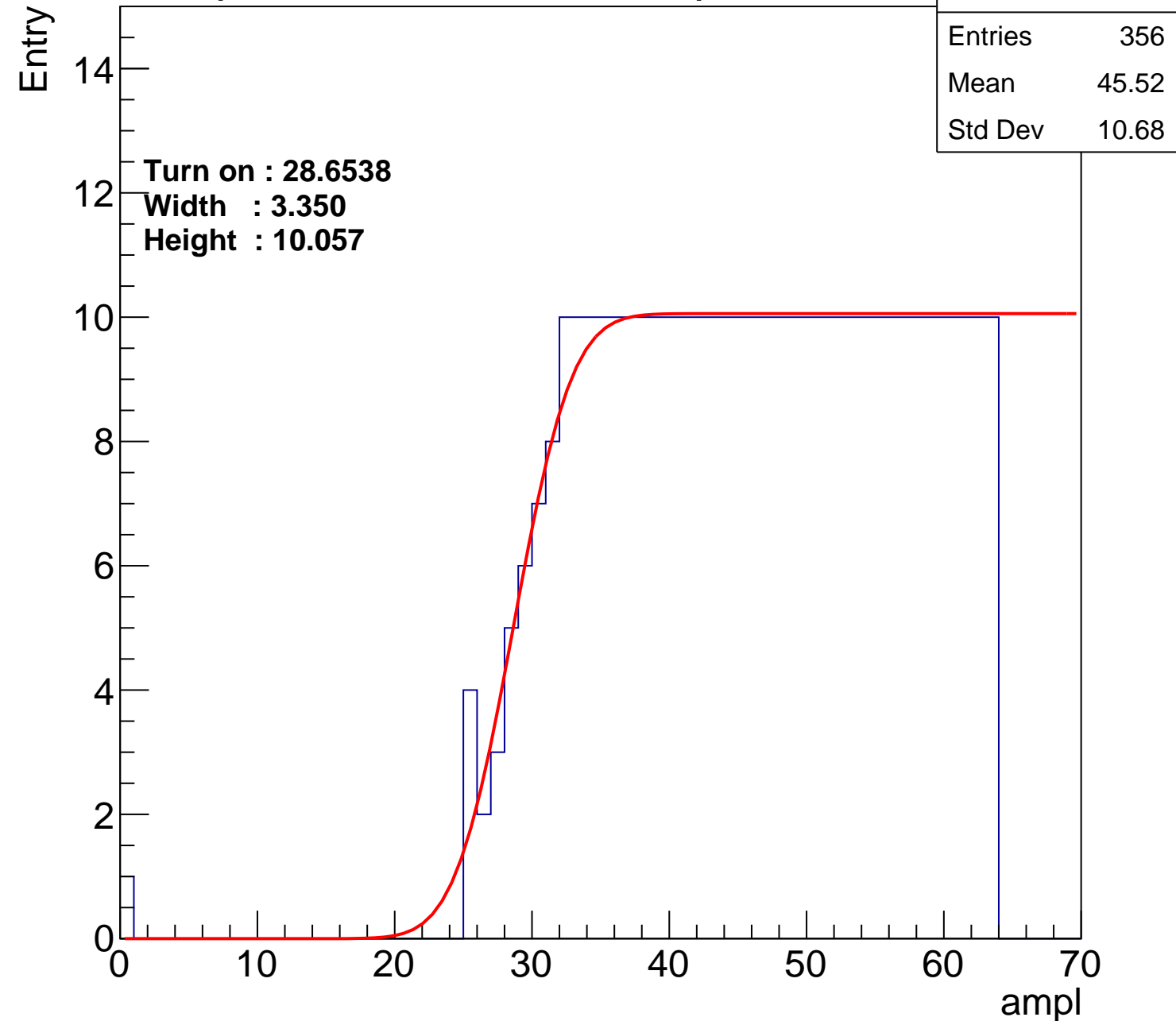
Width : 3.350

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.48
Std Dev	11.75

Turn on : 28.0733

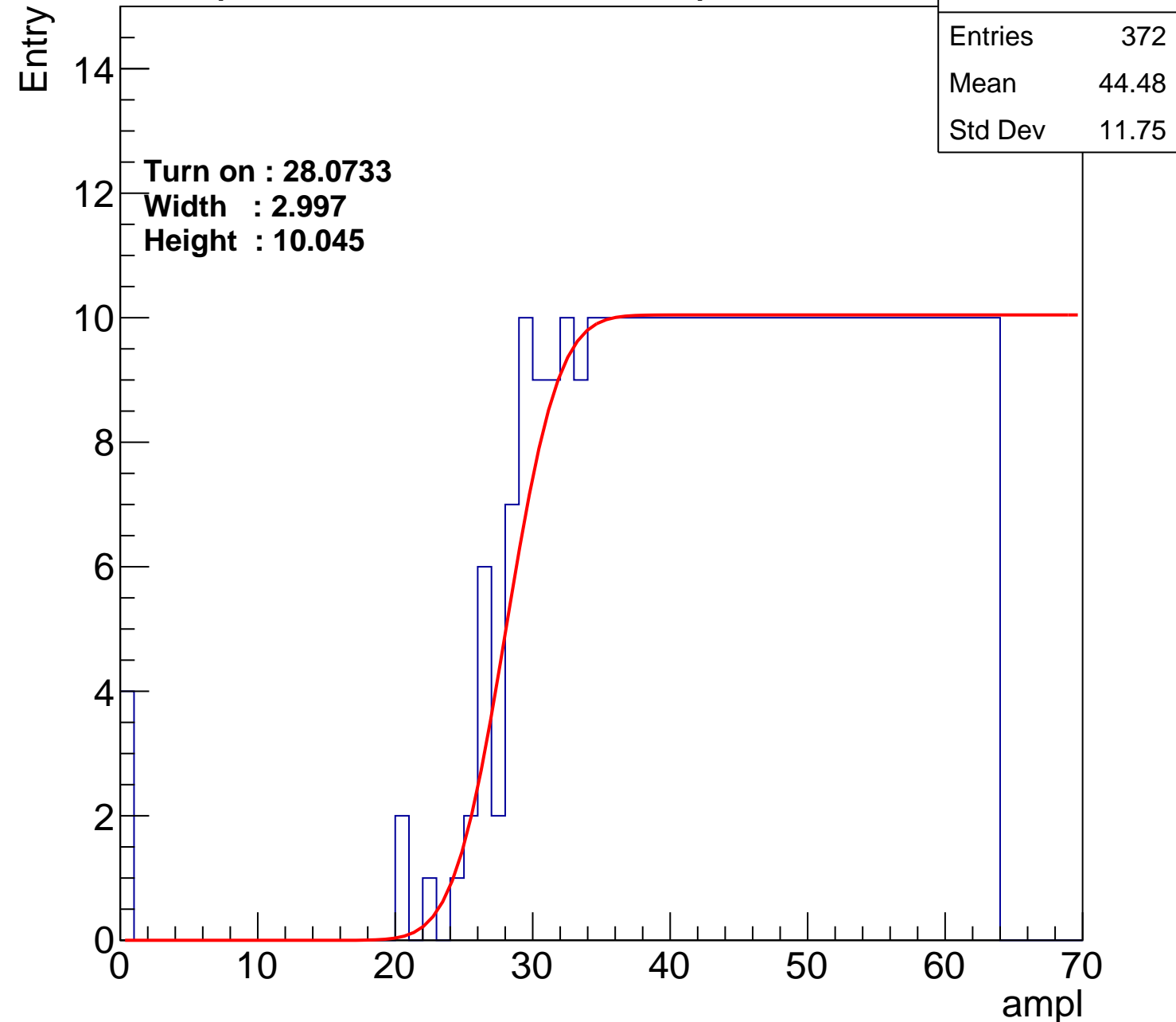
Width : 2.997

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.3522

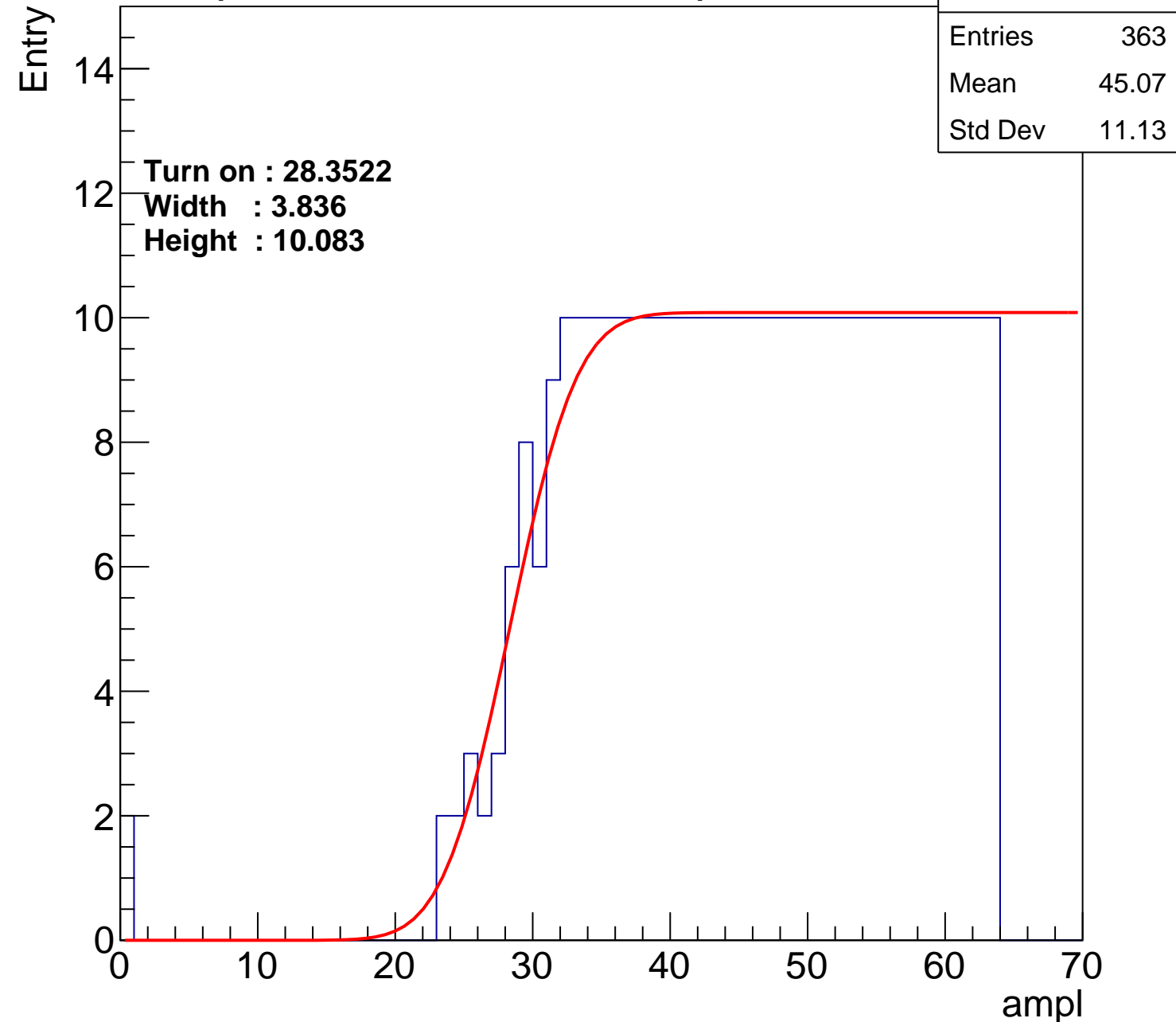
Width : 3.836

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.68
Std Dev	11.19

Turn on : 27.3398

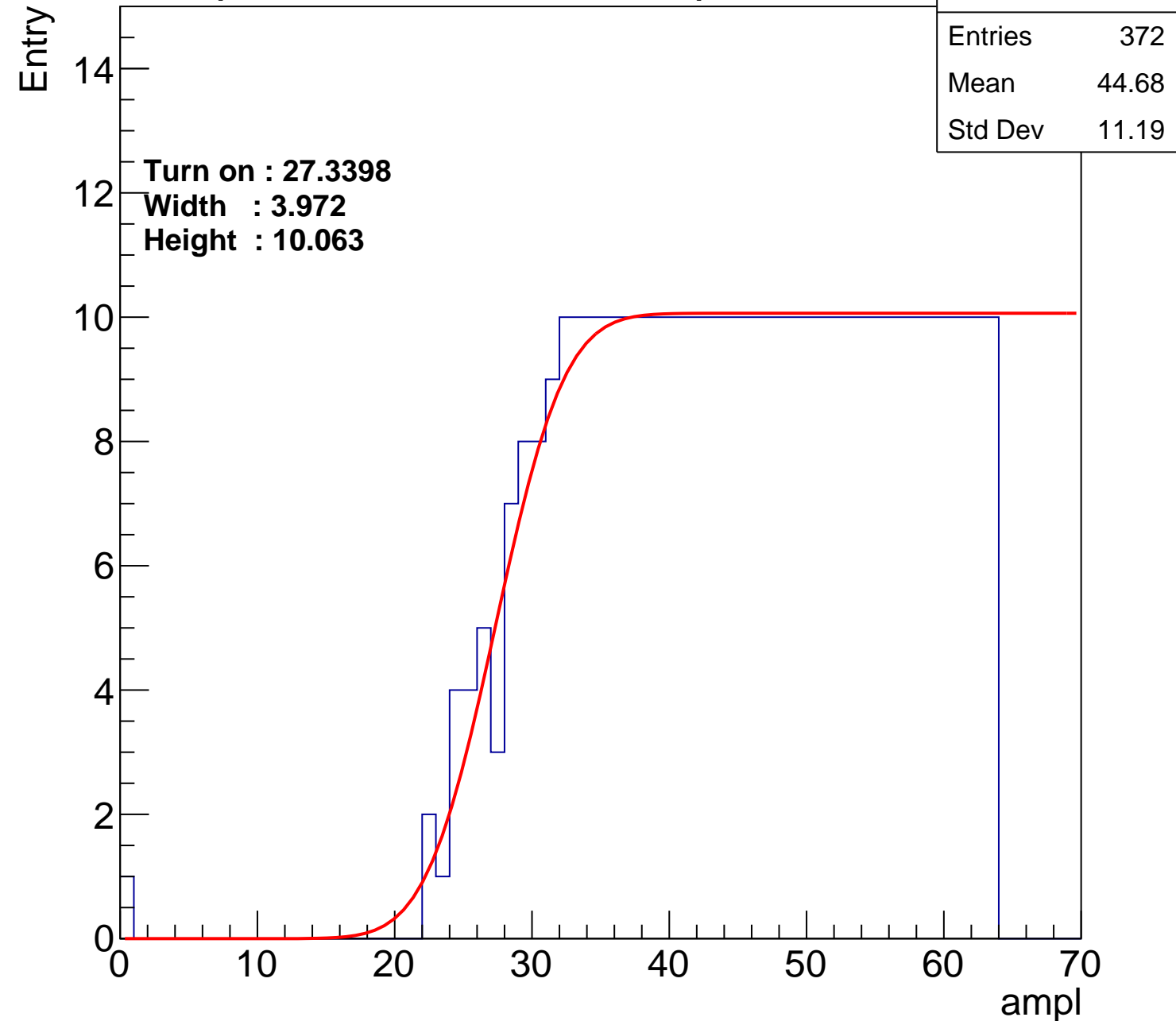
Width : 3.972

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.17
Std Dev	11.03

Turn on : 28.5389

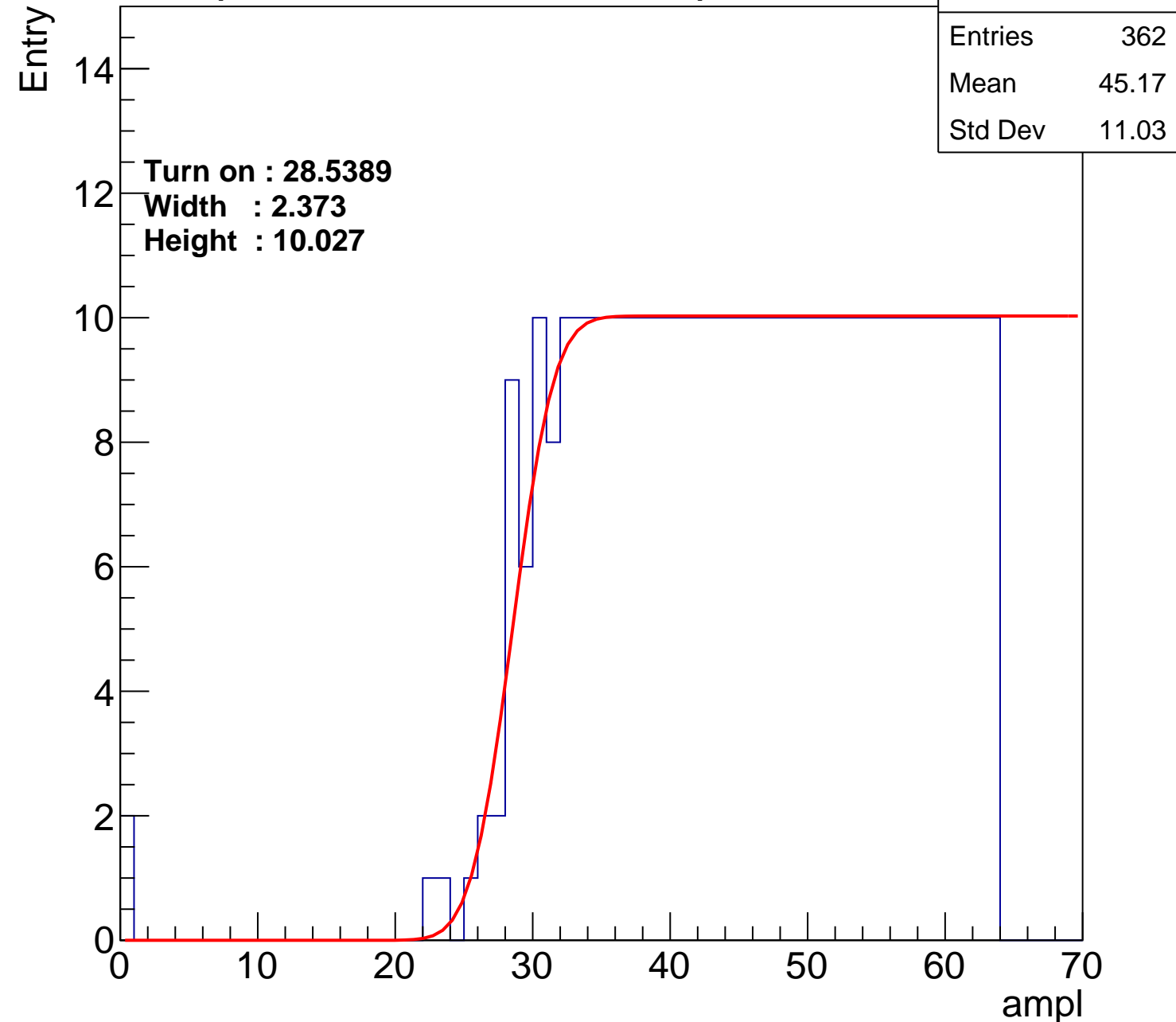
Width : 2.373

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.9
Std Dev	11.95

Turn on : 26.2596

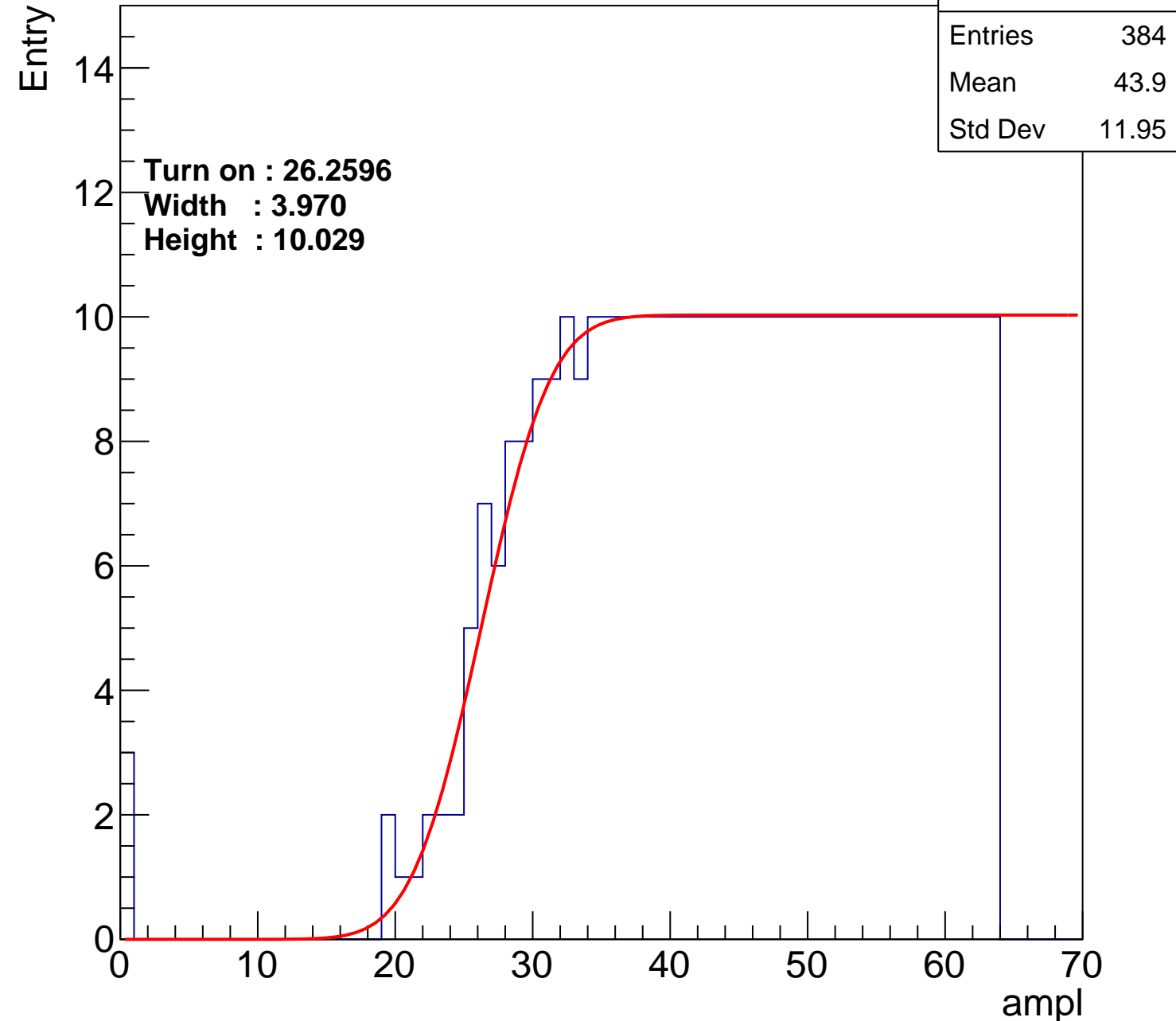
Width : 3.970

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch11

calib_packv5_042523_0143.root, FC#7, port C2

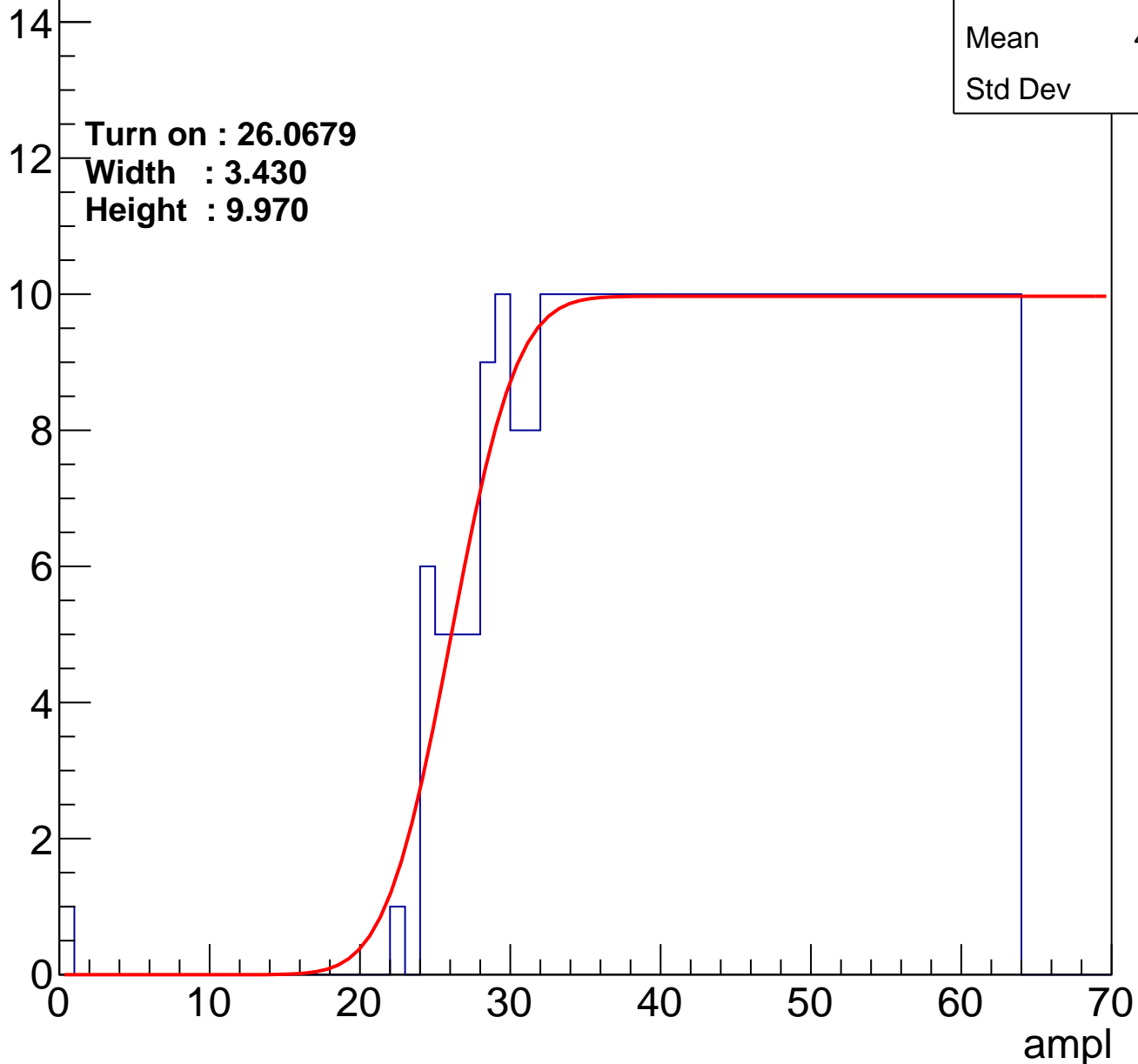
Entries	378
Mean	44.41
Std Dev	11.3

Turn on : 26.0679

Width : 3.430

Height : 9.970

Entry



B1L103S, U16-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.61
Std Dev	11.98

Turn on : 25.4224

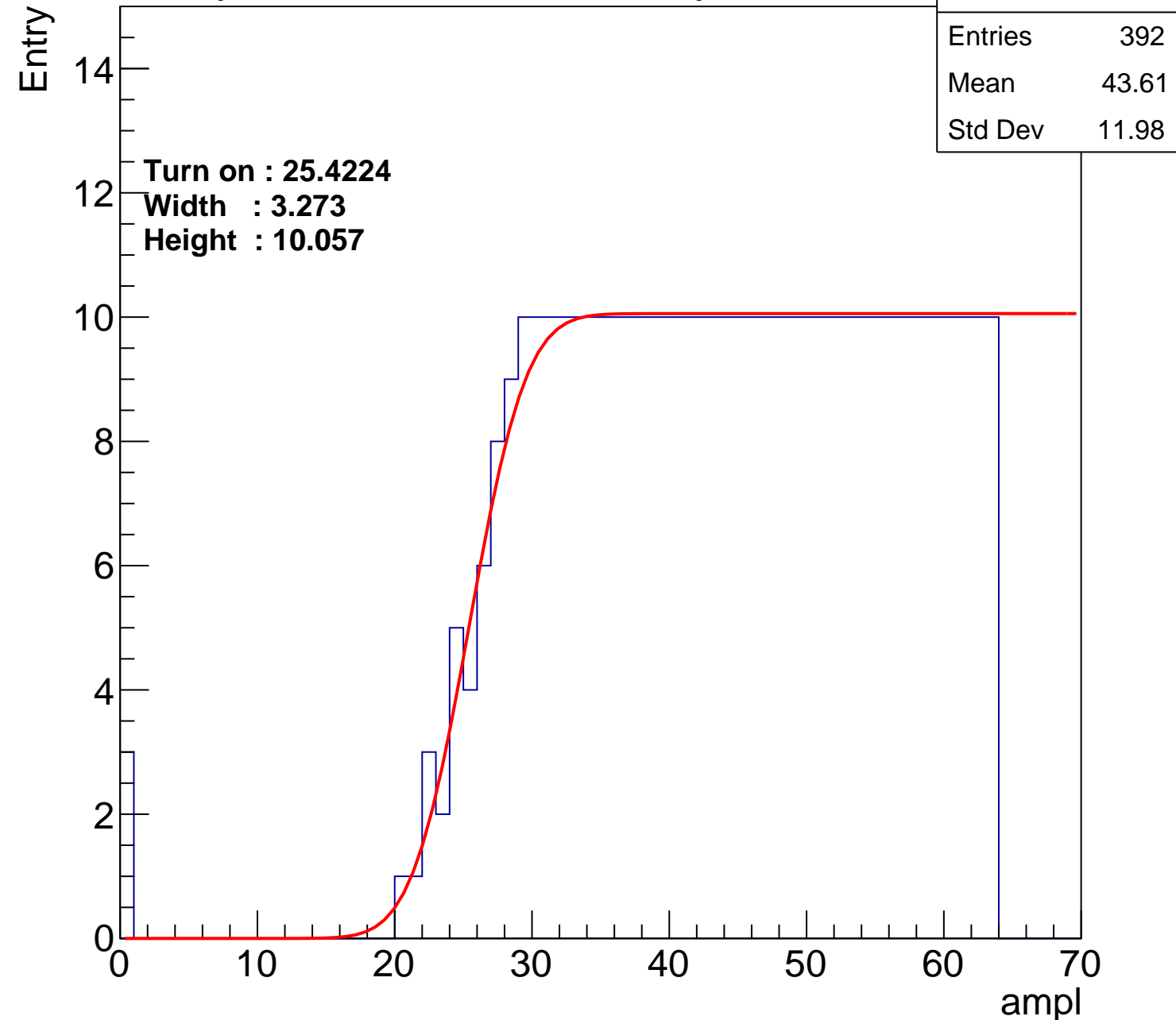
Width : 3.273

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.83
Std Dev	11.38

Turn on : 27.2836

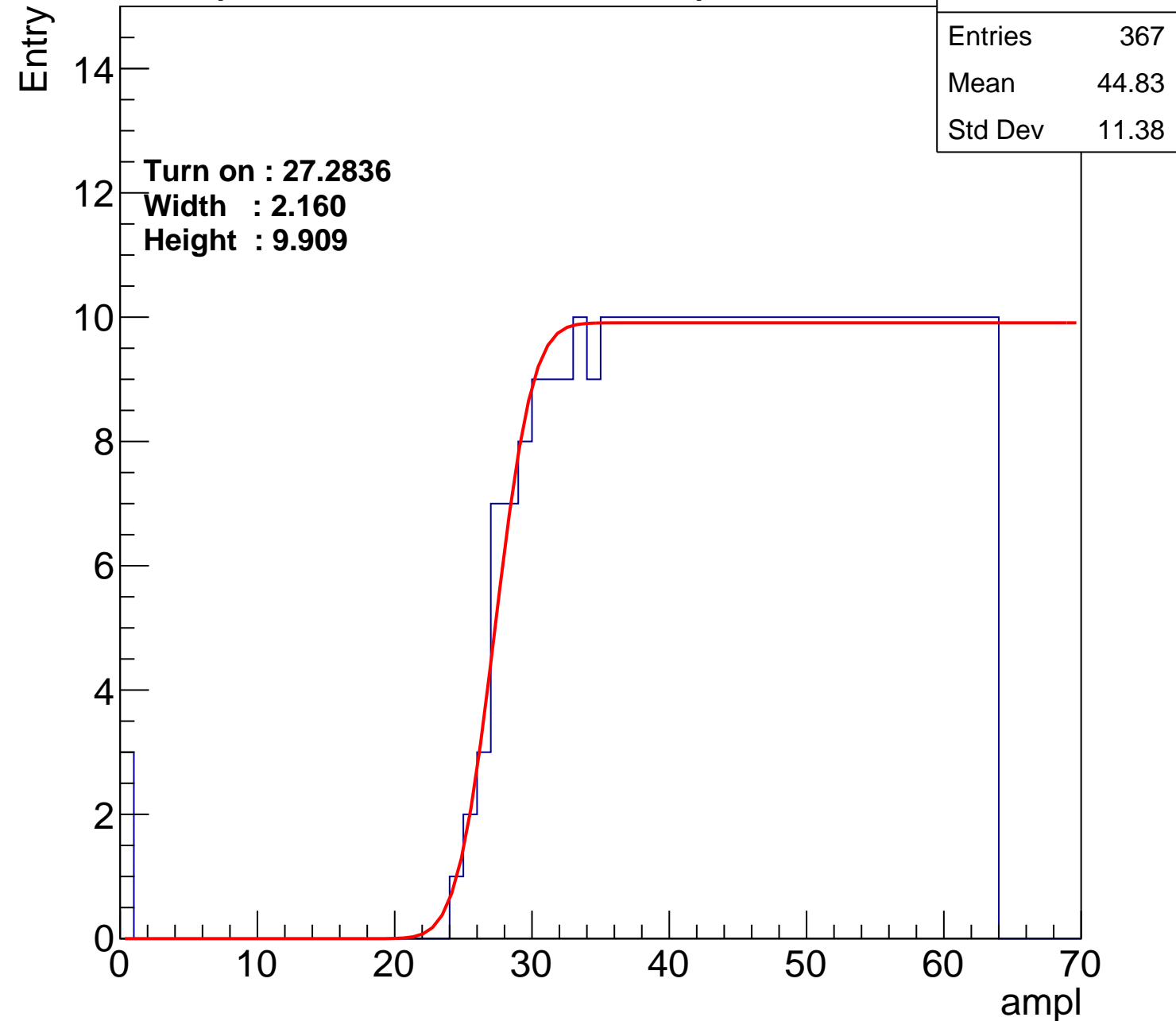
Width : 2.160

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch14

calib_packv5_042523_0143.root, FC#7, port C2

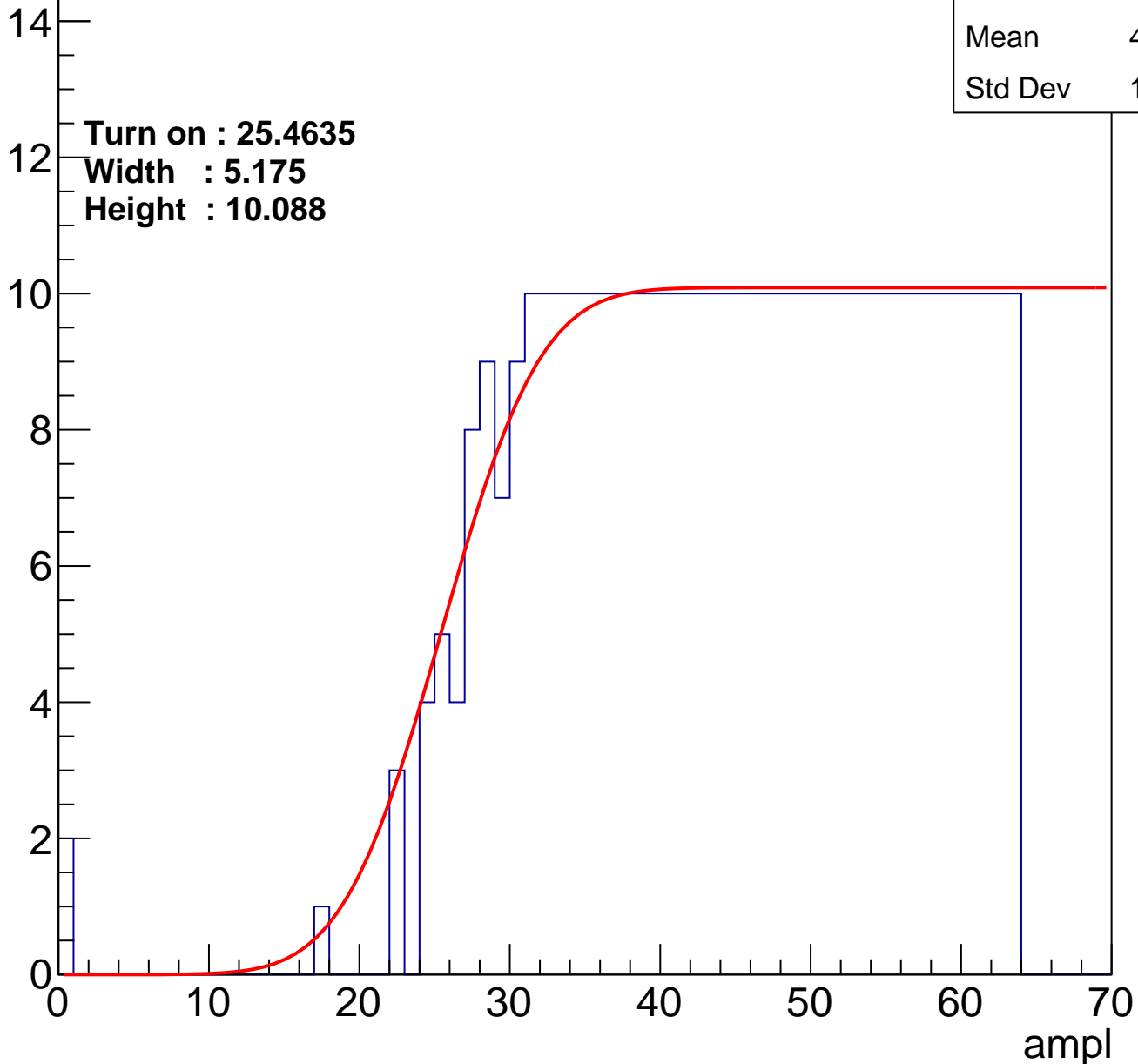
Entries	382
Mean	44.13
Std Dev	11.62

Turn on : 25.4635

Width : 5.175

Height : 10.088

Entry



B1L103S, U16-ch15

calib_packv5_042523_0143.root, FC#7, port C2

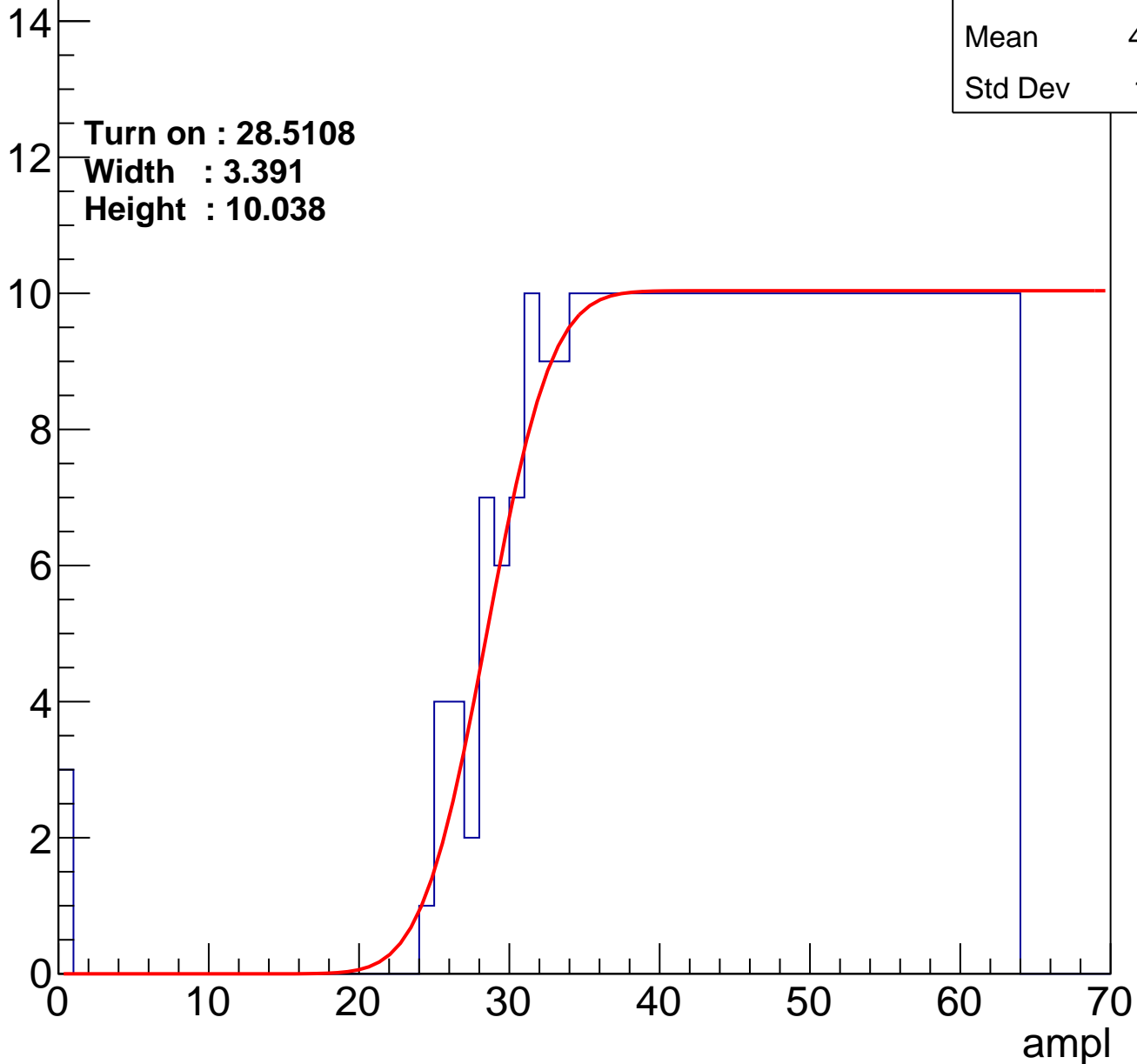
Entries	362
Mean	45.05
Std Dev	11.31

Turn on : 28.5108

Width : 3.391

Height : 10.038

Entry



B1L103S, U16-ch16

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 28.6136

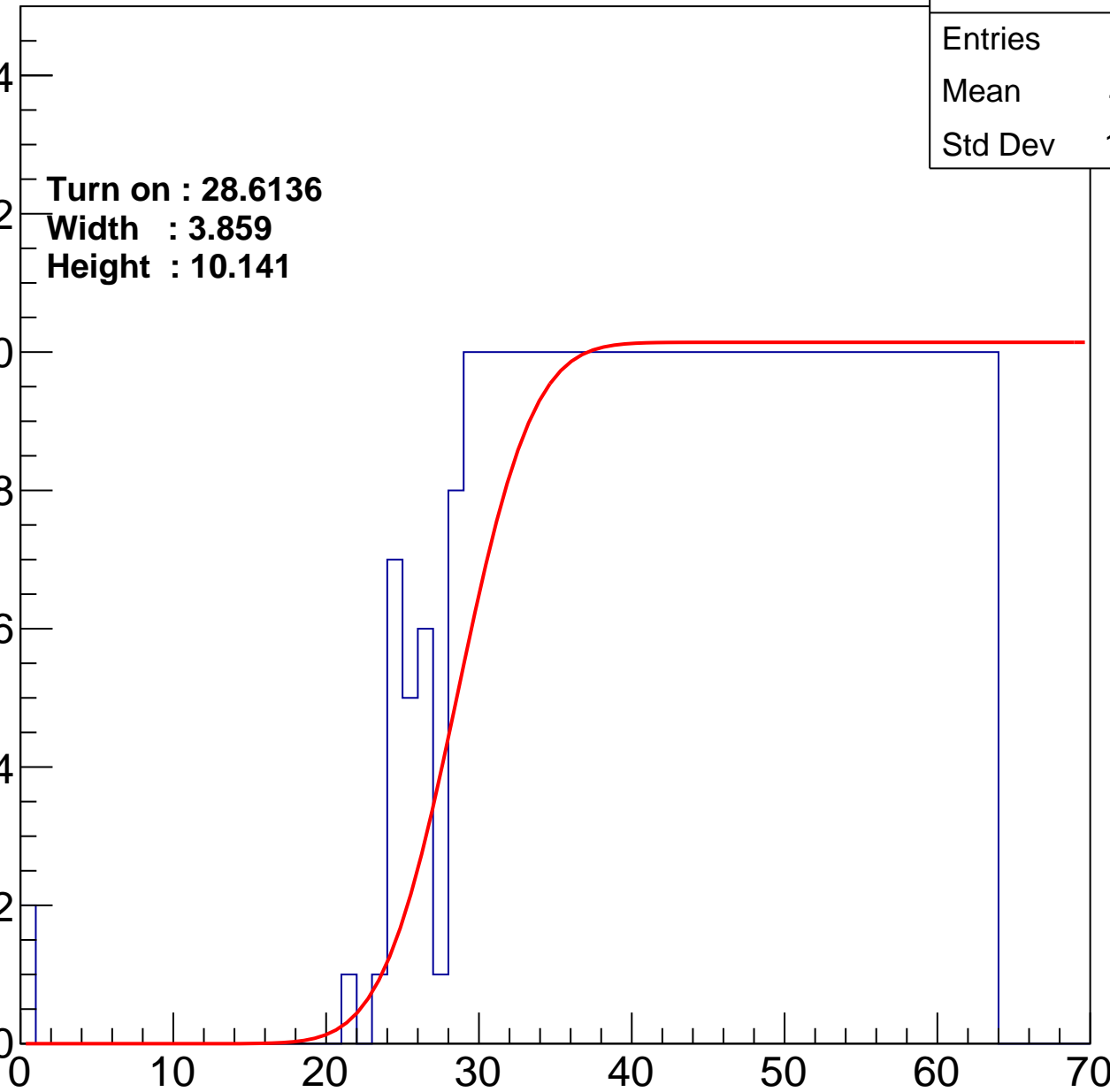
Width : 3.859

Height : 10.141

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch17

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 26.7839

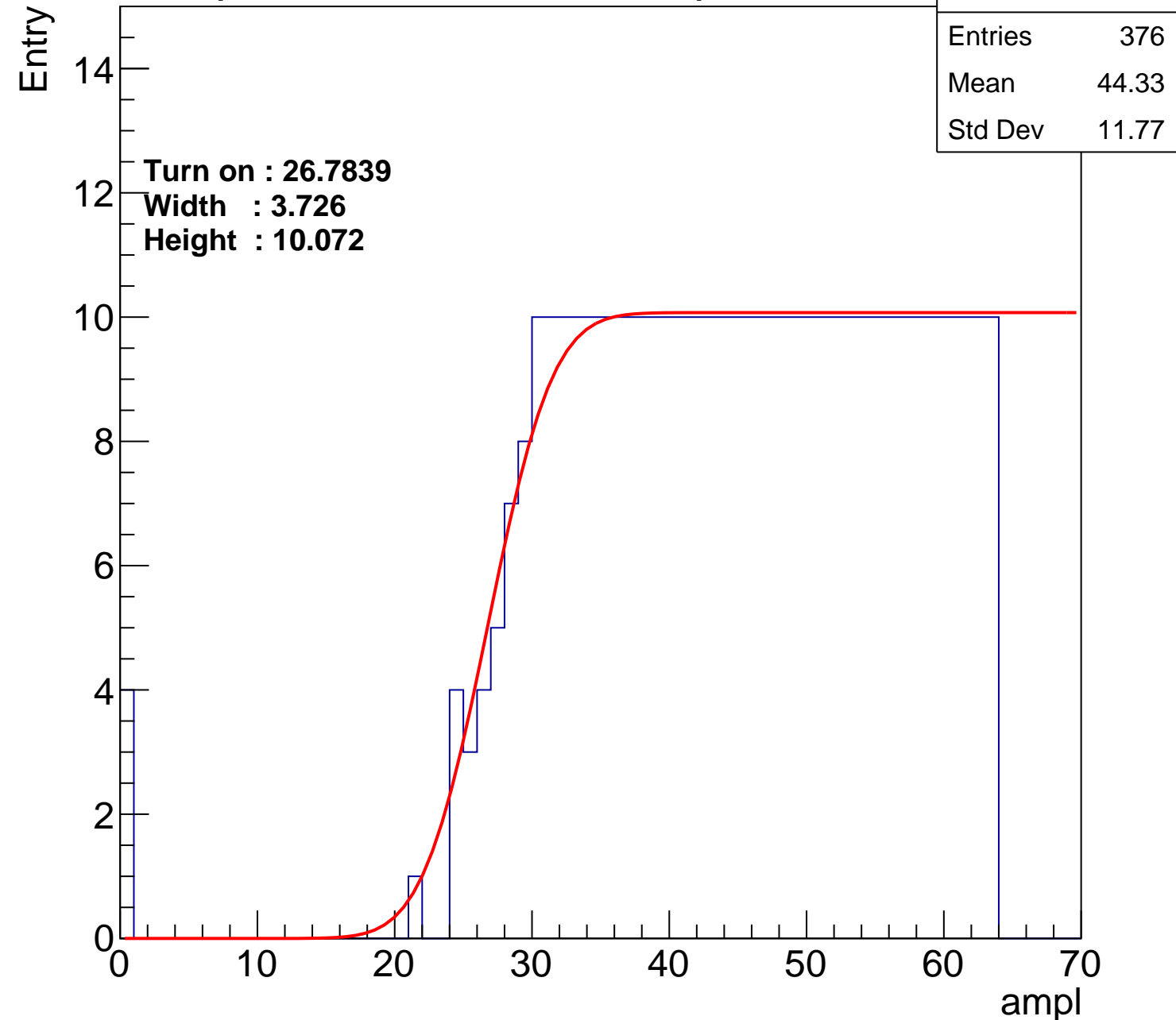
Width : 3.726

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.55
Std Dev	11.83

Turn on : 27.6127

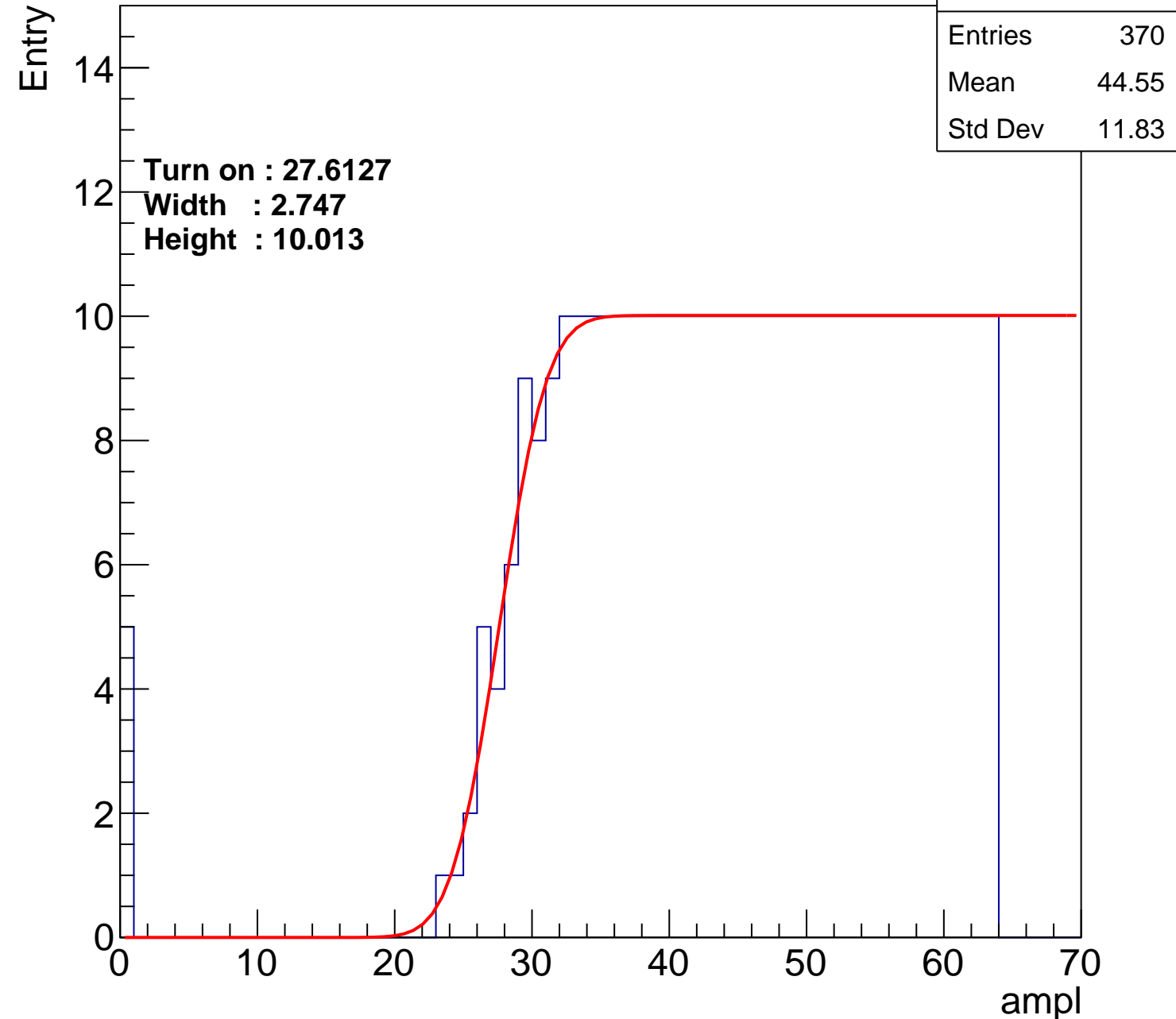
Width : 2.747

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 25.8400

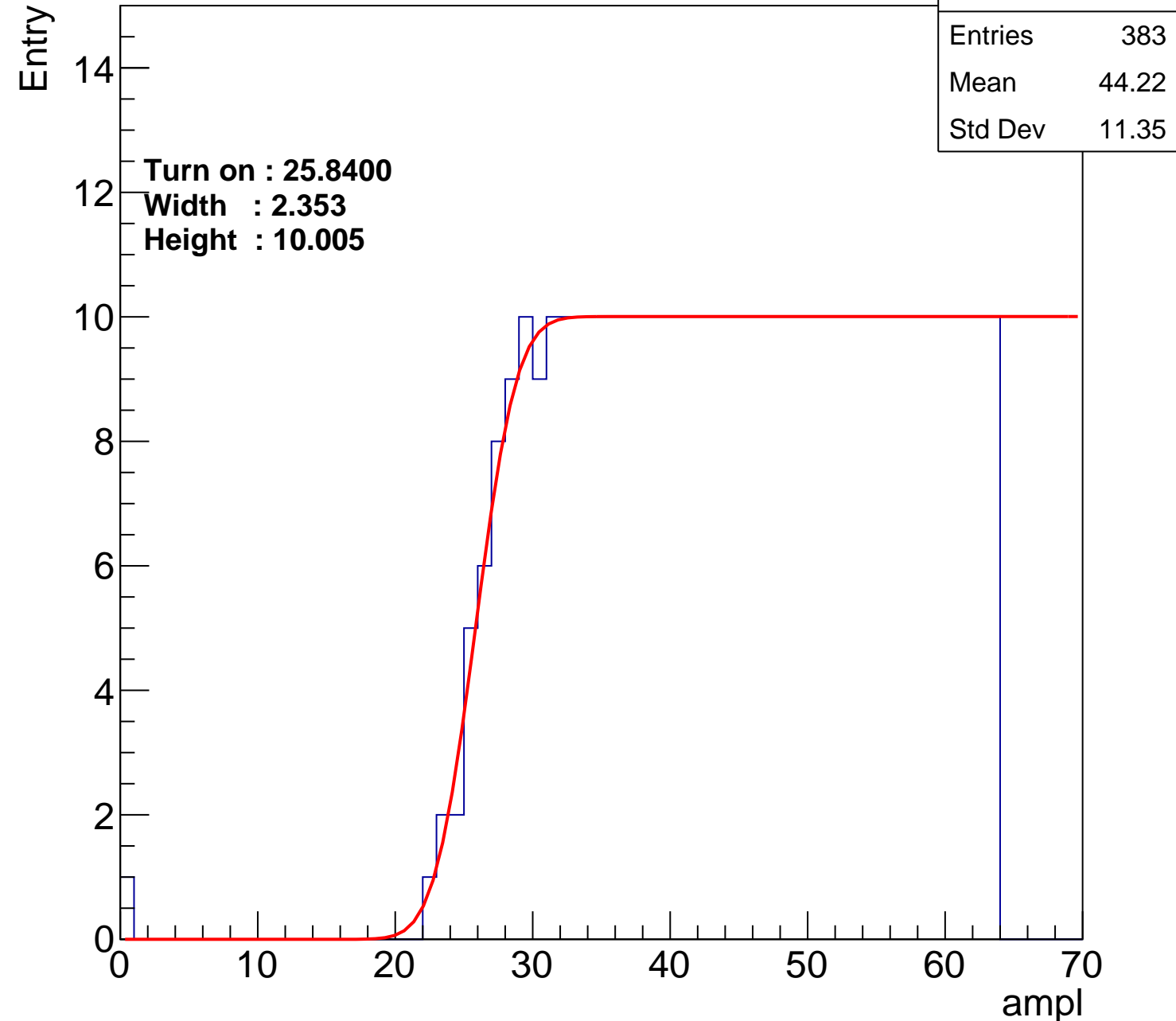
Width : 2.353

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch20

calib_packv5_042523_0143.root, FC#7, port C2

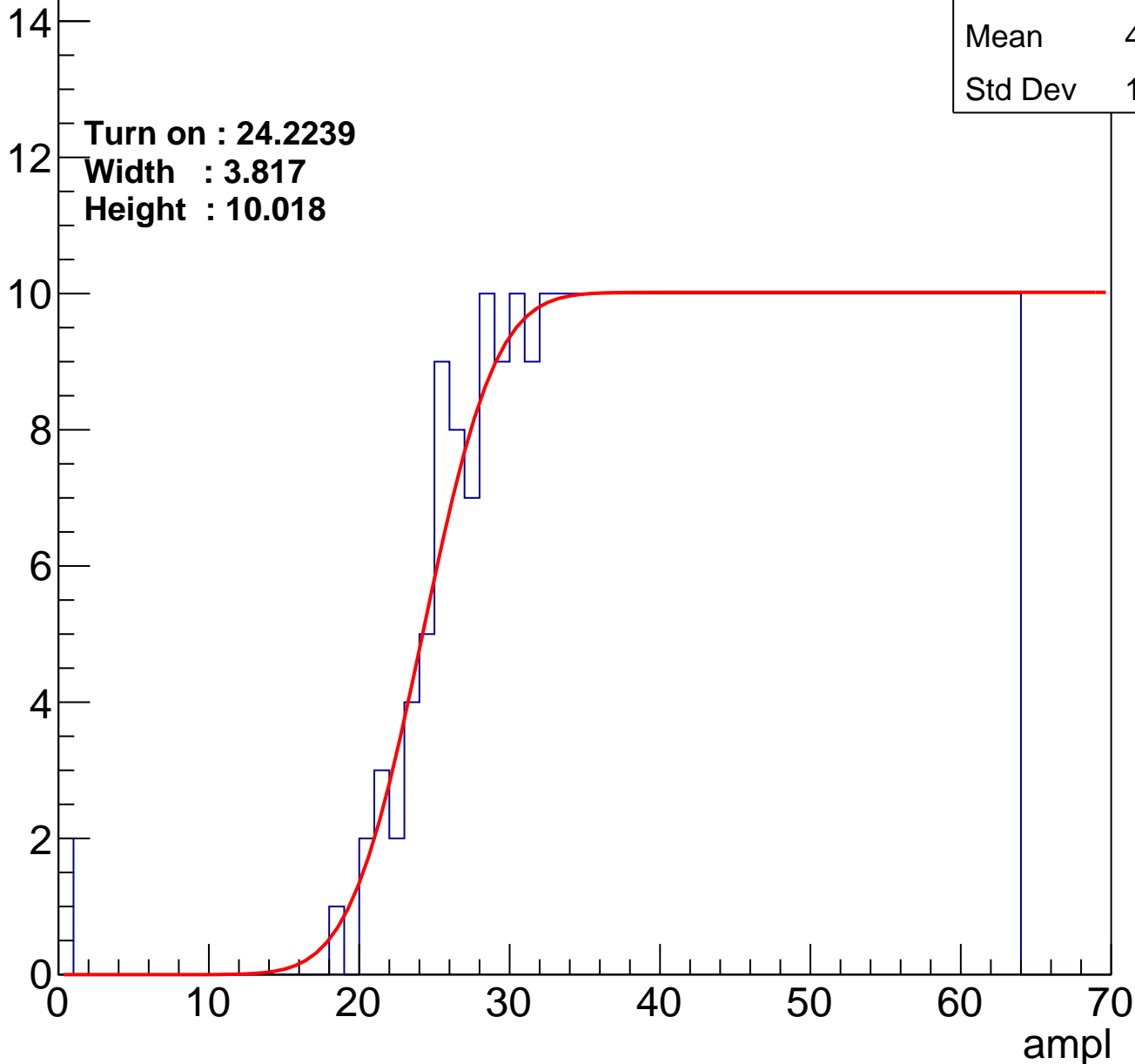
Entries	401
Mean	43.19
Std Dev	12.12

Turn on : 24.2239

Width : 3.817

Height : 10.018

Entry



B1L103S, U16-ch21

calib_packv5_042523_0143.root, FC#7, port C2

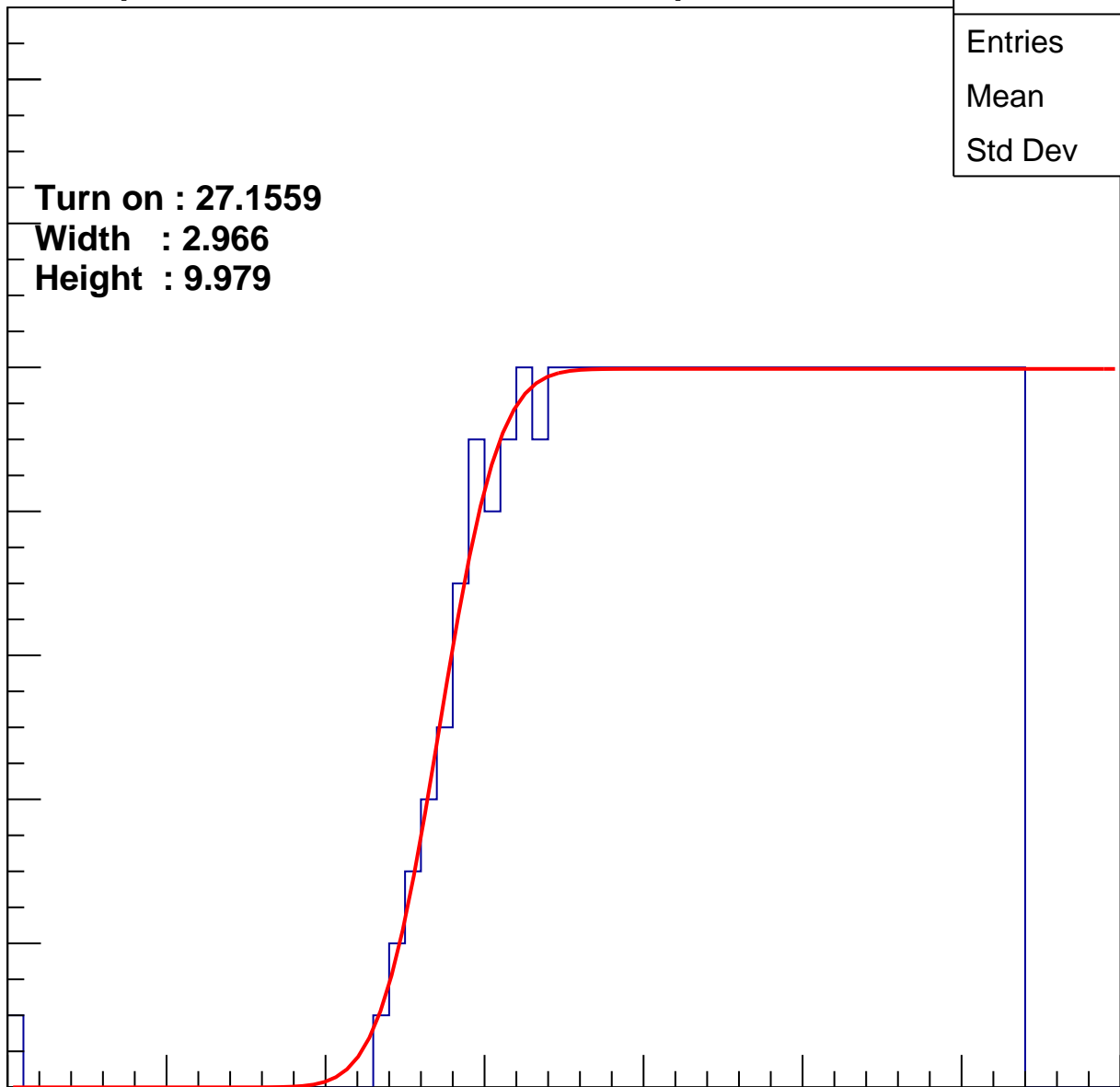
Entry

14
12
10
8
6
4
2
0

Turn on : 27.1559
Width : 2.966
Height : 9.979

Entries	368
Mean	44.91
Std Dev	11.02

ampl



B1L103S, U16-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.44
Std Dev	11.76

Turn on : 27.3016

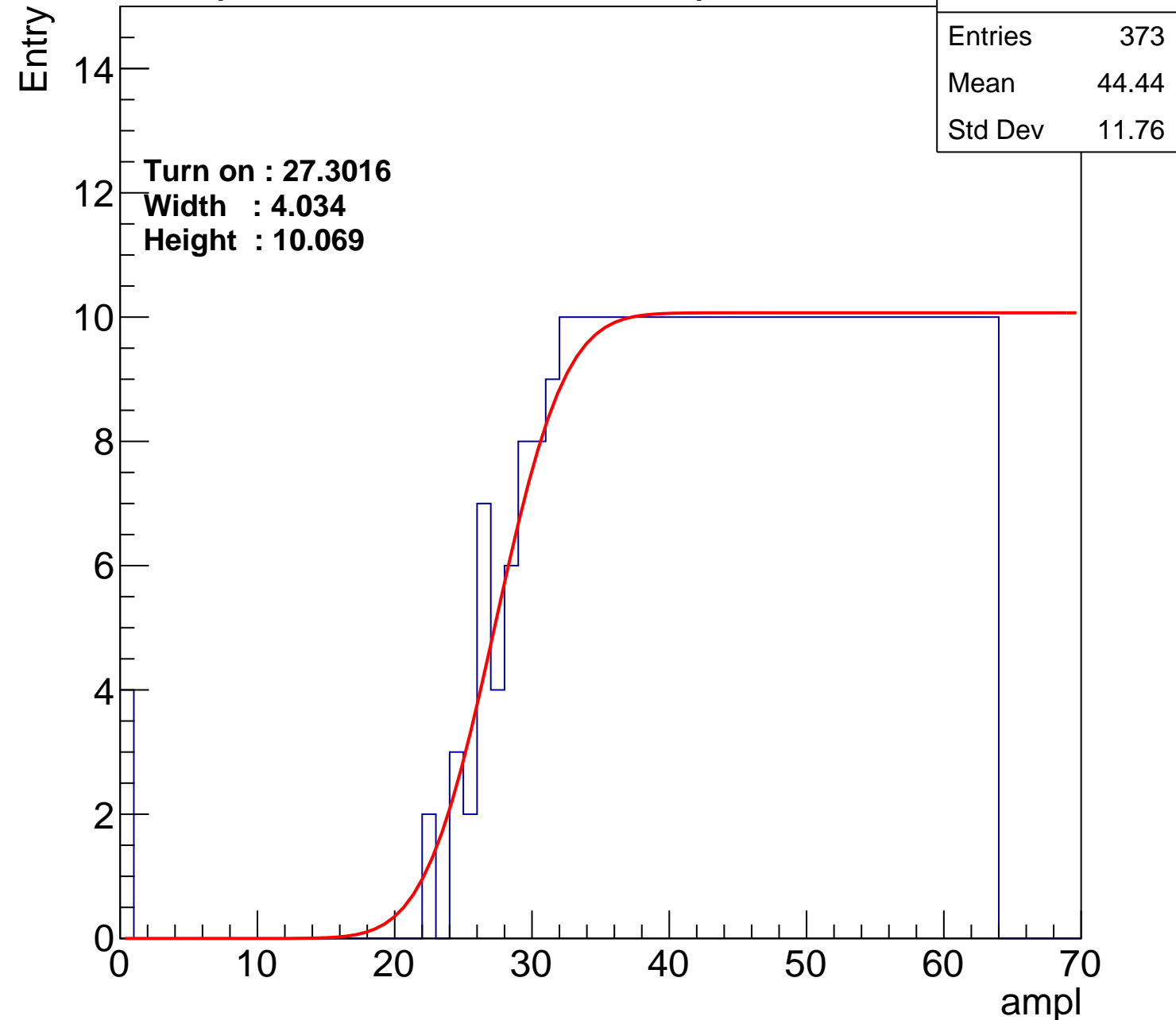
Width : 4.034

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch23

calib_packv5_042523_0143.root, FC#7, port C2

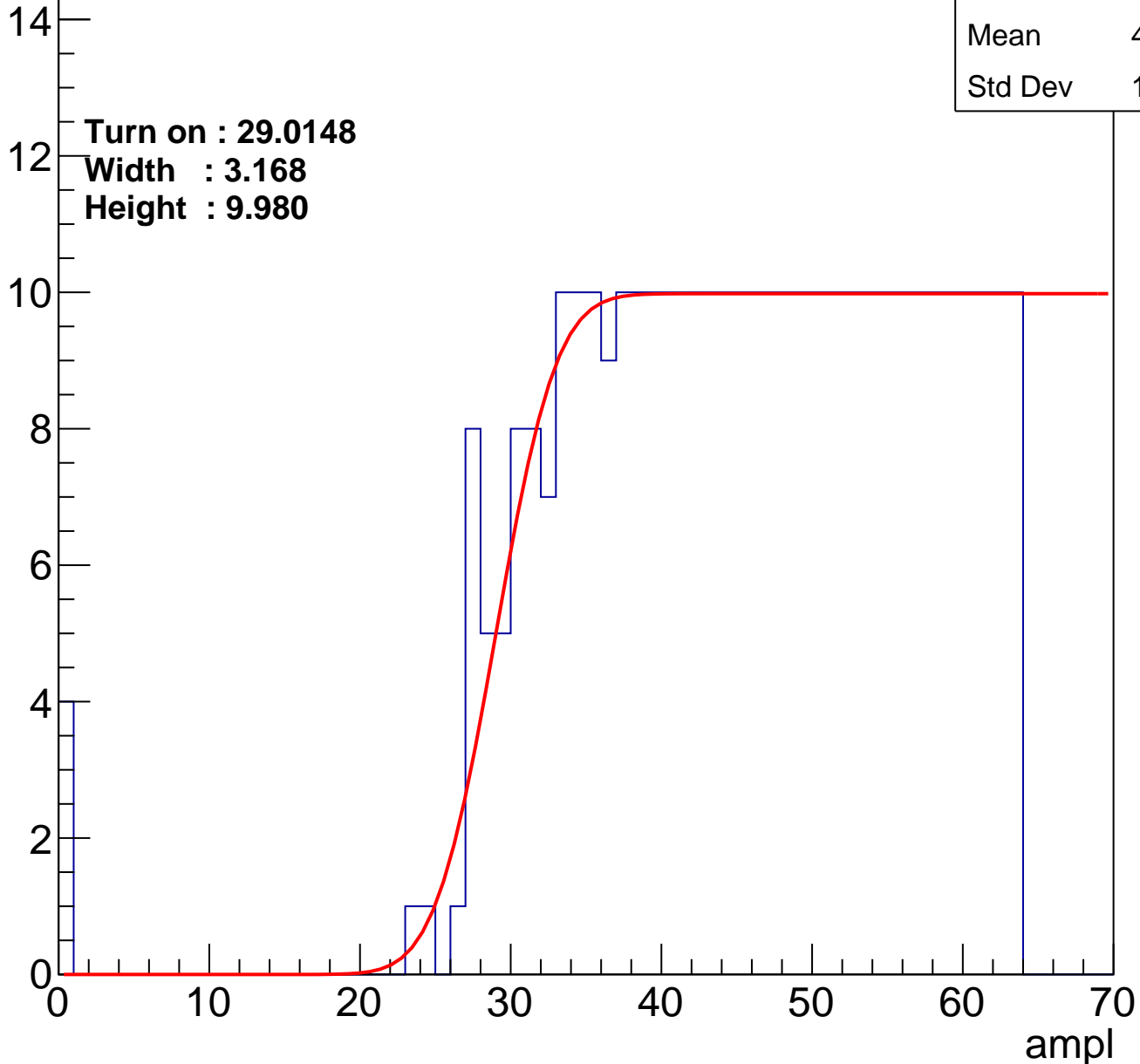
Entries	357
Mean	45.18
Std Dev	11.44

Turn on : 29.0148

Width : 3.168

Height : 9.980

Entry



B1L103S, U16-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.08
Std Dev	11.95

Turn on : 27.1673

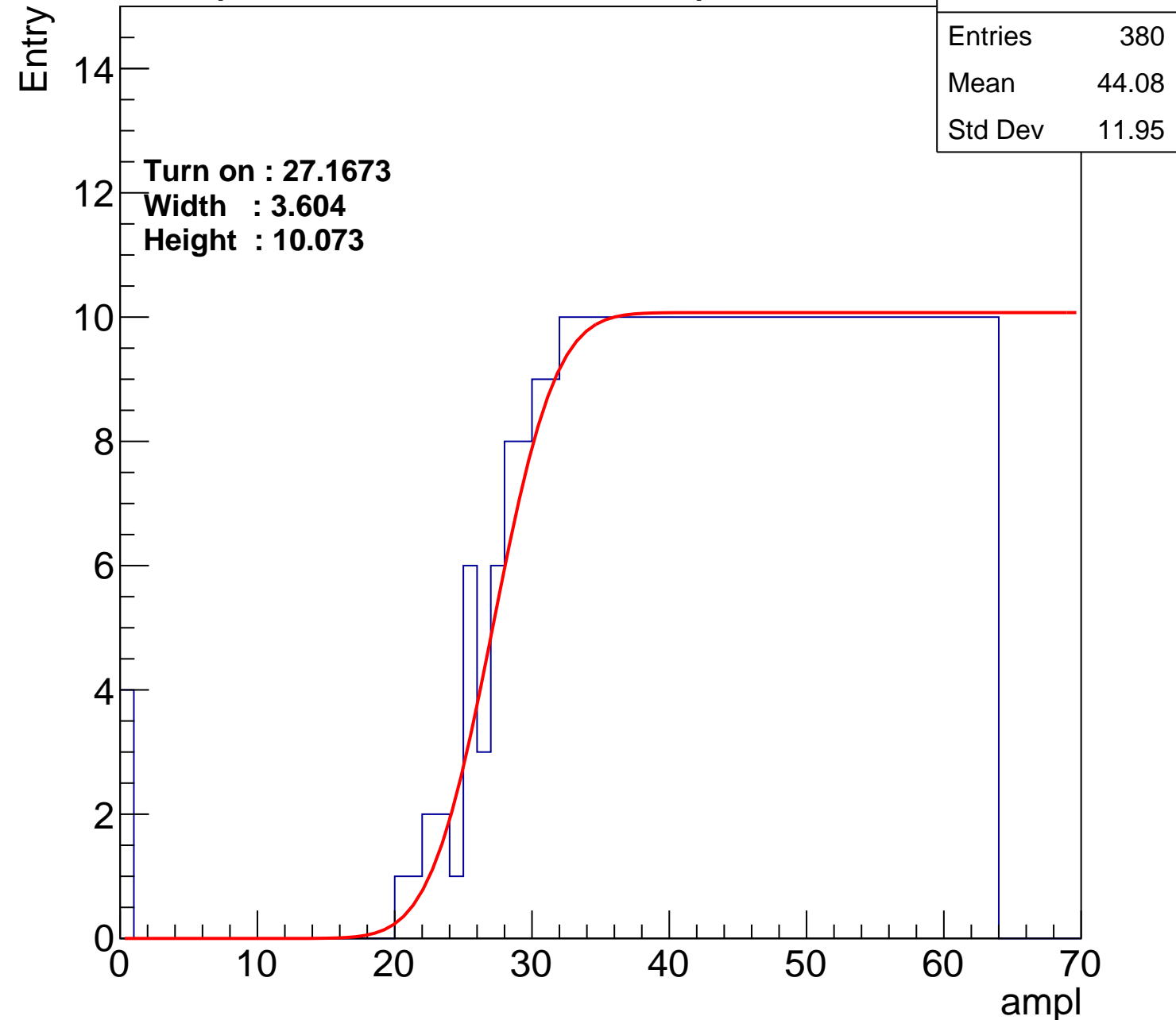
Width : 3.604

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl

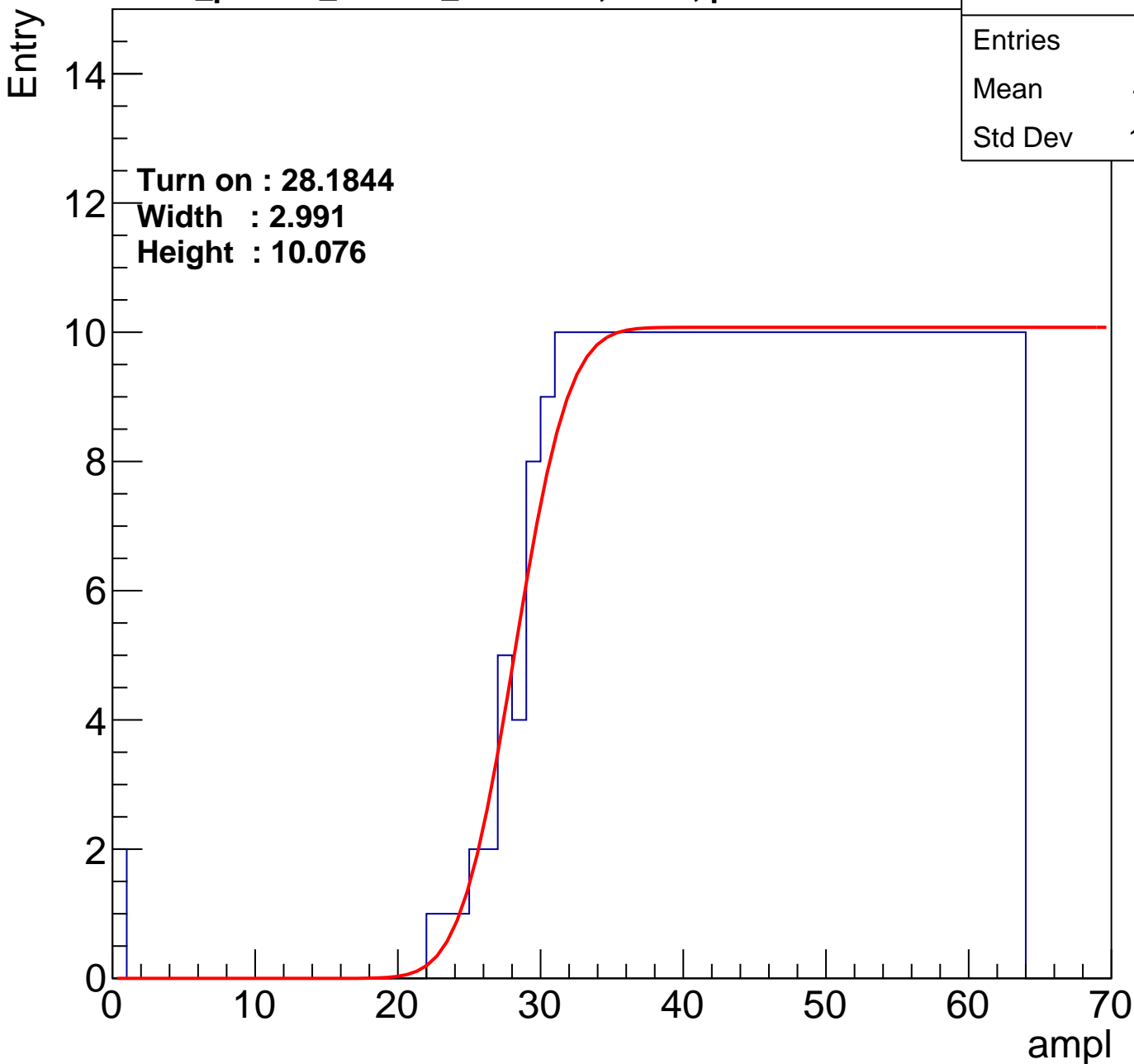


calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	45.01
Std Dev	11.12

Height : 10.076



B1L103S, U16-ch26

calib_packv5_042523_0143.root, FC#7, port C2

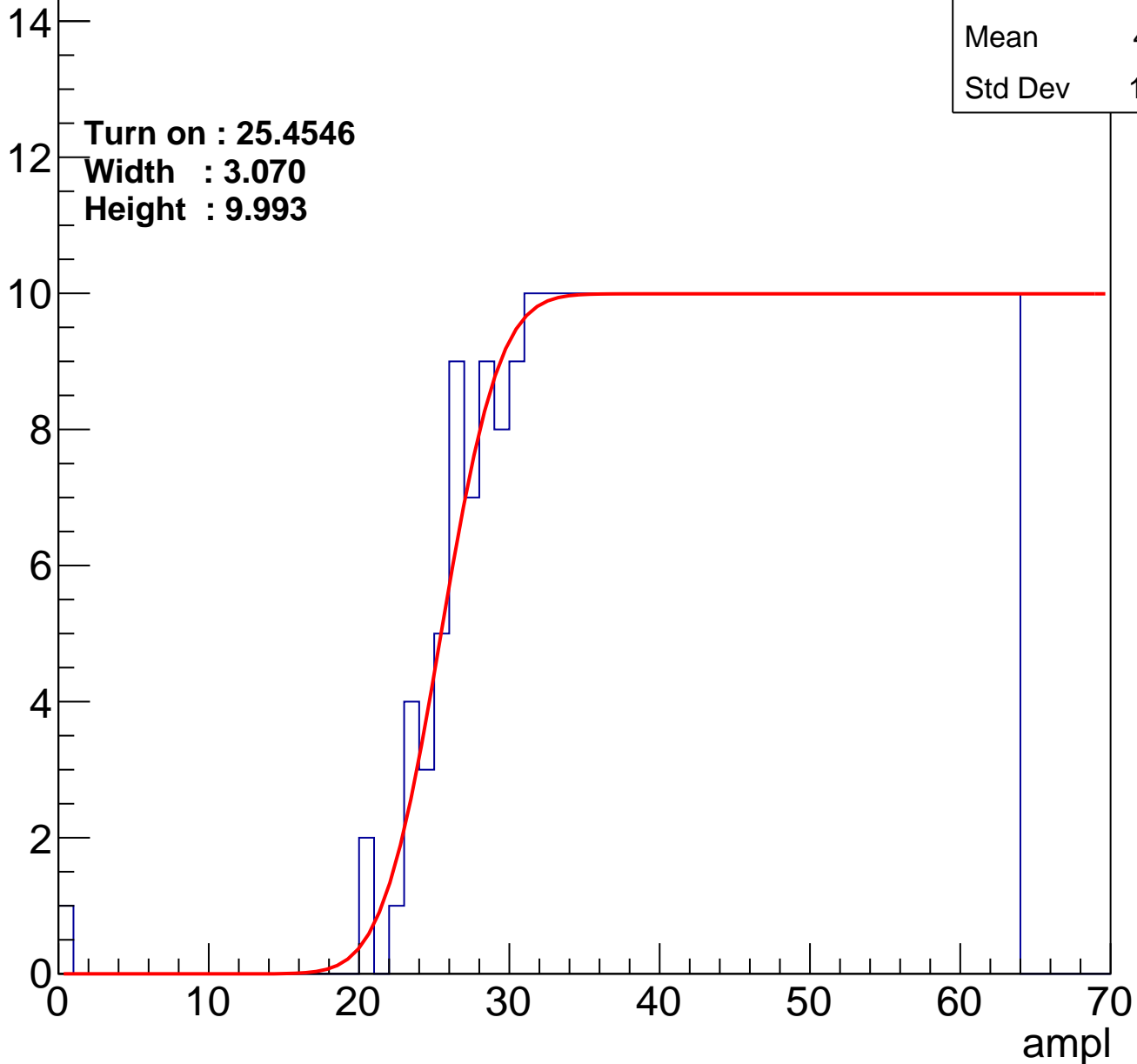
Entries	388
Mean	43.91
Std Dev	11.58

Turn on : 25.4546

Width : 3.070

Height : 9.993

Entry



B1L103S, U16-ch27

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.29
Std Dev	11.71

Turn on : 26.8264

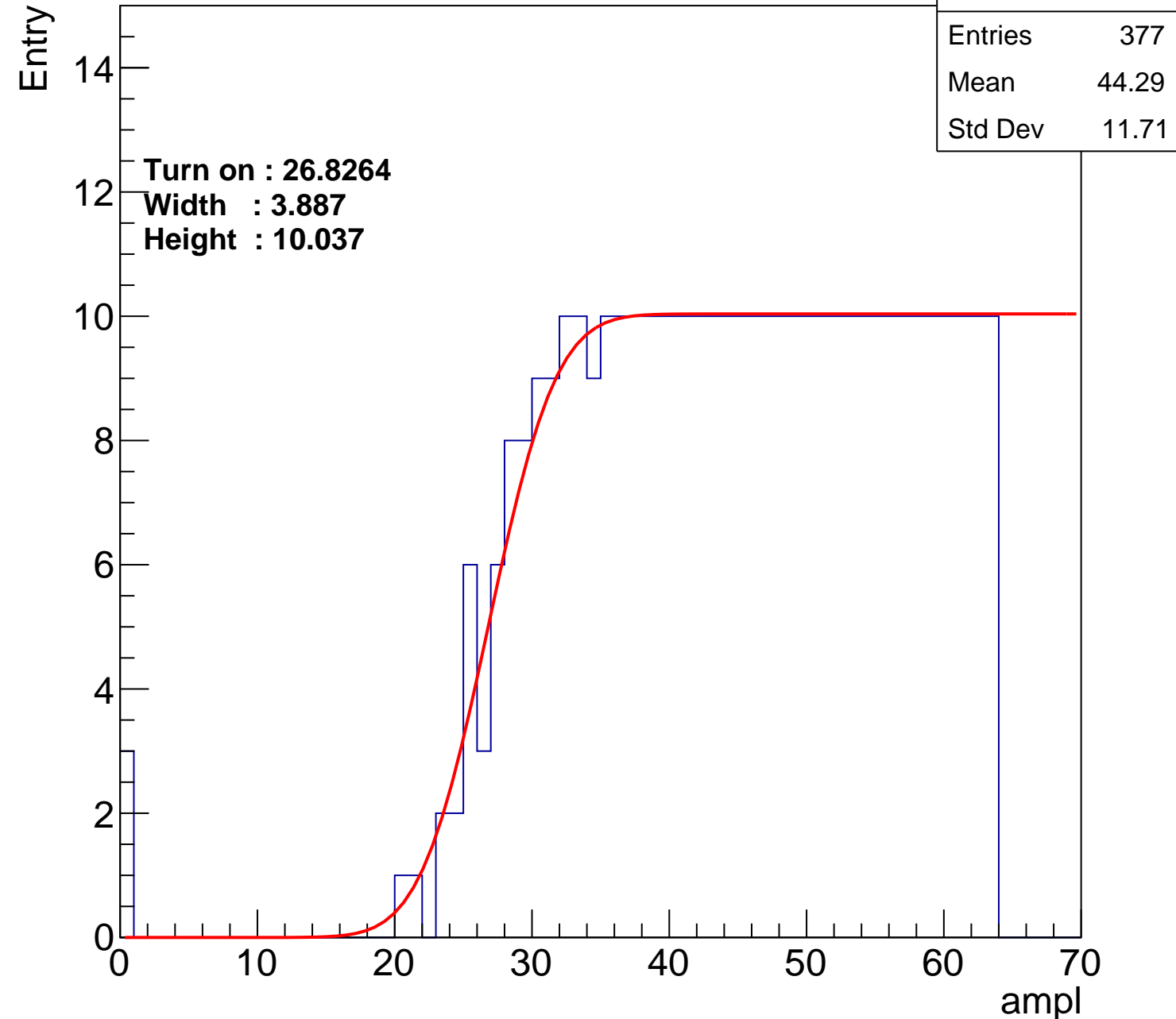
Width : 3.887

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.6
Std Dev	11.41

Turn on : 28.0052

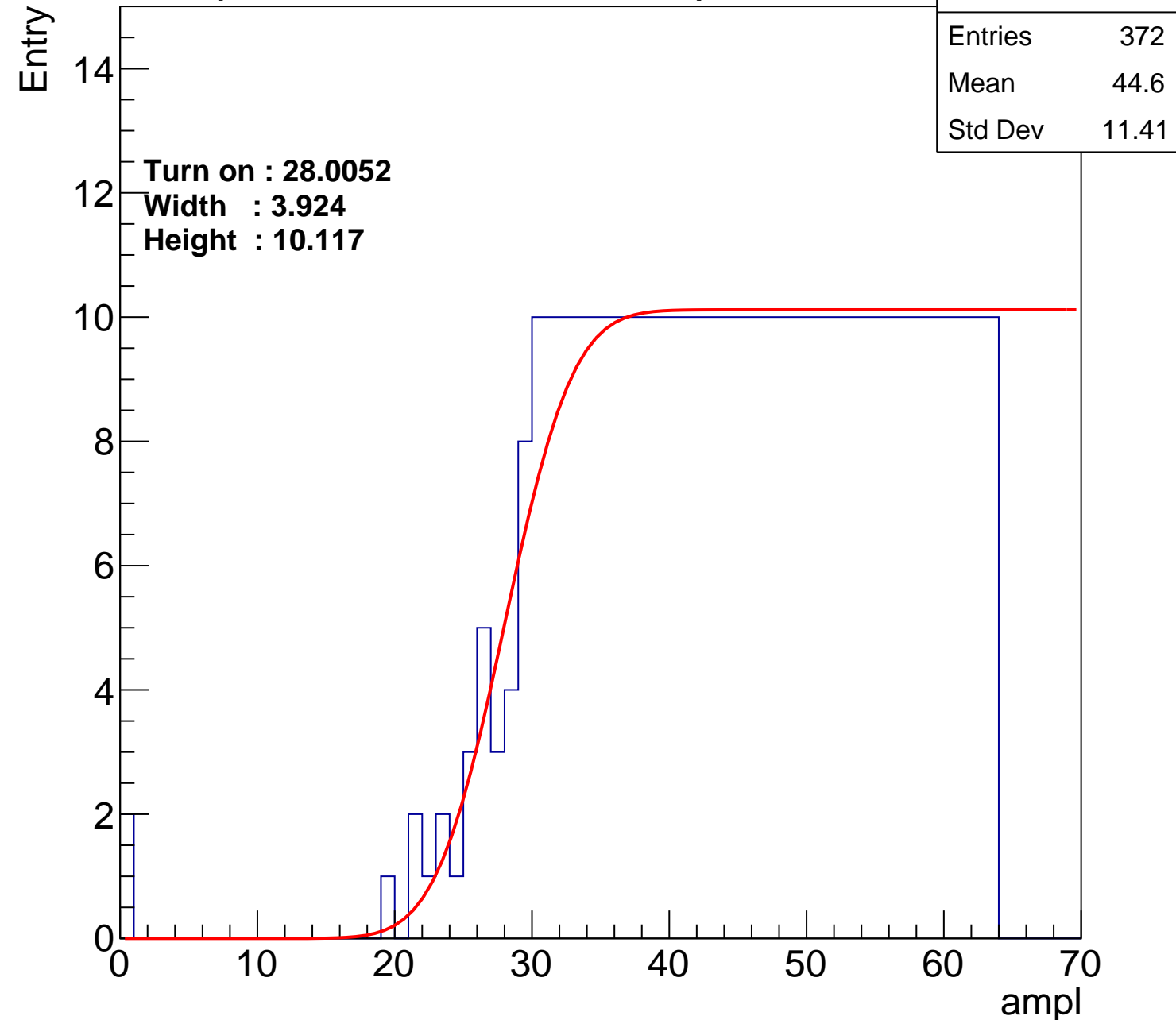
Width : 3.924

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.47
Std Dev	11.77

Turn on : 27.5146

Width : 3.177

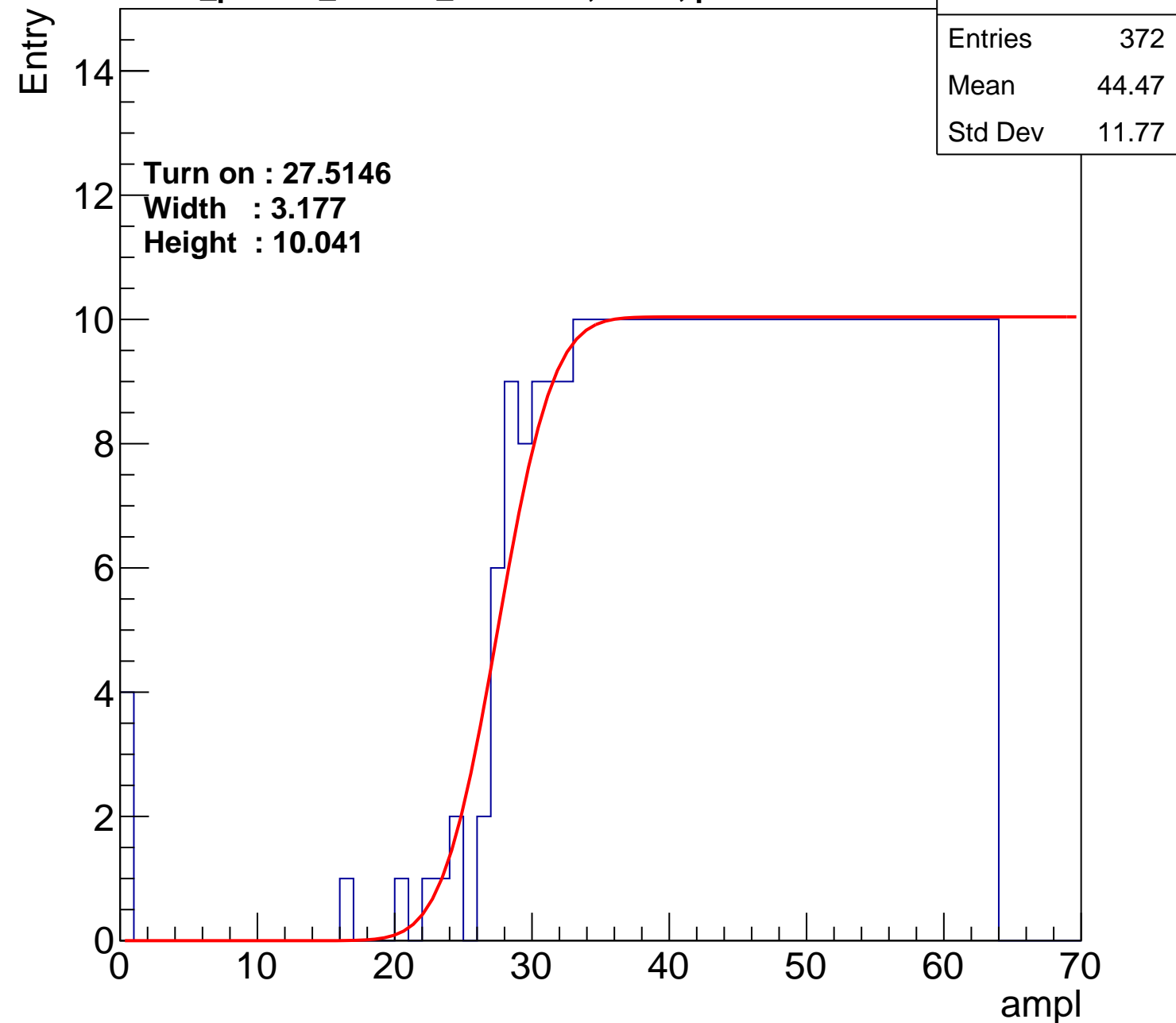
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U16-ch30

calib_packv5_042523_0143.root, FC#7, port C2

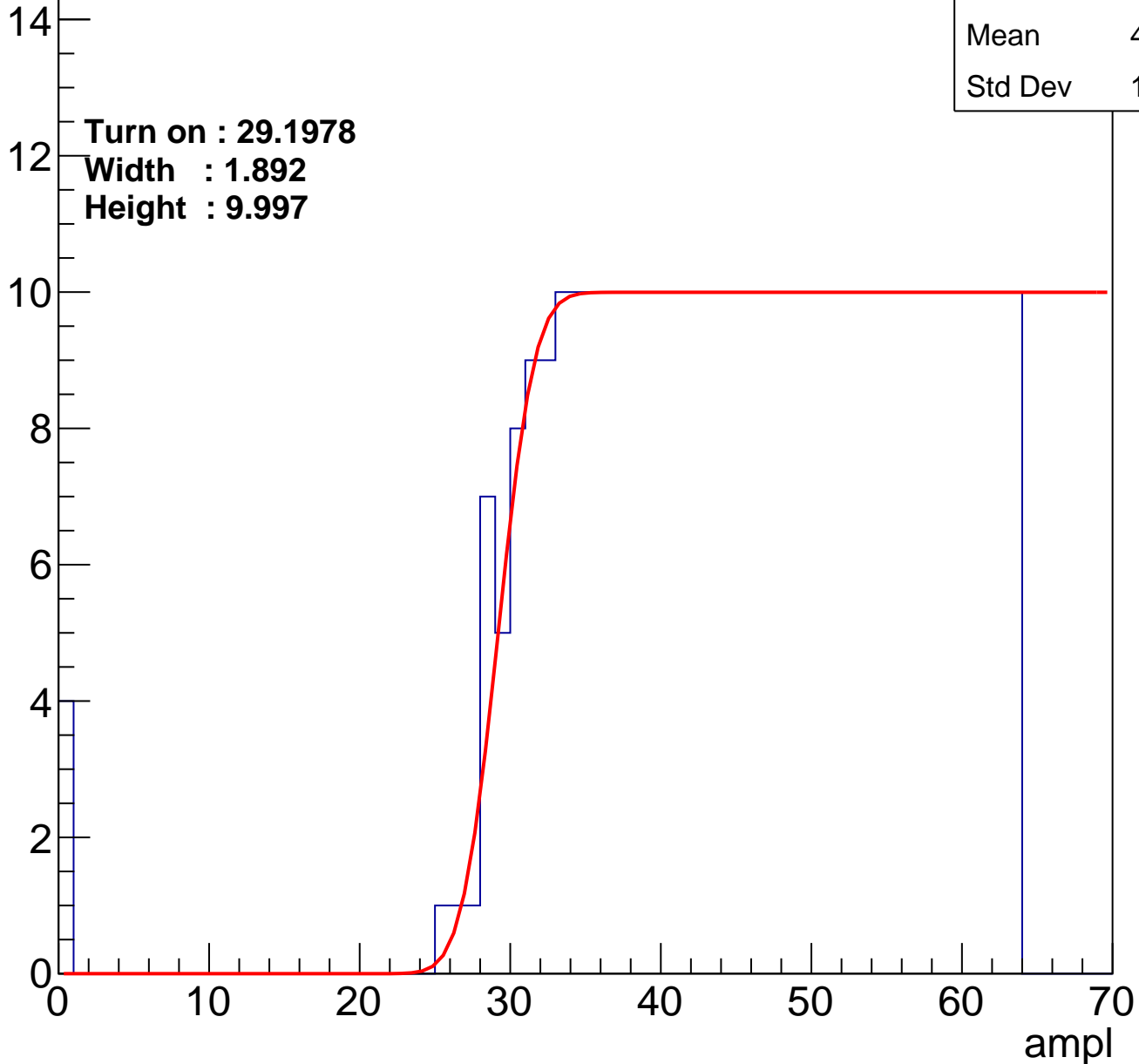
Entries	355
Mean	45.37
Std Dev	11.27

Turn on : 29.1978

Width : 1.892

Height : 9.997

Entry



B1L103S, U16-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.39
Std Dev	11.6

Turn on : 26.6617

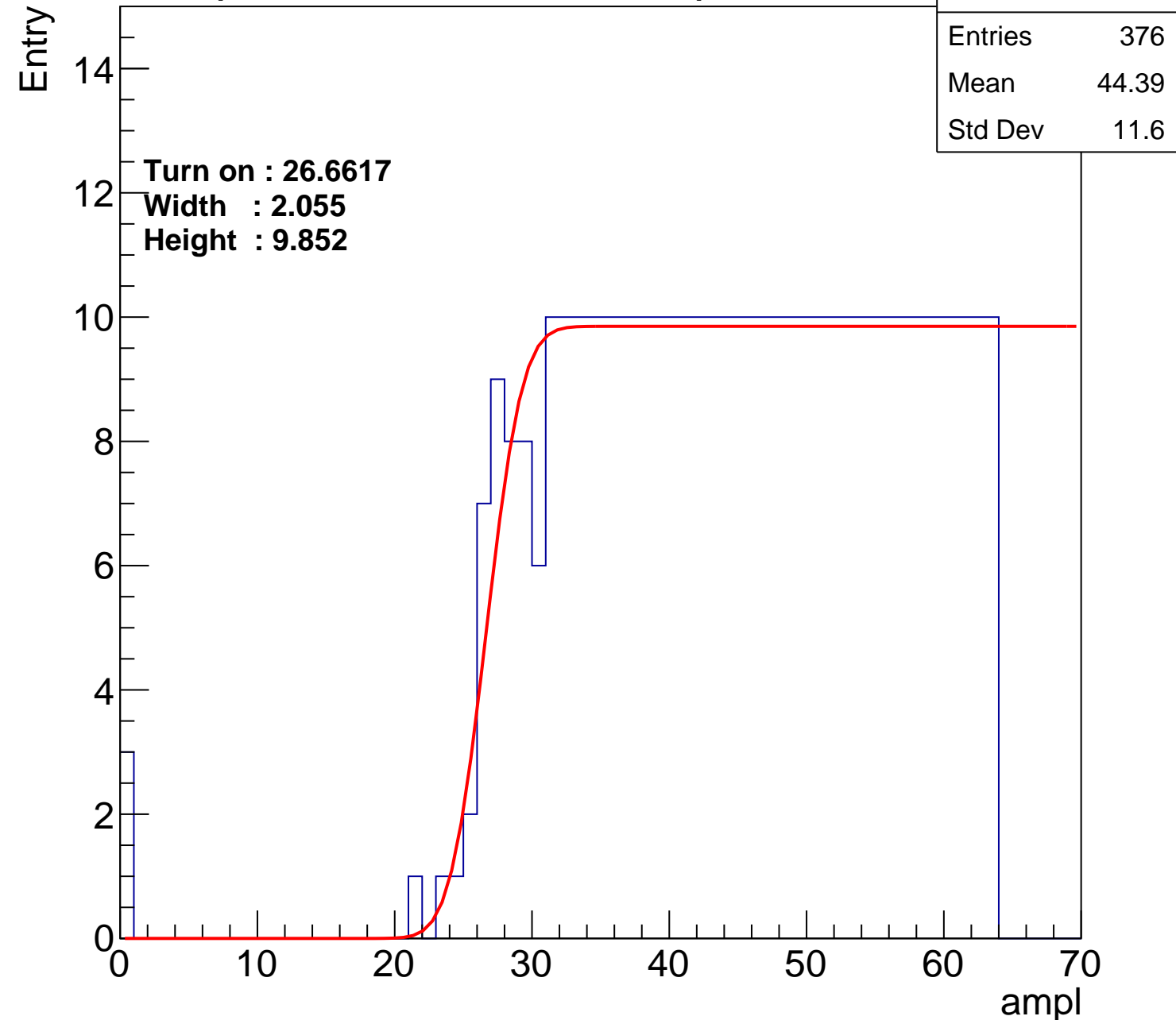
Width : 2.055

Height : 9.852

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.87
Std Dev	11.09

Turn on : 27.4270

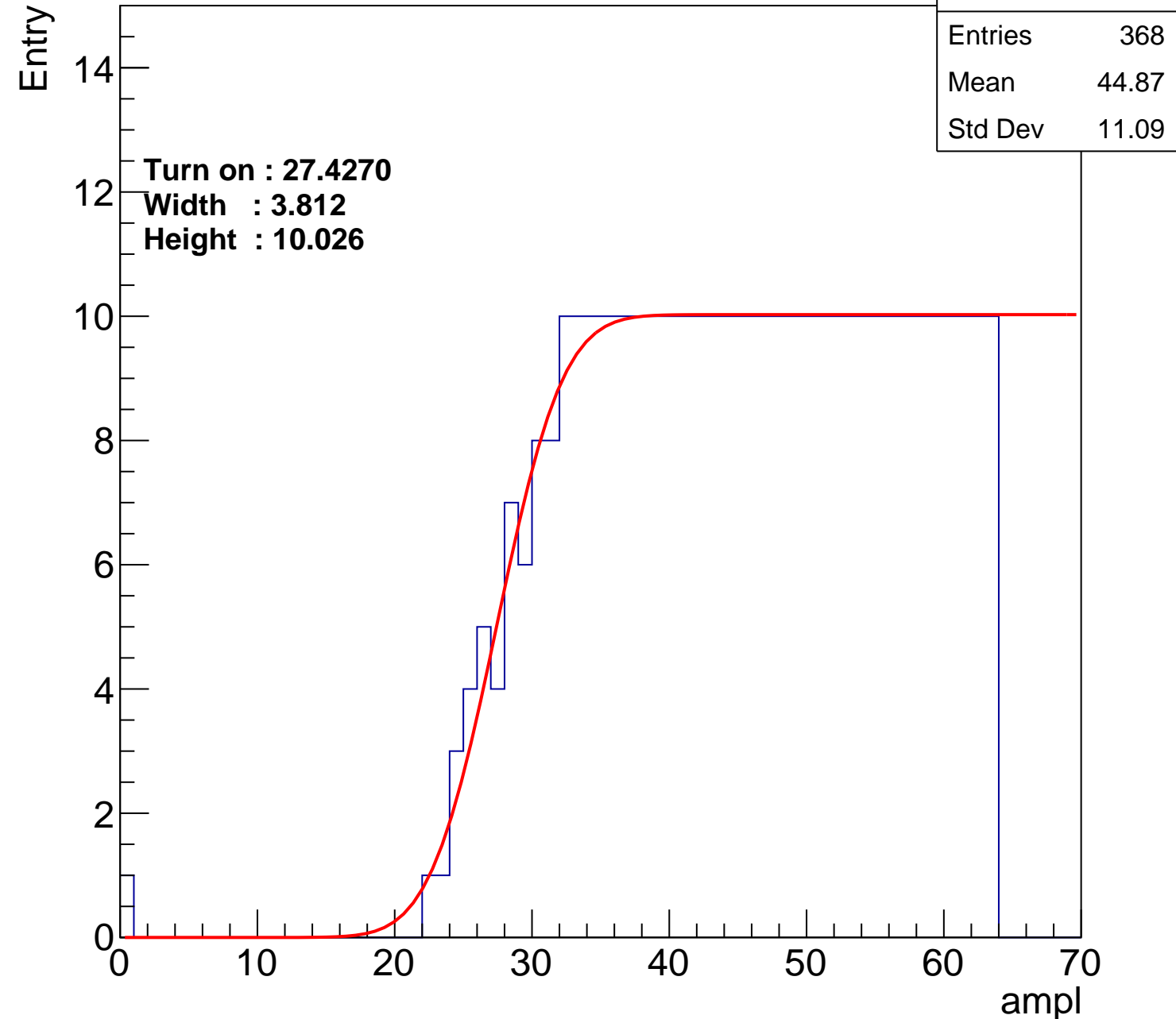
Width : 3.812

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch33

calib_packv5_042523_0143.root, FC#7, port C2

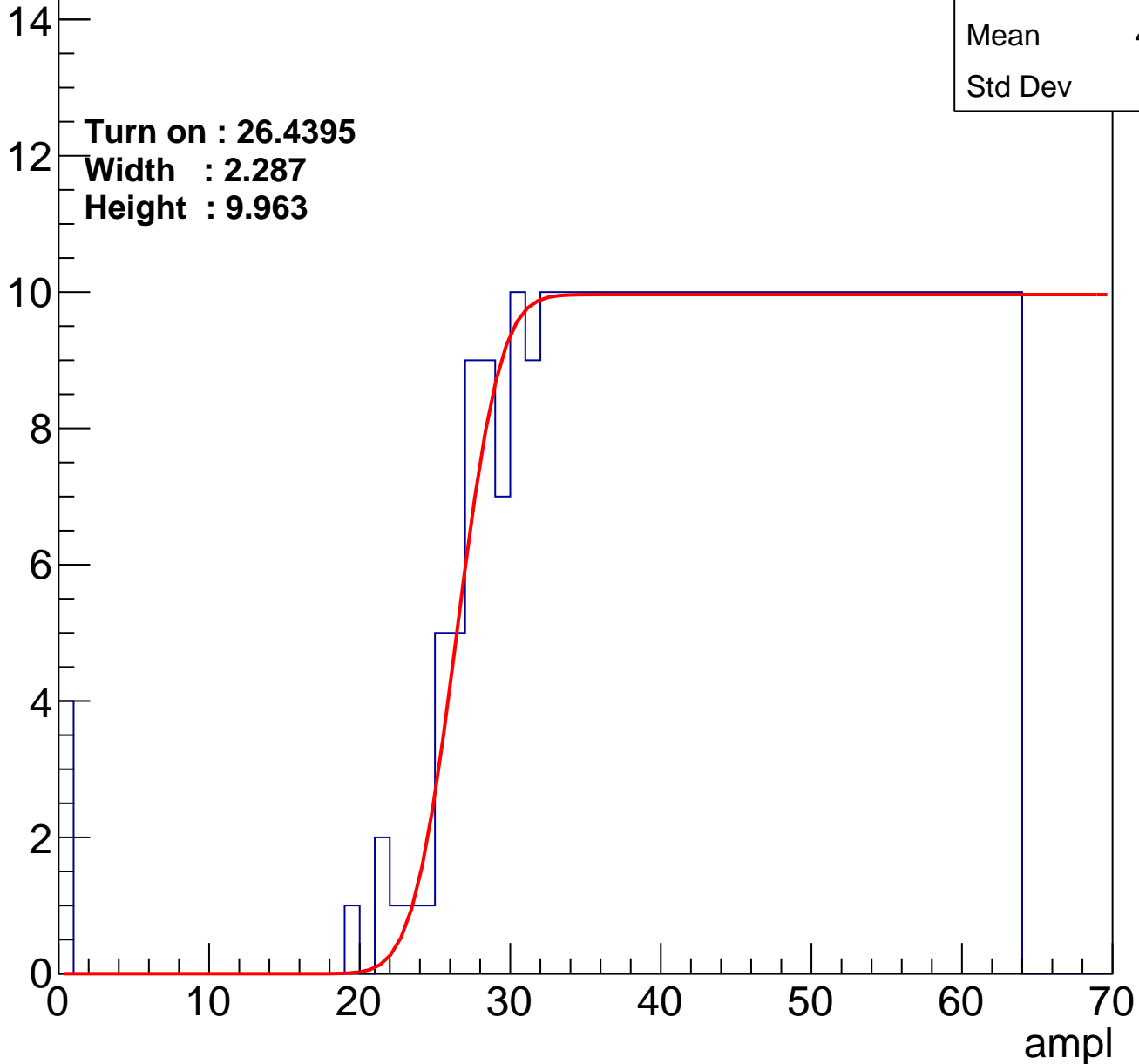
Entries	384
Mean	43.91
Std Dev	12

Turn on : 26.4395

Width : 2.287

Height : 9.963

Entry



B1L103S, U16-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.2415

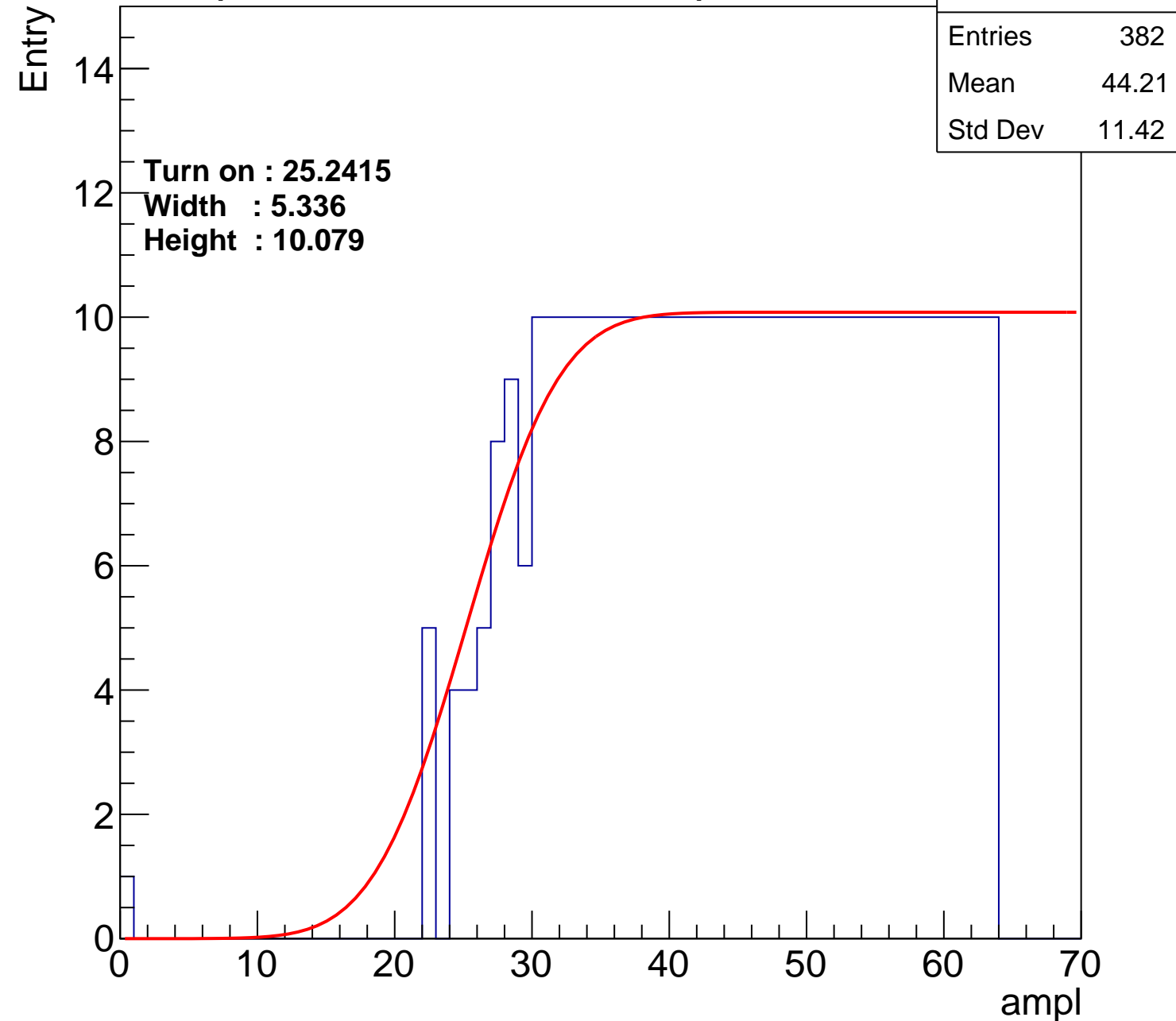
Width : 5.336

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch35

calib_packv5_042523_0143.root, FC#7, port C2

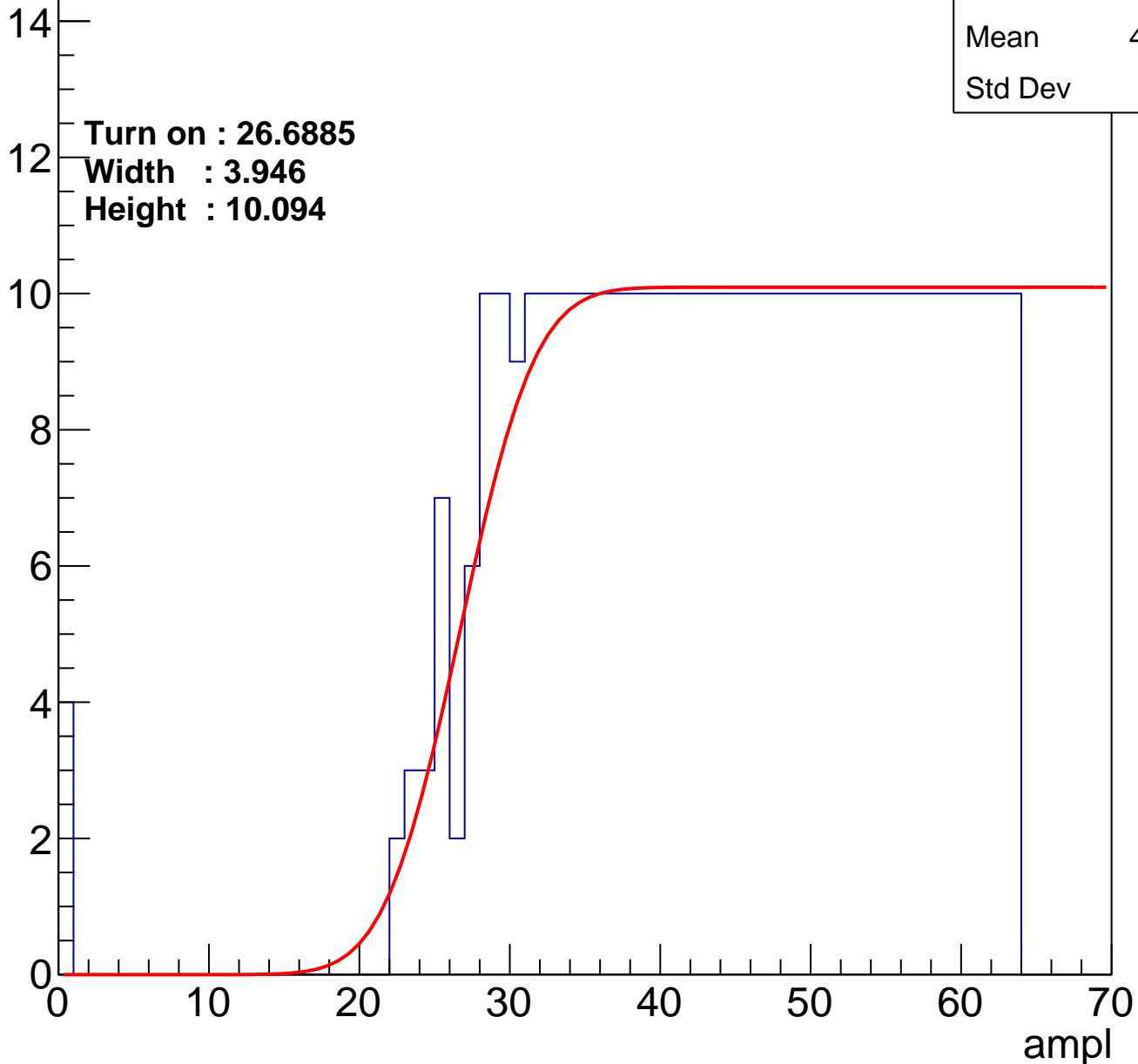
Entries	386
Mean	43.84
Std Dev	12

Turn on : 26.6885

Width : 3.946

Height : 10.094

Entry



B1L103S, U16-ch36

calib_packv5_042523_0143.root, FC#7, port C2

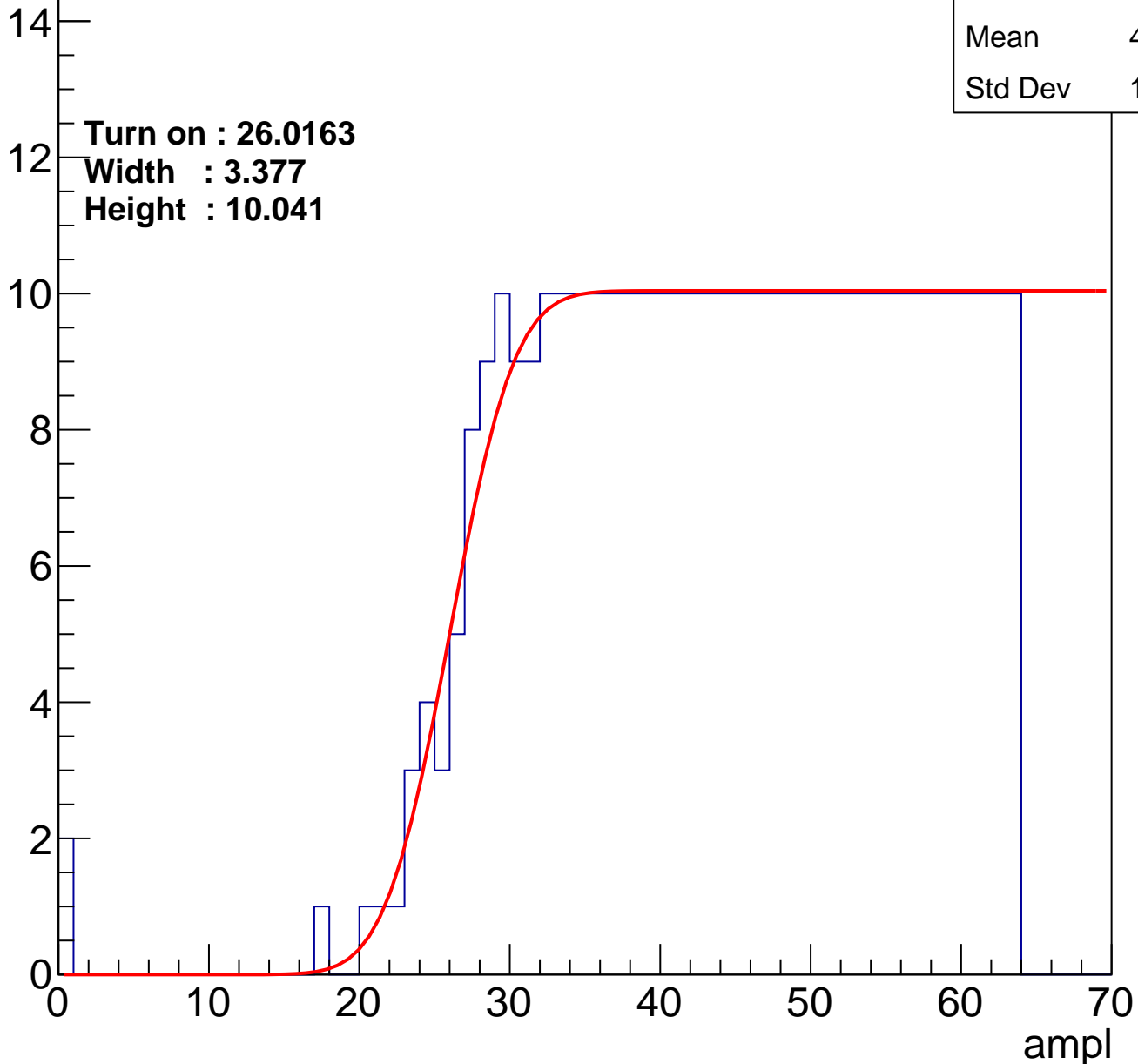
Entries	386
Mean	43.93
Std Dev	11.74

Turn on : 26.0163

Width : 3.377

Height : 10.041

Entry



B1L103S, U16-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.47
Std Dev	10.92

Turn on : 28.9840

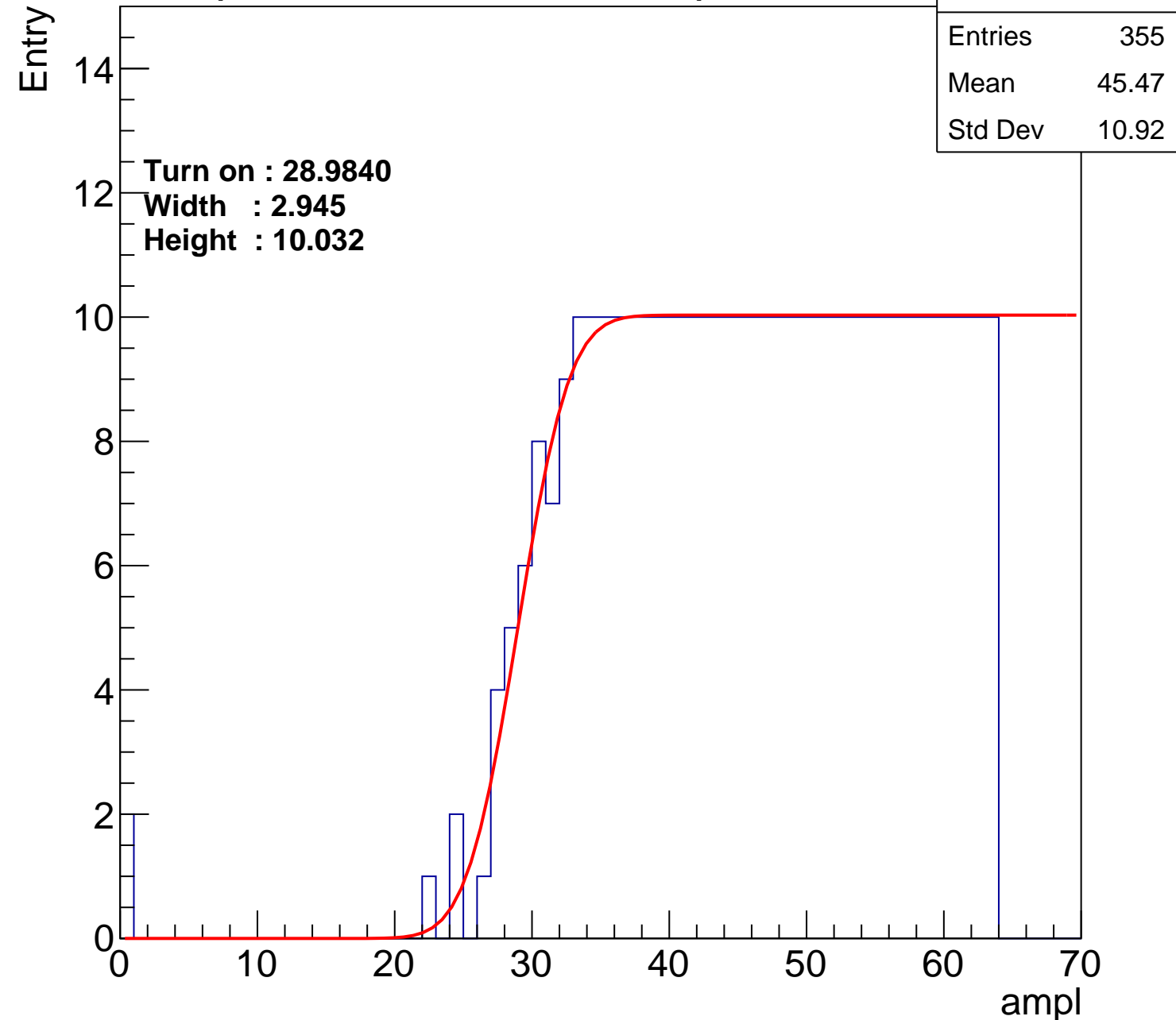
Width : 2.945

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	405
Mean	42.96
Std Dev	12.12

Turn on : 23.8229

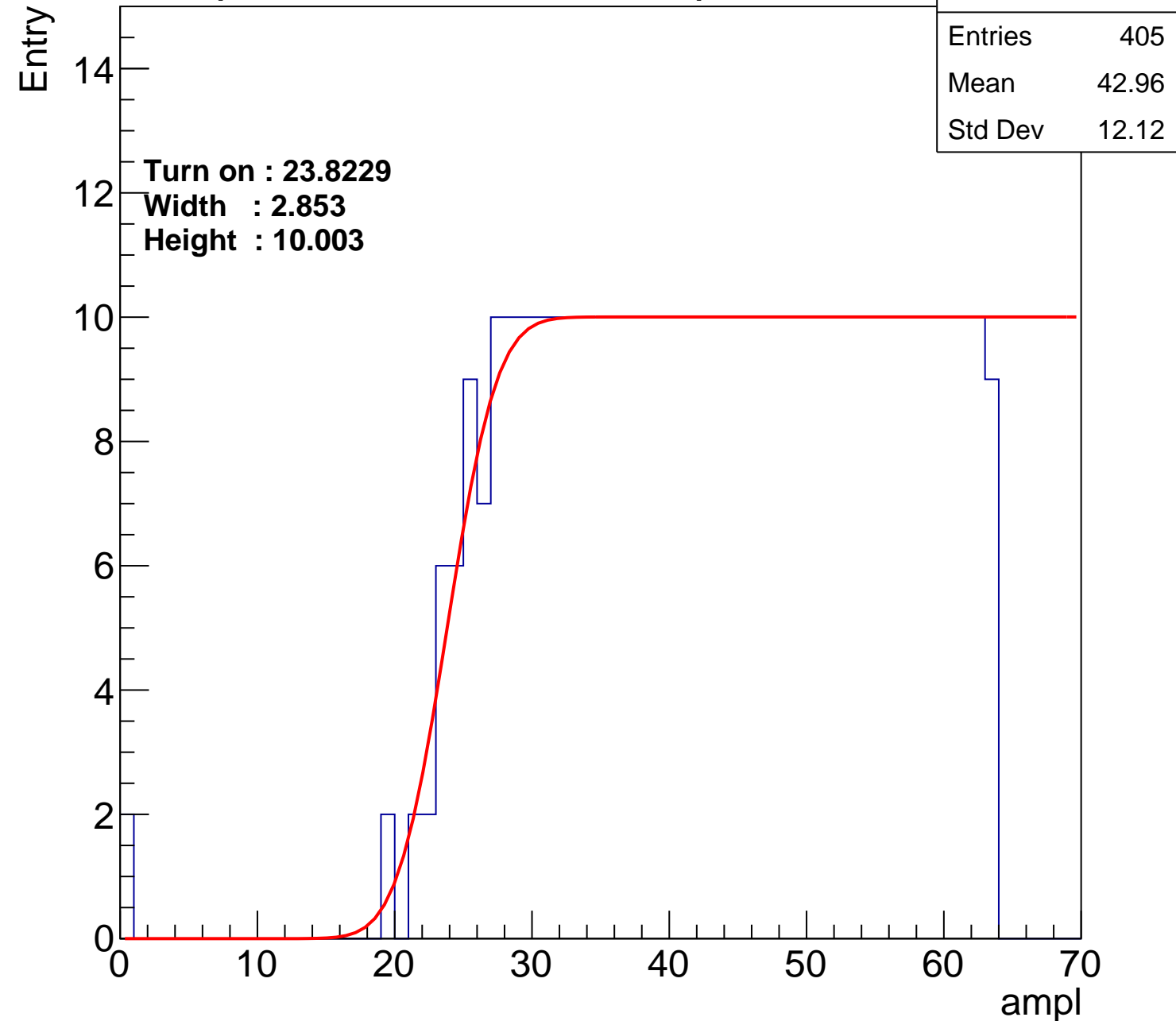
Width : 2.853

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.77
Std Dev	11.57

Turn on : 27.9983

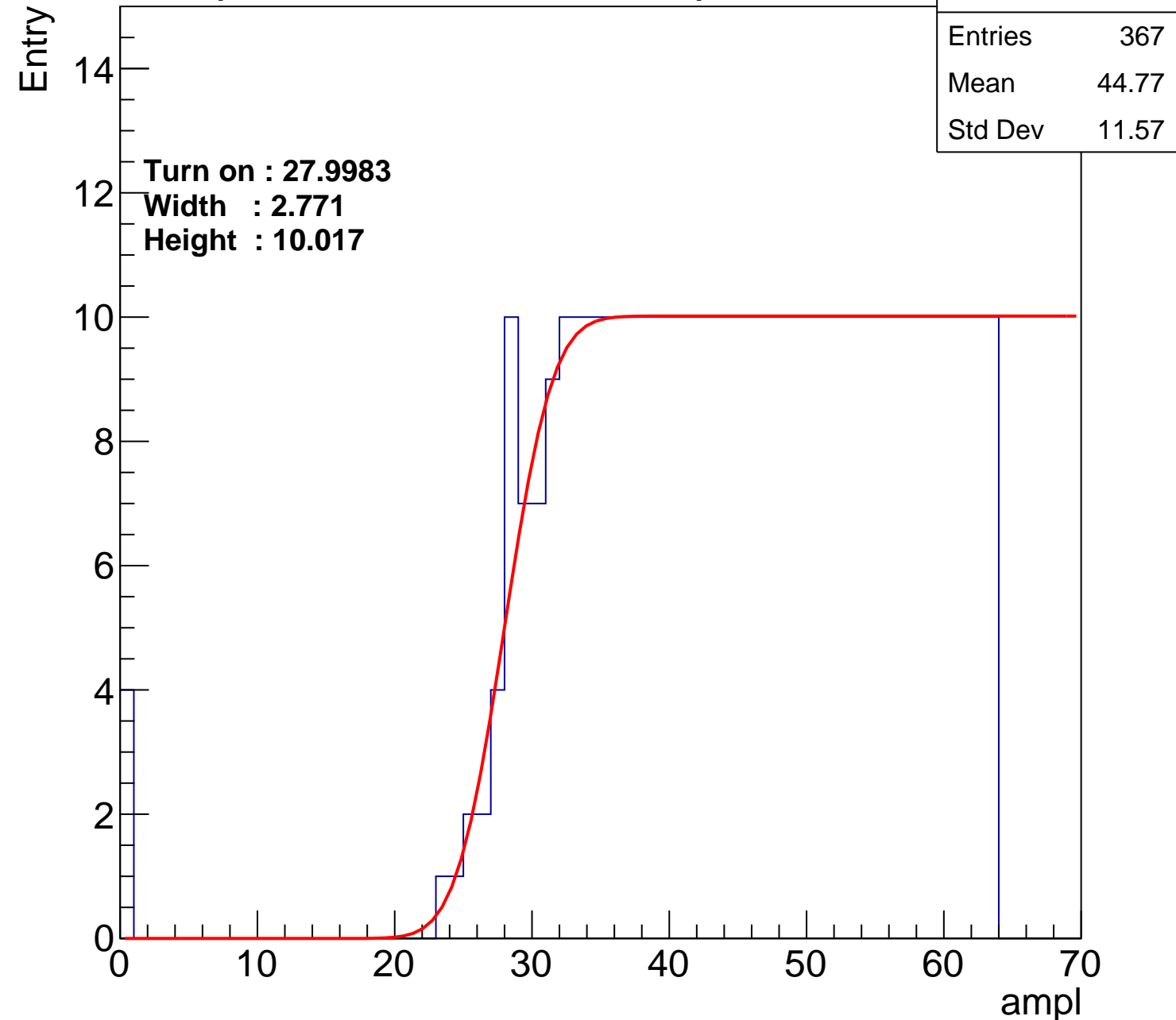
Width : 2.771

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch40

calib_packv5_042523_0143.root, FC#7, port C2

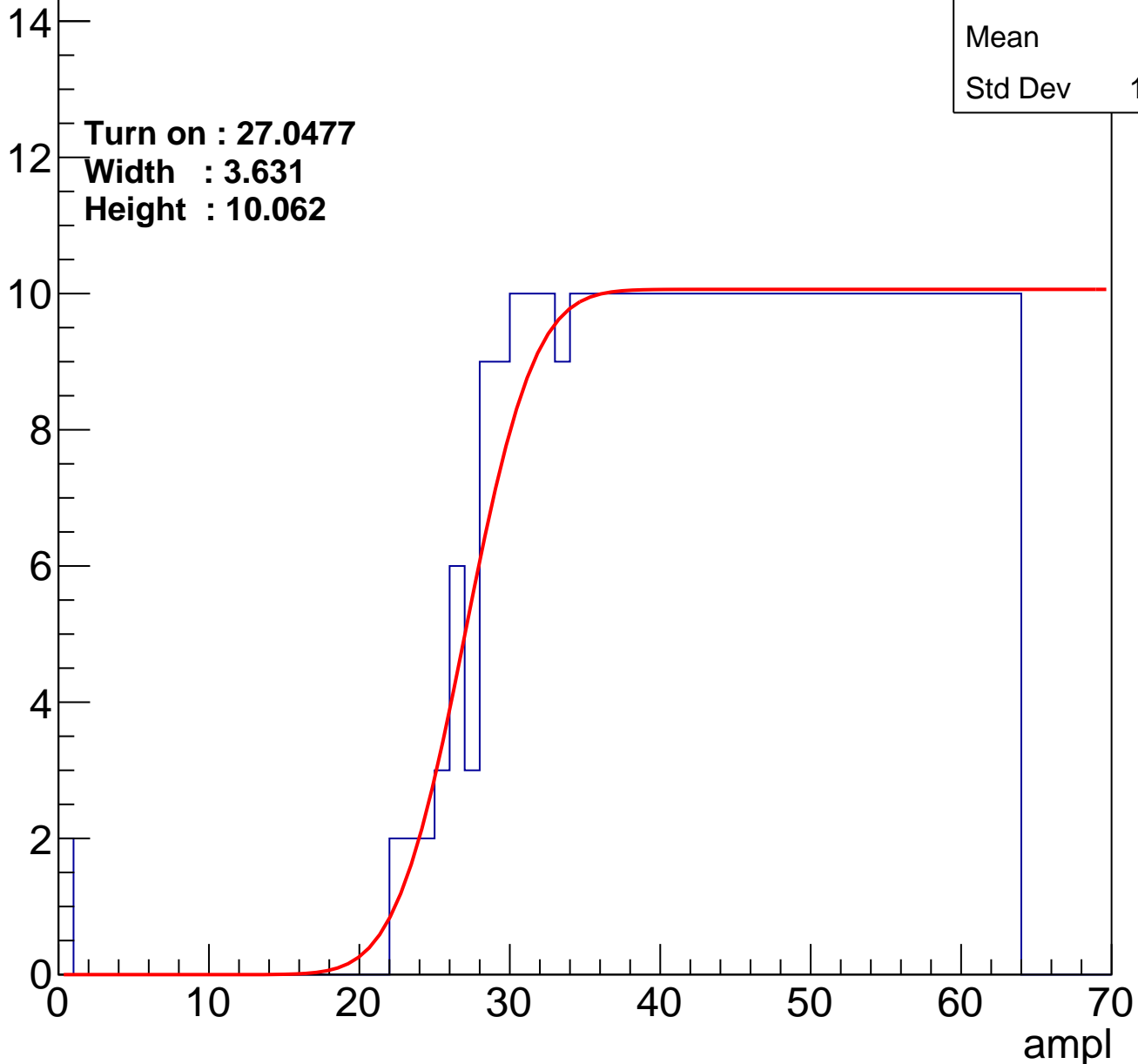
Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 27.0477

Width : 3.631

Height : 10.062

Entry



B1L103S, U16-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.74
Std Dev	11.56

Turn on : 27.5507

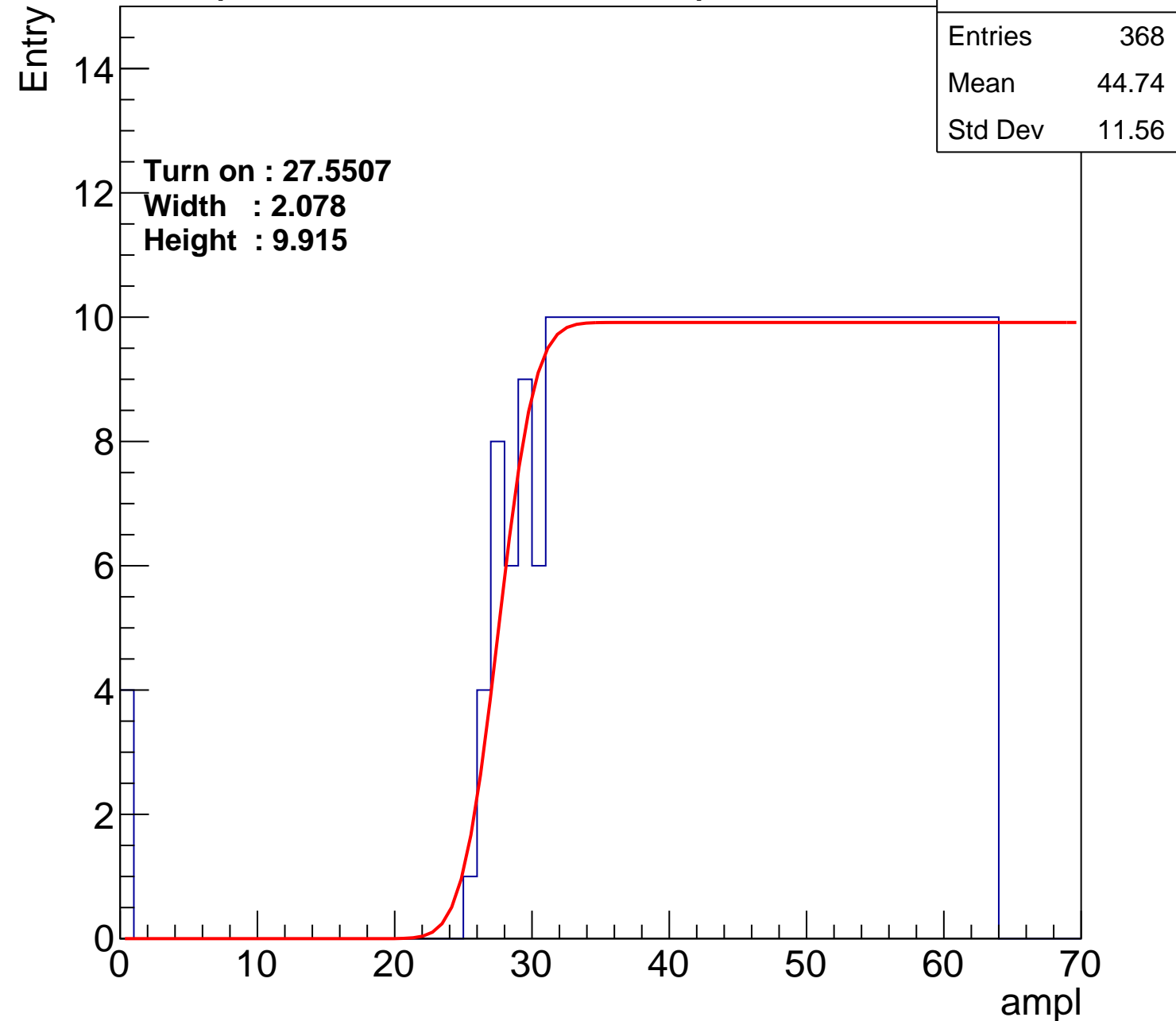
Width : 2.078

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch42

calib_packv5_042523_0143.root, FC#7, port C2

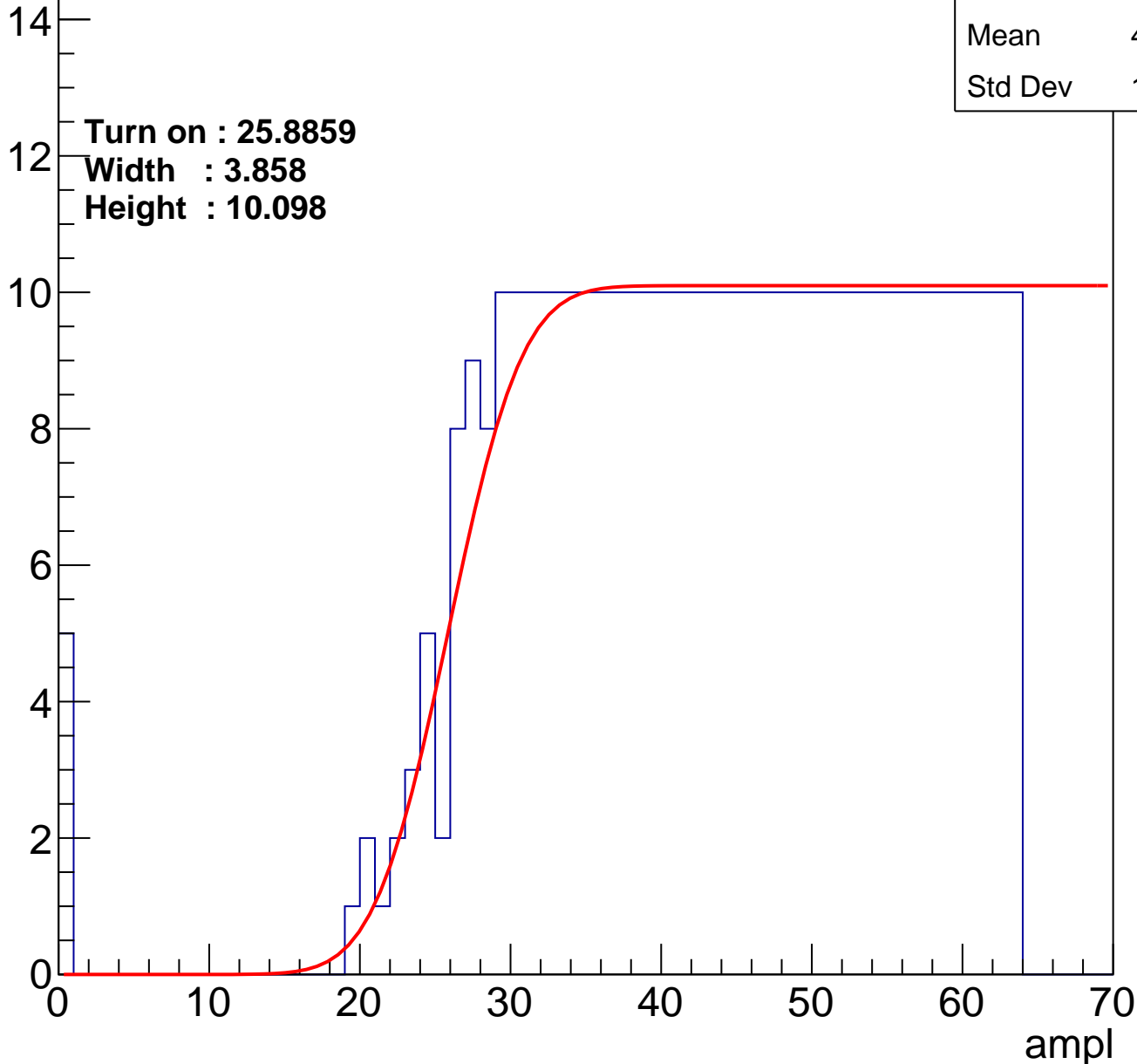
Entries	396
Mean	43.28
Std Dev	12.42

Turn on : 25.8859

Width : 3.858

Height : 10.098

Entry



B1L103S, U16-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45
Std Dev	11.27

Turn on : 28.3214

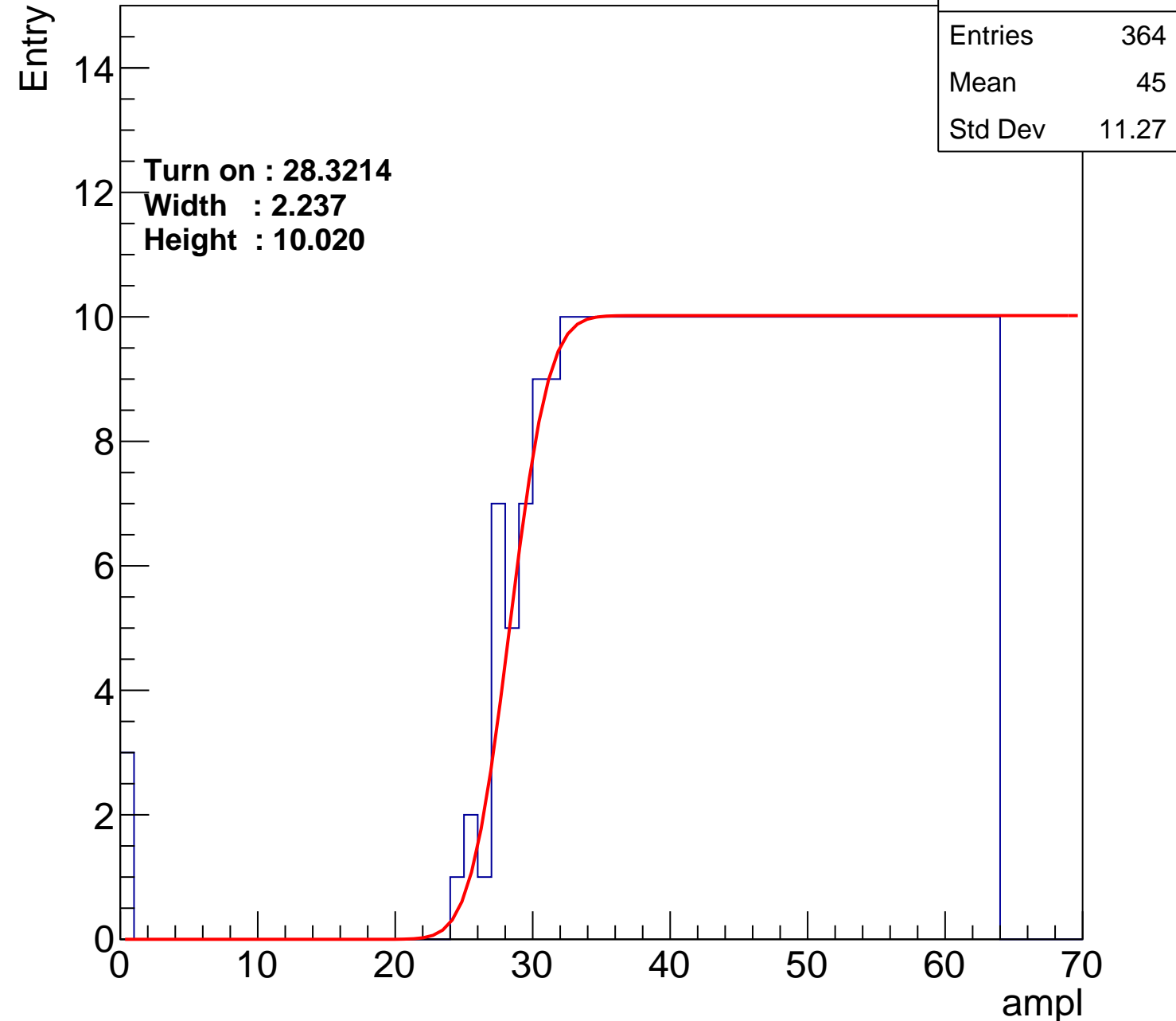
Width : 2.237

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch44

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.78
Std Dev	11.82

Turn on : 25.5128

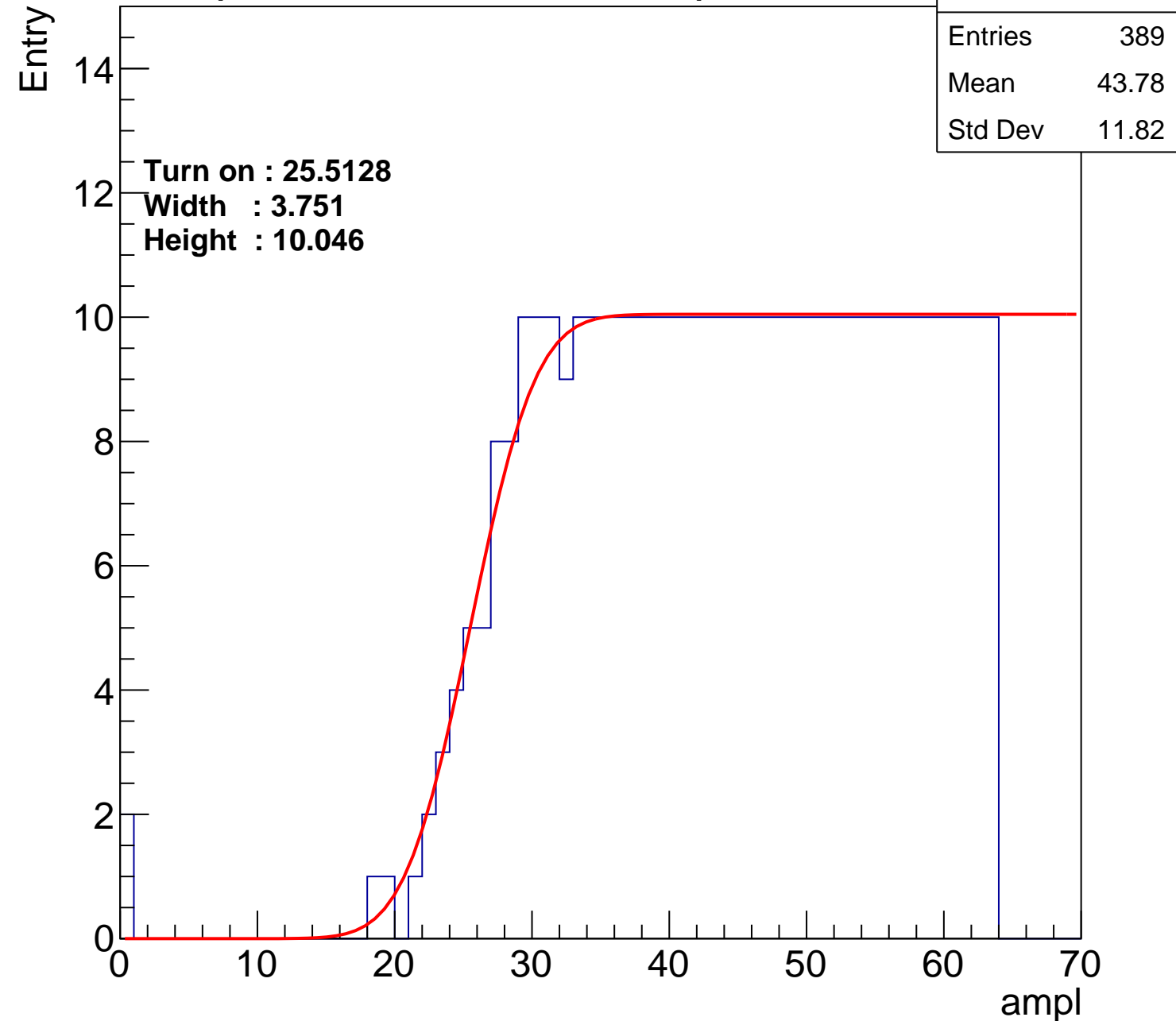
Width : 3.751

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.98
Std Dev	11.17

Turn on : 28.1780

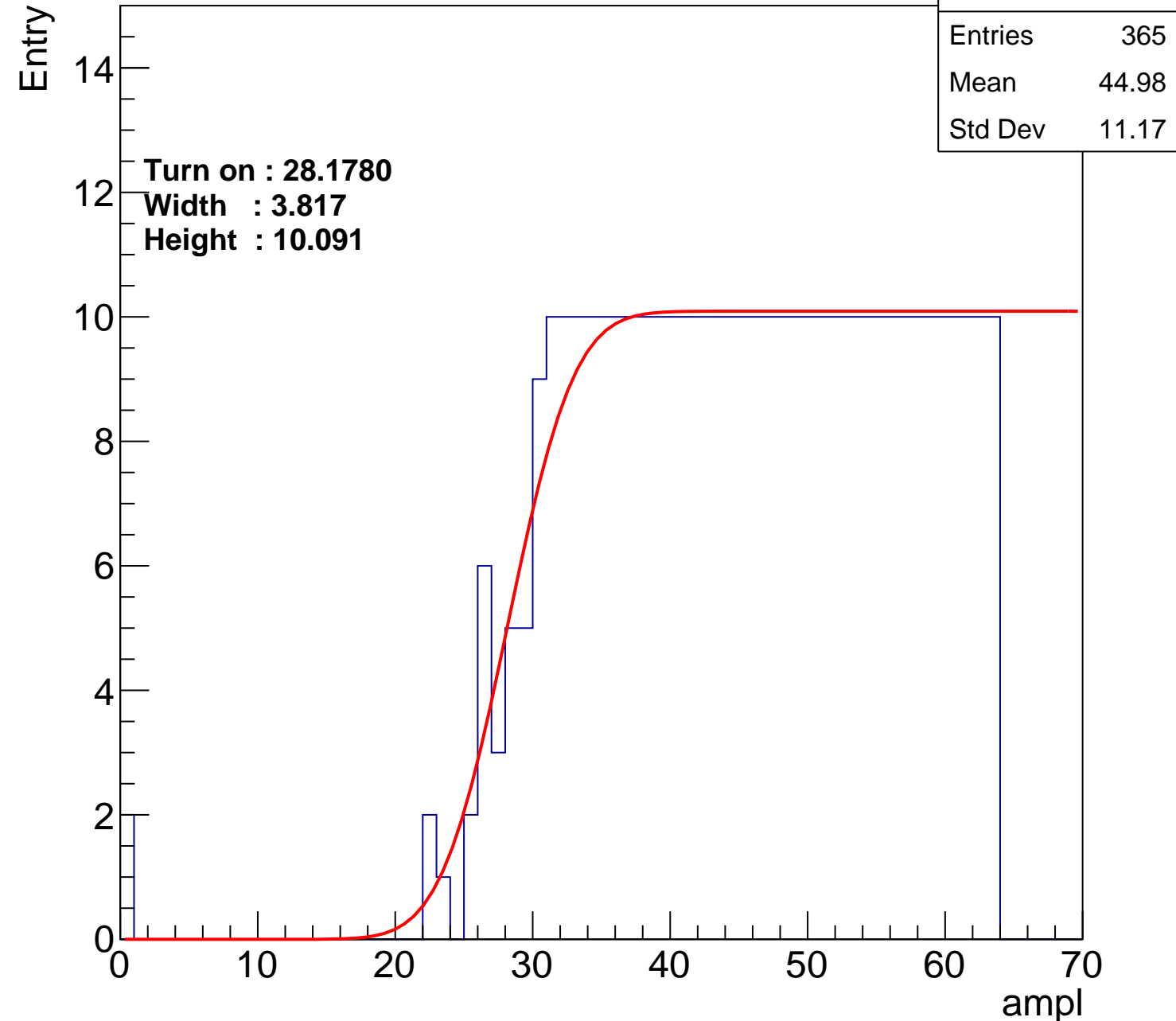
Width : 3.817

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch46

calib_packv5_042523_0143.root, FC#7, port C2

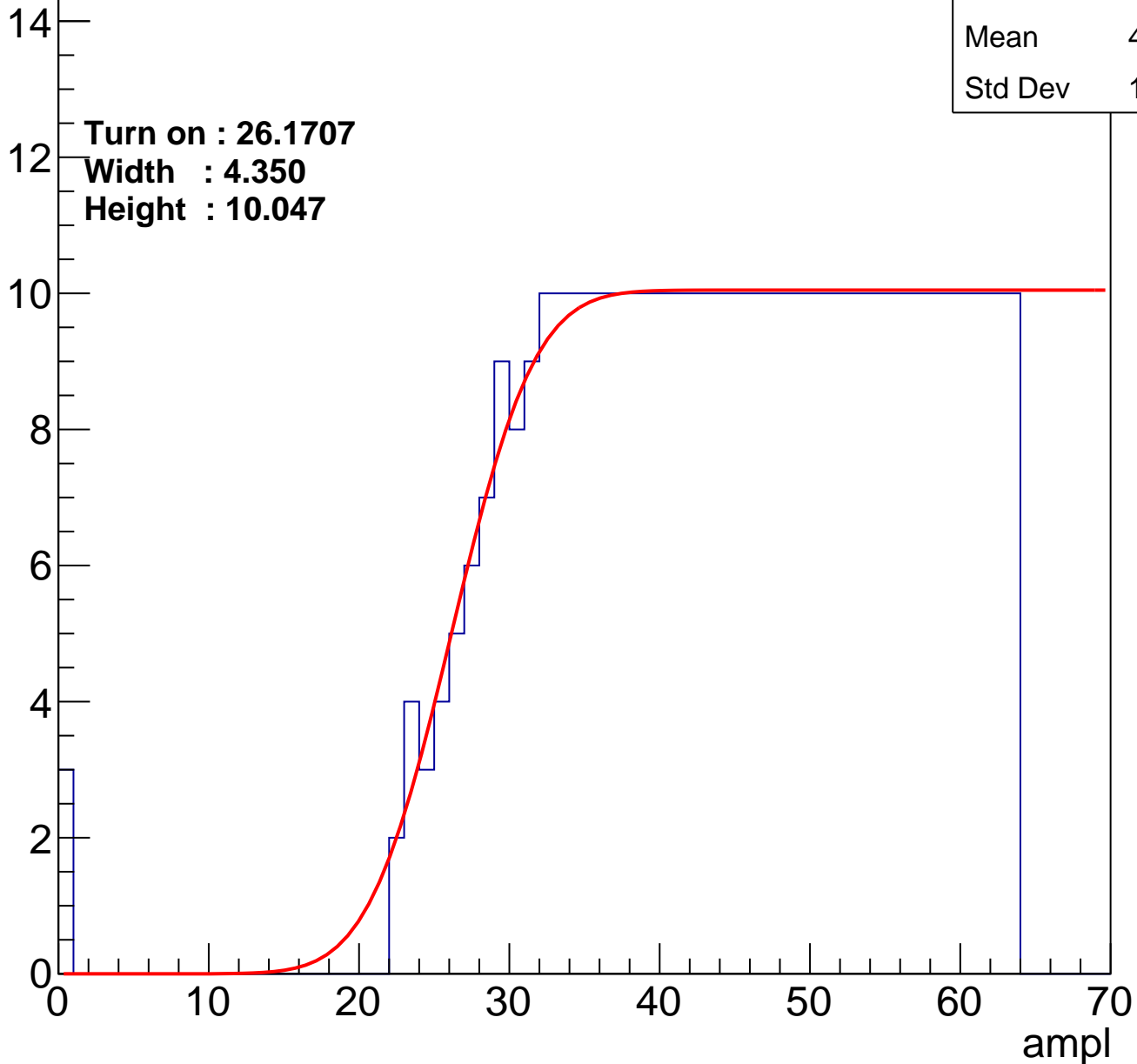
Entries	380
Mean	44.15
Std Dev	11.77

Turn on : 26.1707

Width : 4.350

Height : 10.047

Entry



B1L103S, U16-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	406
Mean	42.96
Std Dev	12.29

Turn on : 23.9247

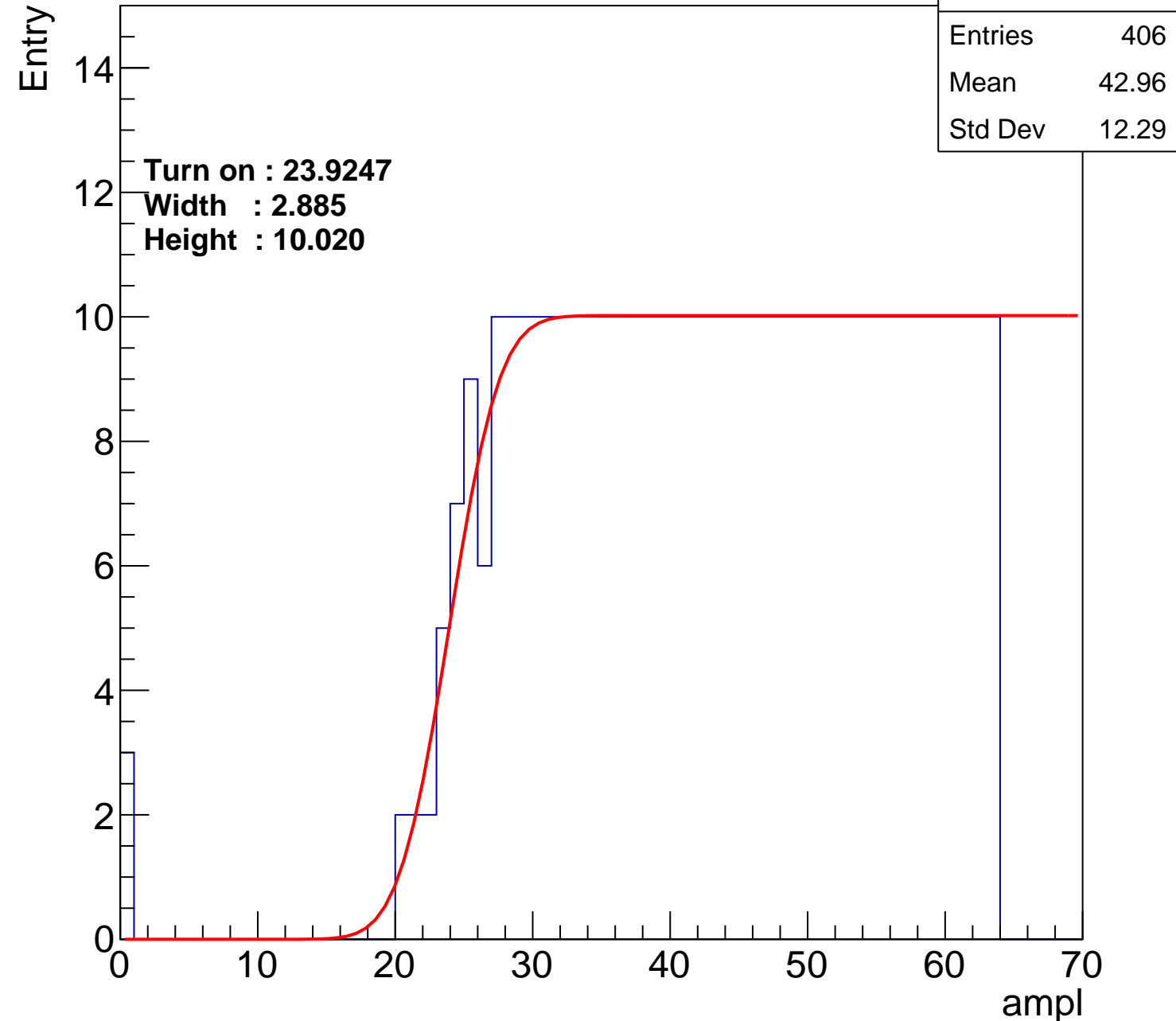
Width : 2.885

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.55
Std Dev	12.03

Turn on : 25.6581

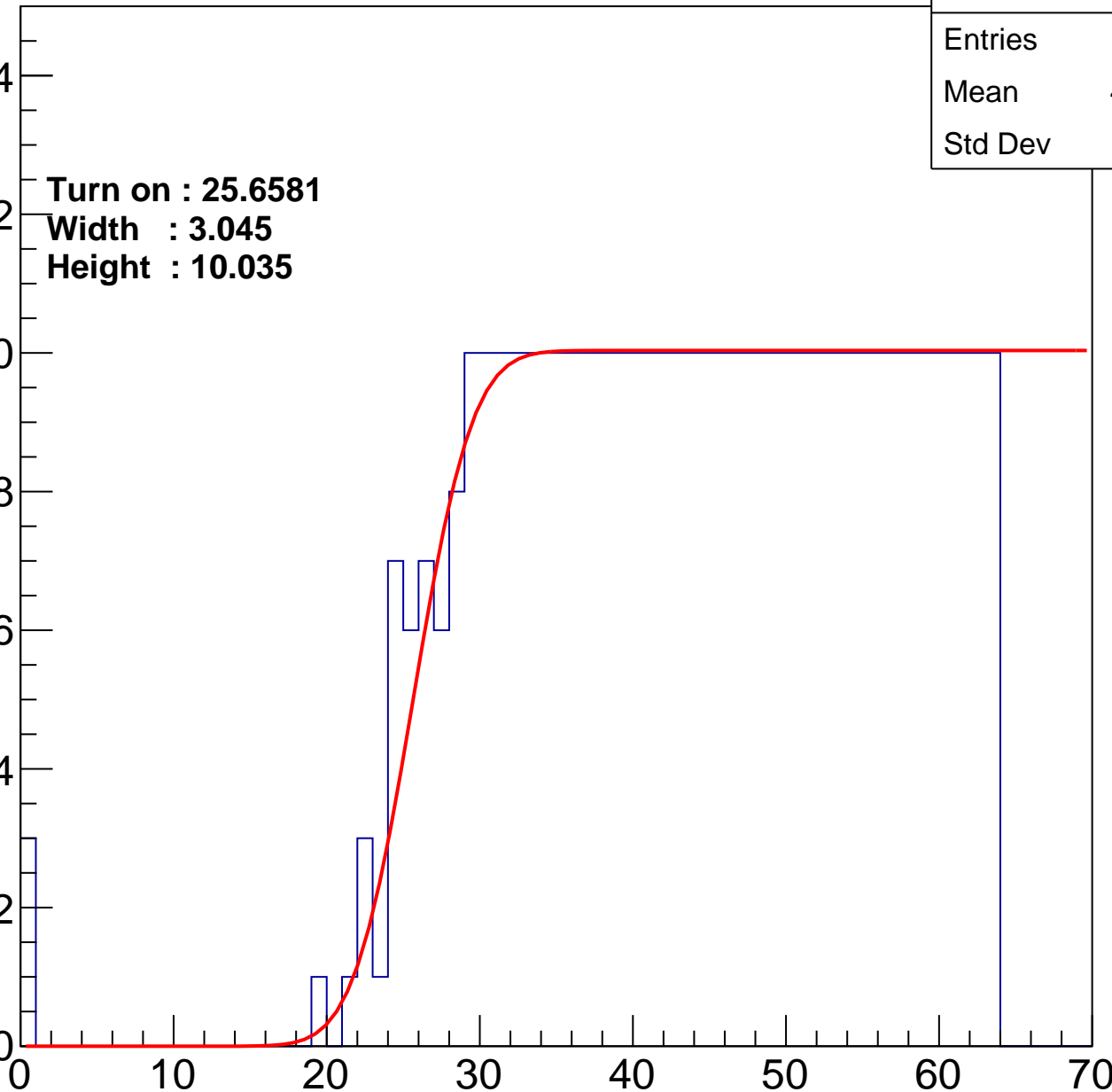
Width : 3.045

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch49

calib_packv5_042523_0143.root, FC#7, port C2

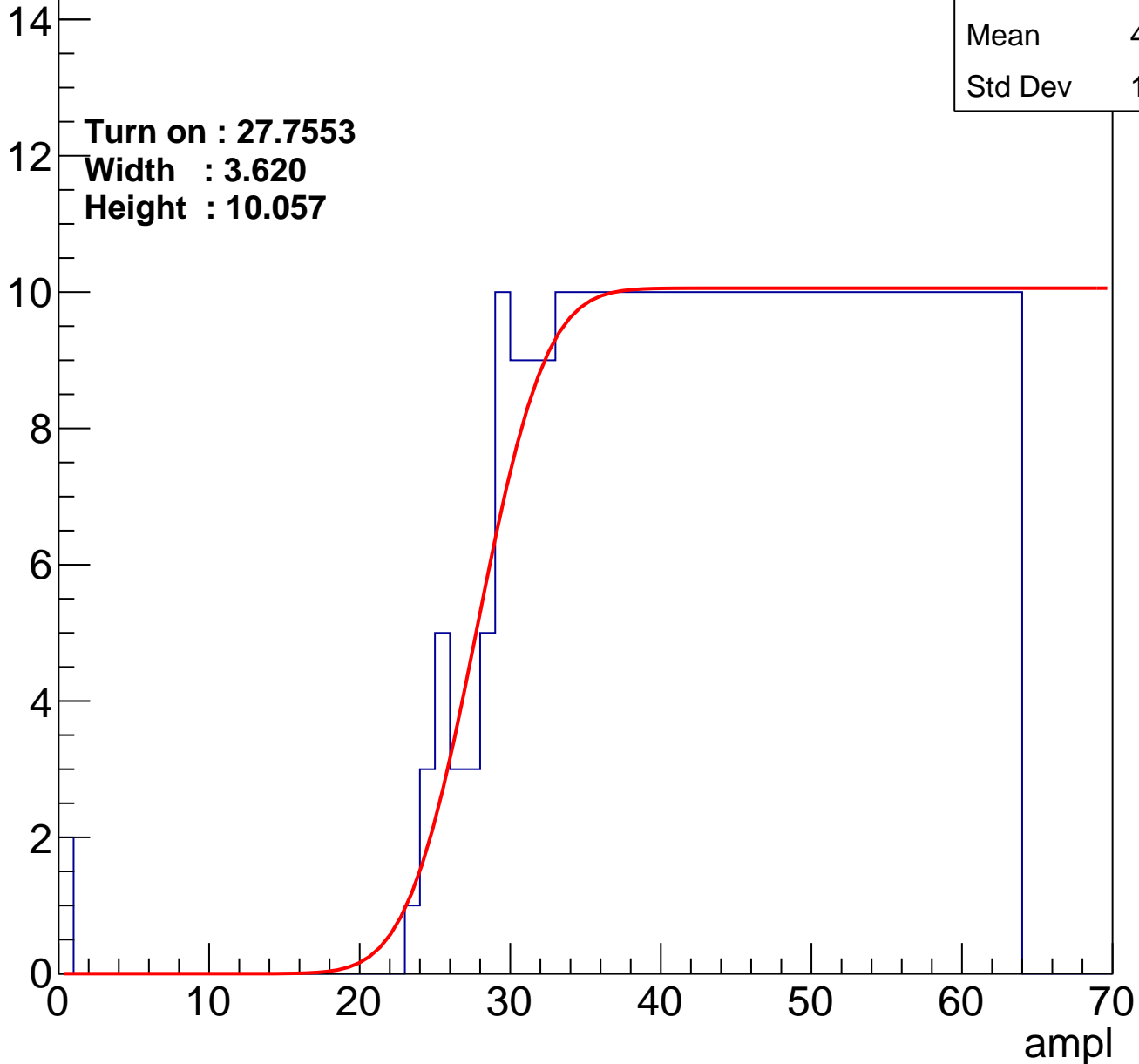
Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.7553

Width : 3.620

Height : 10.057

Entry



B1L103S, U16-ch50

calib_packv5_042523_0143.root, FC#7, port C2

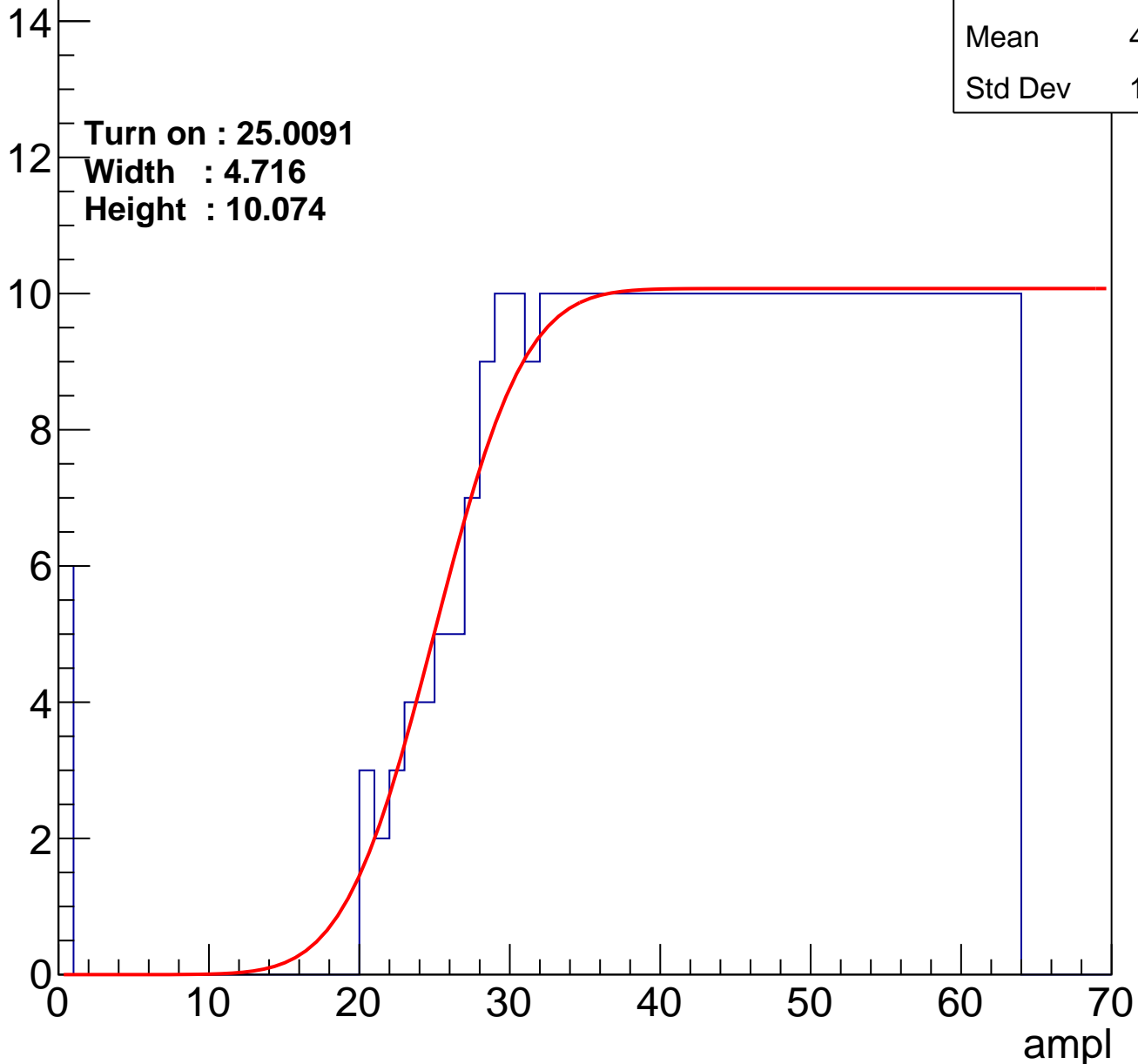
Entries	397
Mean	43.13
Std Dev	12.66

Turn on : 25.0091

Width : 4.716

Height : 10.074

Entry



B1L103S, U16-ch51

calib_packv5_042523_0143.root, FC#7, port C2

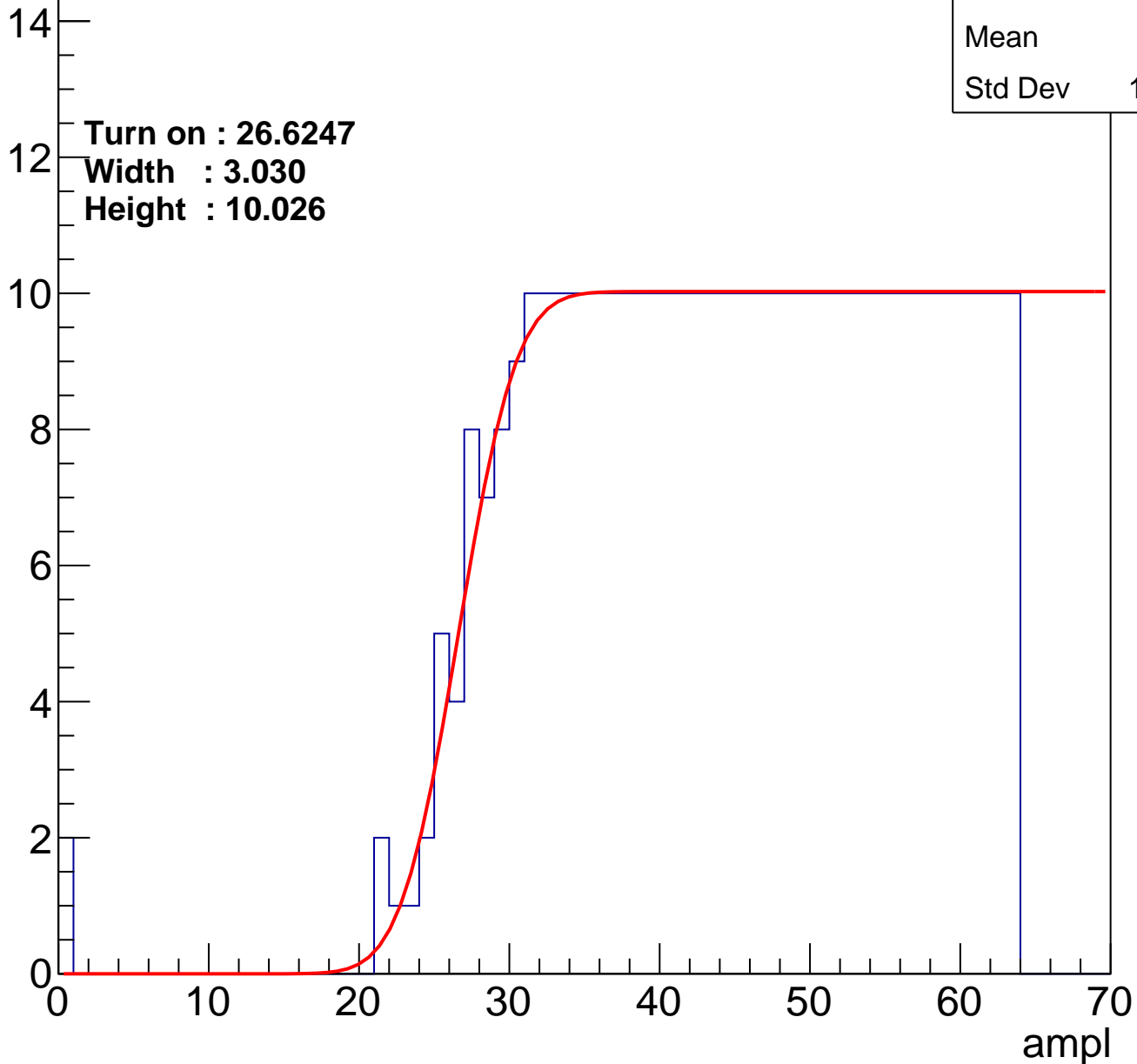
Entries	379
Mean	44.3
Std Dev	11.52

Turn on : 26.6247

Width : 3.030

Height : 10.026

Entry



B1L103S, U16-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.37
Std Dev	12.15

Turn on : 25.0139

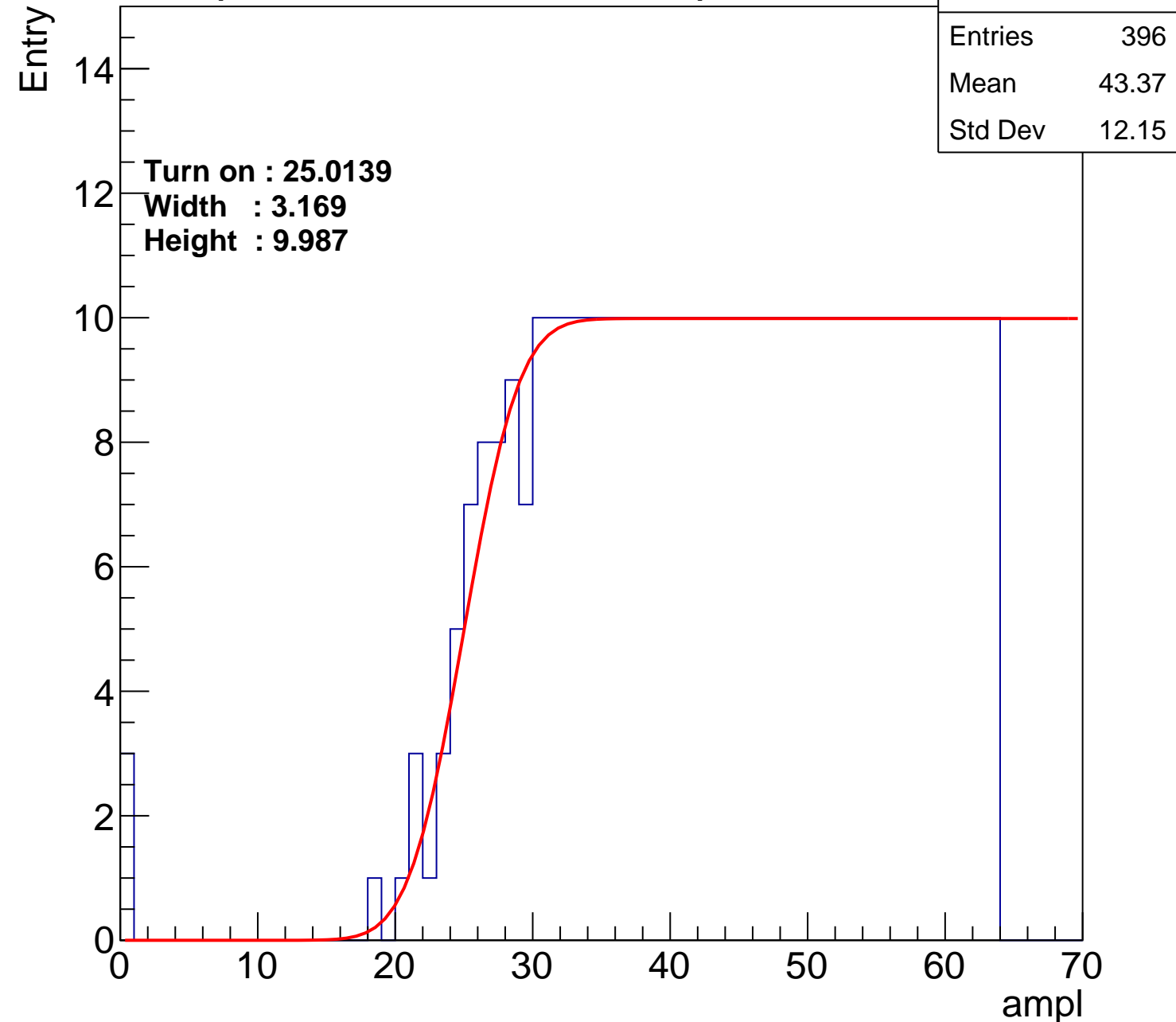
Width : 3.169

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.8066

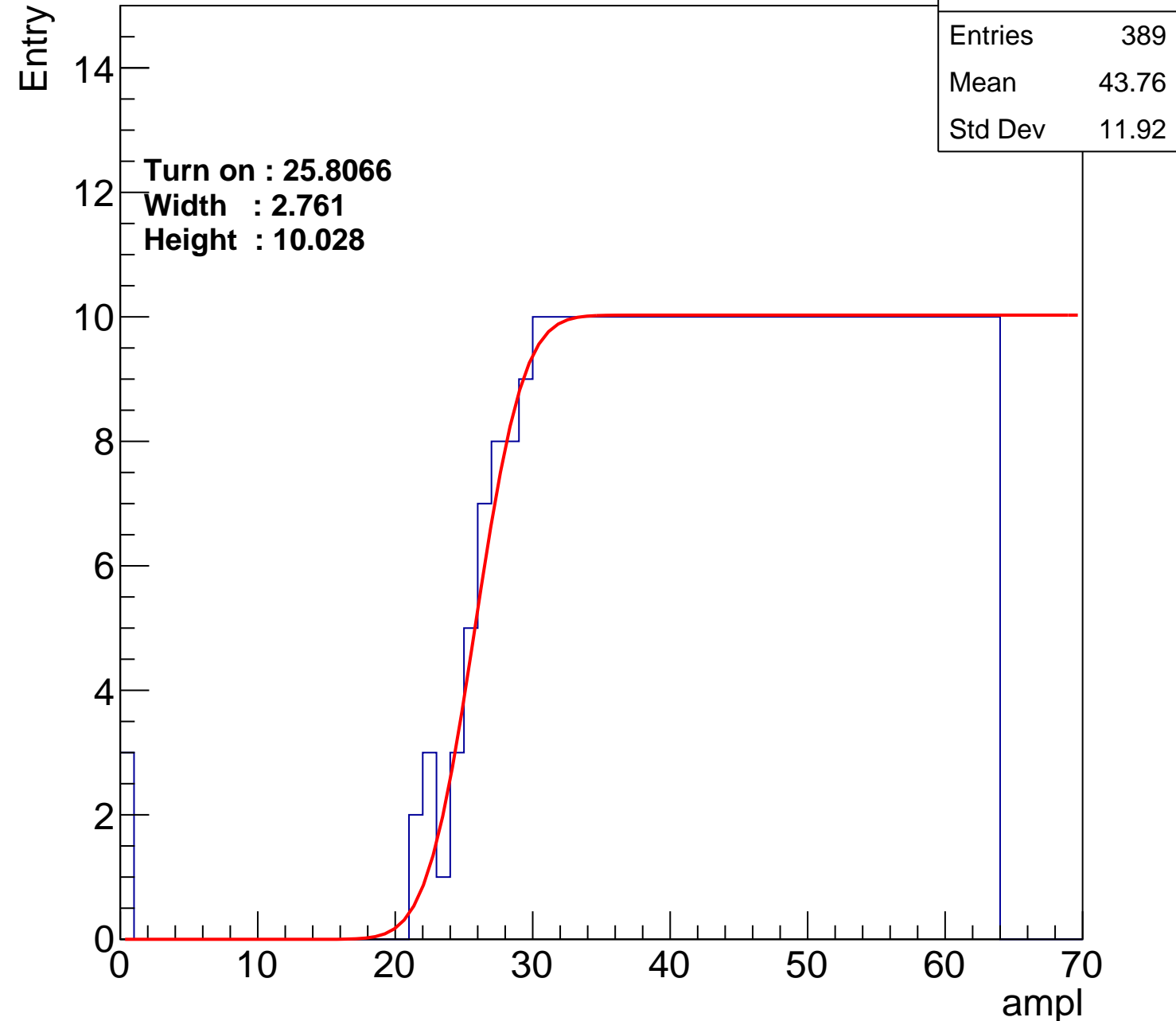
Width : 2.761

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.6515

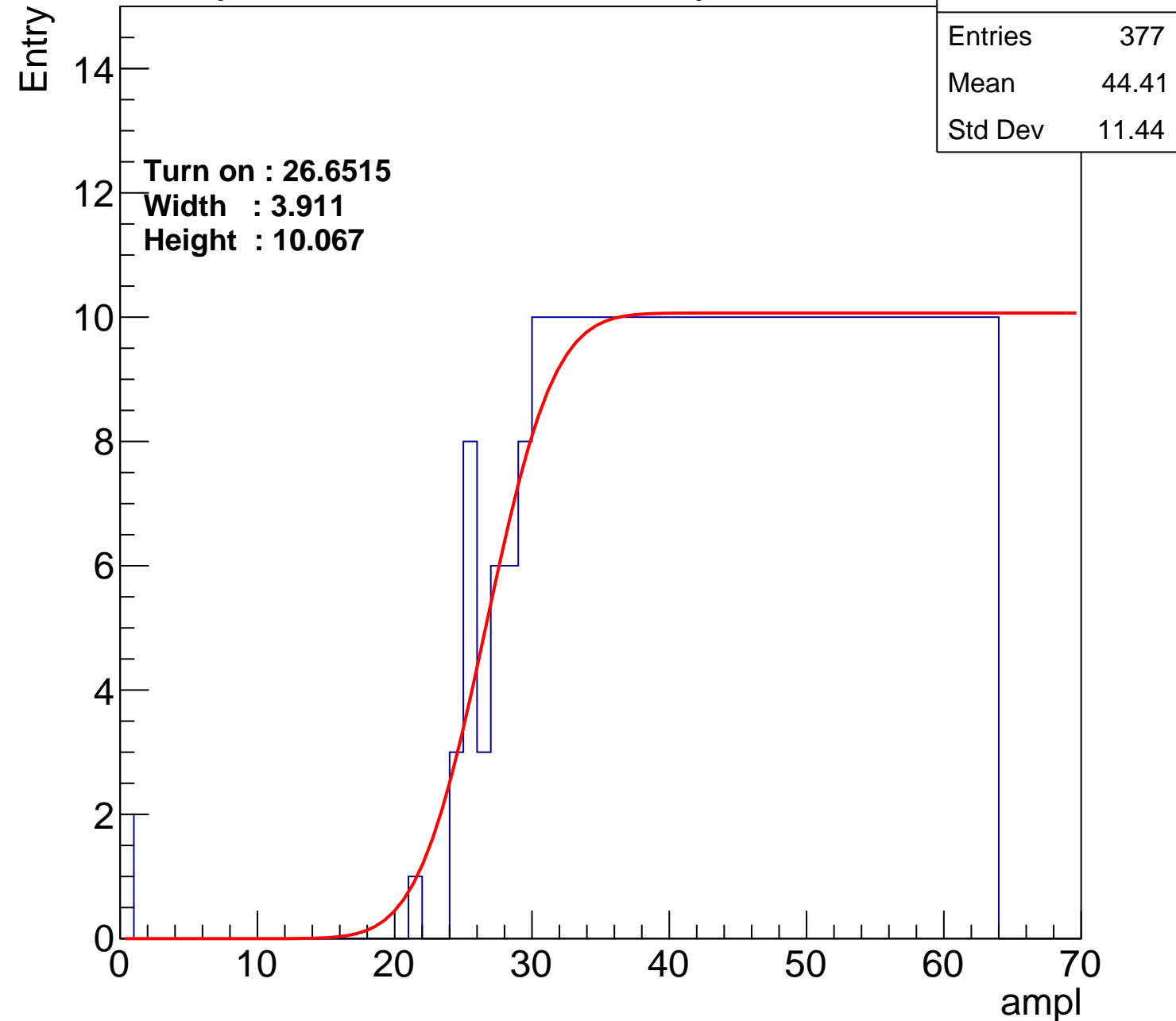
Width : 3.911

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.21
Std Dev	11.28

Turn on : 28.7224

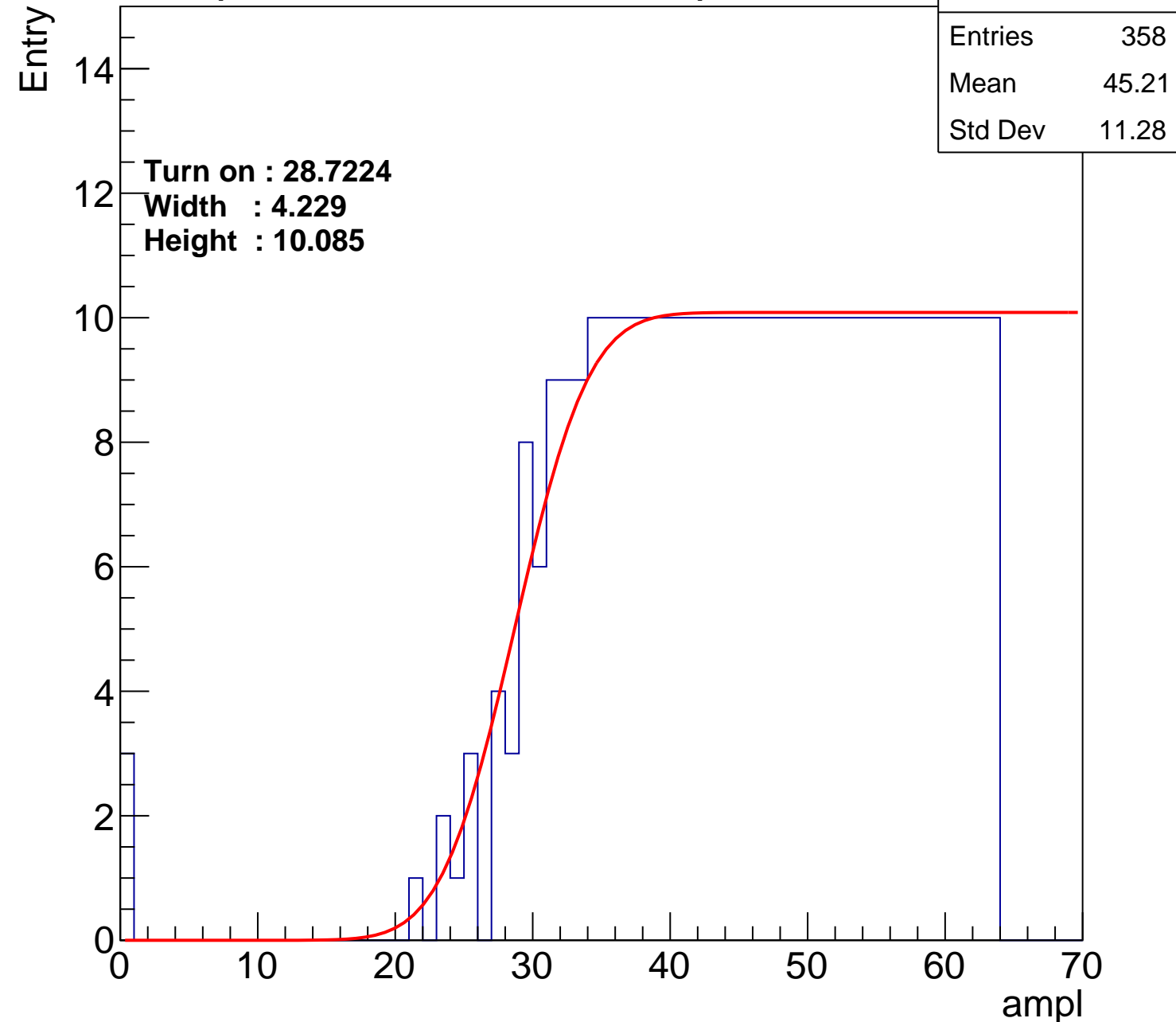
Width : 4.229

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch56

calib_packv5_042523_0143.root, FC#7, port C2

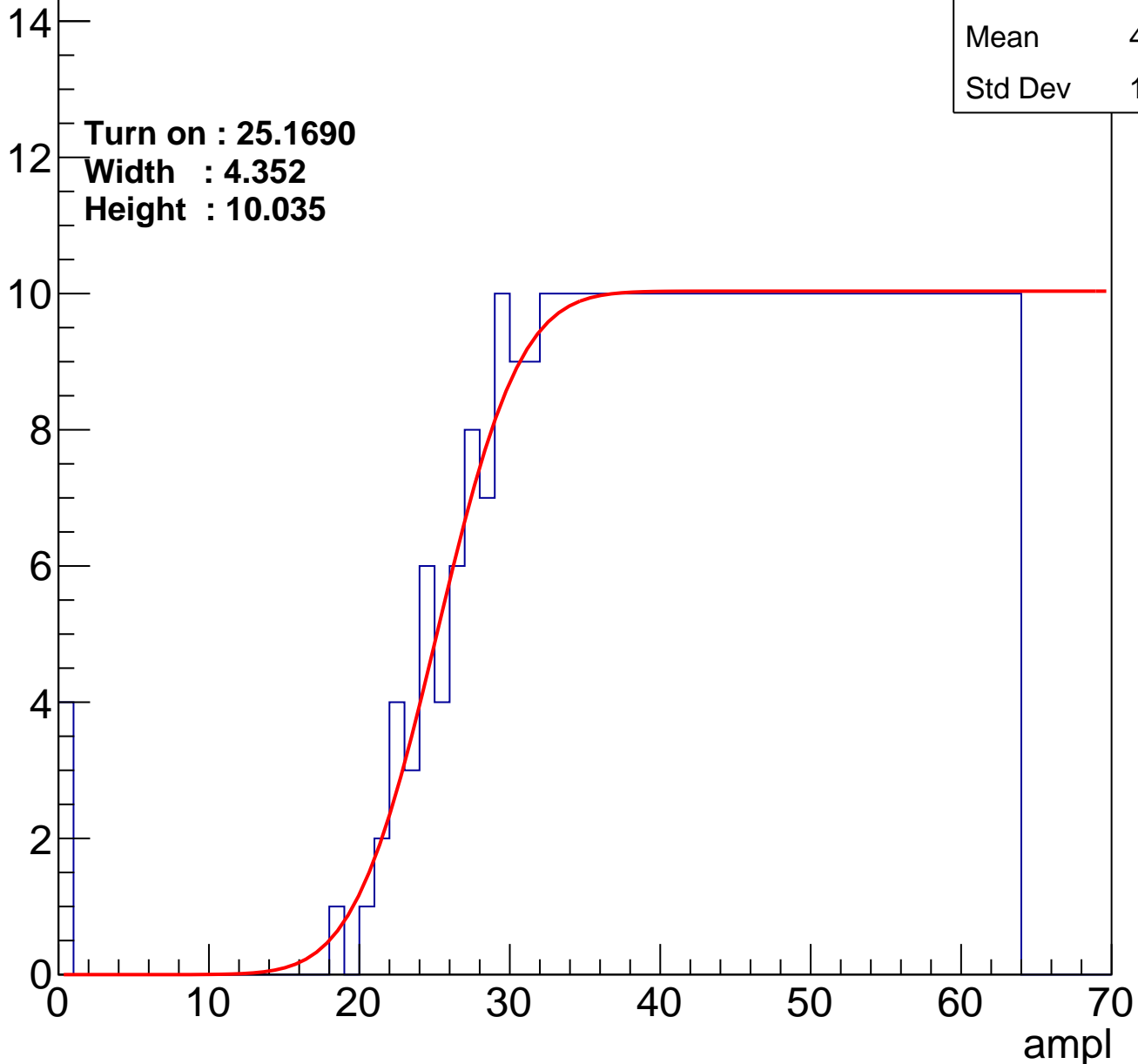
Entries	394
Mean	43.37
Std Dev	12.32

Turn on : 25.1690

Width : 4.352

Height : 10.035

Entry



B1L103S, U16-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.58
Std Dev	11.06

Turn on : 29.7873

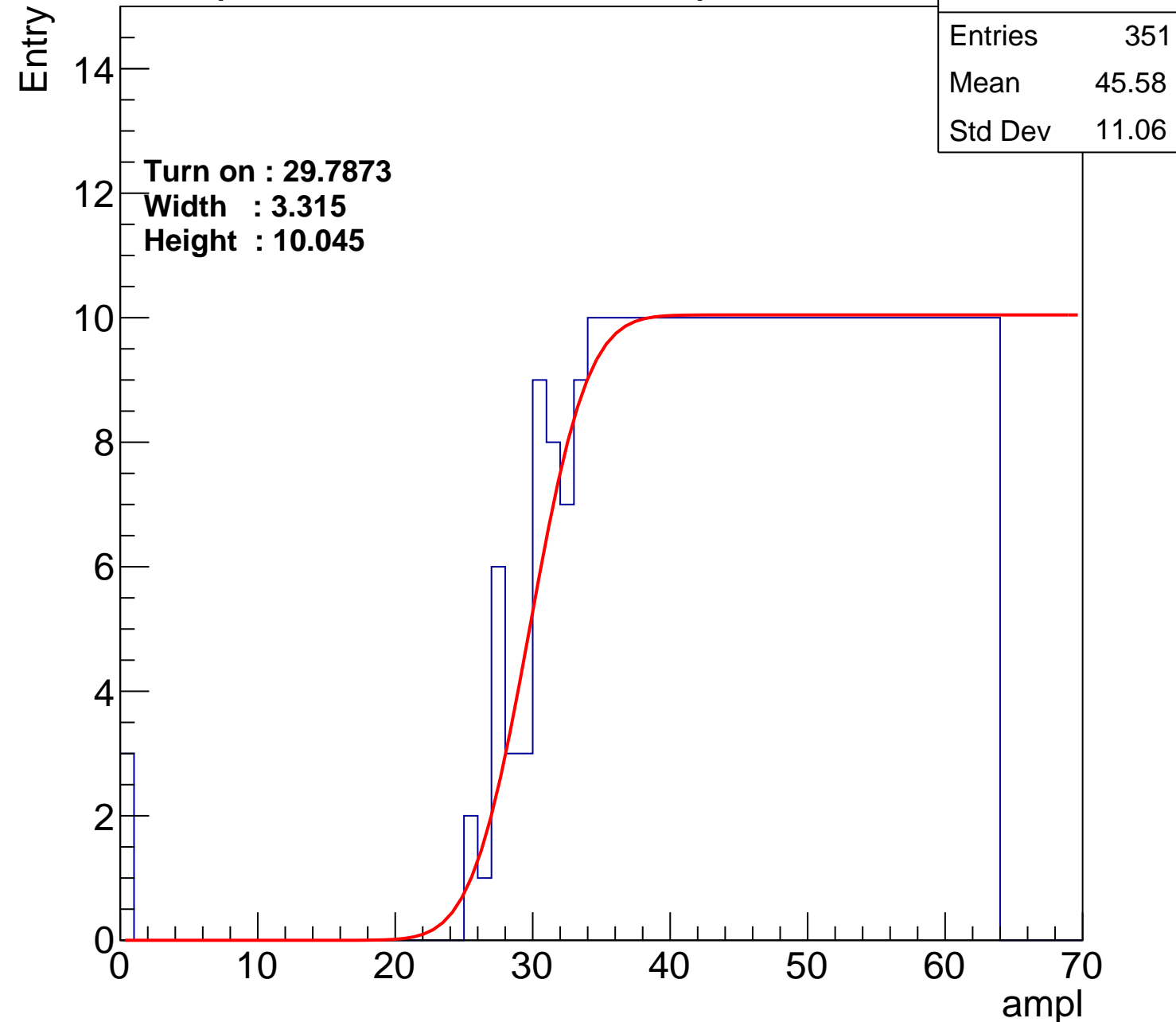
Width : 3.315

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.42
Std Dev	12.09

Turn on : 25.1162

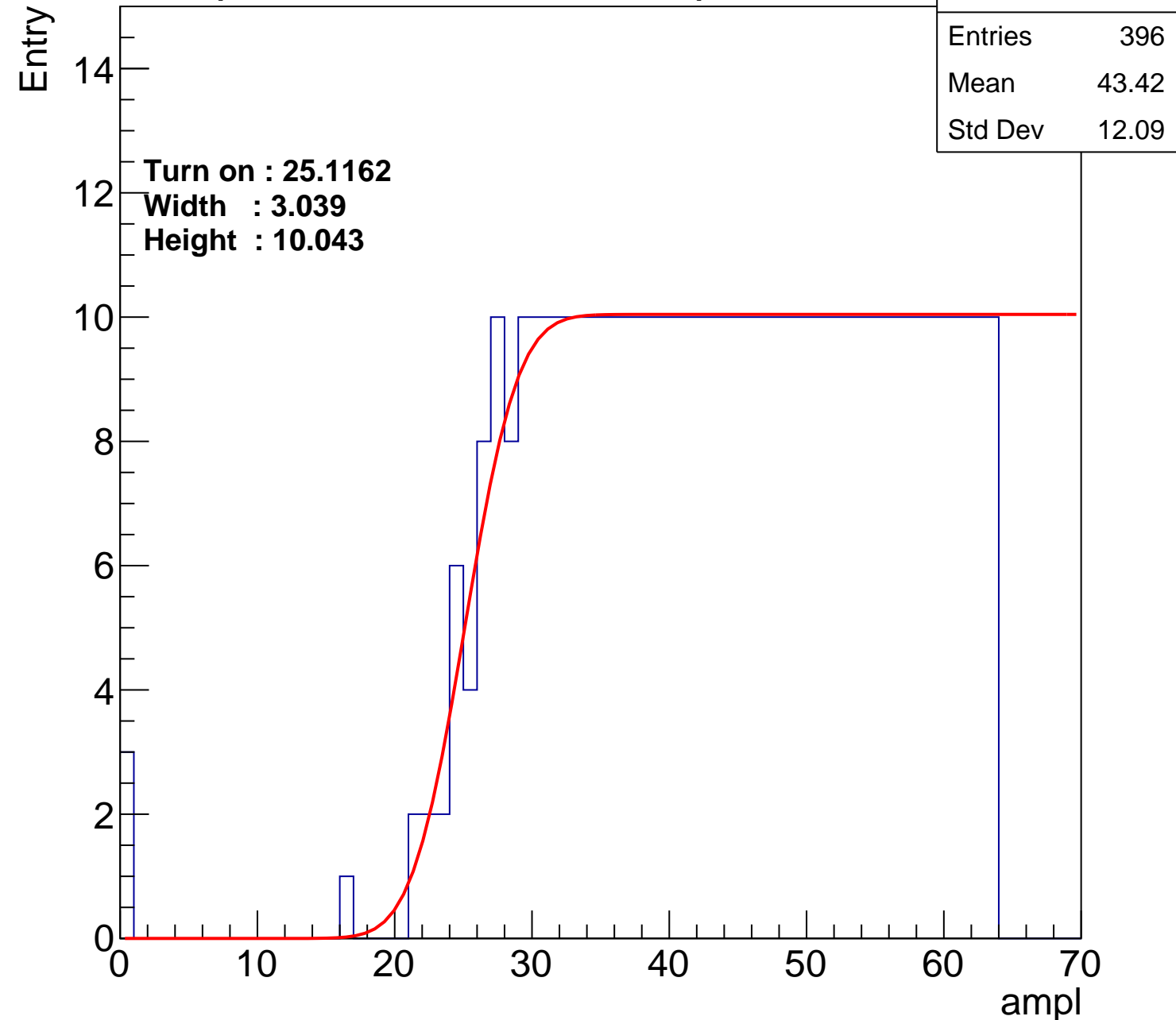
Width : 3.039

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch59

calib_packv5_042523_0143.root, FC#7, port C2

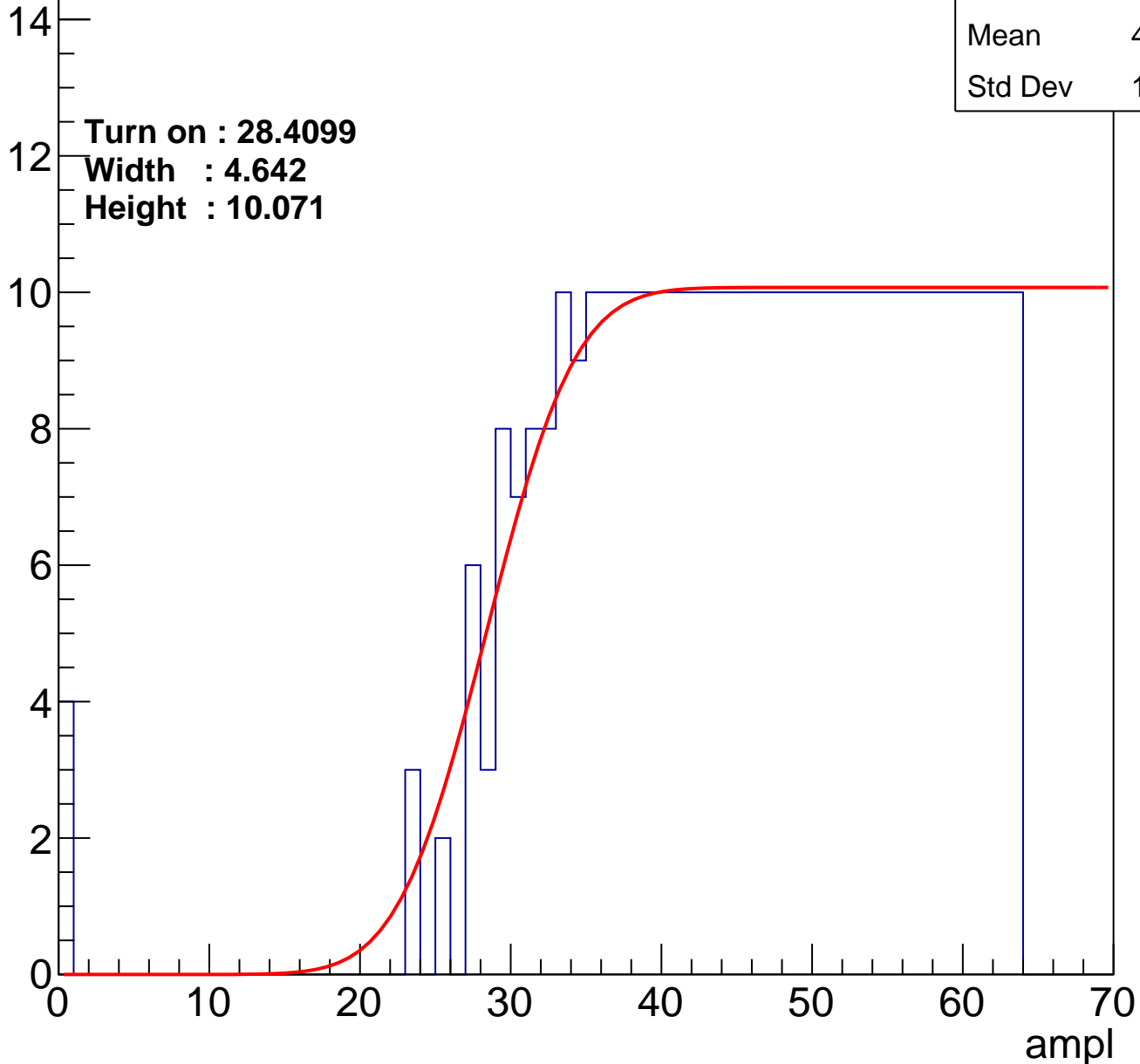
Entries	358
Mean	45.13
Std Dev	11.48

Turn on : 28.4099

Width : 4.642

Height : 10.071

Entry



B1L103S, U16-ch60

calib_packv5_042523_0143.root, FC#7, port C2

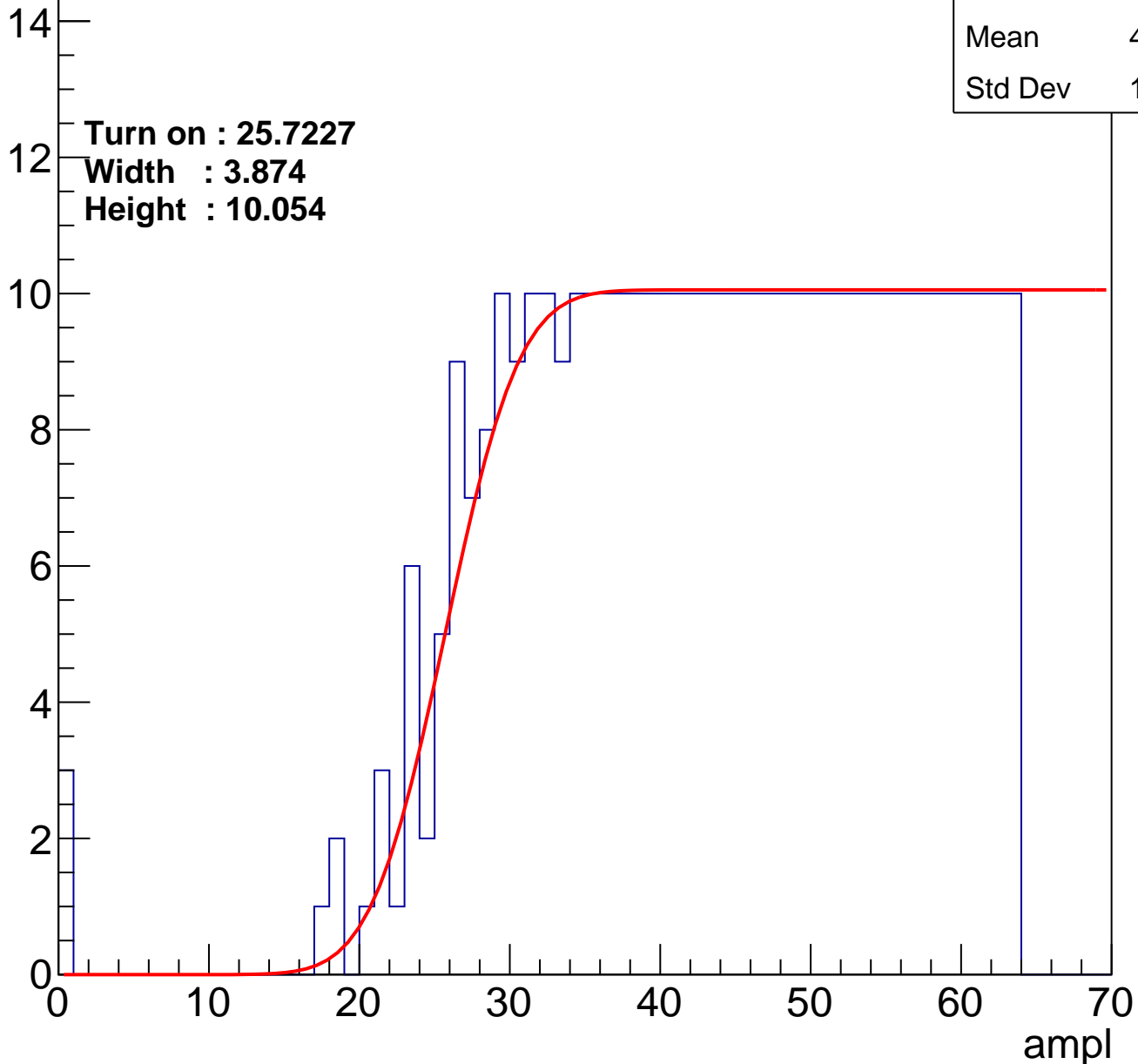
Entries	396
Mean	43.32
Std Dev	12.24

Turn on : 25.7227

Width : 3.874

Height : 10.054

Entry



B1L103S, U16-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 27.7449

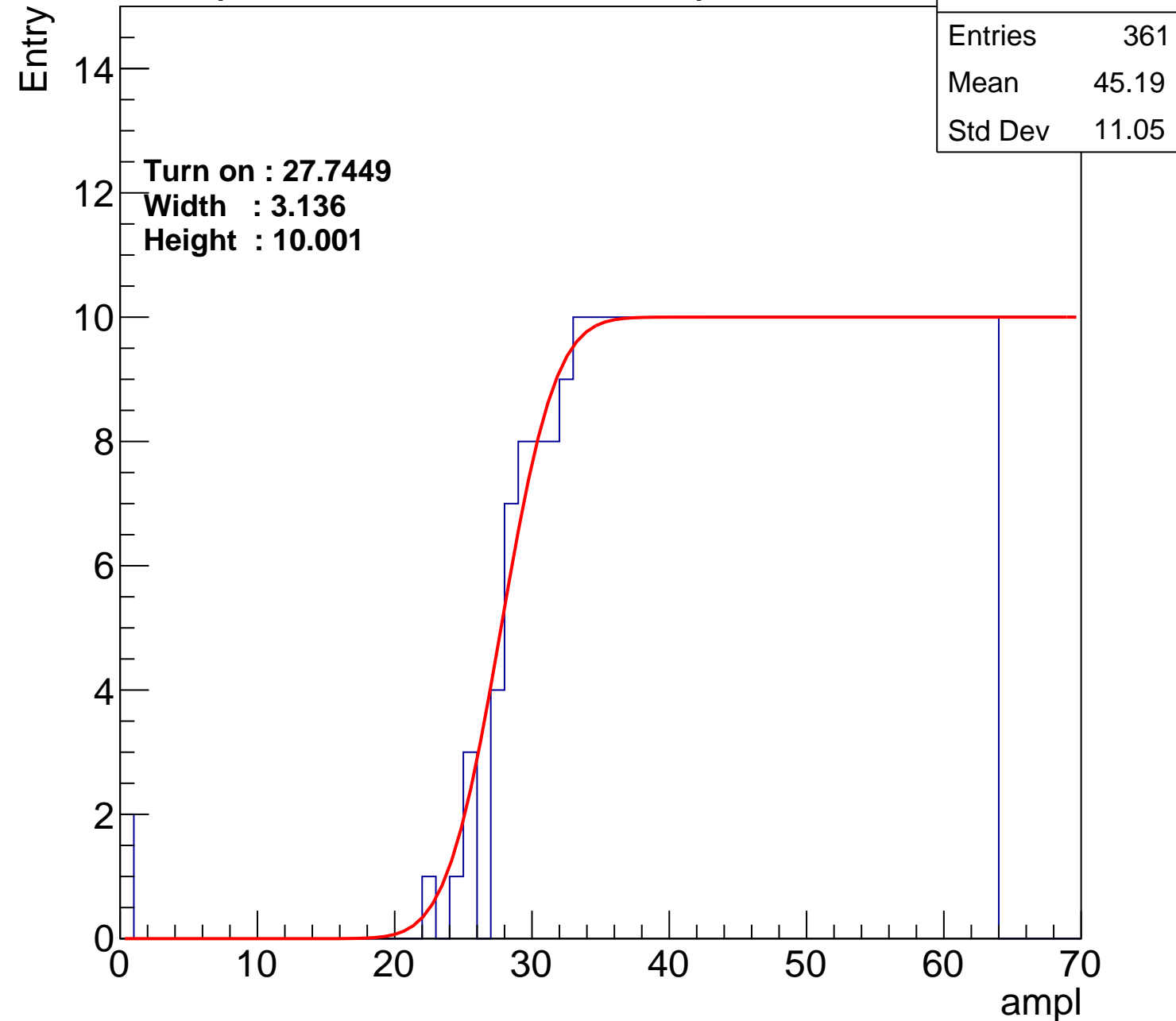
Width : 3.136

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.06
Std Dev	11.69

Turn on : 26.4491

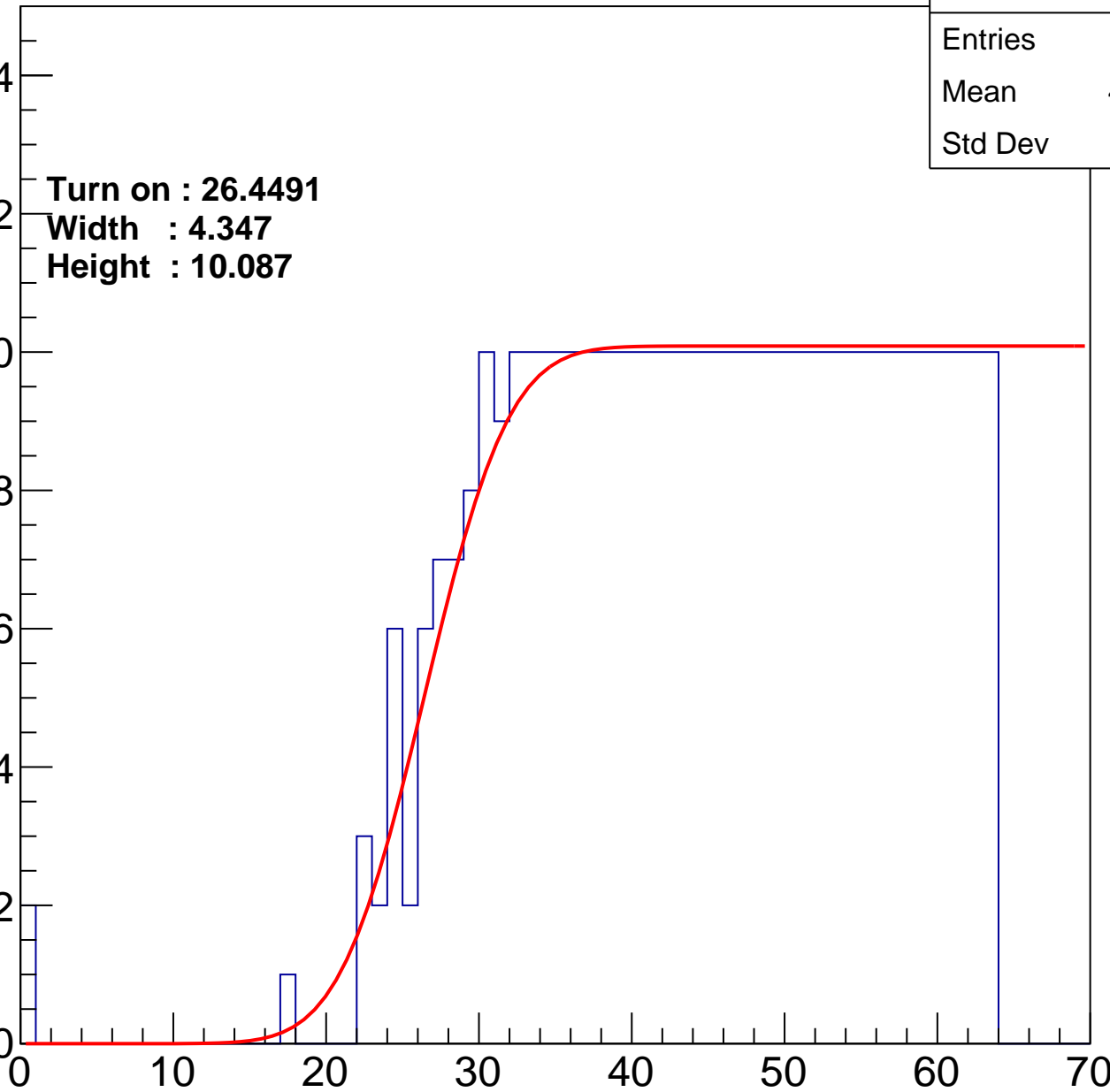
Width : 4.347

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.61
Std Dev	11.55

Turn on : 27.1269

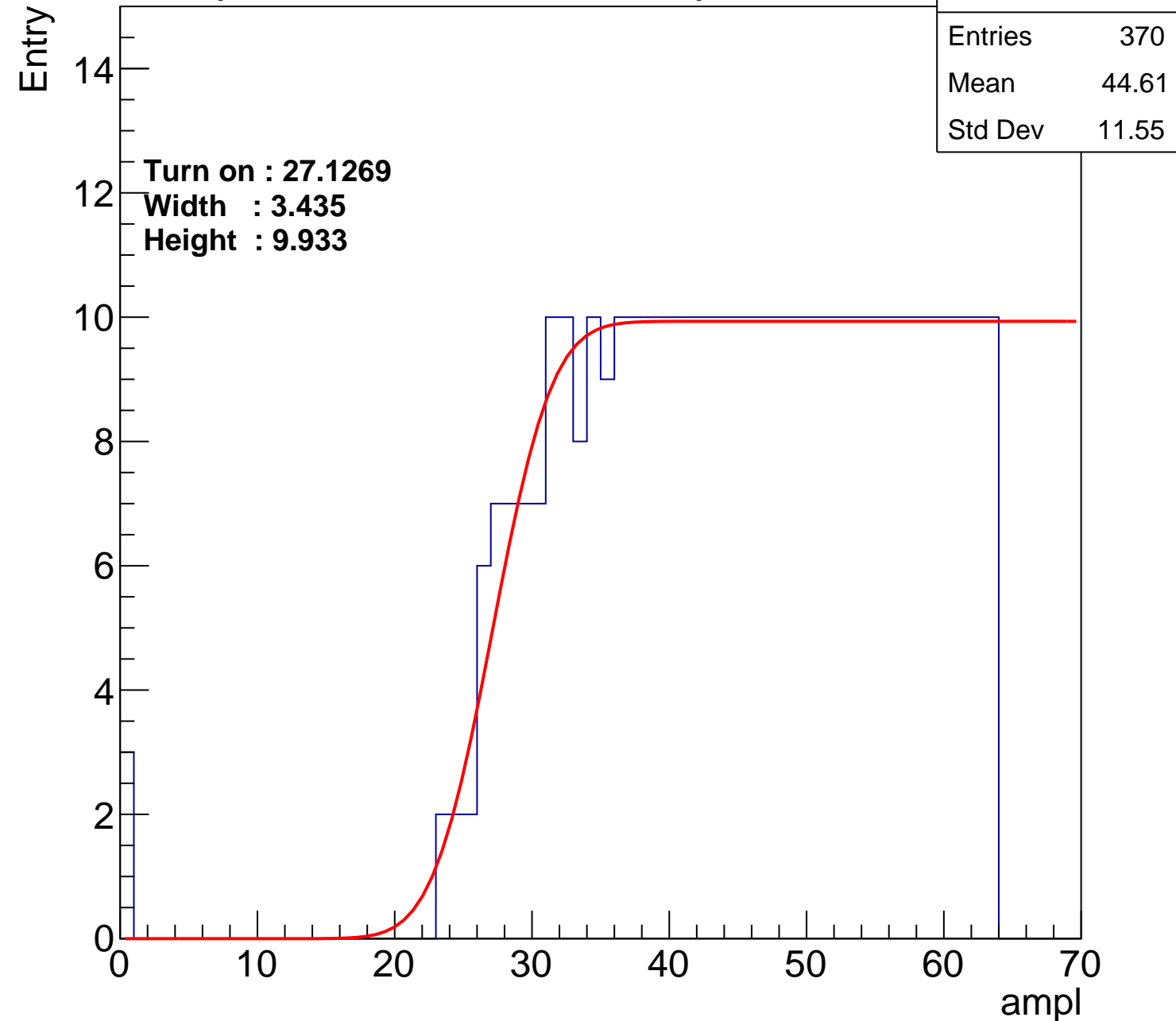
Width : 3.435

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch64

calib_packv5_042523_0143.root, FC#7, port C2

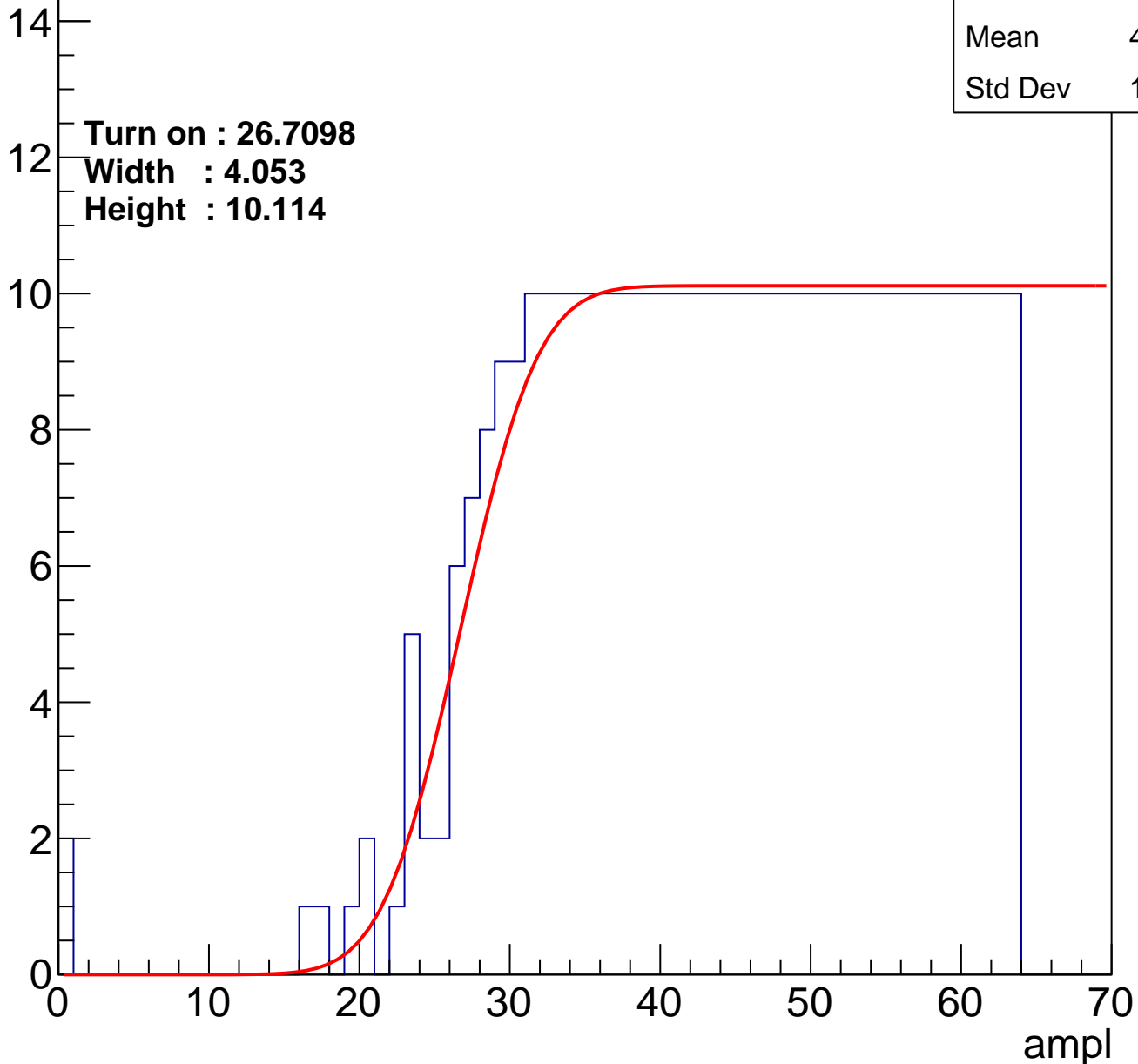
Entries	386
Mean	43.88
Std Dev	11.83

Turn on : 26.7098

Width : 4.053

Height : 10.114

Entry



B1L103S, U16-ch65

calib_packv5_042523_0143.root, FC#7, port C2

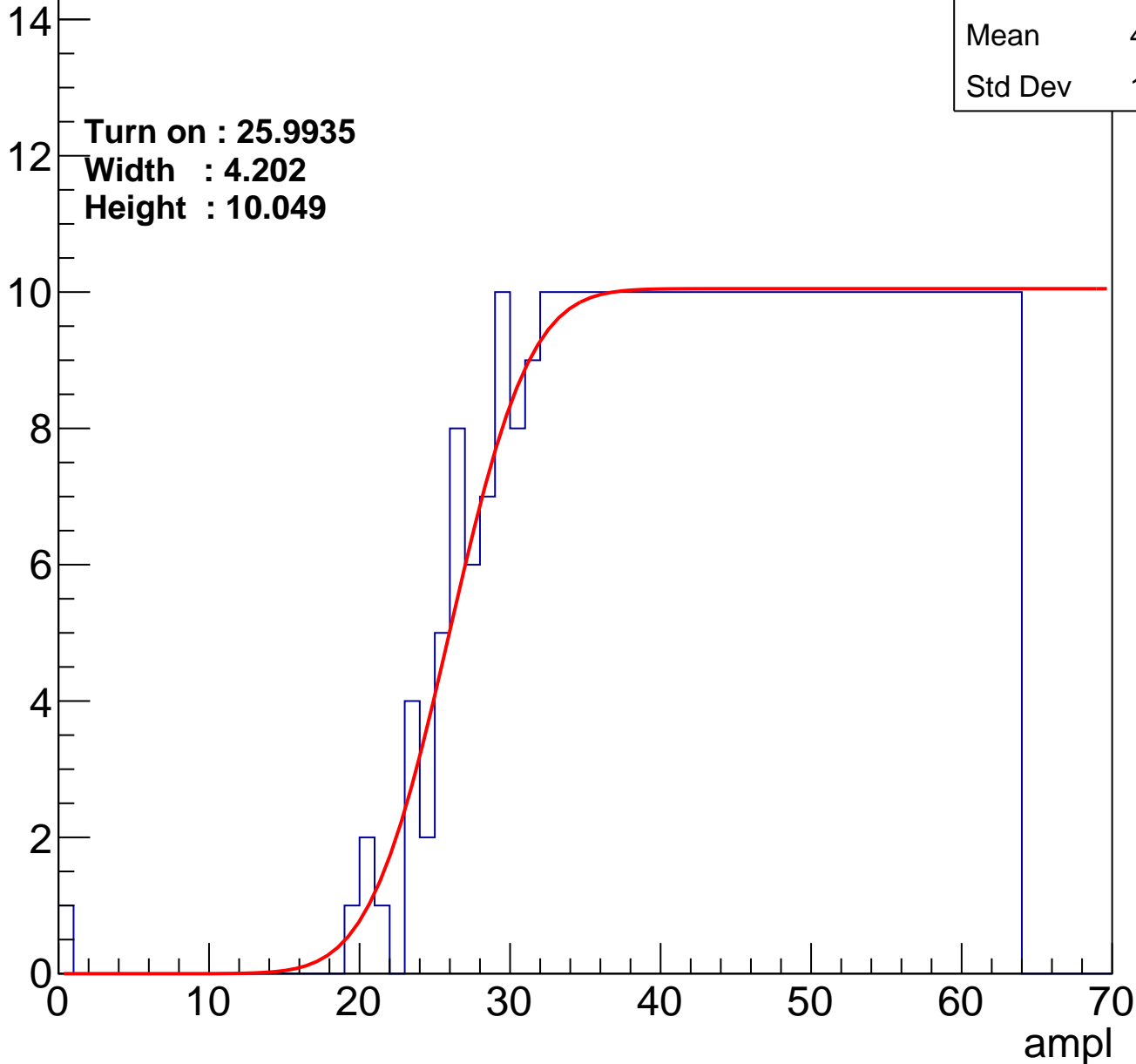
Entries	384
Mean	44.06
Std Dev	11.55

Turn on : 25.9935

Width : 4.202

Height : 10.049

Entry



B1L103S, U16-ch66

calib_packv5_042523_0143.root, FC#7, port C2

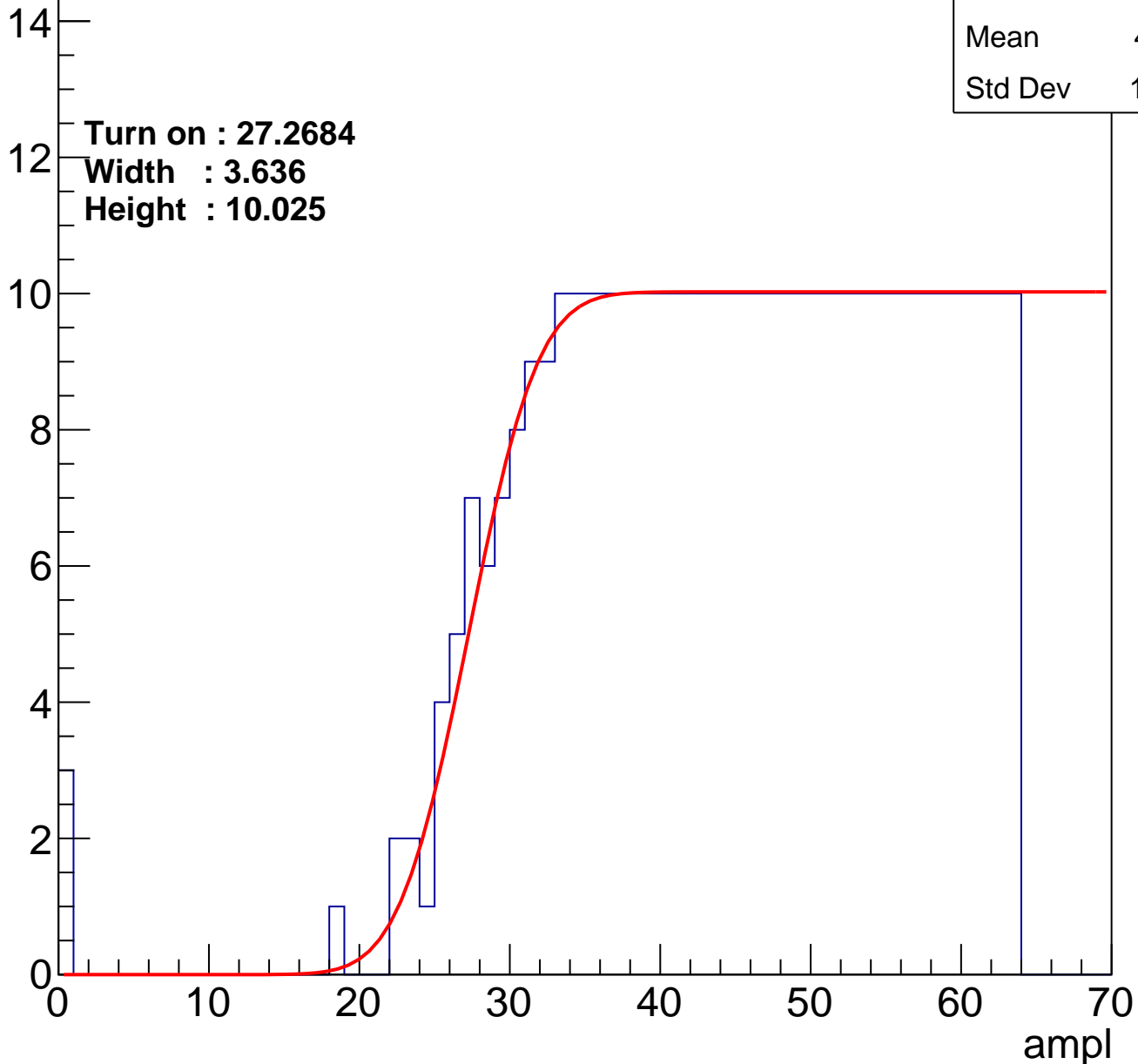
Entries	374
Mean	44.41
Std Dev	11.68

Turn on : 27.2684

Width : 3.636

Height : 10.025

Entry



B1L103S, U16-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.81
Std Dev	11.69

Turn on : 25.5609

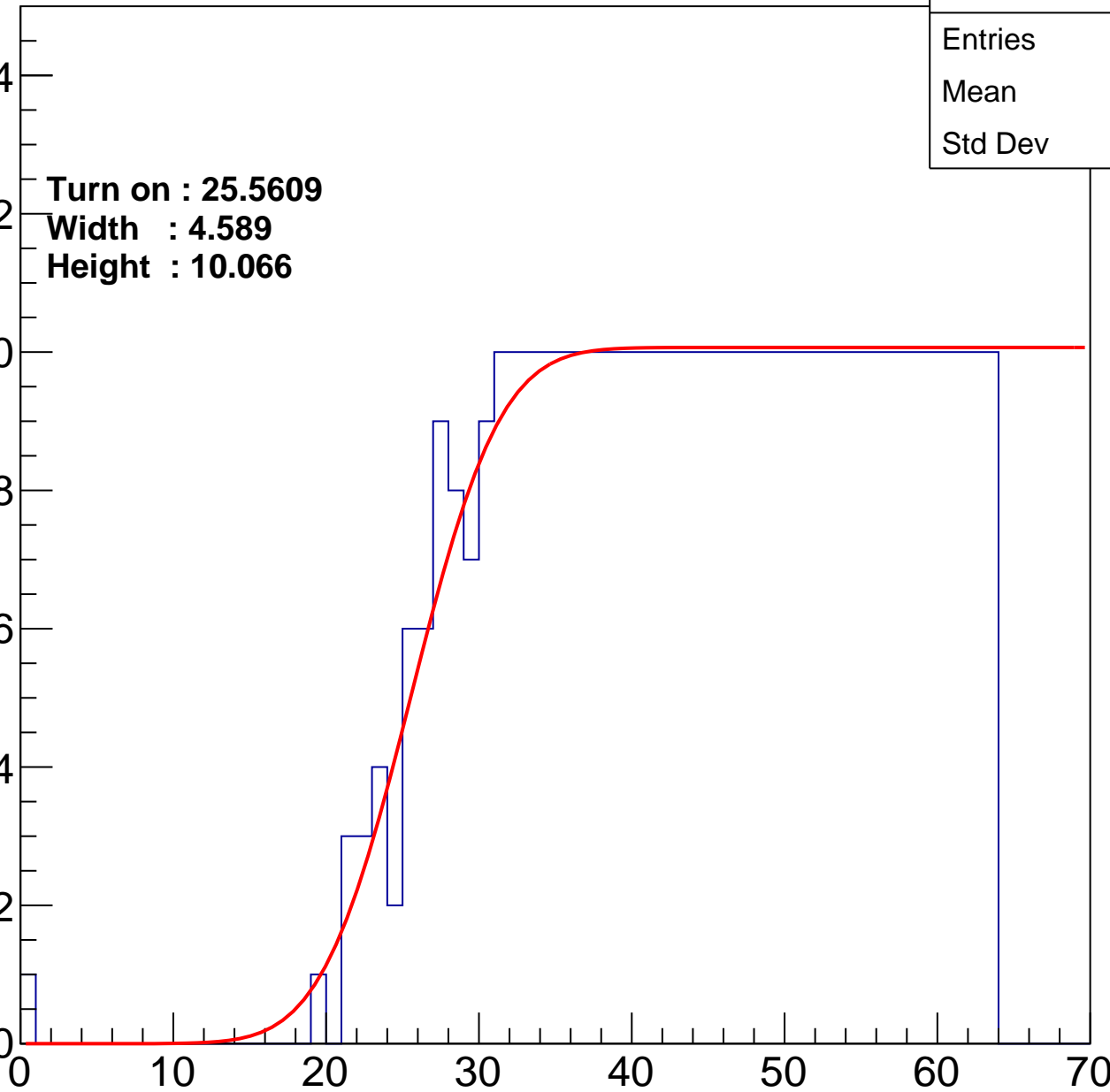
Width : 4.589

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.25
Std Dev	12.02

Turn on : 28.0333

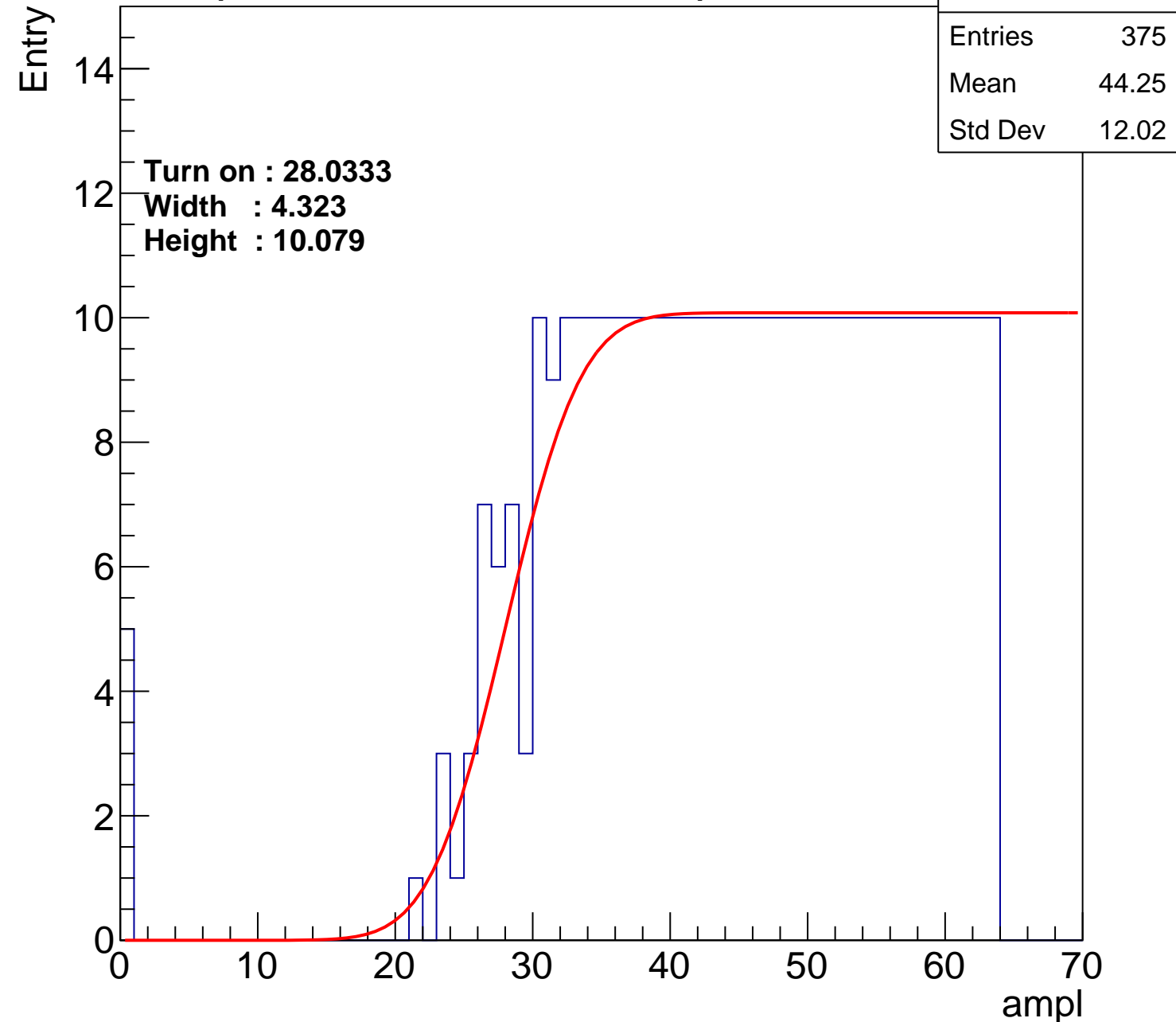
Width : 4.323

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch69

calib_packv5_042523_0143.root, FC#7, port C2

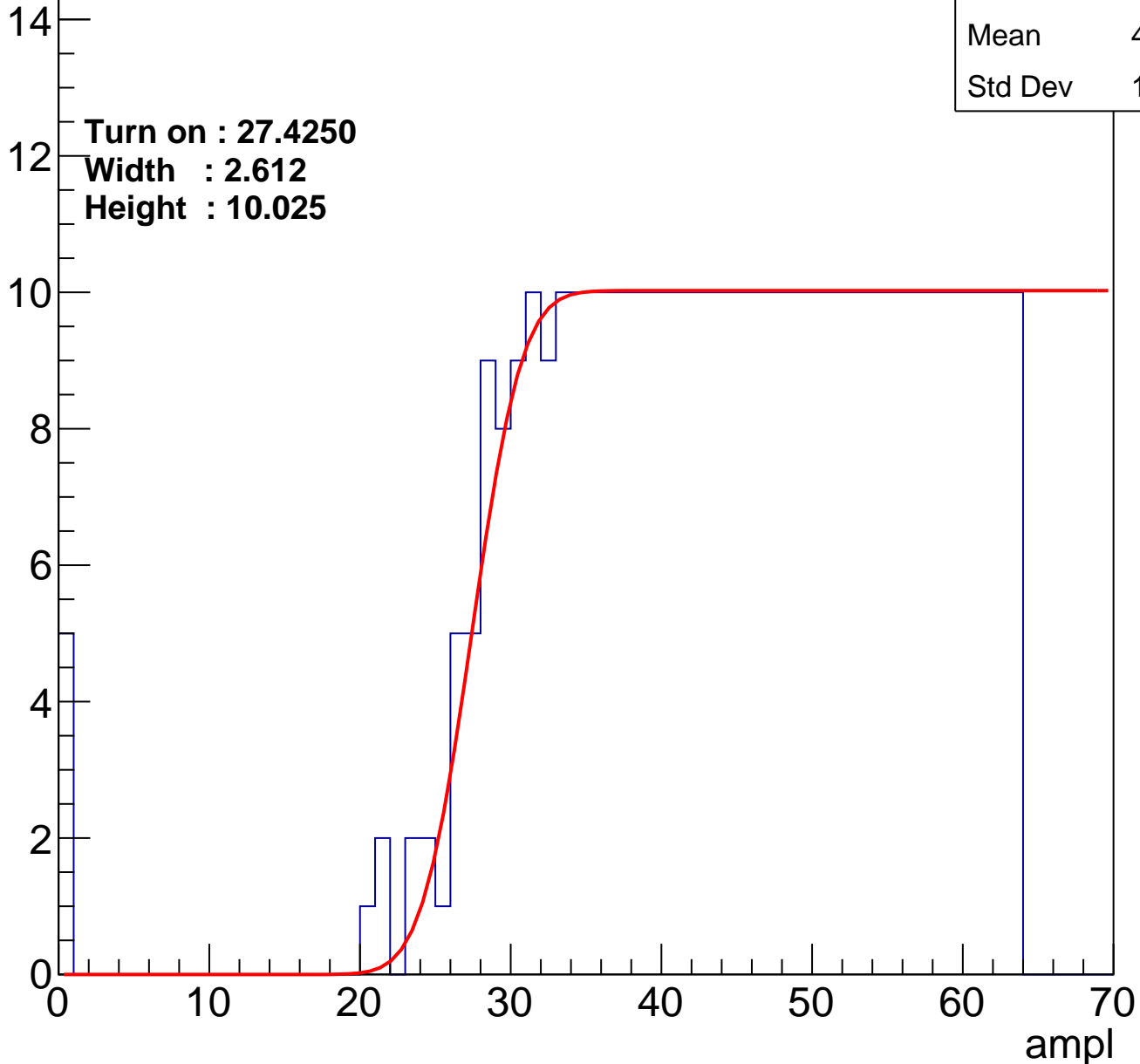
Entries	378
Mean	44.12
Std Dev	12.07

Turn on : 27.4250

Width : 2.612

Height : 10.025

Entry



B1L103S, U16-ch70

calib_packv5_042523_0143.root, FC#7, port C2

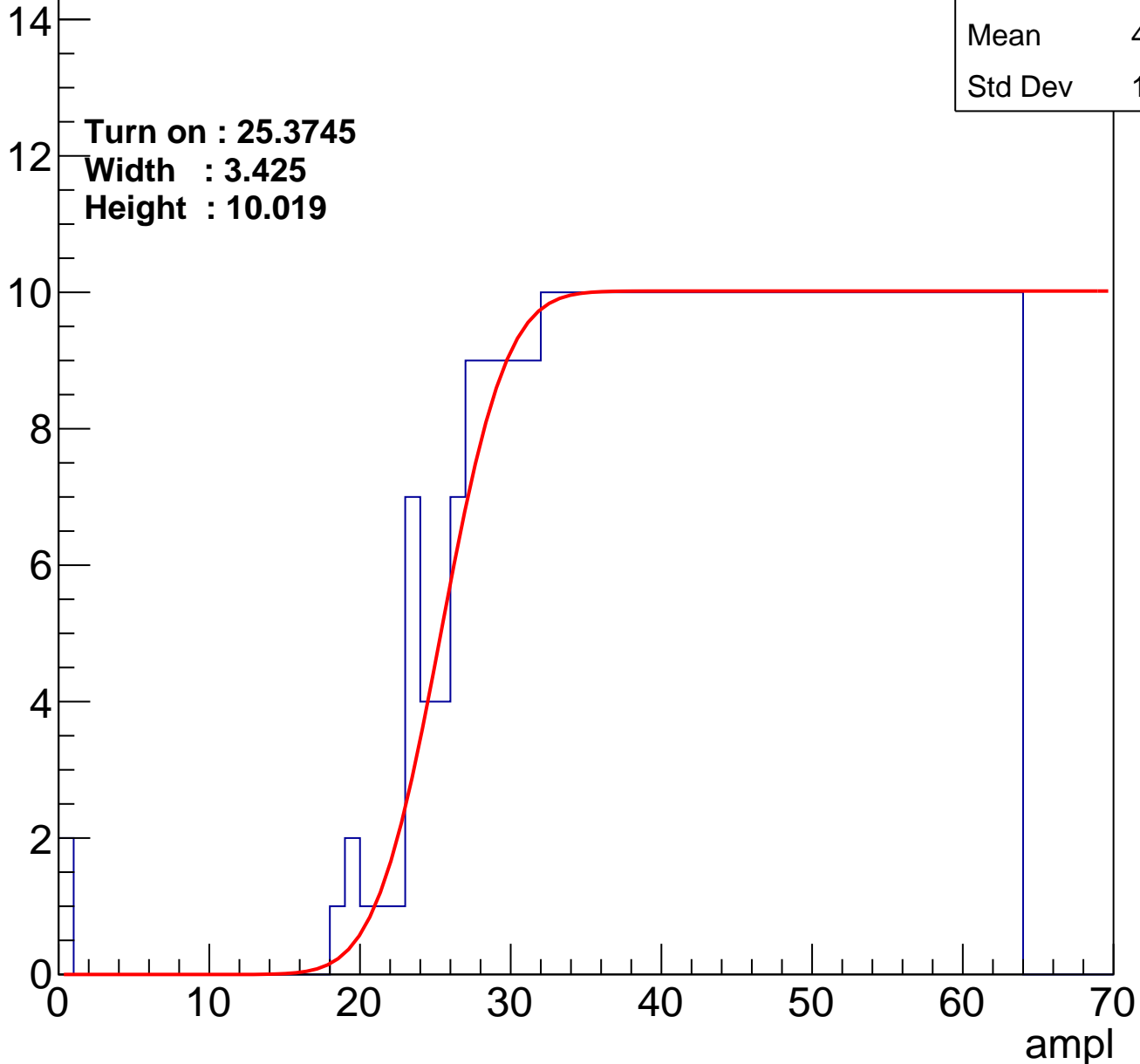
Entries	395
Mean	43.45
Std Dev	12.02

Turn on : 25.3745

Width : 3.425

Height : 10.019

Entry



B1L103S, U16-ch71

calib_packv5_042523_0143.root, FC#7, port C2

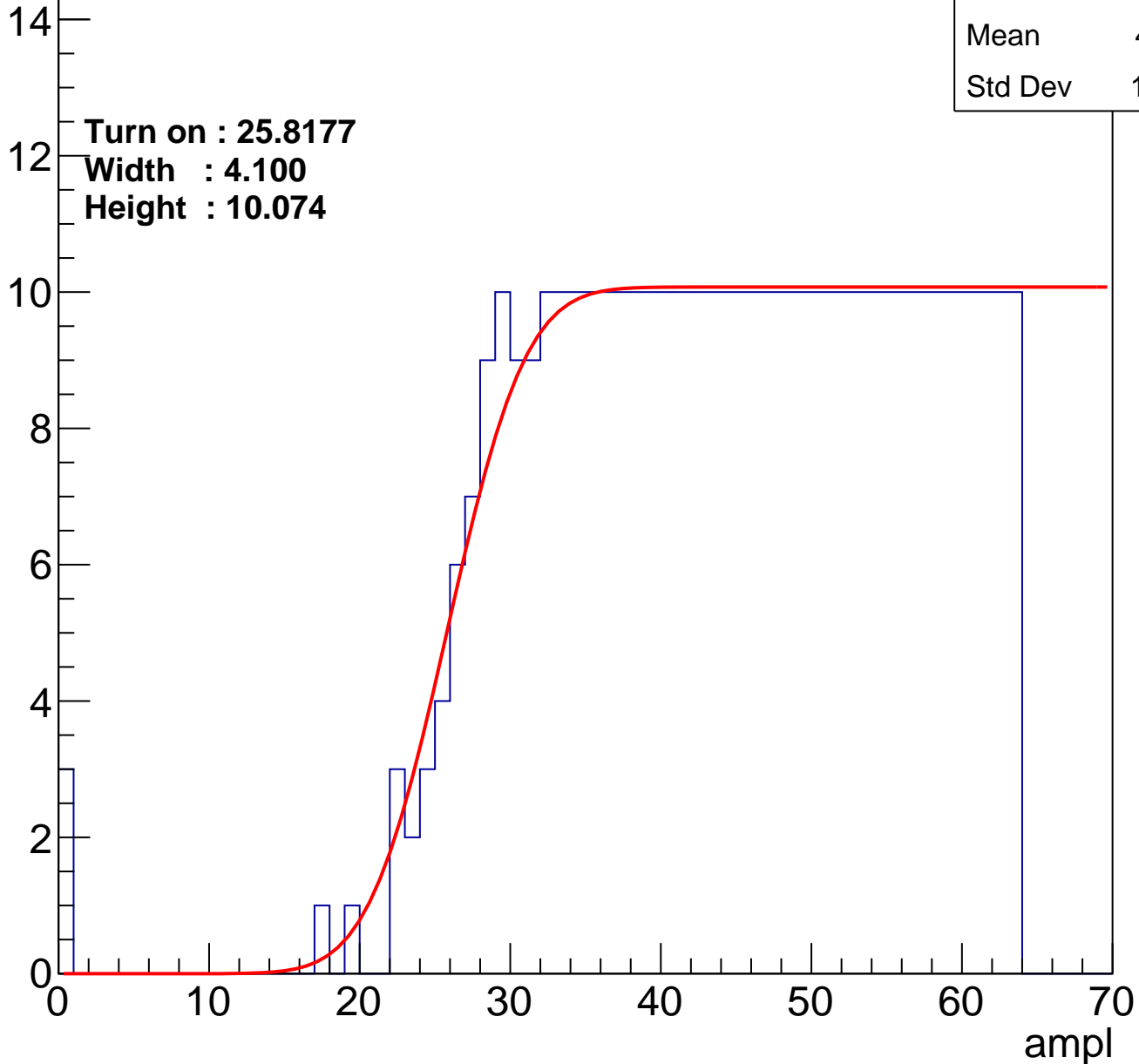
Entries	387
Mean	43.81
Std Dev	11.94

Turn on : 25.8177

Width : 4.100

Height : 10.074

Entry



B1L103S, U16-ch72

calib_packv5_042523_0143.root, FC#7, port C2

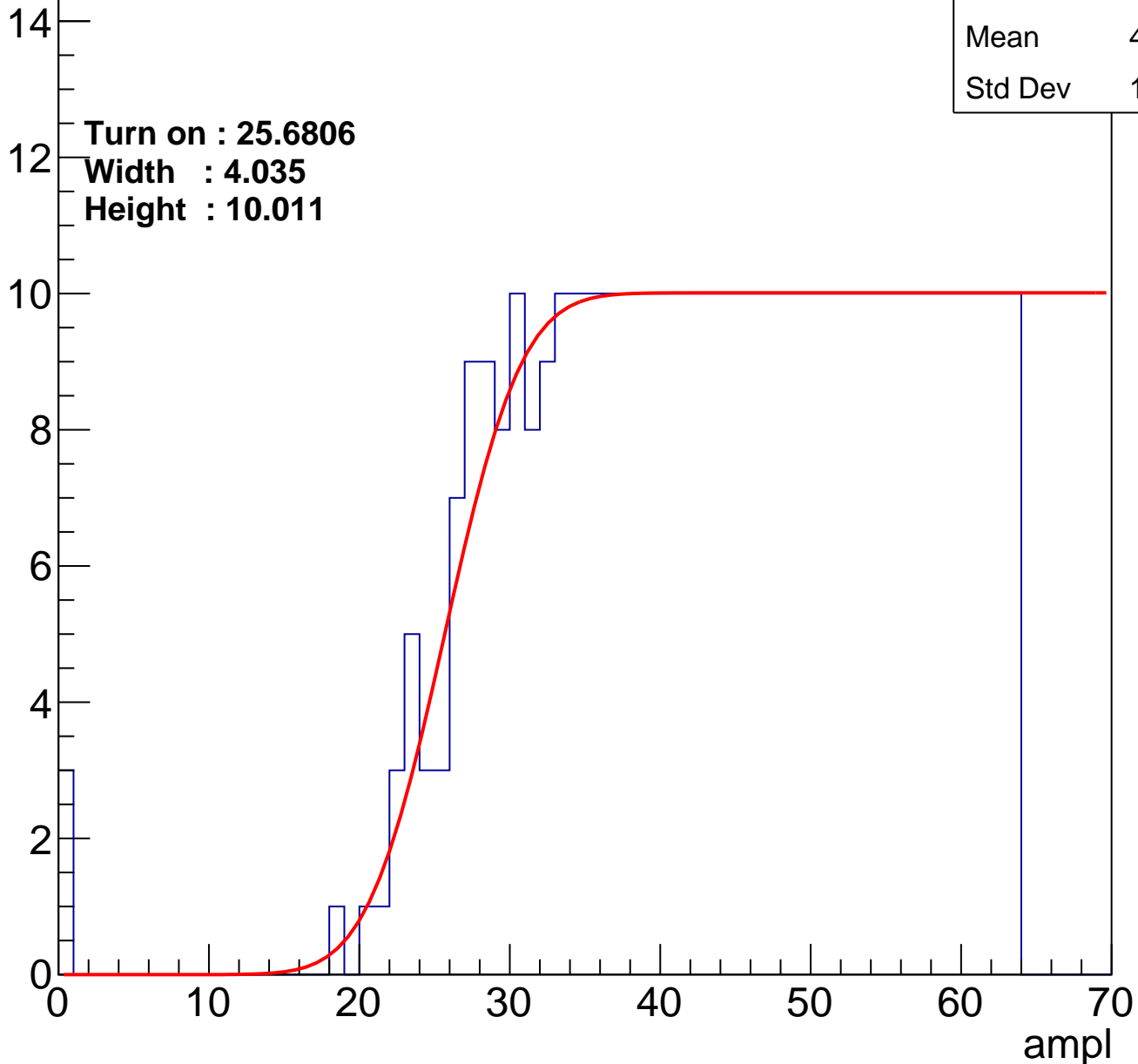
Entries	390
Mean	43.62
Std Dev	12.07

Turn on : 25.6806

Width : 4.035

Height : 10.011

Entry



B1L103S, U16-ch73

calib_packv5_042523_0143.root, FC#7, port C2

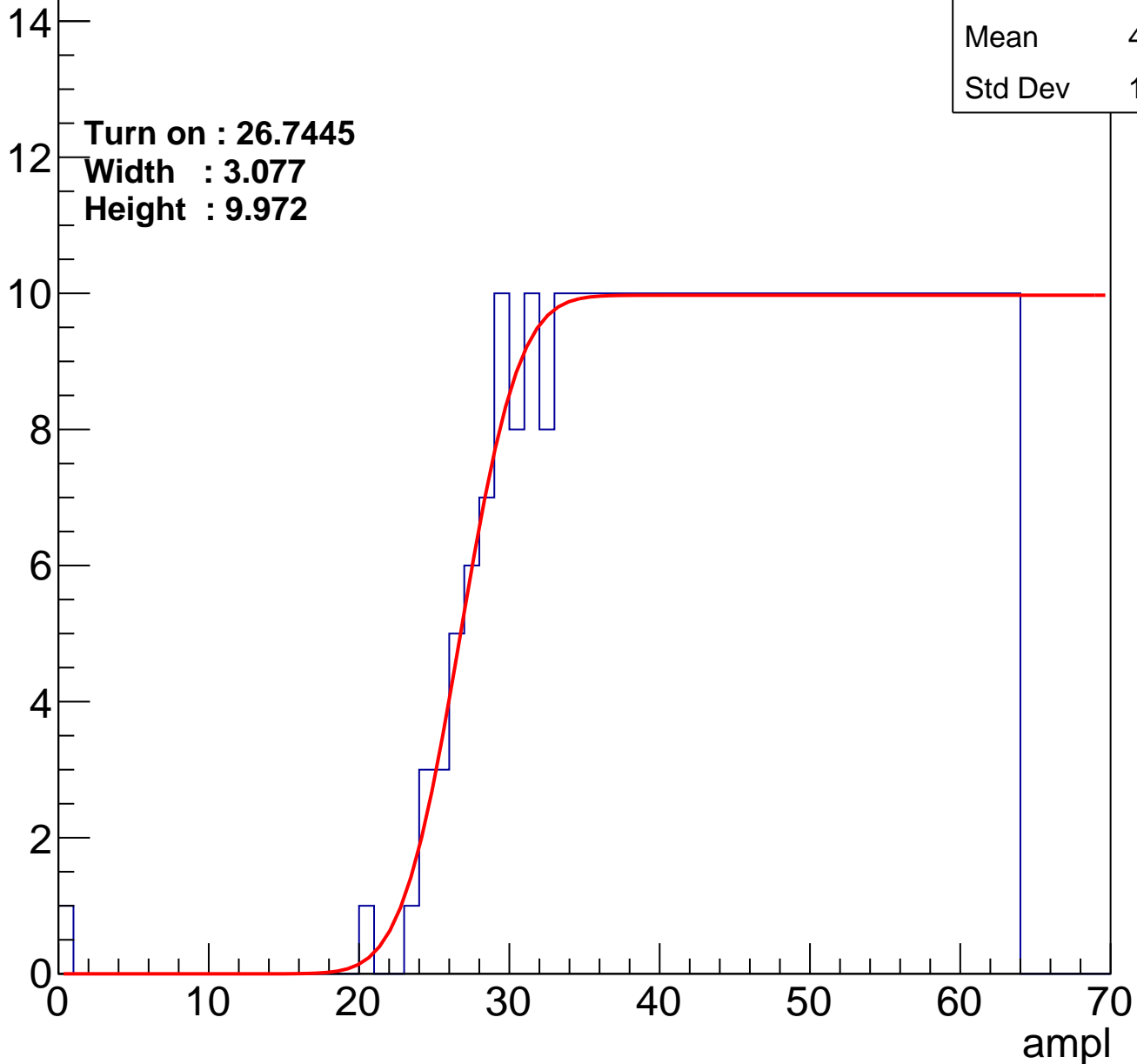
Entries	373
Mean	44.65
Std Dev	11.18

Turn on : 26.7445

Width : 3.077

Height : 9.972

Entry



B1L103S, U16-ch74

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.47
Std Dev	11.98

Turn on : 24.7436

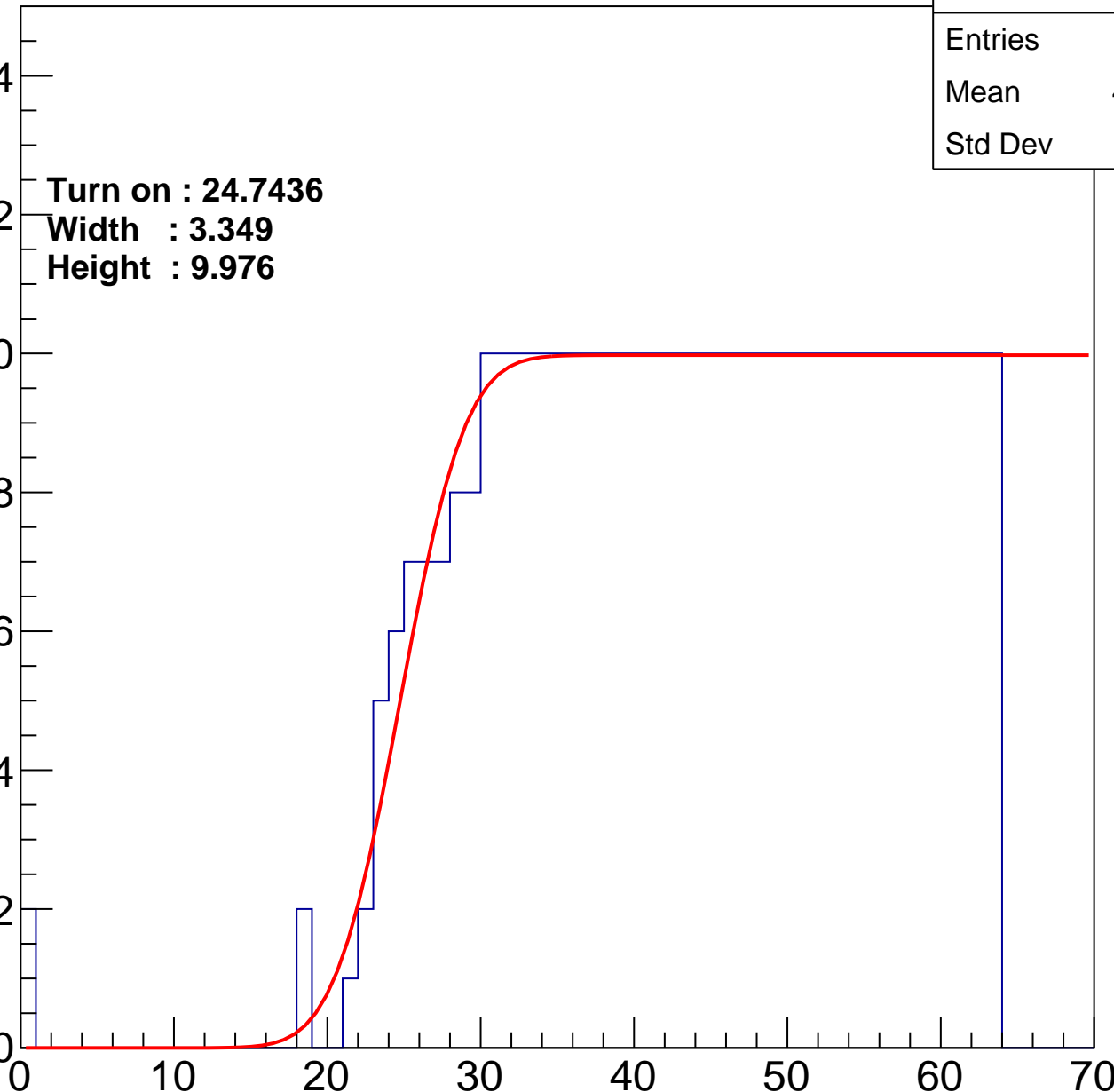
Width : 3.349

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 26.2750

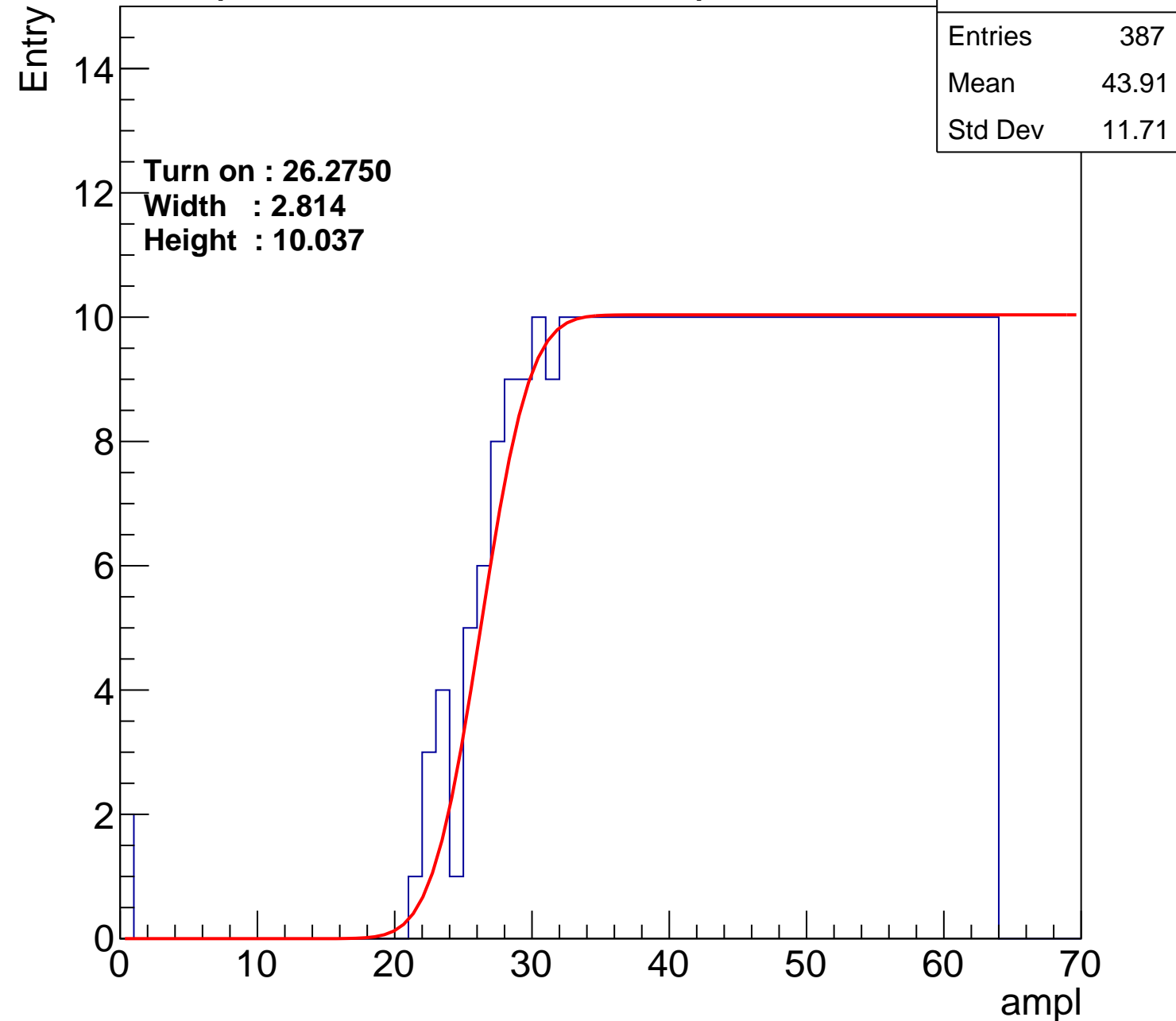
Width : 2.814

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch76

calib_packv5_042523_0143.root, FC#7, port C2

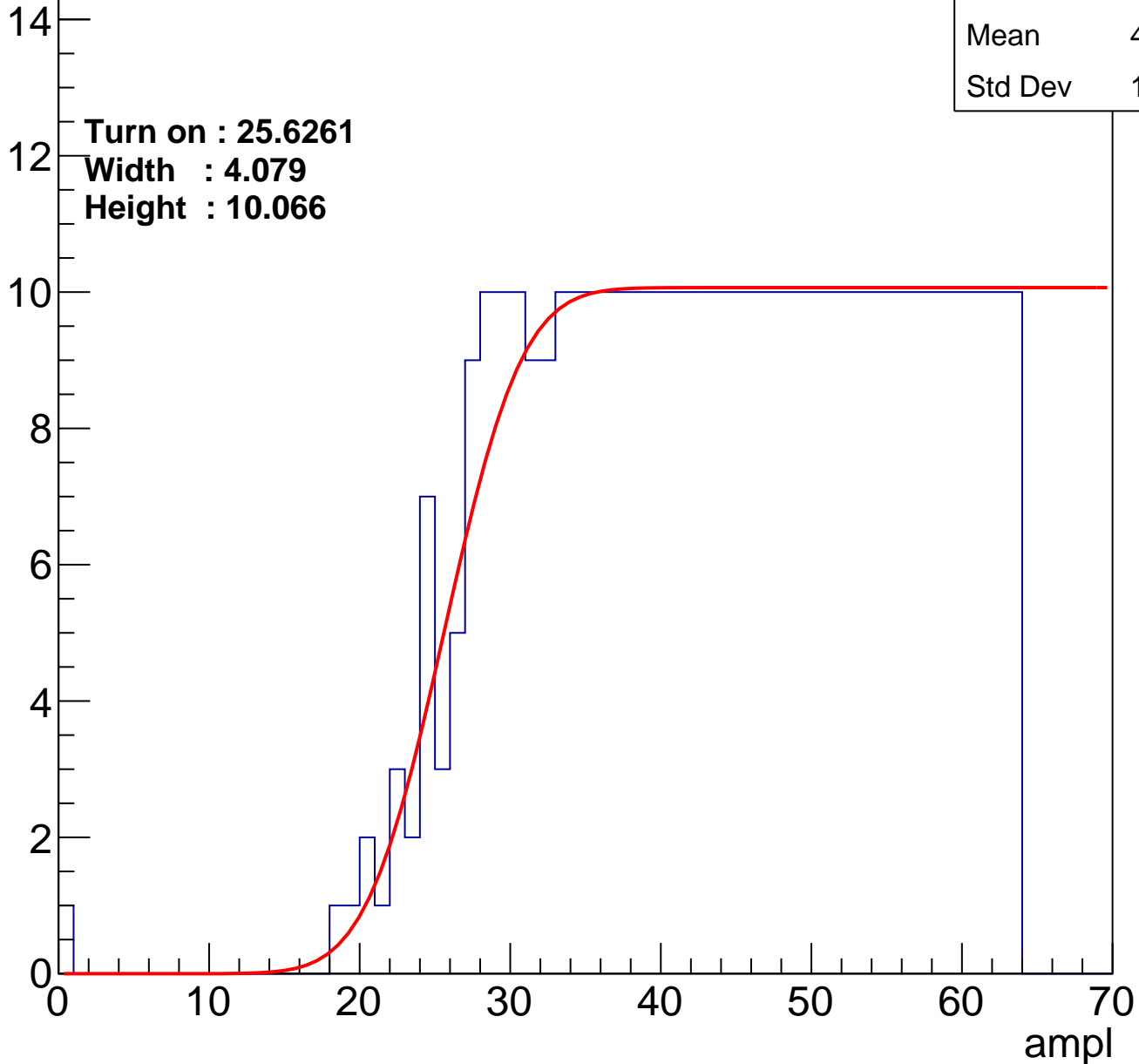
Entries	393
Mean	43.62
Std Dev	11.79

Turn on : 25.6261

Width : 4.079

Height : 10.066

Entry



B1L103S, U16-ch77

calib_packv5_042523_0143.root, FC#7, port C2

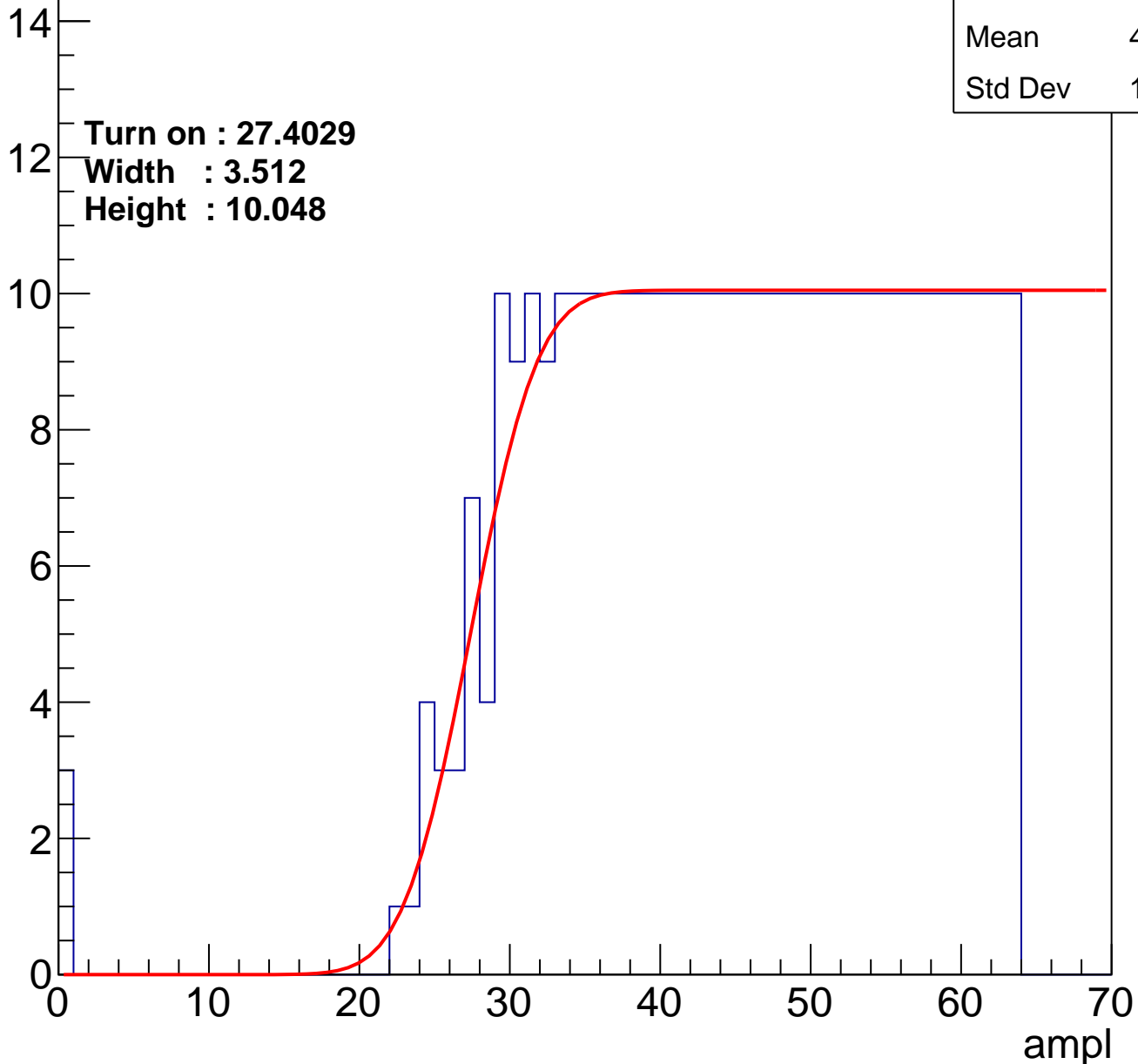
Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 27.4029

Width : 3.512

Height : 10.048

Entry



B1L103S, U16-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.4734

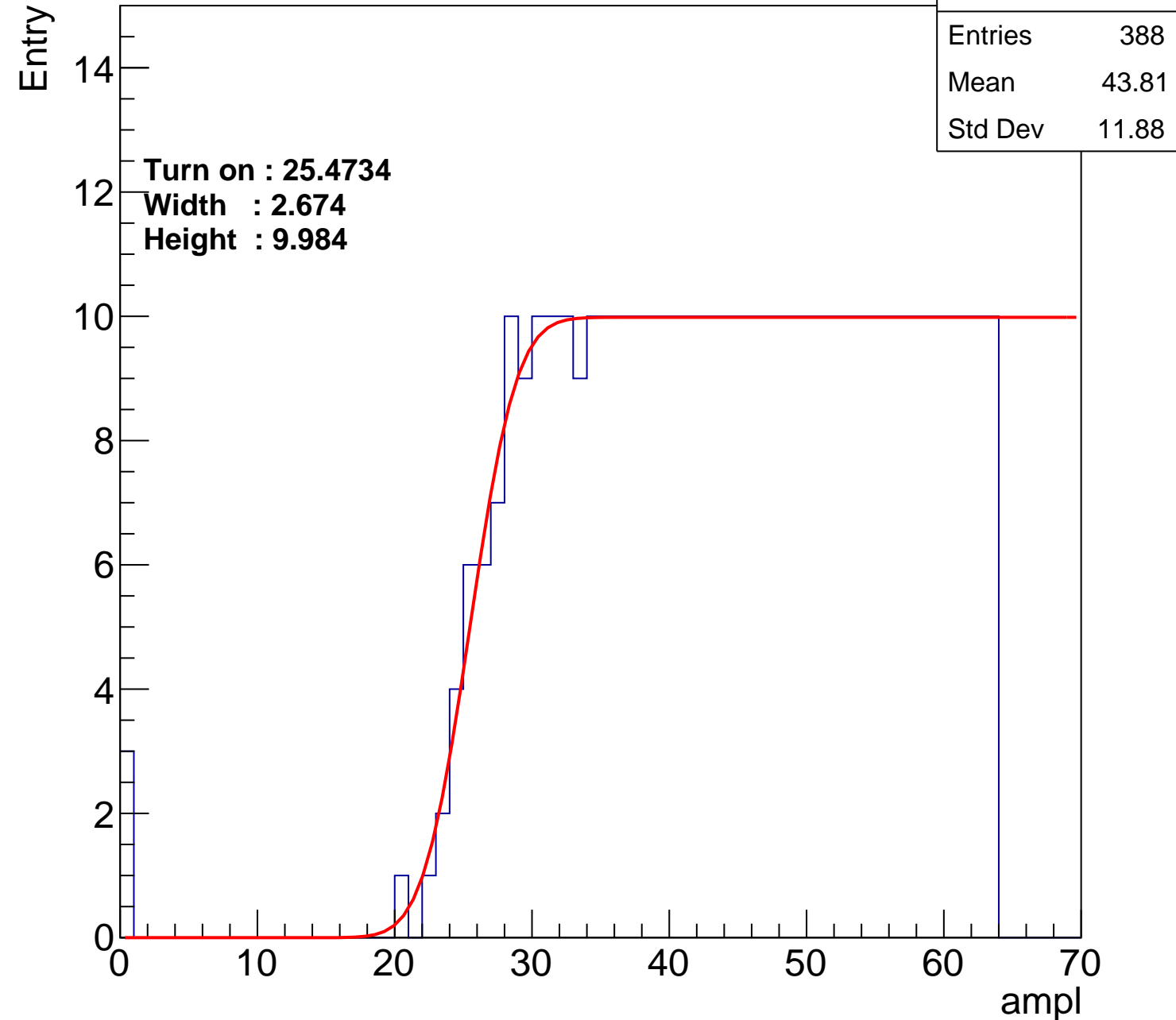
Width : 2.674

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.32
Std Dev	12.18

Turn on : 25.1660

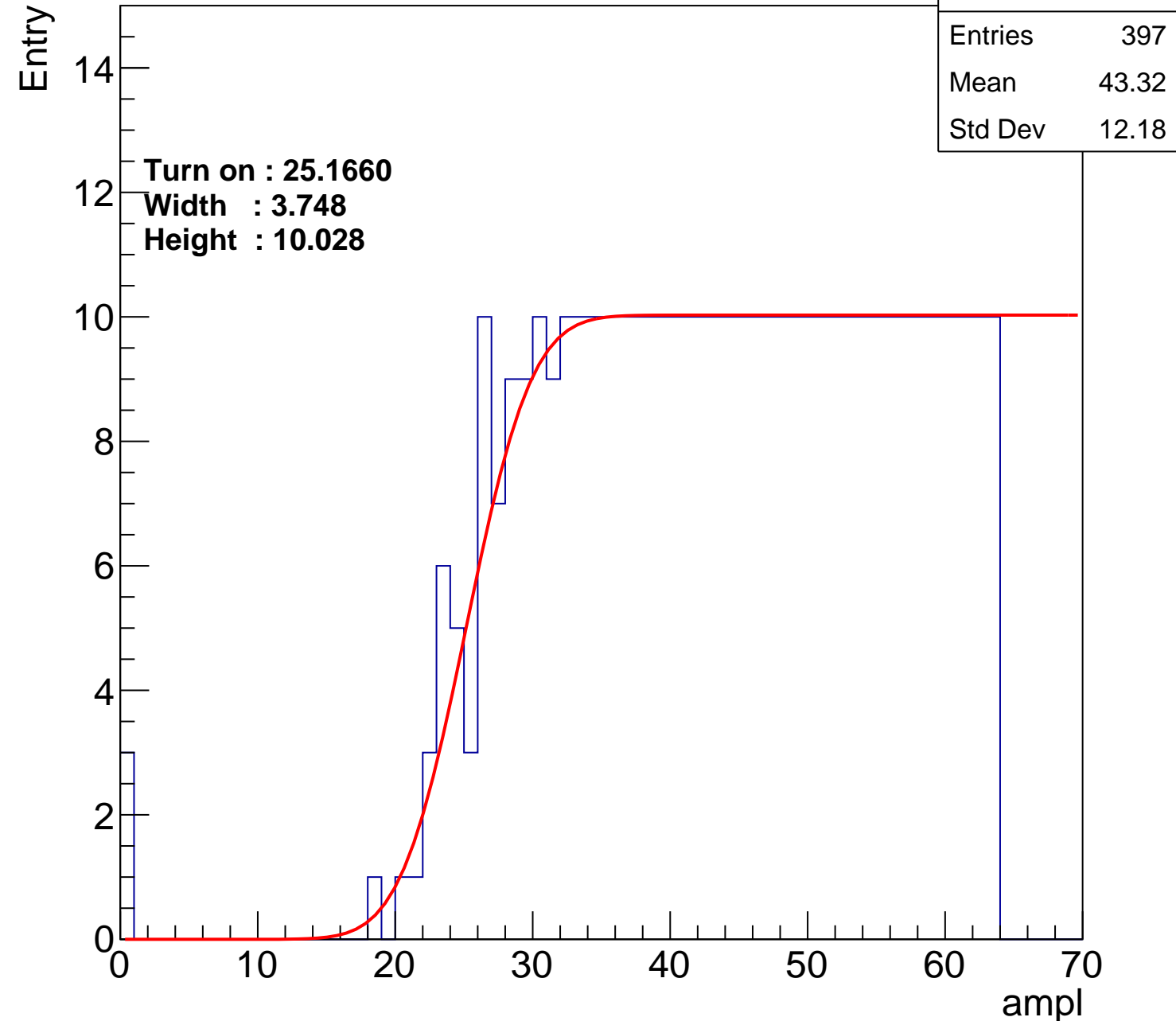
Width : 3.748

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch80

calib_packv5_042523_0143.root, FC#7, port C2

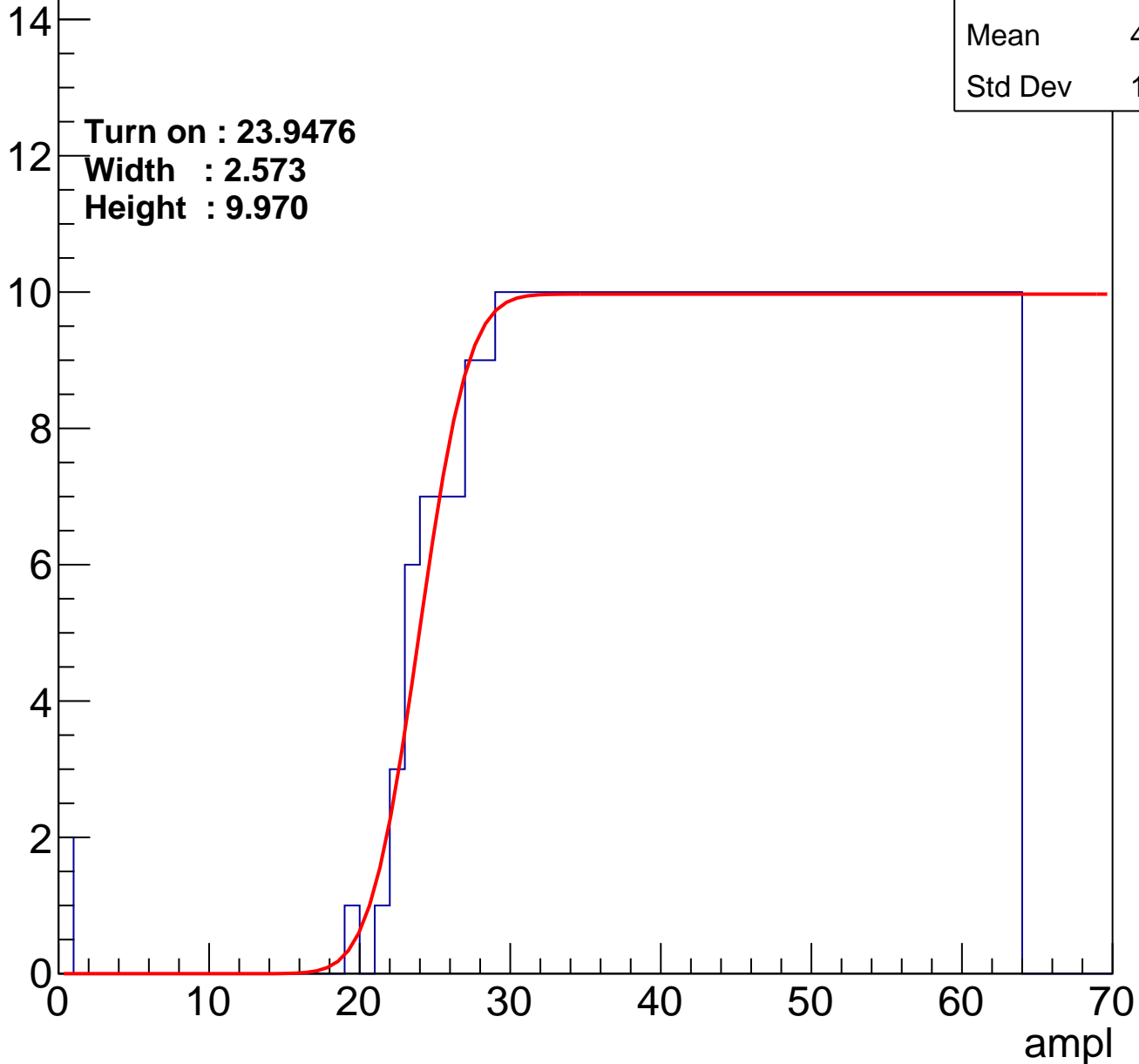
Entries	402
Mean	43.19
Std Dev	12.06

Turn on : 23.9476

Width : 2.573

Height : 9.970

Entry



B1L103S, U16-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.22
Std Dev	11.95

Turn on : 27.2635

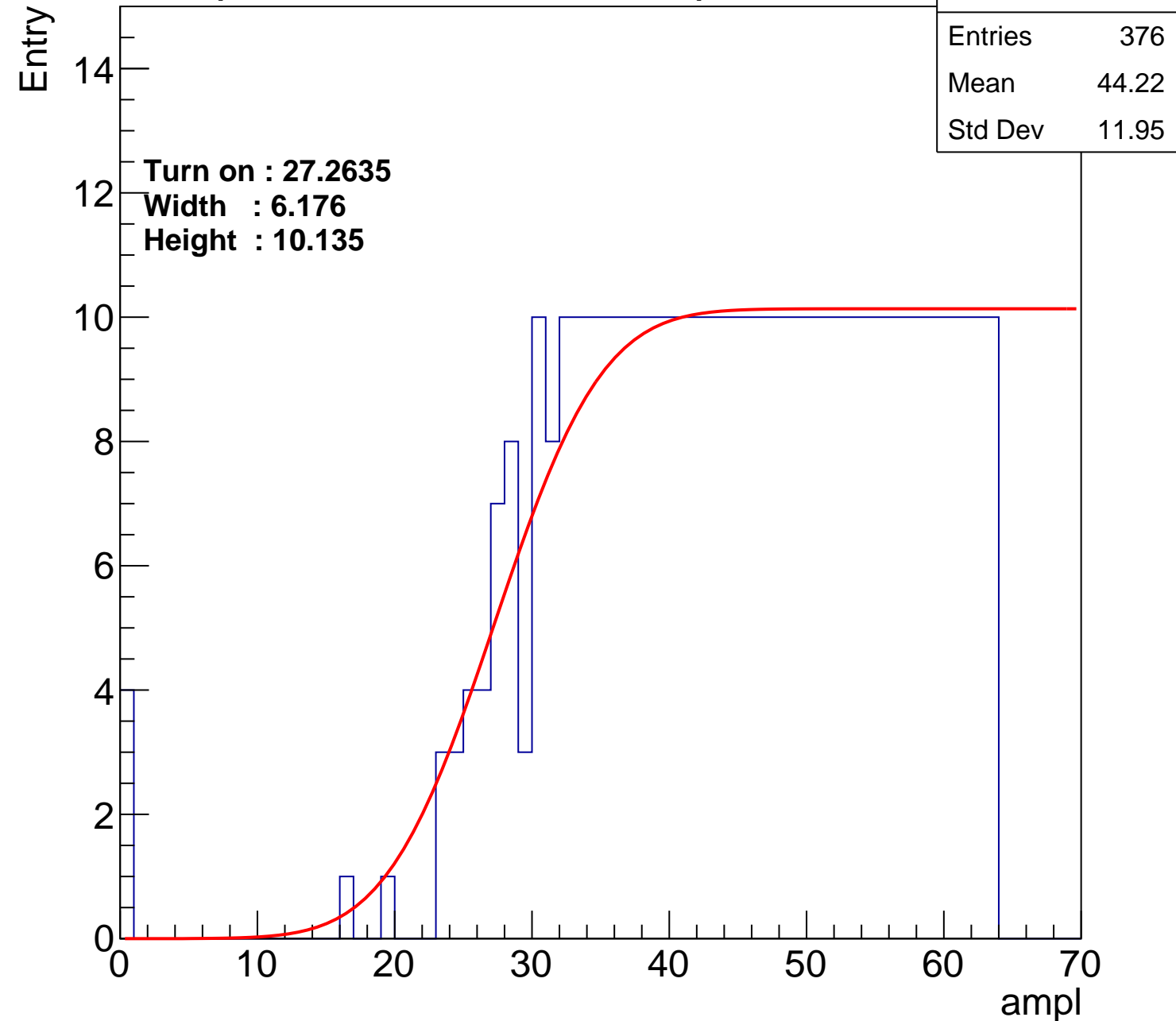
Width : 6.176

Height : 10.135

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.67
Std Dev	12.18

Turn on : 26.8637

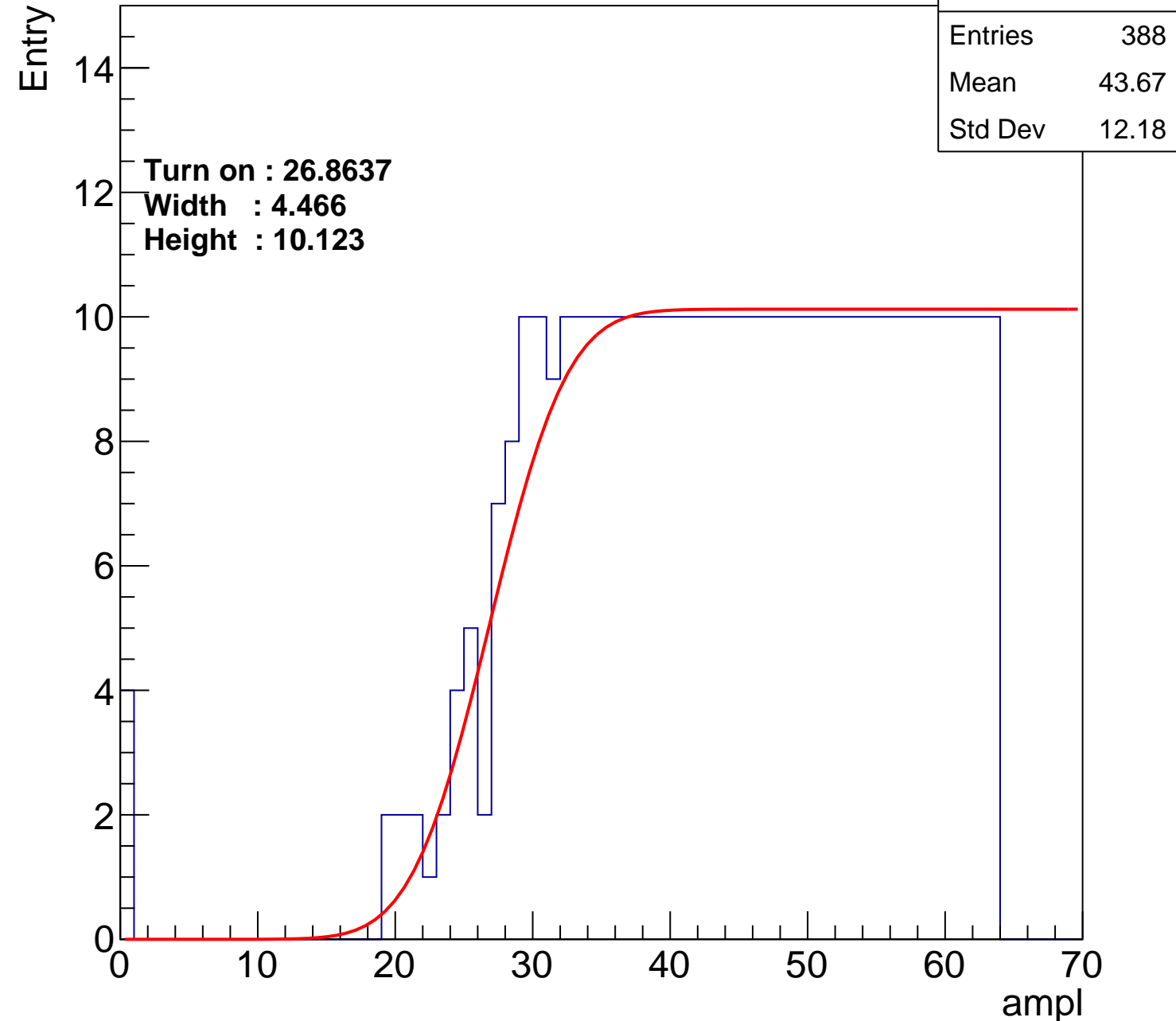
Width : 4.466

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.6971

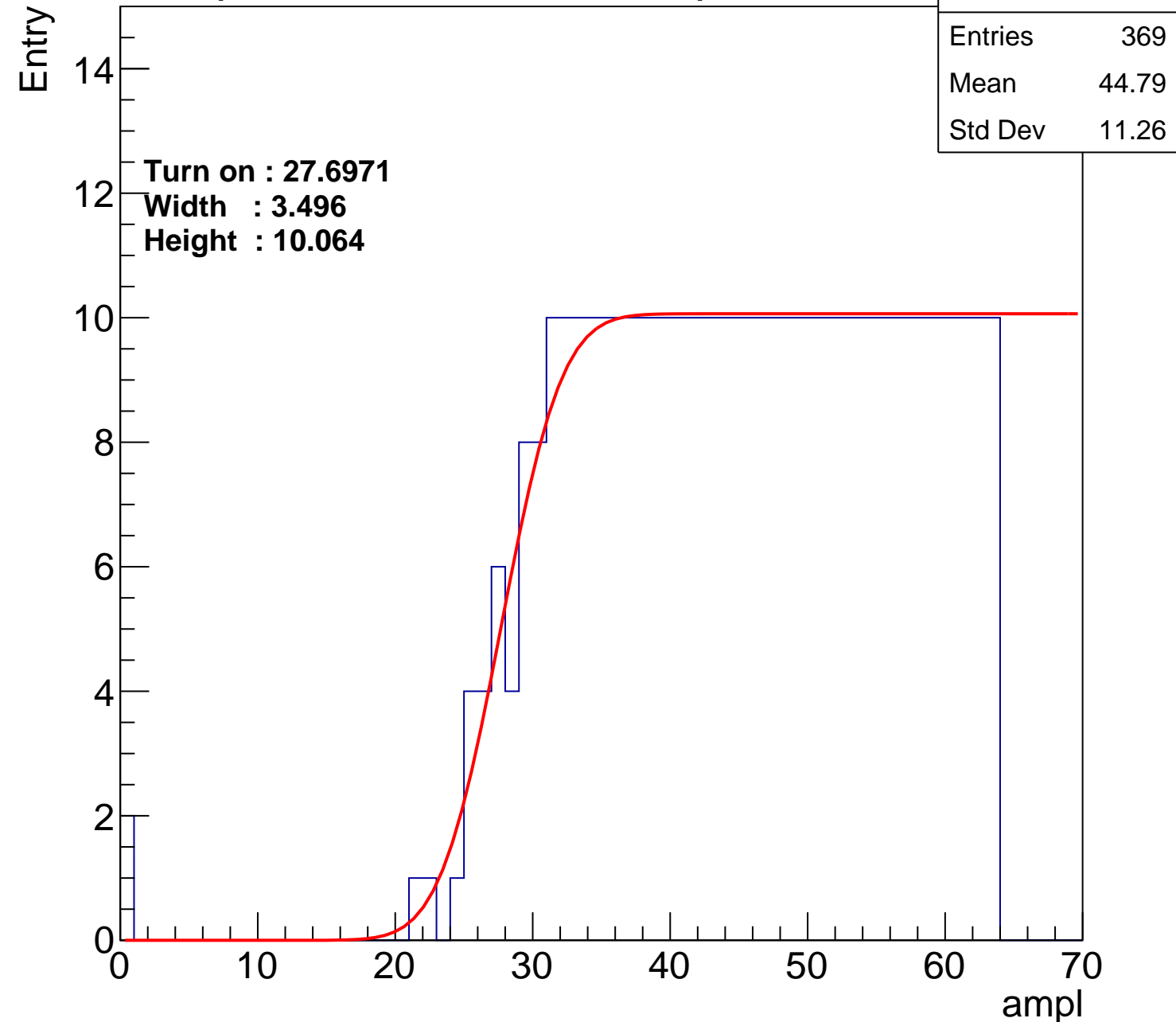
Width : 3.496

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.4
Std Dev	11.76

Turn on : 27.3511

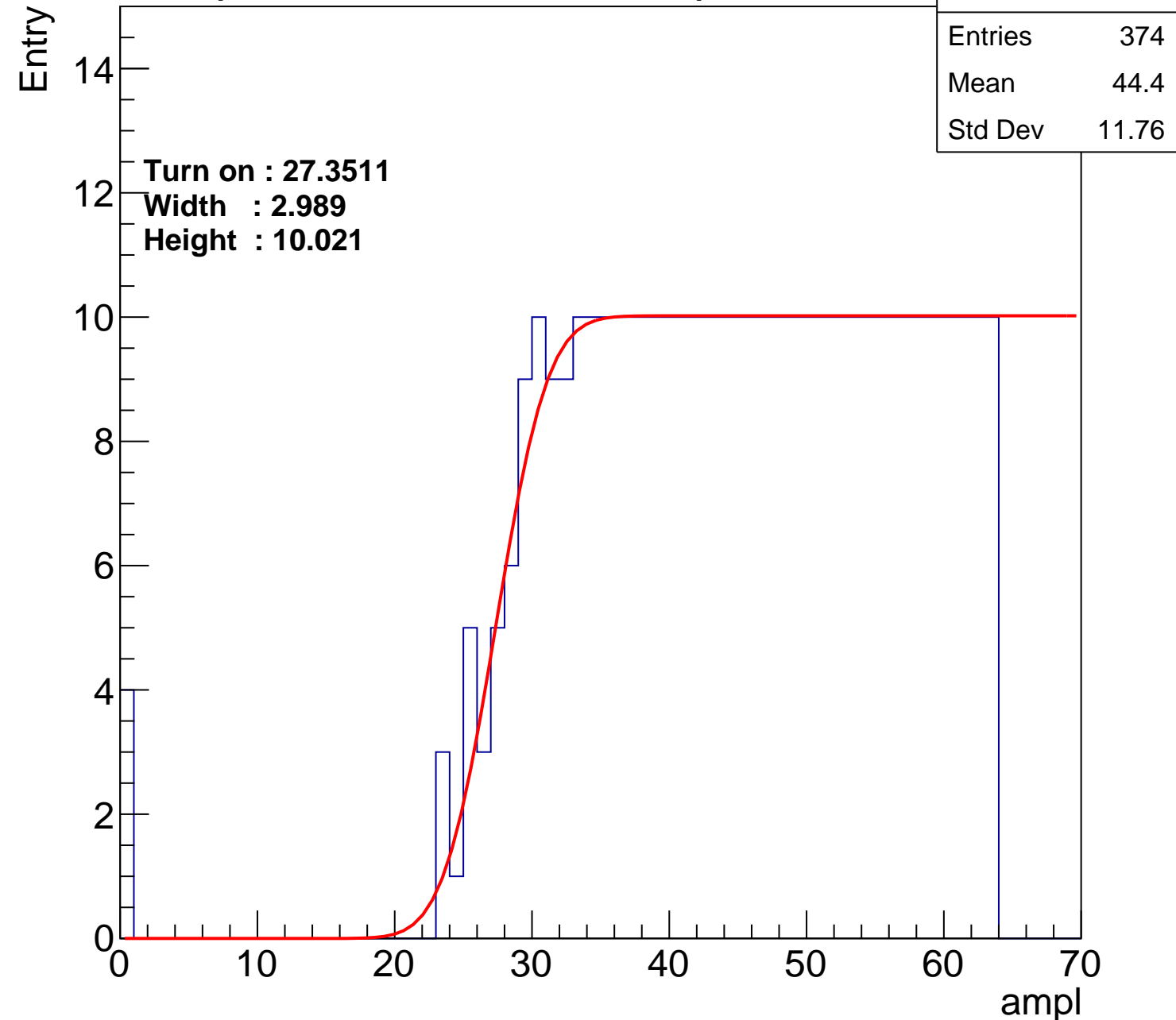
Width : 2.989

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch85

calib_packv5_042523_0143.root, FC#7, port C2

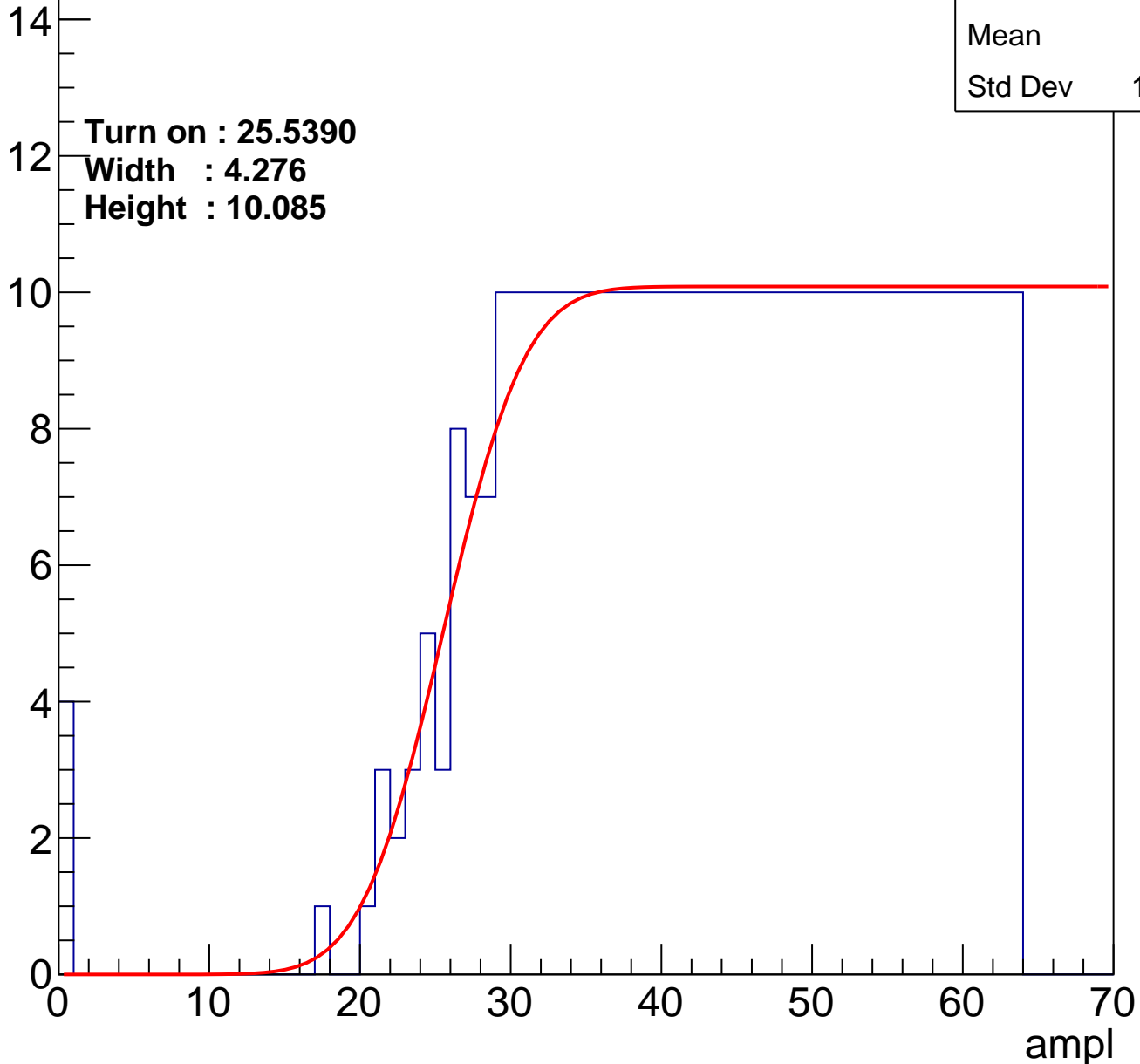
Entries	394
Mean	43.4
Std Dev	12.28

Turn on : 25.5390

Width : 4.276

Height : 10.085

Entry



B1L103S, U16-ch86

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.27
Std Dev	12.06

Turn on : 24.6016

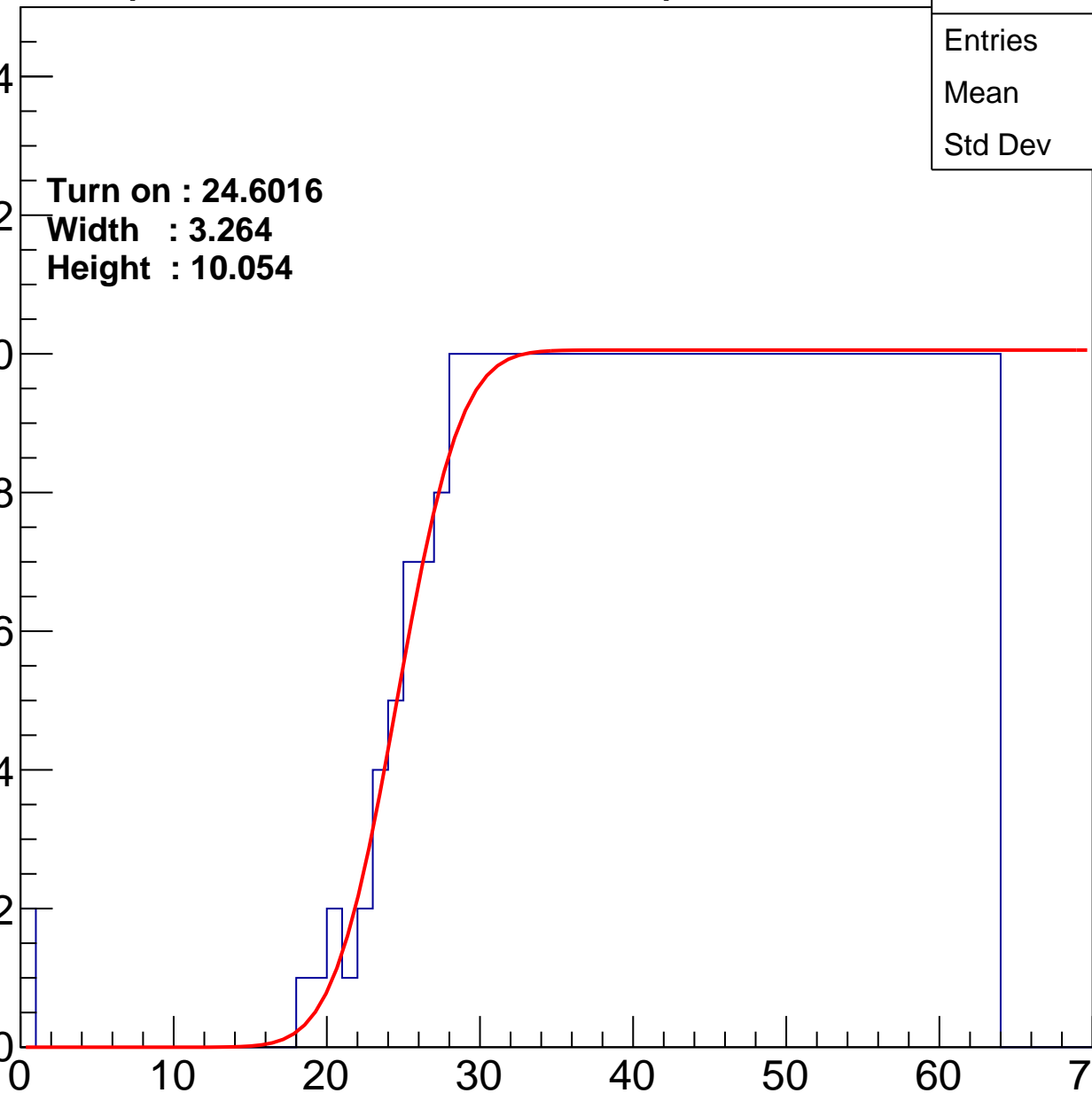
Width : 3.264

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch87

calib_packv5_042523_0143.root, FC#7, port C2

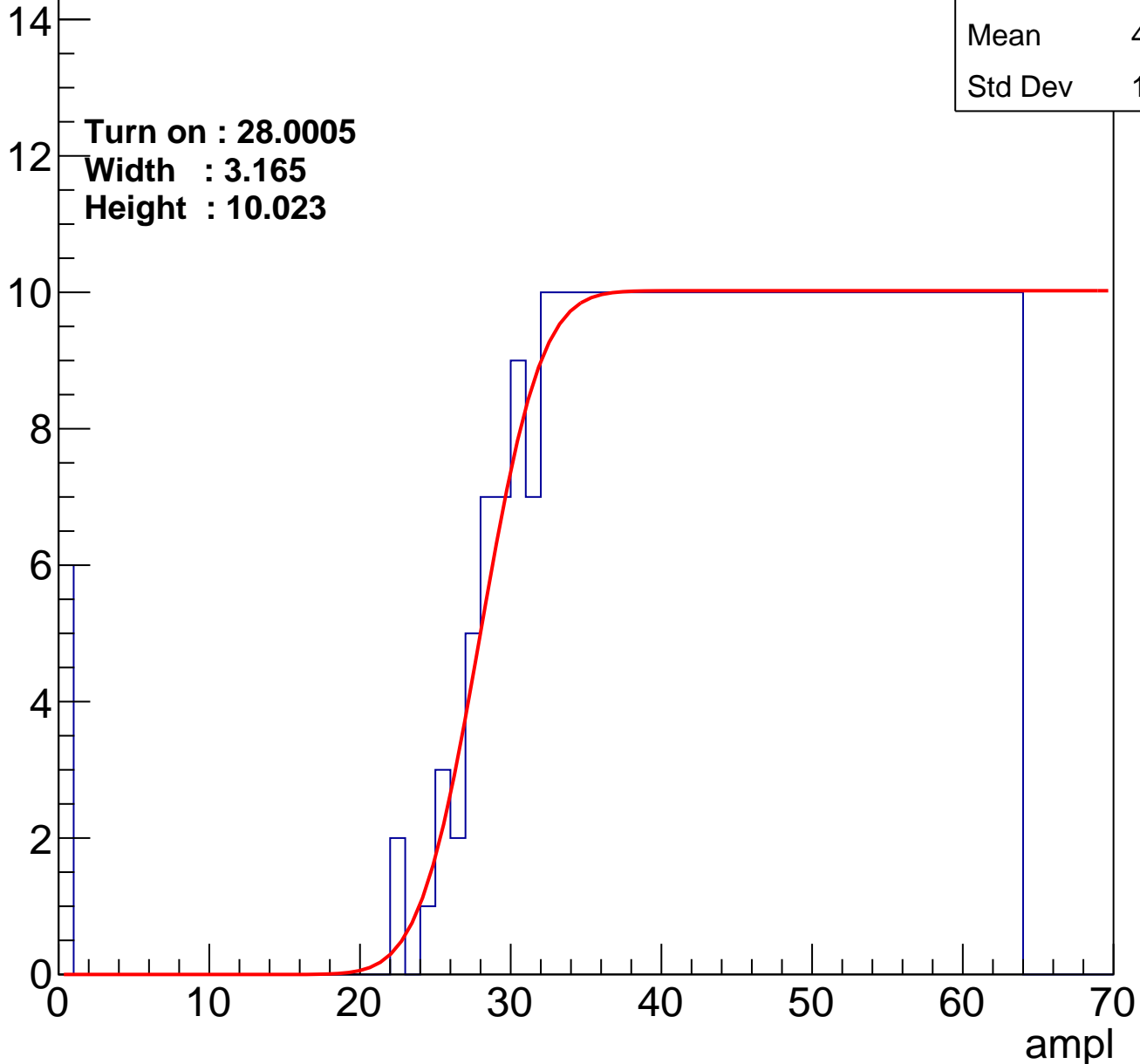
Entries	369
Mean	44.49
Std Dev	12.05

Turn on : 28.0005

Width : 3.165

Height : 10.023

Entry



B1L103S, U16-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.67
Std Dev	12.32

Turn on : 26.9361

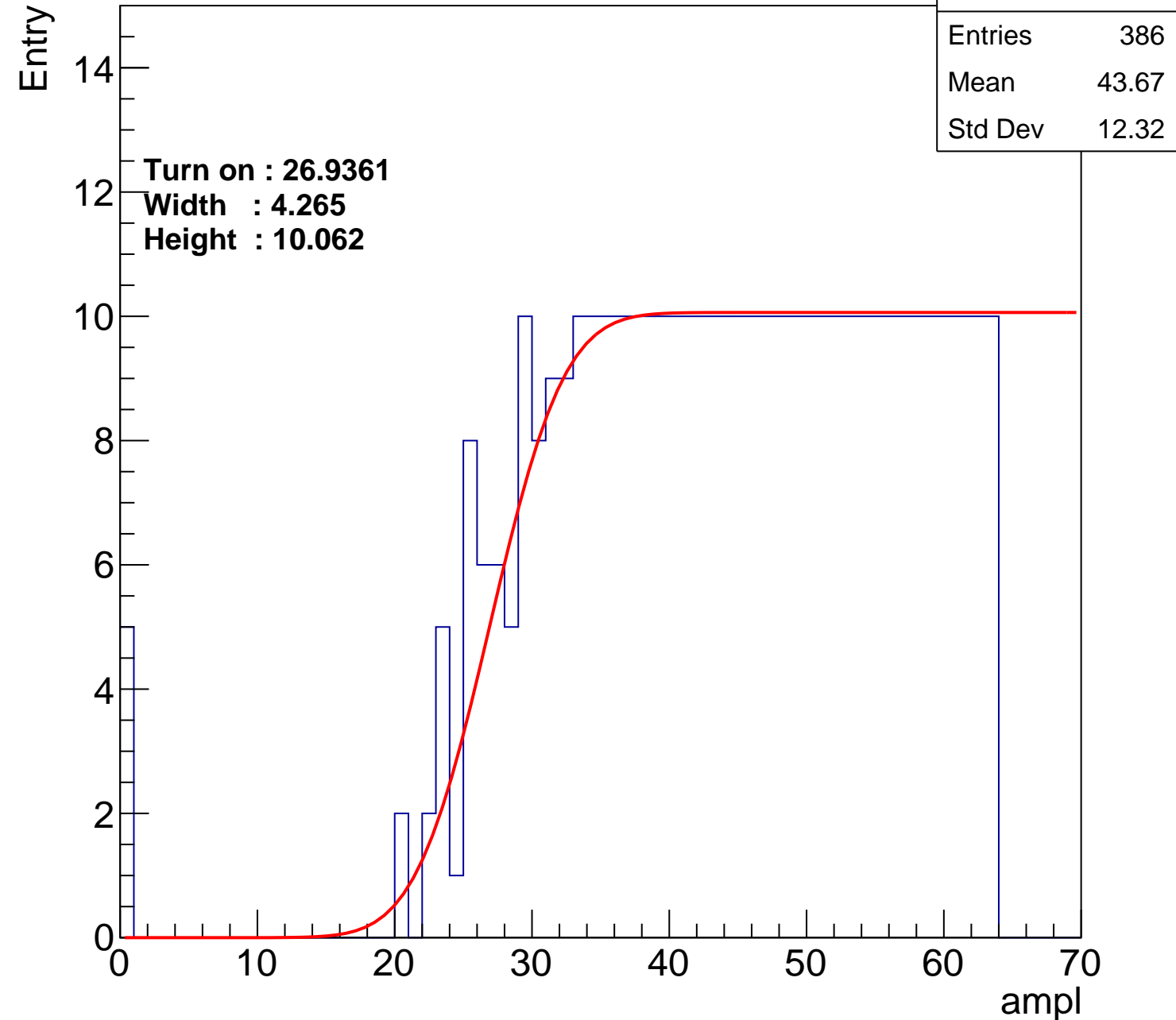
Width : 4.265

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch89

calib_packv5_042523_0143.root, FC#7, port C2

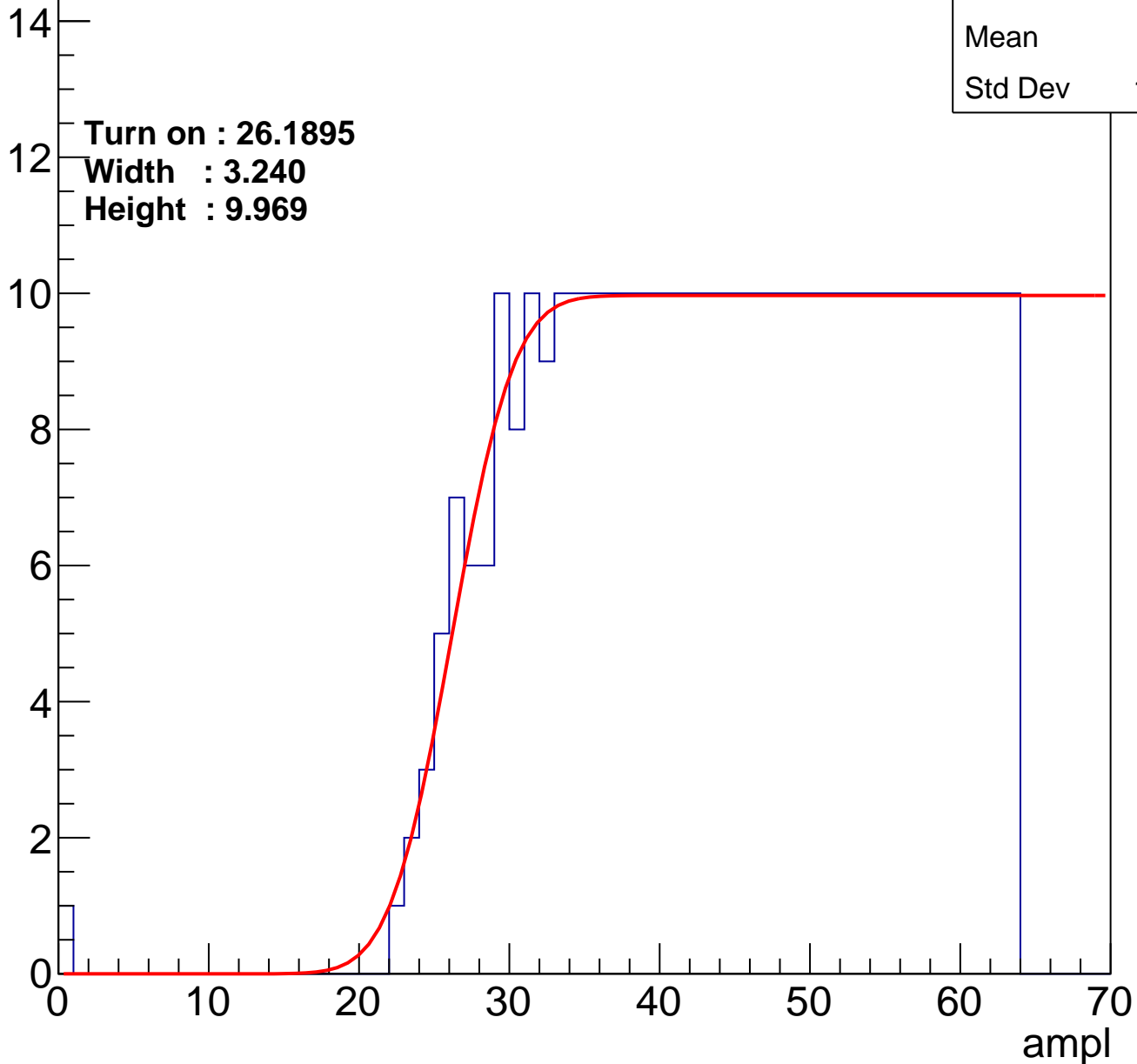
Entries	378
Mean	44.4
Std Dev	11.31

Turn on : 26.1895

Width : 3.240

Height : 9.969

Entry



B1L103S, U16-ch90

calib_packv5_042523_0143.root, FC#7, port C2

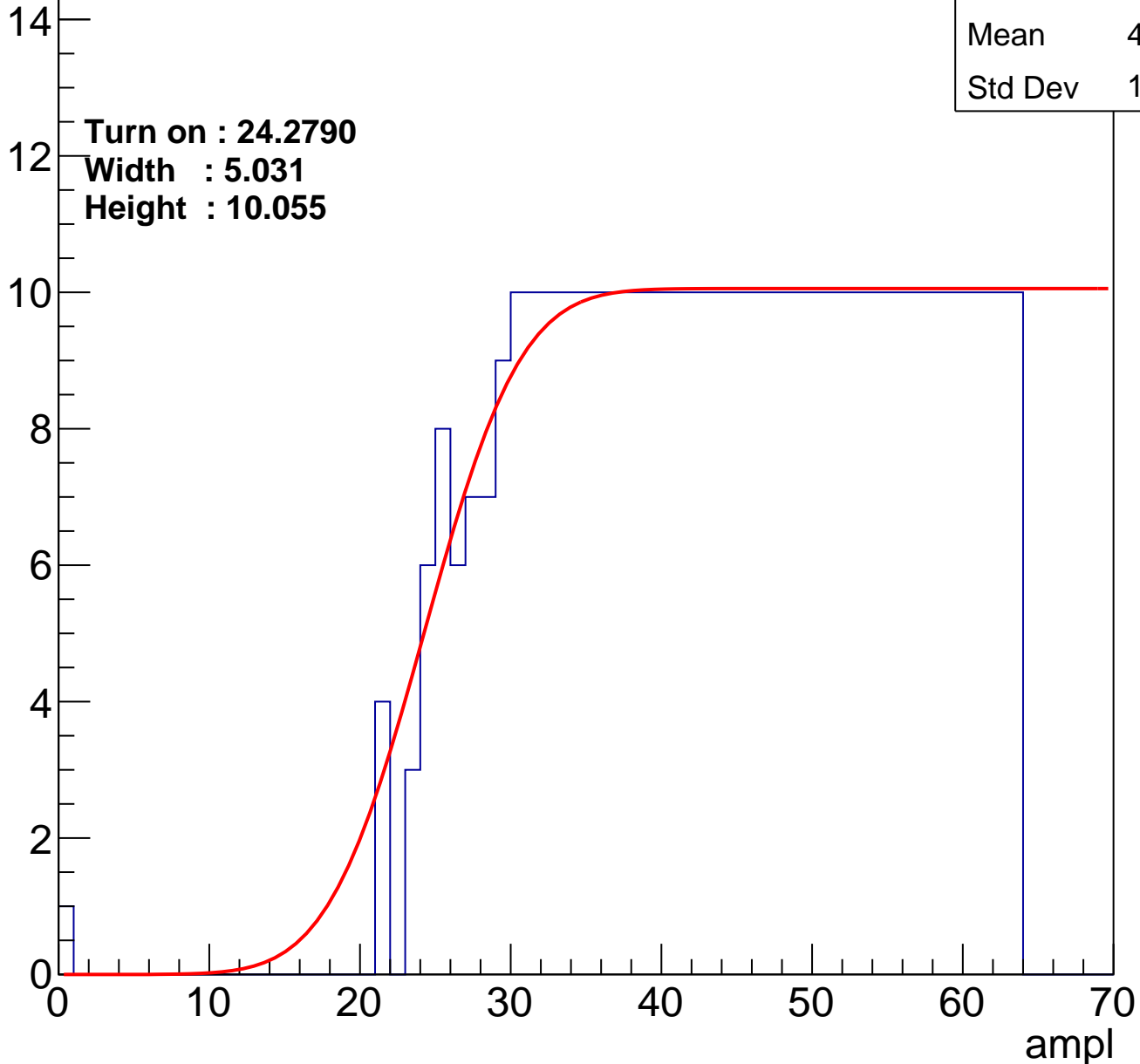
Entries	391
Mean	43.76
Std Dev	11.67

Turn on : 24.2790

Width : 5.031

Height : 10.055

Entry



B1L103S, U16-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.57
Std Dev	11.73

Turn on : 27.9796

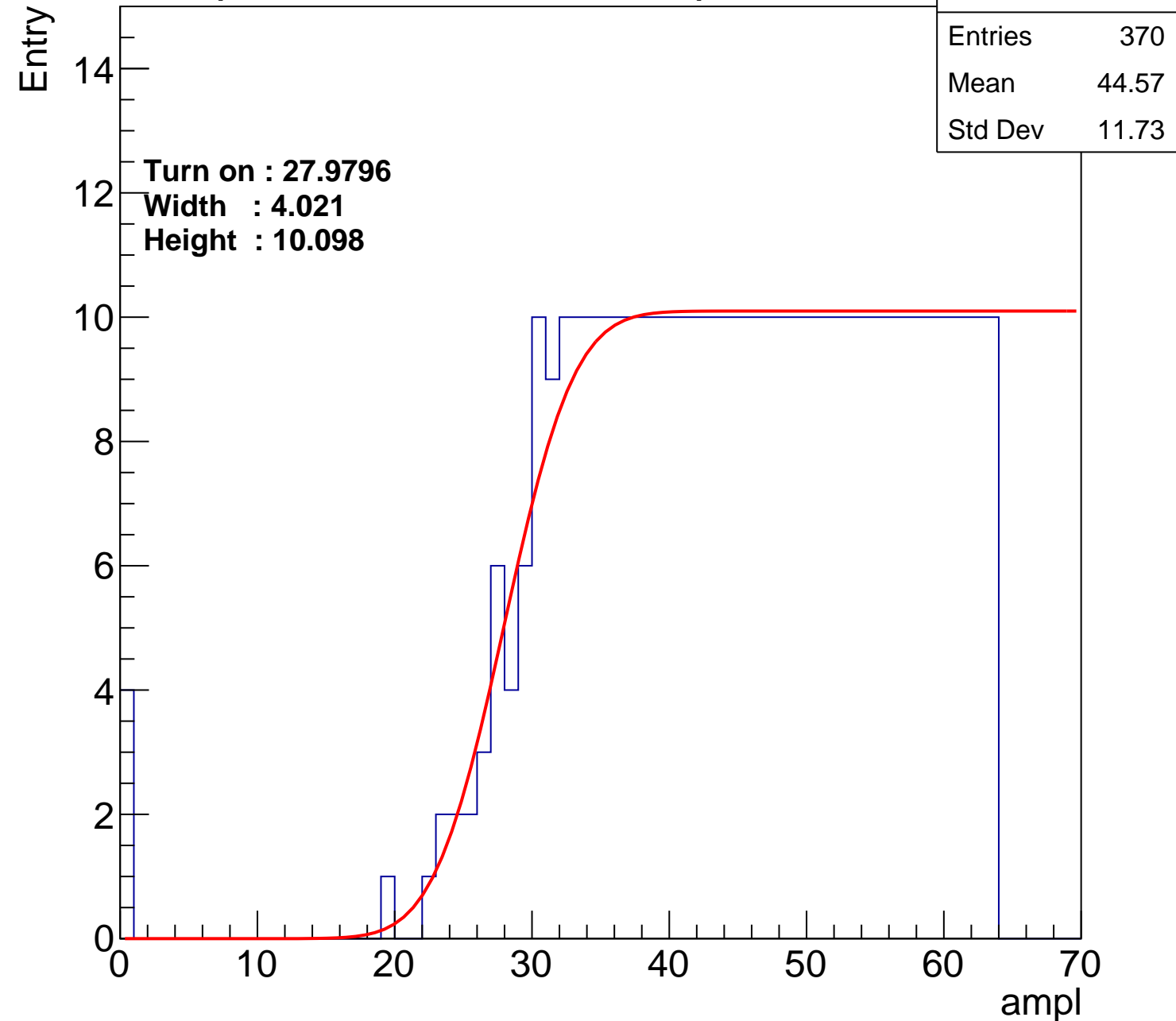
Width : 4.021

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.19
Std Dev	11.49

Turn on : 26.2738

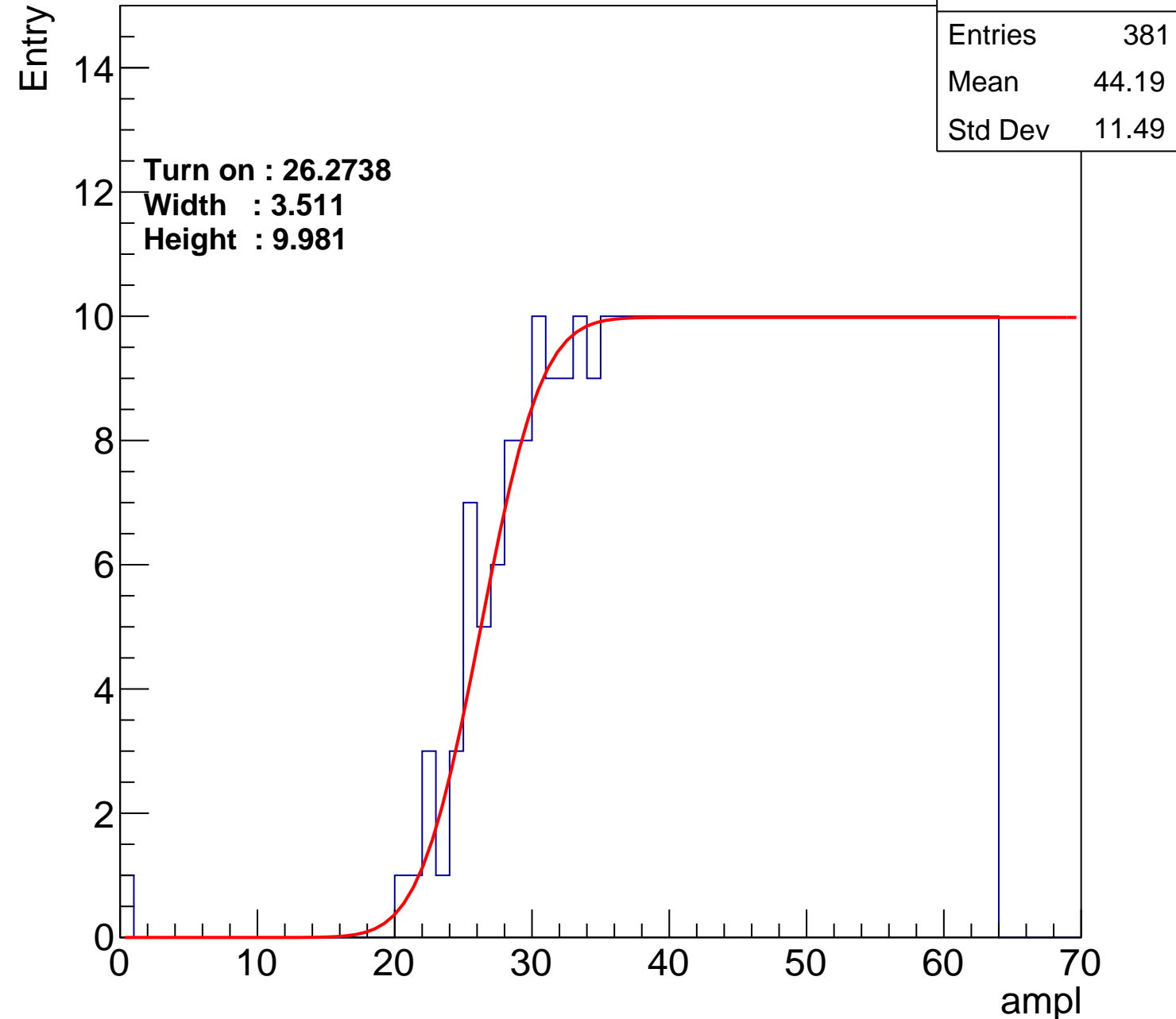
Width : 3.511

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch93

calib_packv5_042523_0143.root, FC#7, port C2

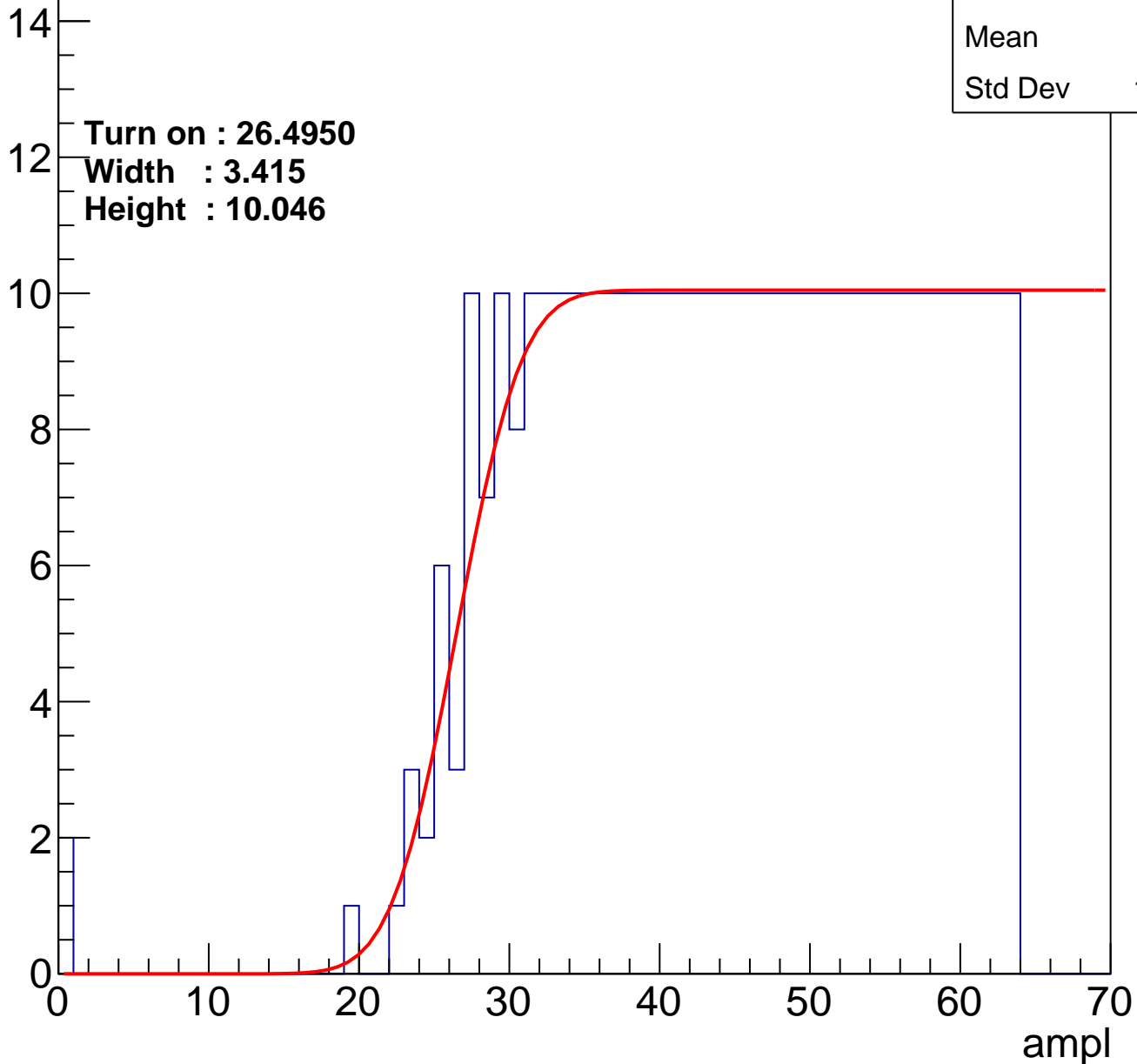
Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.4950

Width : 3.415

Height : 10.046

Entry



B1L103S, U16-ch94

calib_packv5_042523_0143.root, FC#7, port C2

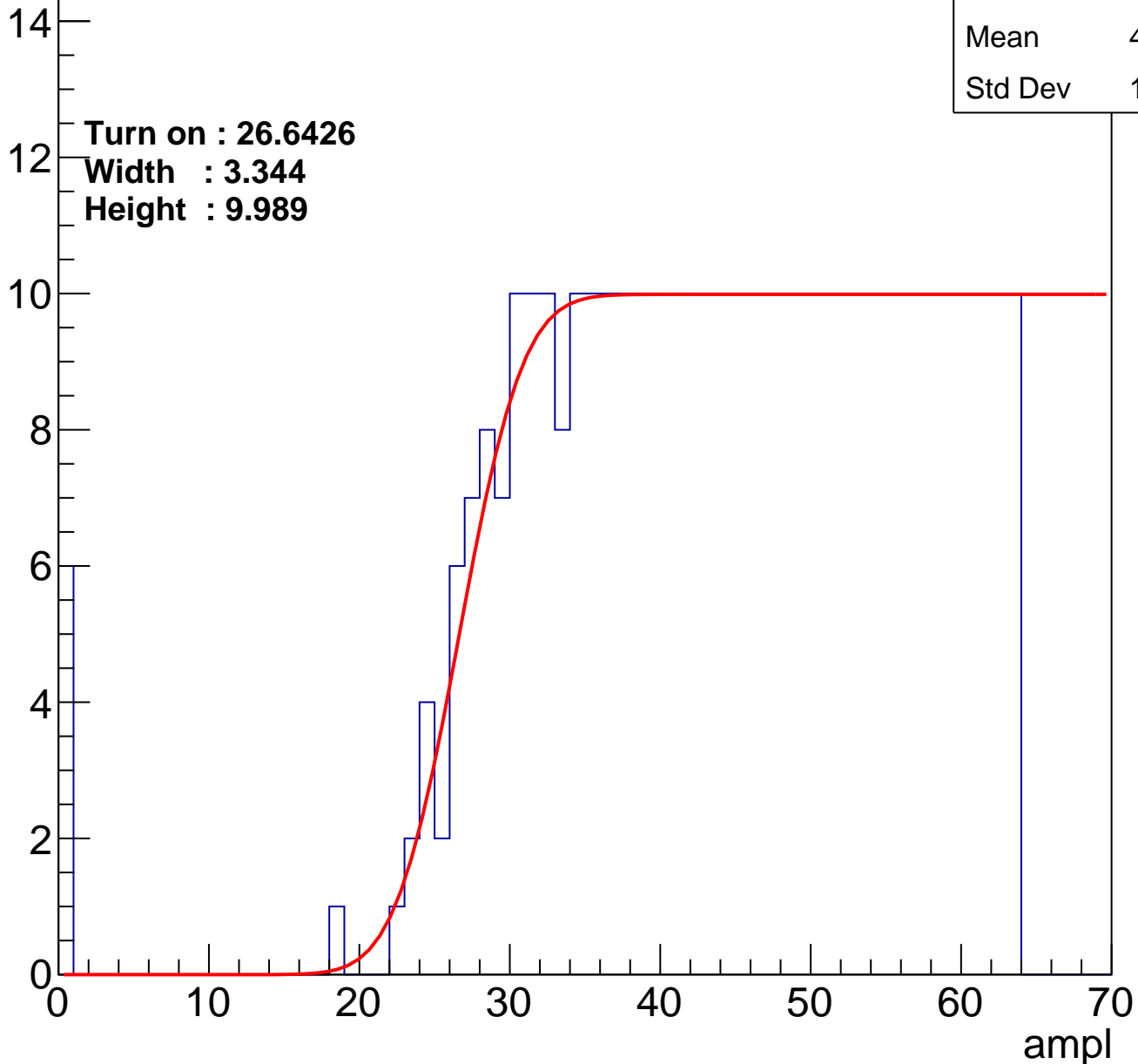
Entries	382
Mean	43.84
Std Dev	12.34

Turn on : 26.6426

Width : 3.344

Height : 9.989

Entry



B1L103S, U16-ch95

calib_packv5_042523_0143.root, FC#7, port C2

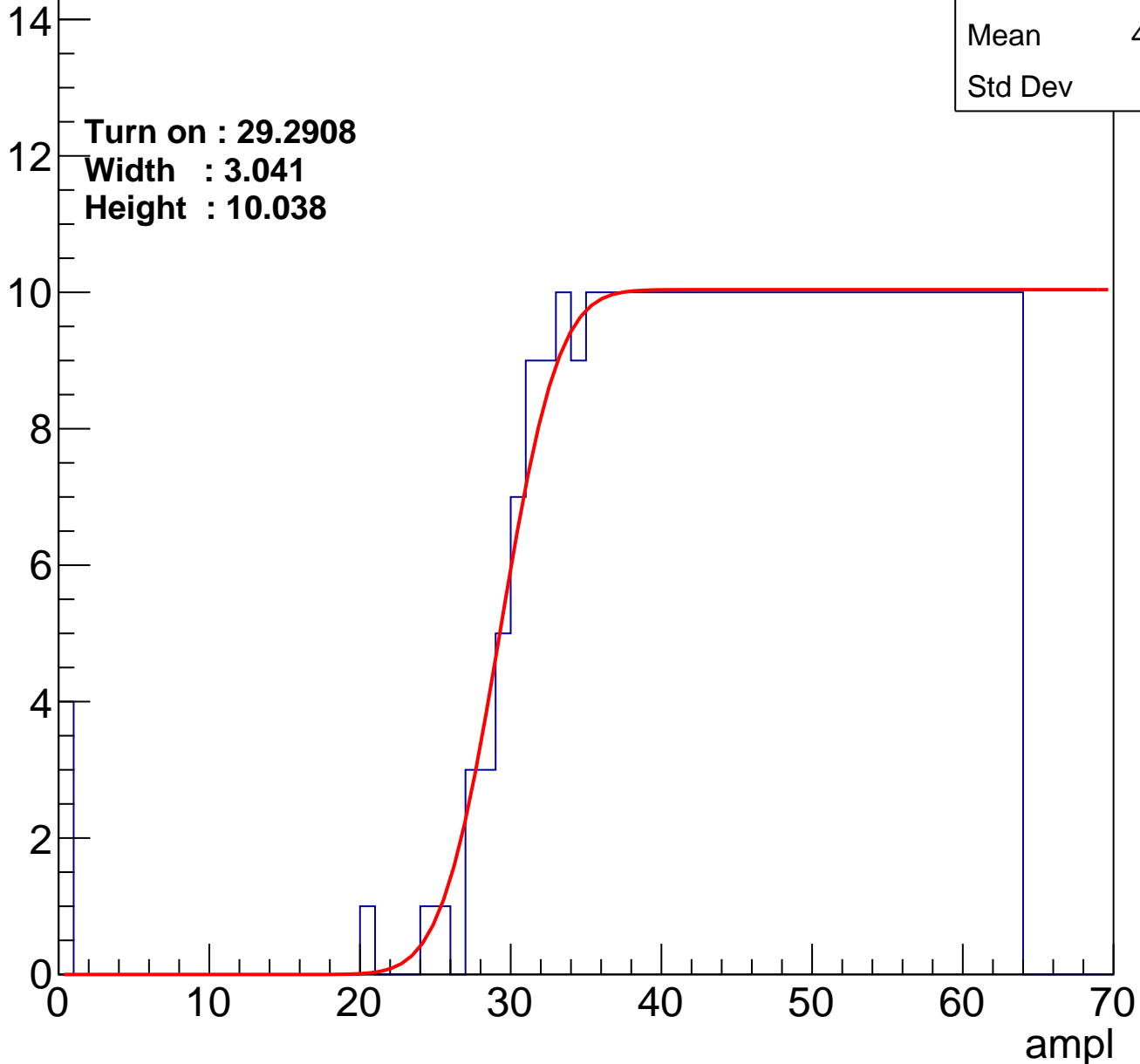
Entries	352
Mean	45.46
Std Dev	11.3

Turn on : 29.2908

Width : 3.041

Height : 10.038

Entry



B1L103S, U16-ch96

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.32
Std Dev	11.78

Turn on : 27.0797

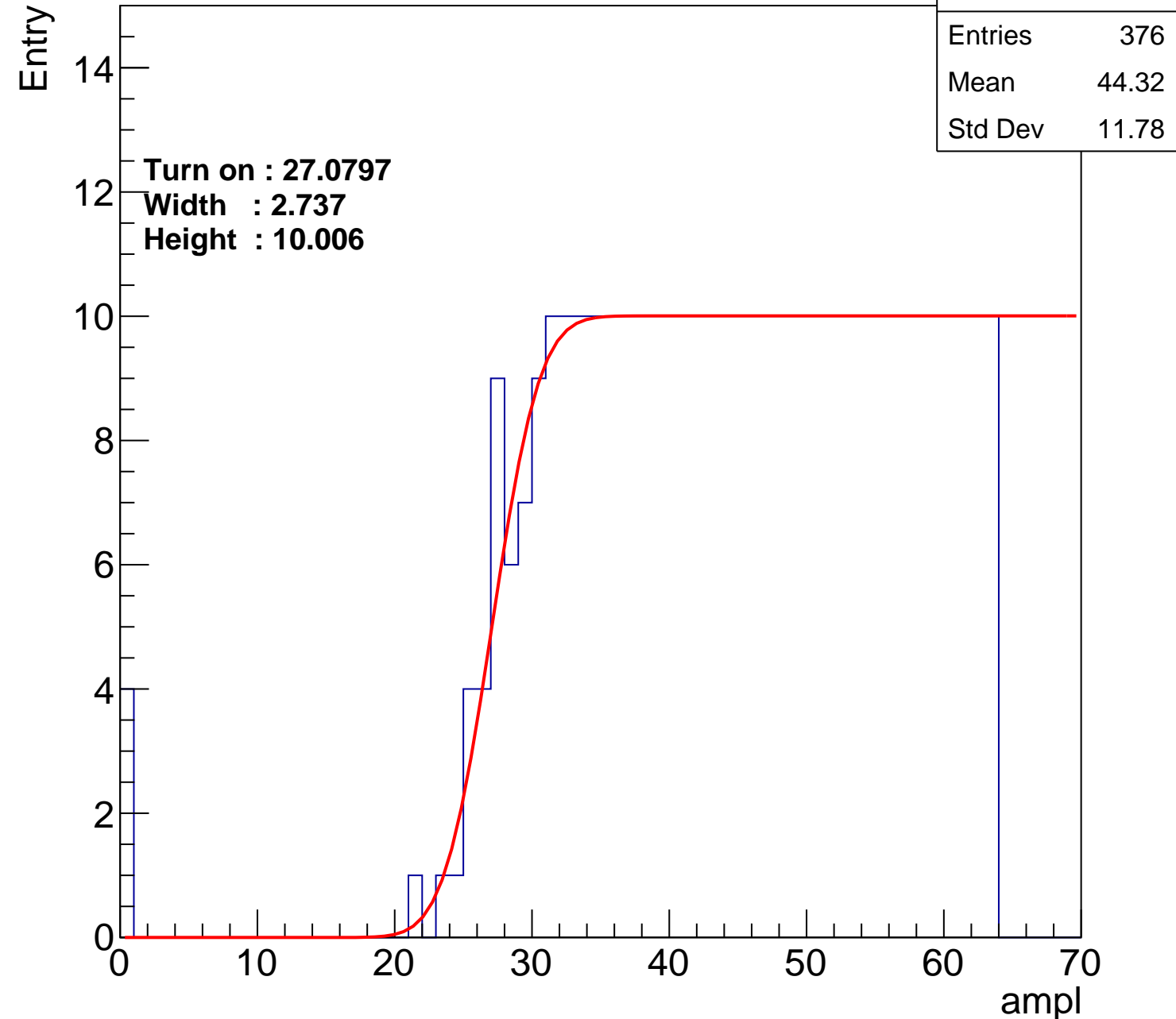
Width : 2.737

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch97

calib_packv5_042523_0143.root, FC#7, port C2

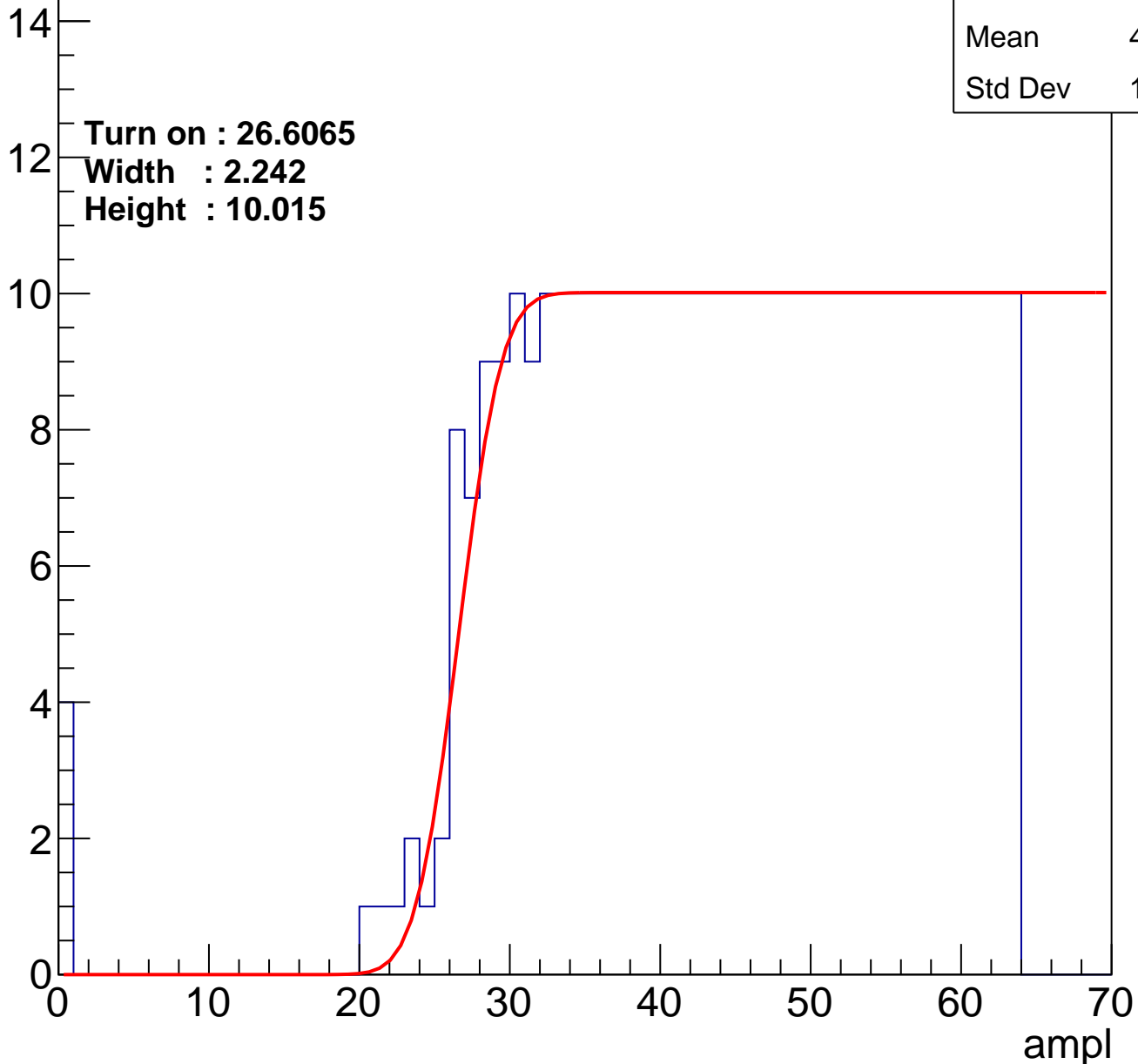
Entries	384
Mean	43.94
Std Dev	11.96

Turn on : 26.6065

Width : 2.242

Height : 10.015

Entry



B1L103S, U16-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.42
Std Dev	11.97

Turn on : 25.2337

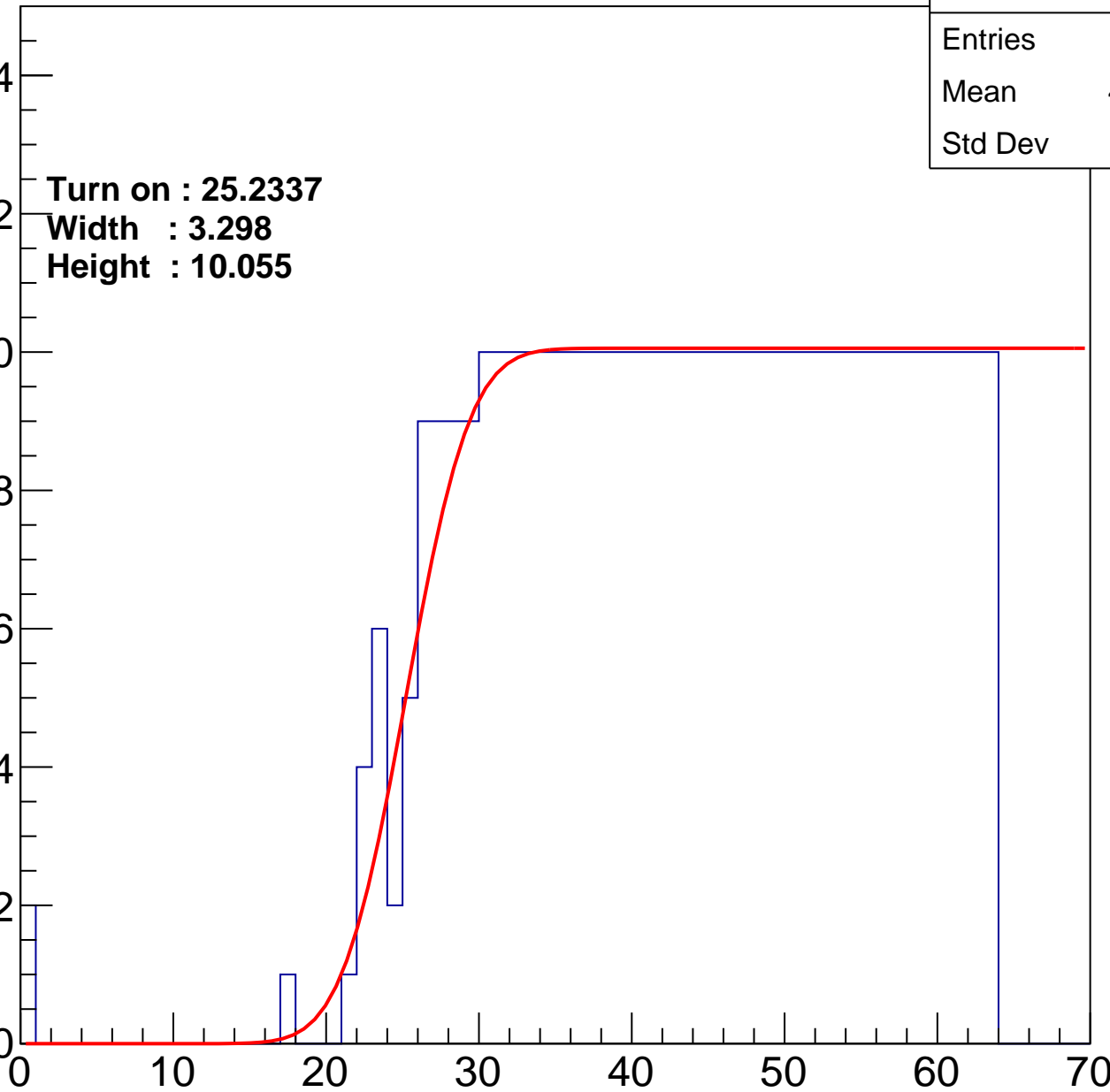
Width : 3.298

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch99

calib_packv5_042523_0143.root, FC#7, port C2

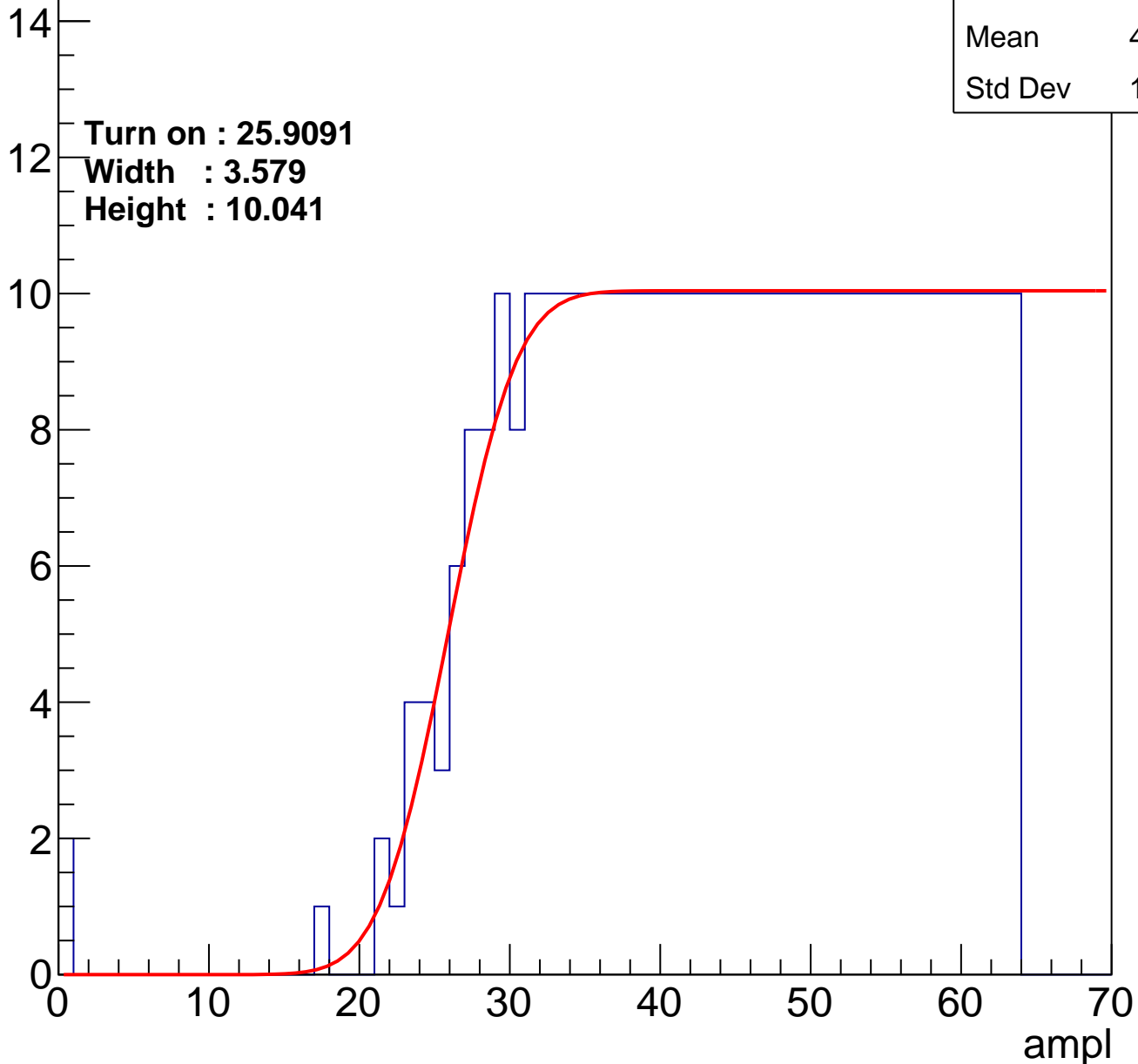
Entries	387
Mean	43.88
Std Dev	11.77

Turn on : 25.9091

Width : 3.579

Height : 10.041

Entry



B1L103S, U16-ch100

calib_packv5_042523_0143.root, FC#7, port C2

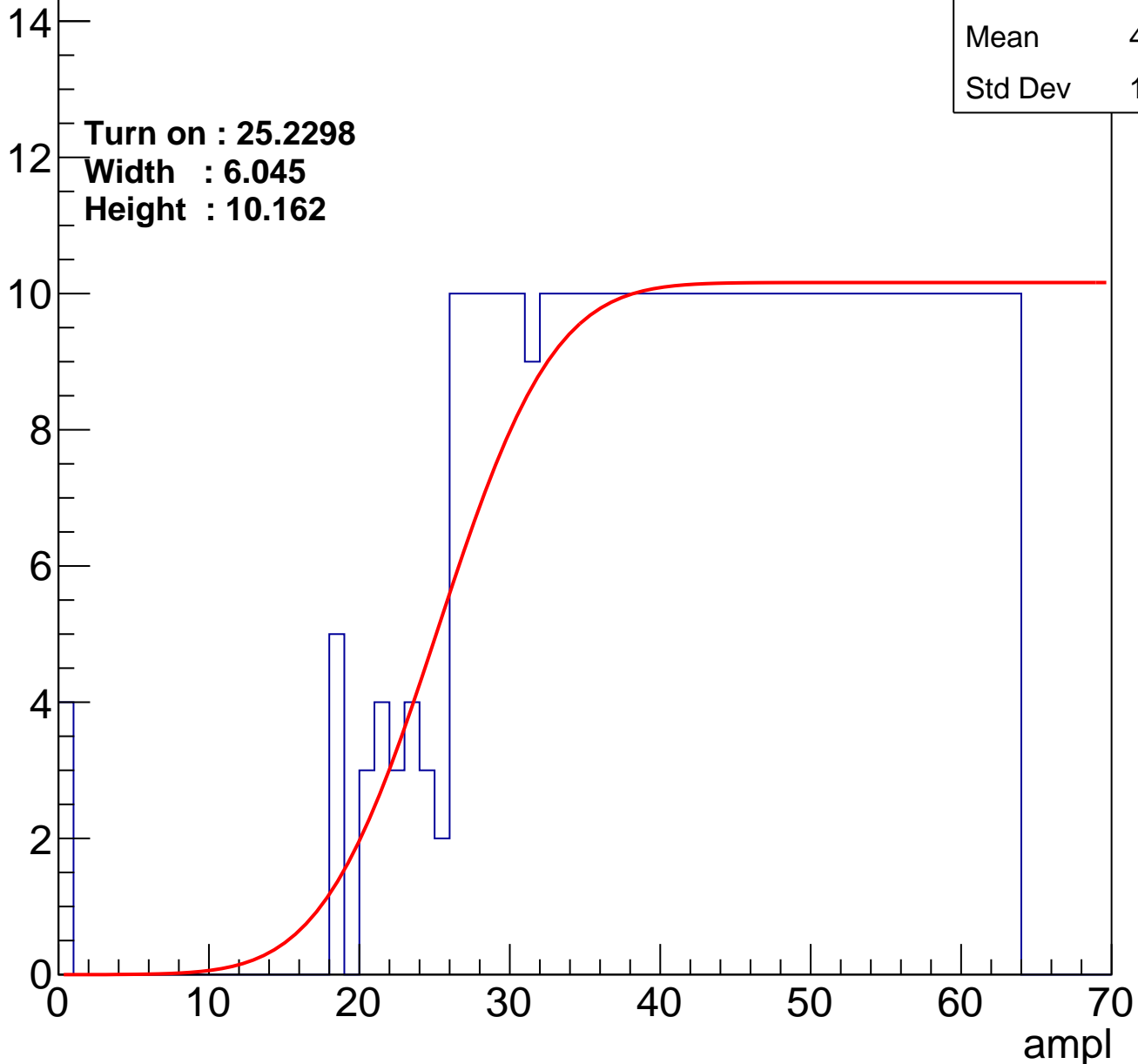
Entries	407
Mean	42.73
Std Dev	12.64

Turn on : 25.2298

Width : 6.045

Height : 10.162

Entry



B1L103S, U16-ch101

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.34
Std Dev	11.82

Turn on : 27.2783

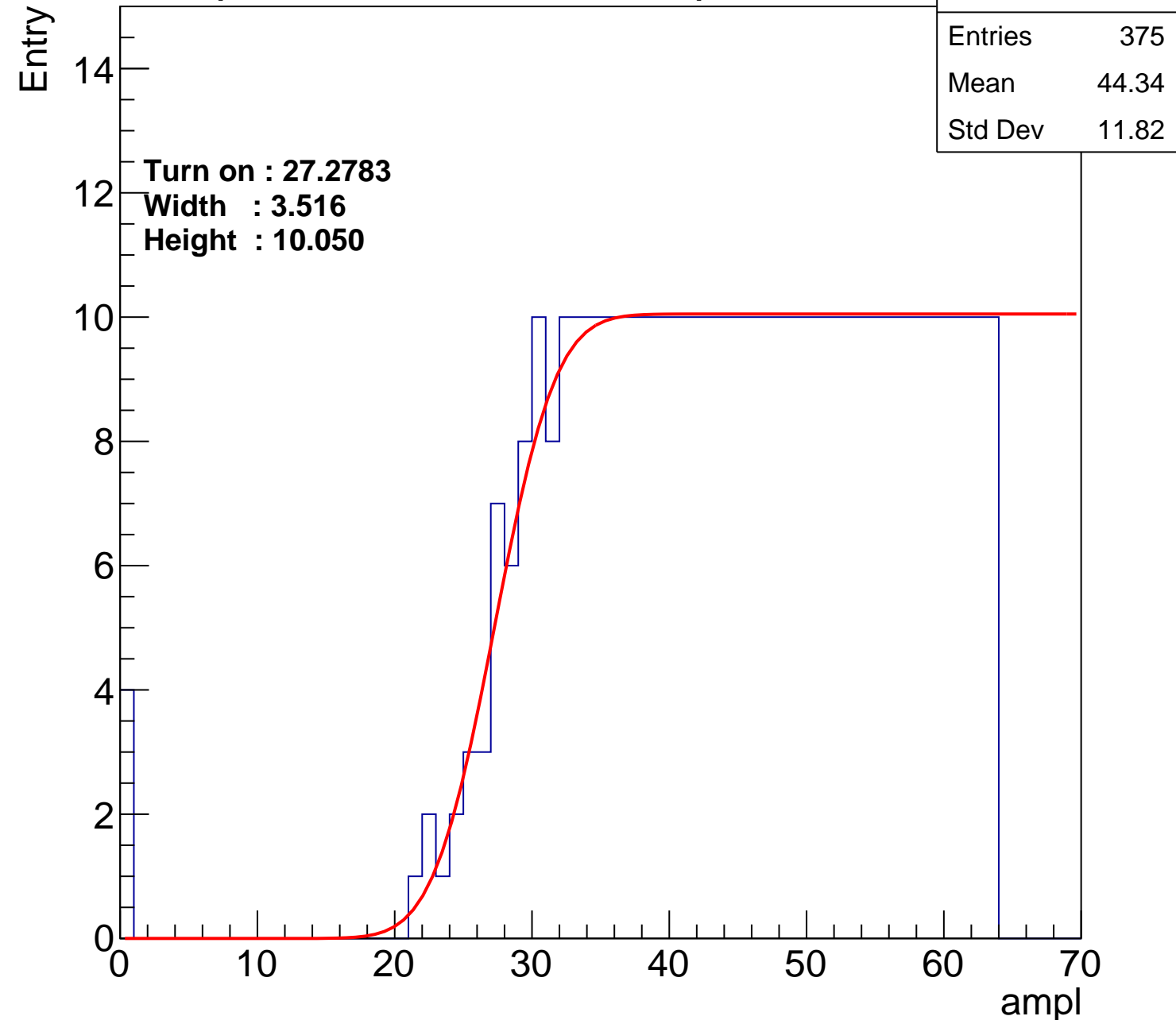
Width : 3.516

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch102

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43
Std Dev	12.43

Turn on : 24.5778

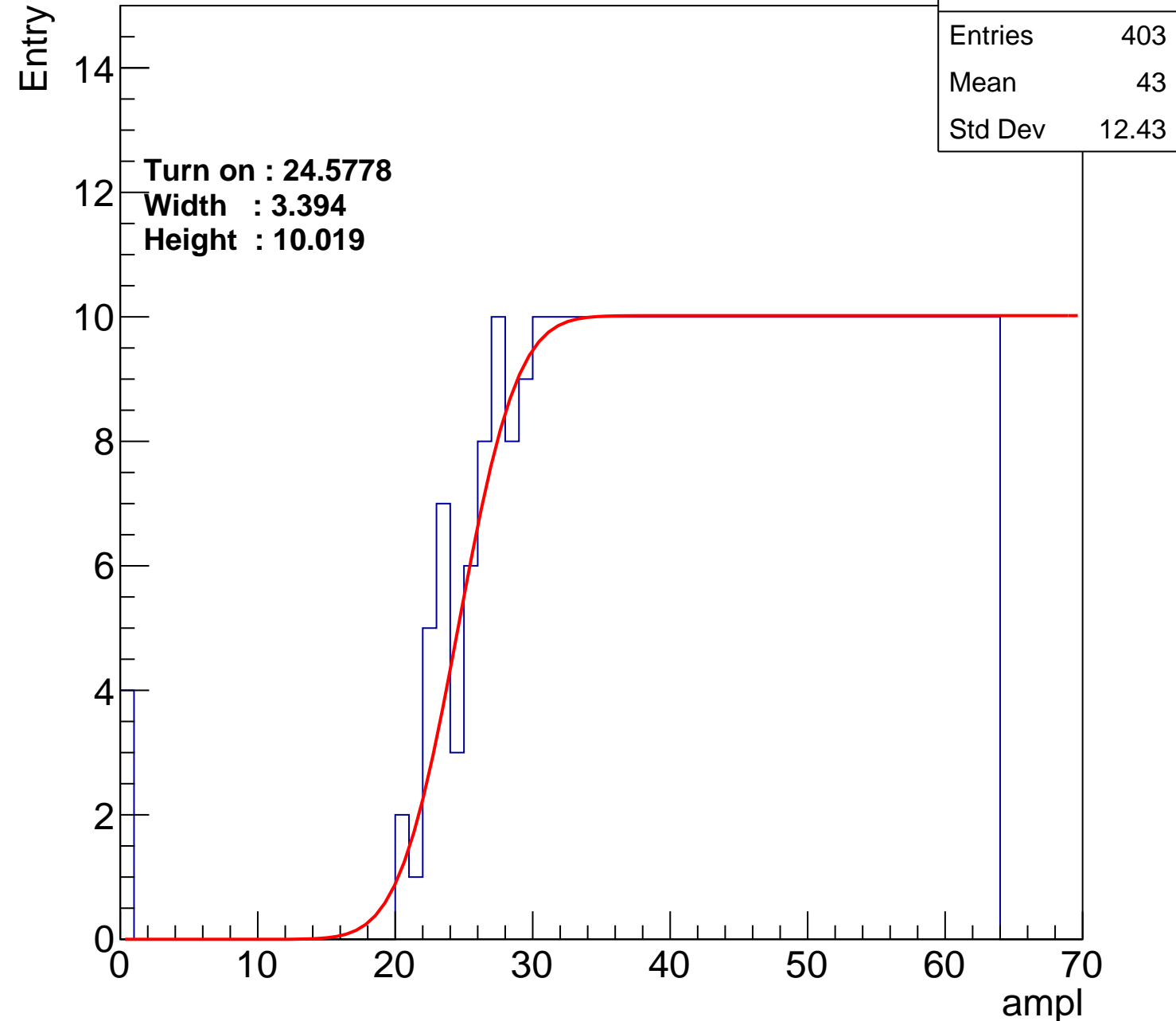
Width : 3.394

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.73
Std Dev	11.31

Turn on : 27.7122

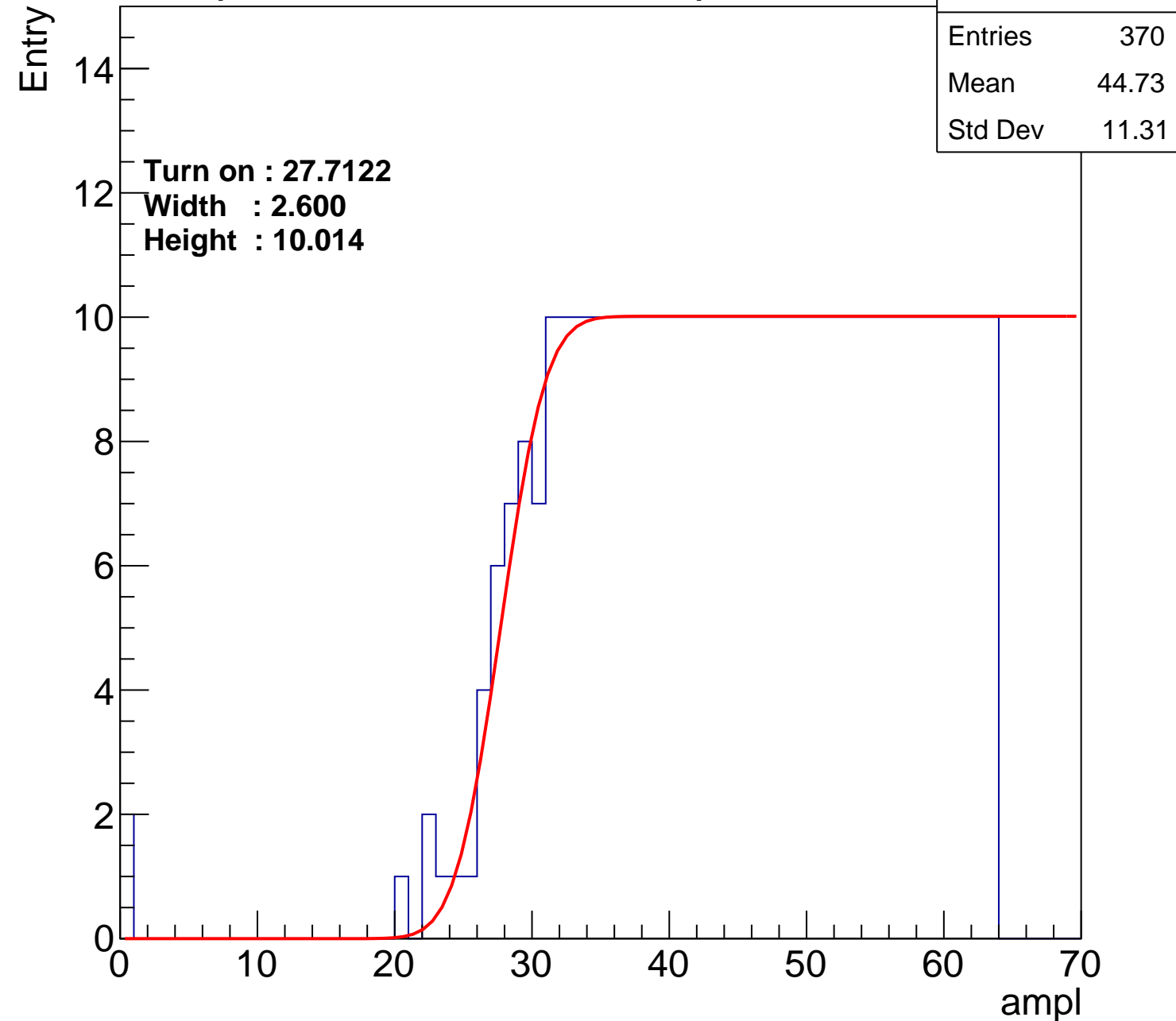
Width : 2.600

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch104

calib_packv5_042523_0143.root, FC#7, port C2

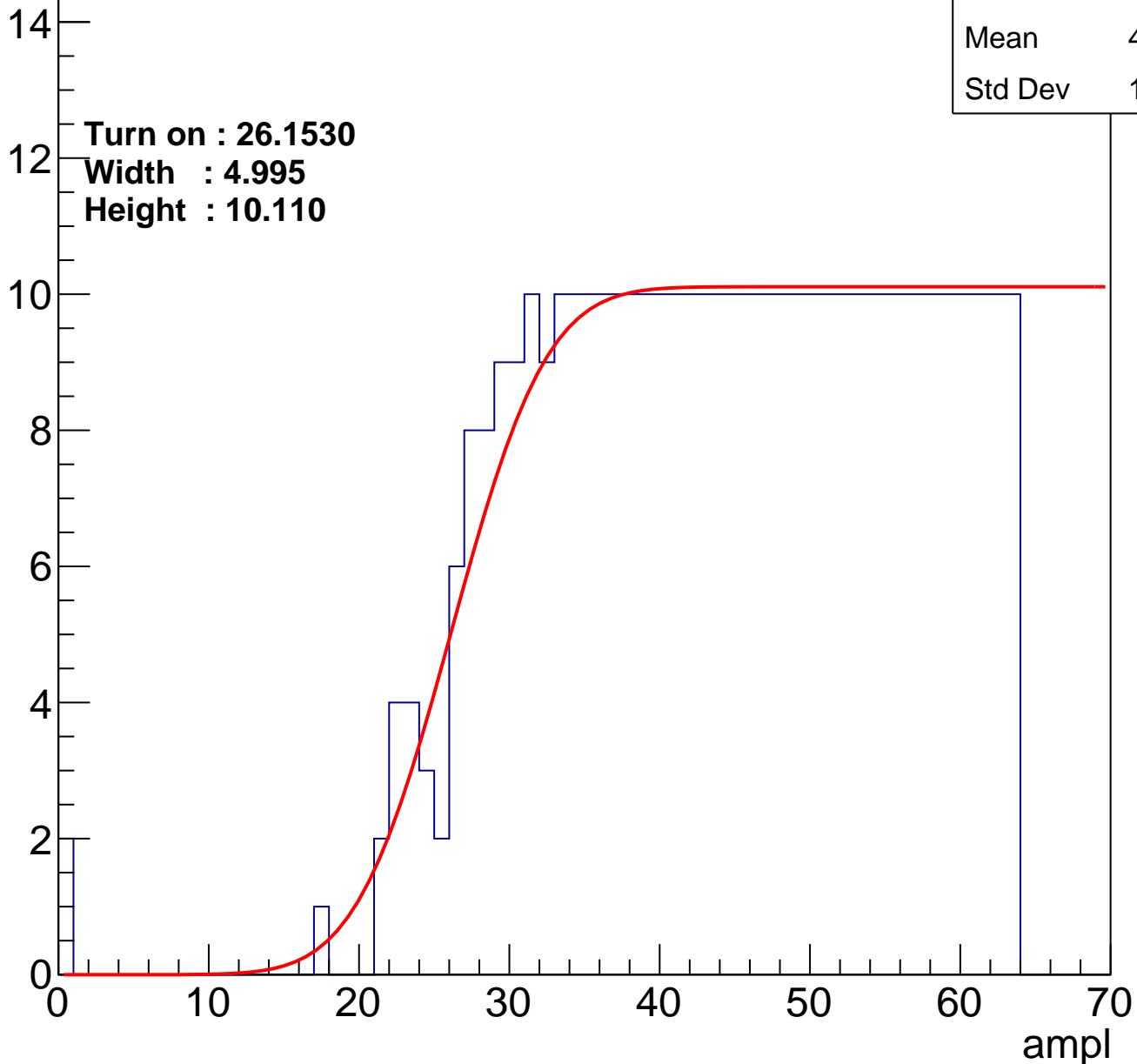
Entries	387
Mean	43.84
Std Dev	11.82

Turn on : 26.1530

Width : 4.995

Height : 10.110

Entry



B1L103S, U16-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	43.83
Std Dev	12.37

Turn on : 27.6849

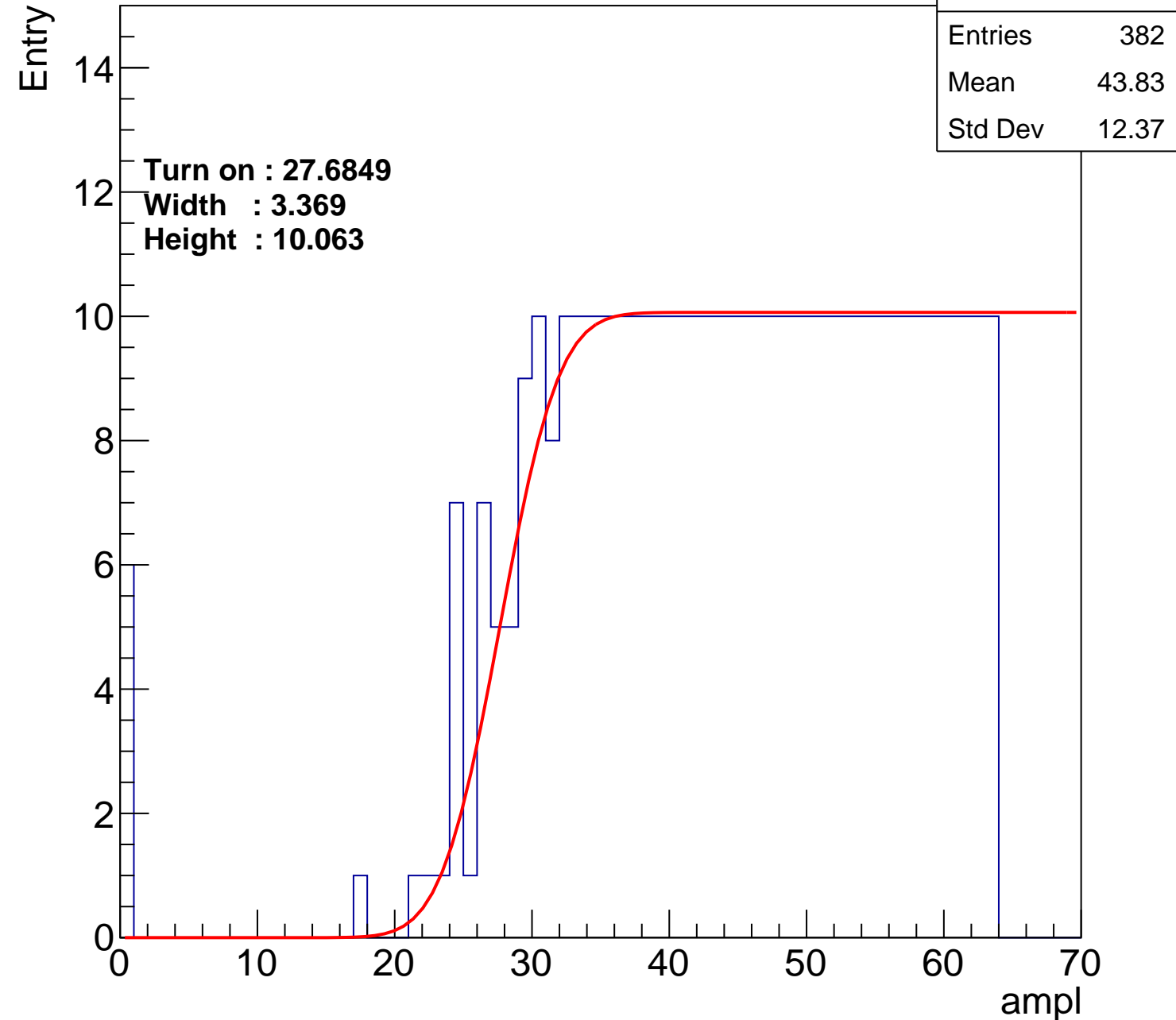
Width : 3.369

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch106

calib_packv5_042523_0143.root, FC#7, port C2

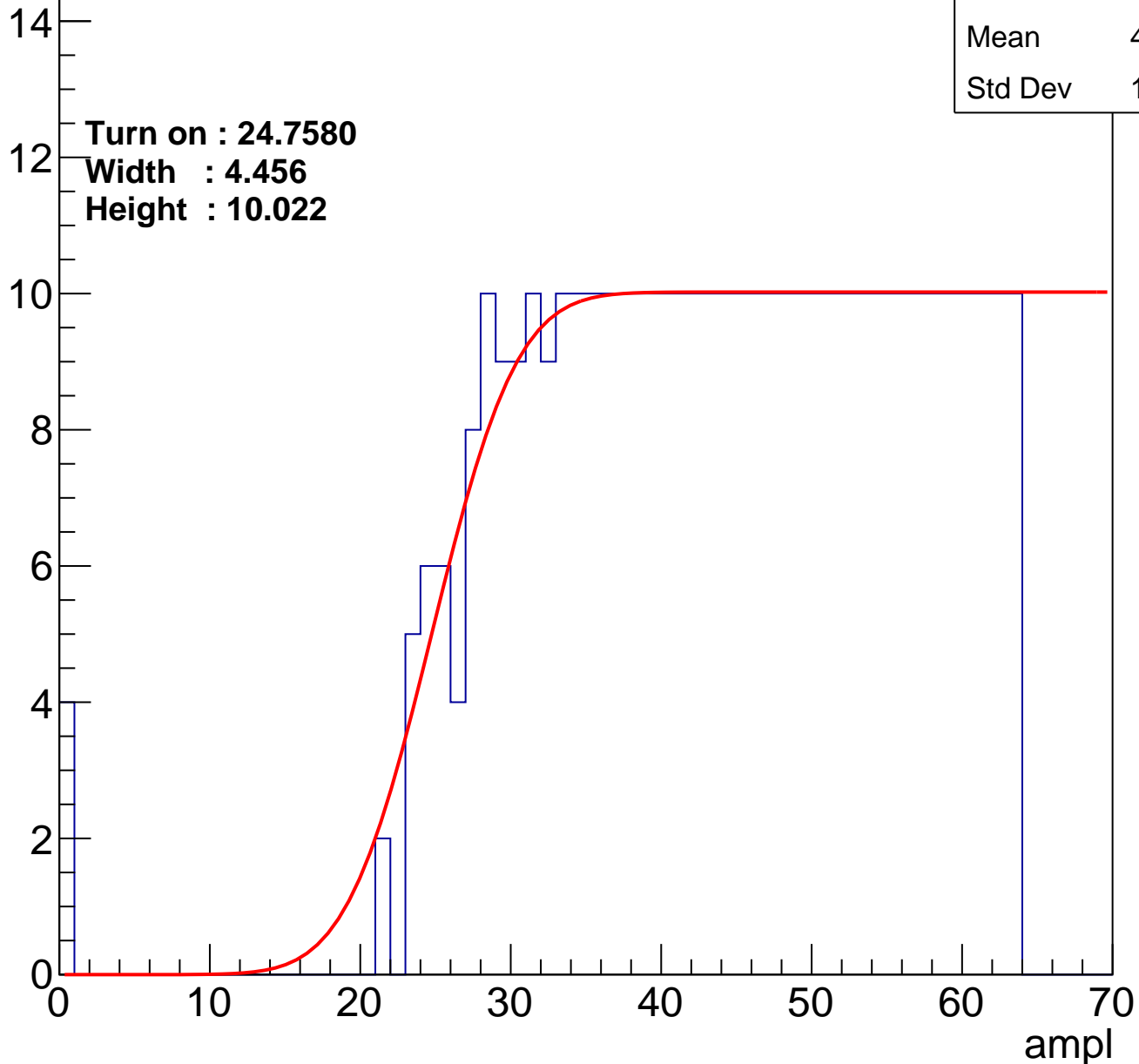
Entries	392
Mean	43.52
Std Dev	12.18

Turn on : 24.7580

Width : 4.456

Height : 10.022

Entry



B1L103S, U16-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	45.02
Std Dev	11

Turn on : 27.9482

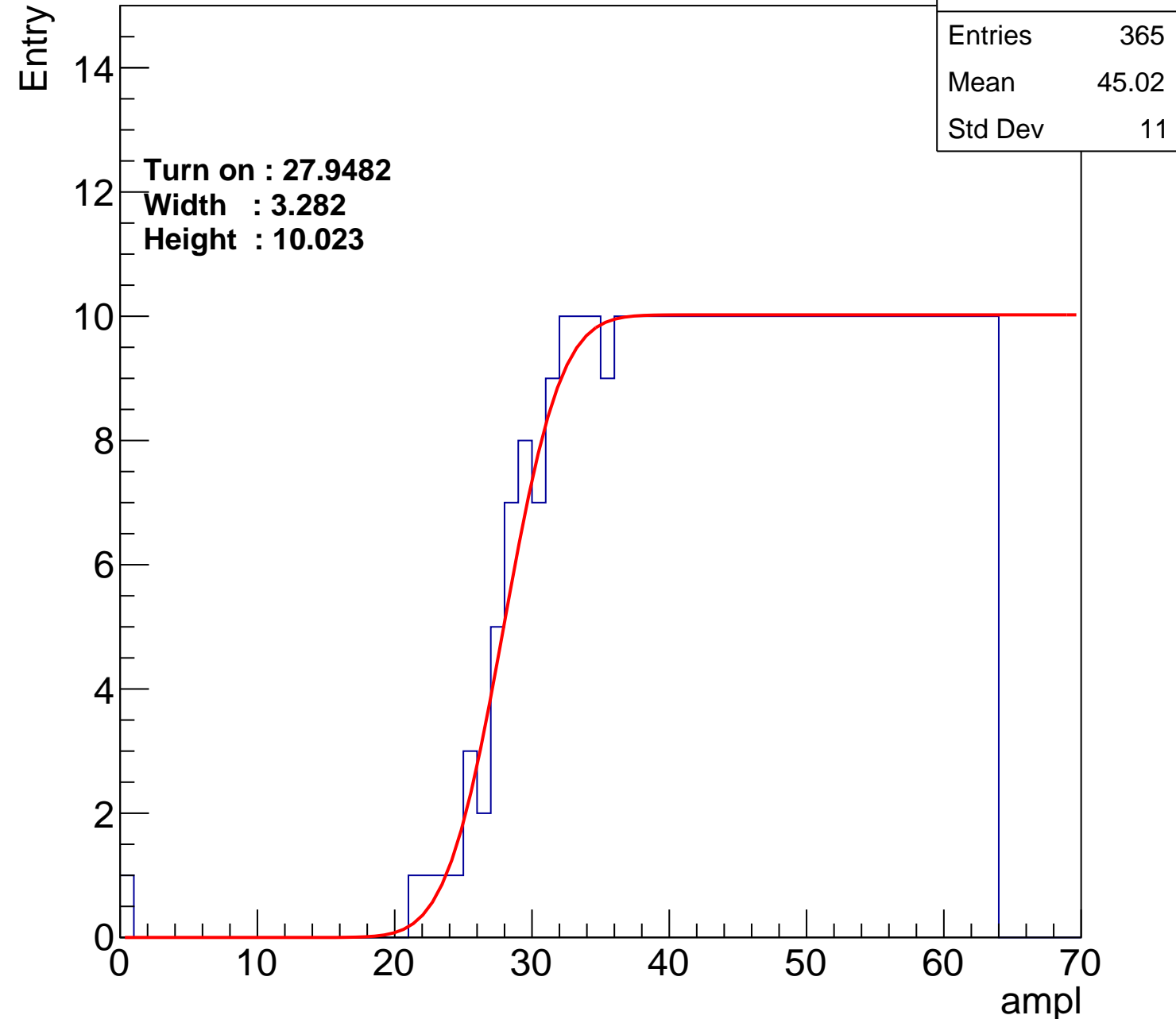
Width : 3.282

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.8
Std Dev	11.83

Turn on : 25.7150

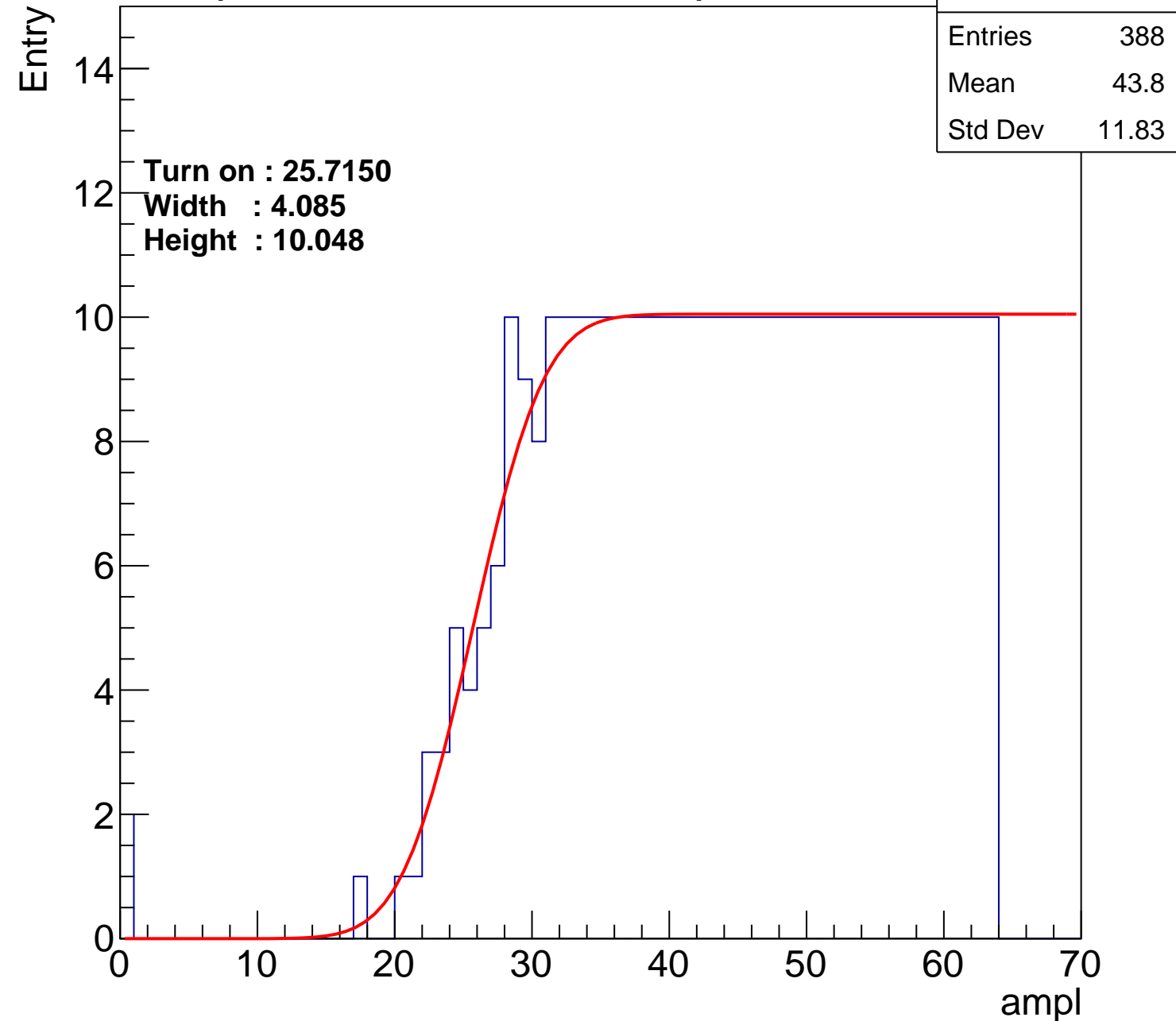
Width : 4.085

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.21
Std Dev	11.25

Turn on : 28.1329

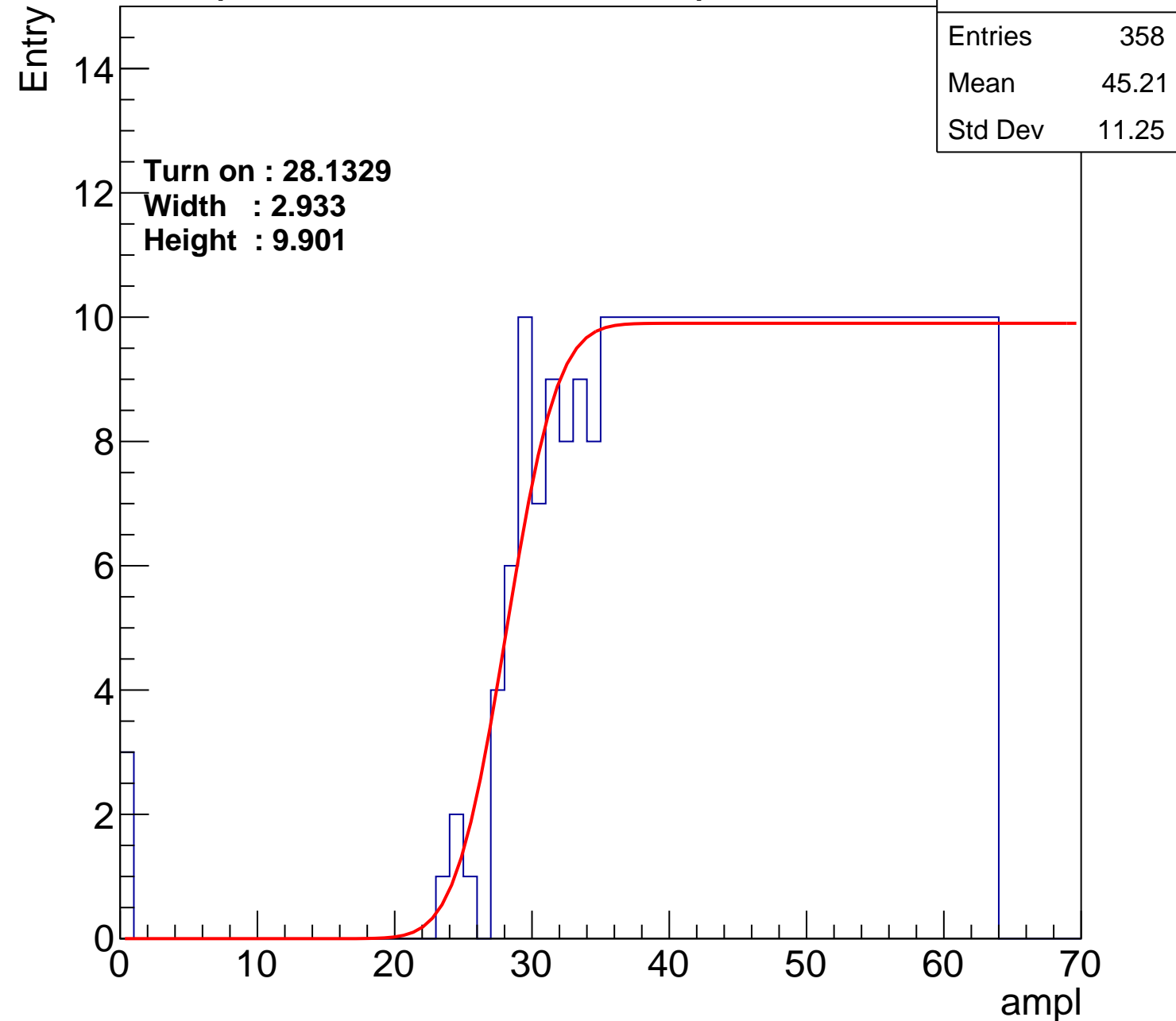
Width : 2.933

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.2
Std Dev	12.59

Turn on : 25.4325

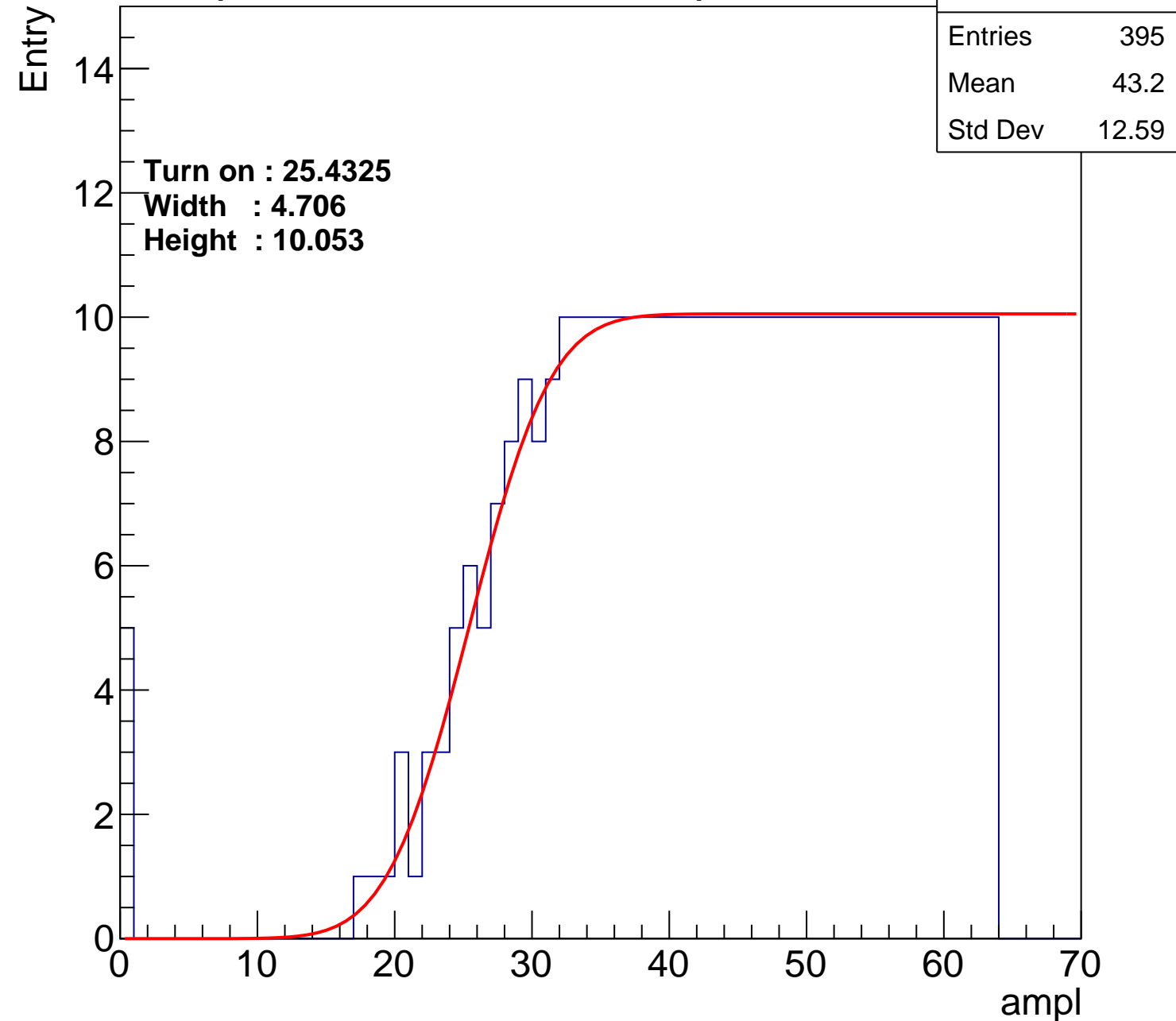
Width : 4.706

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.66
Std Dev	12.01

Turn on : 25.0053

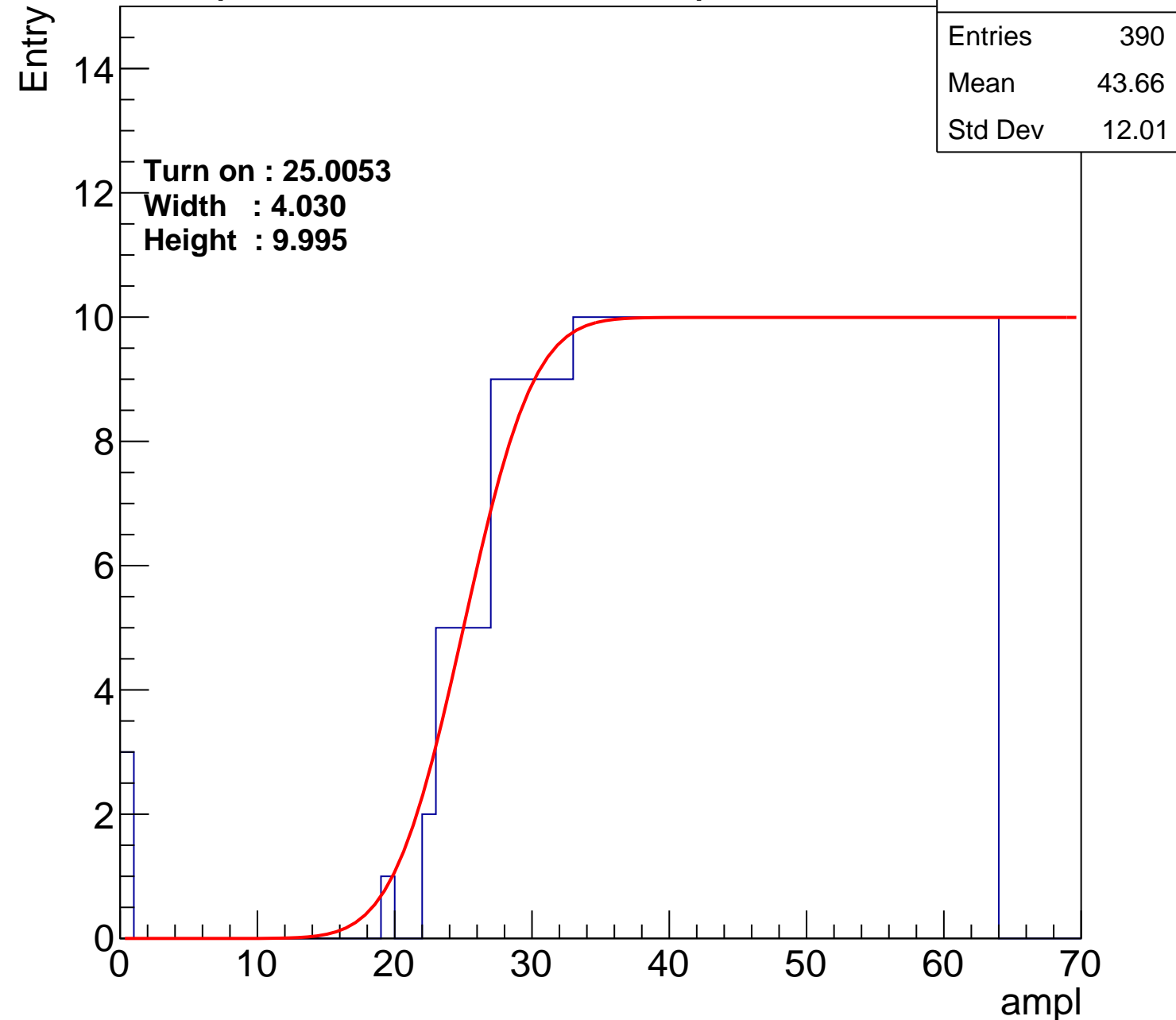
Width : 4.030

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.73
Std Dev	11.96

Turn on : 25.9871

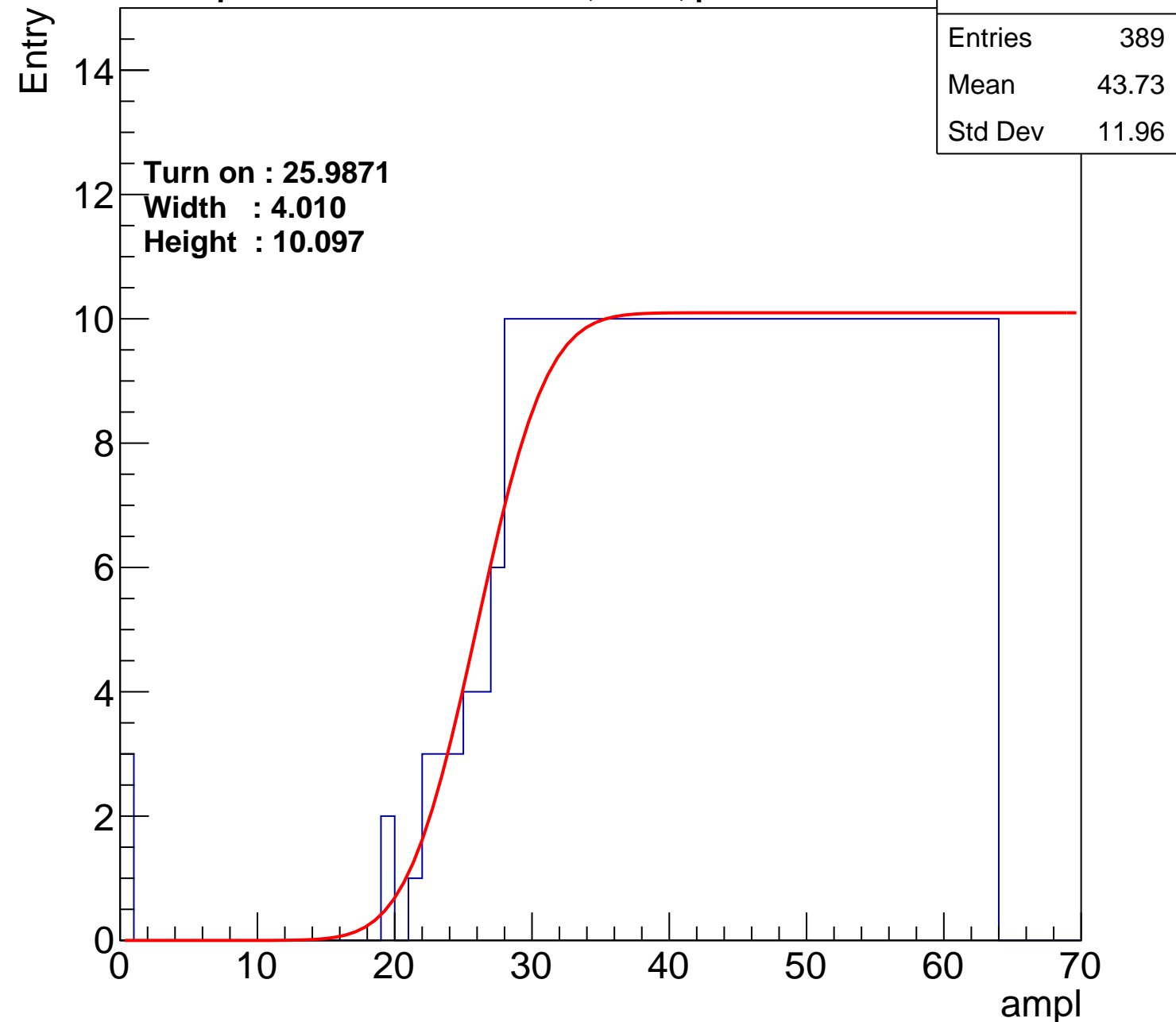
Width : 4.010

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch113

calib_packv5_042523_0143.root, FC#7, port C2

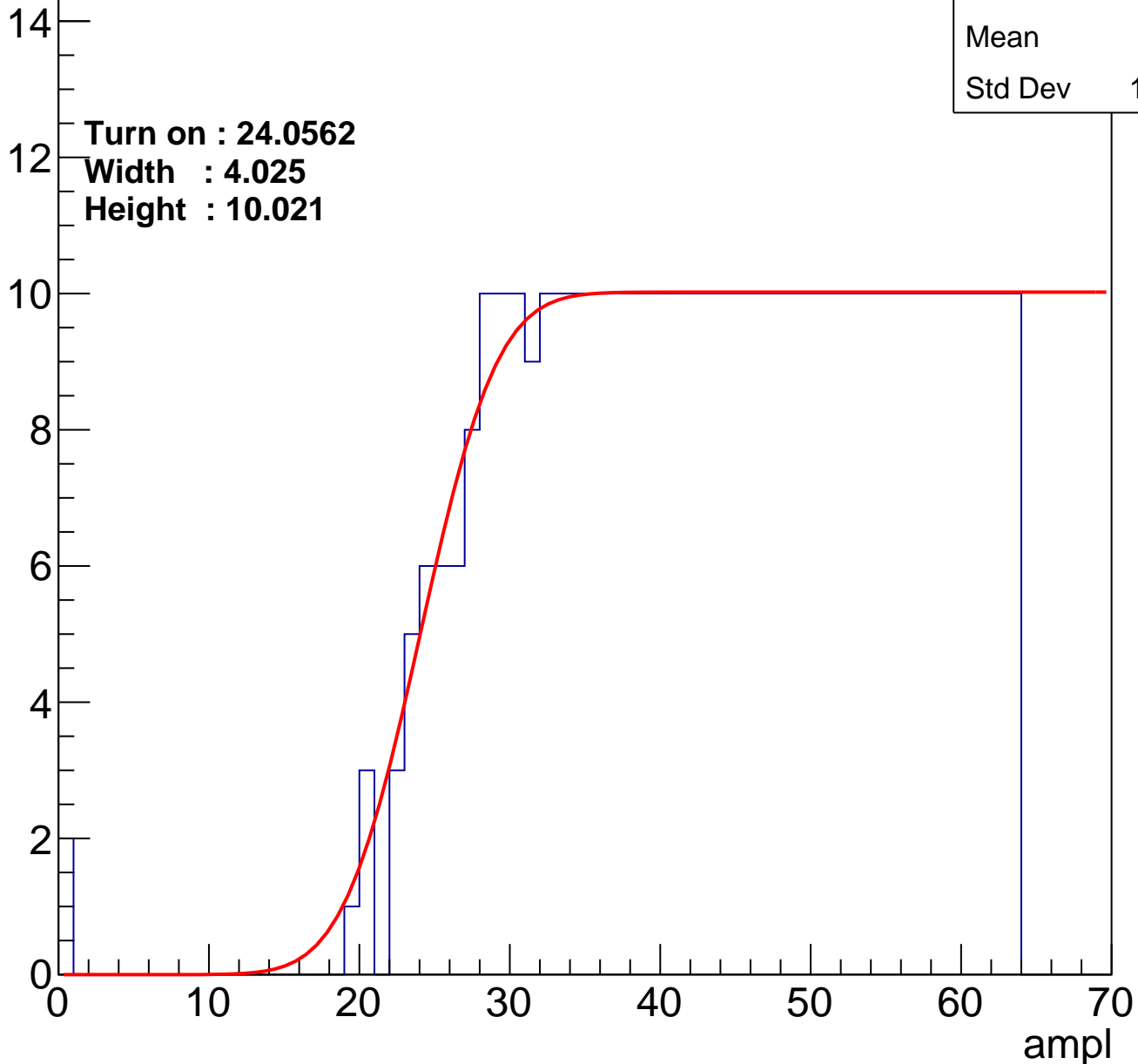
Entries	399
Mean	43.3
Std Dev	12.06

Turn on : 24.0562

Width : 4.025

Height : 10.021

Entry



B1L103S, U16-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.01
Std Dev	11.76

Turn on : 25.7256

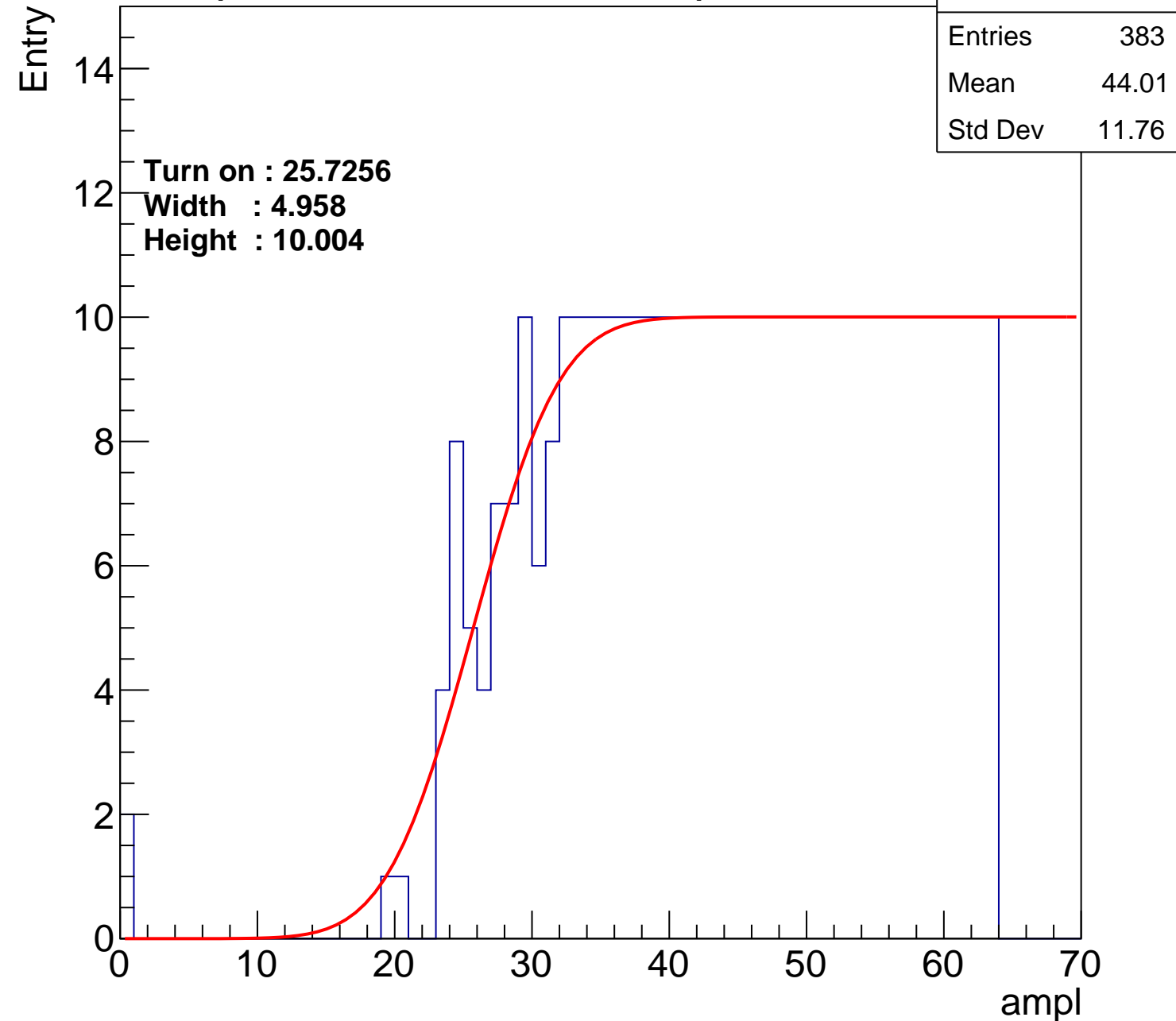
Width : 4.958

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch115

calib_packv5_042523_0143.root, FC#7, port C2

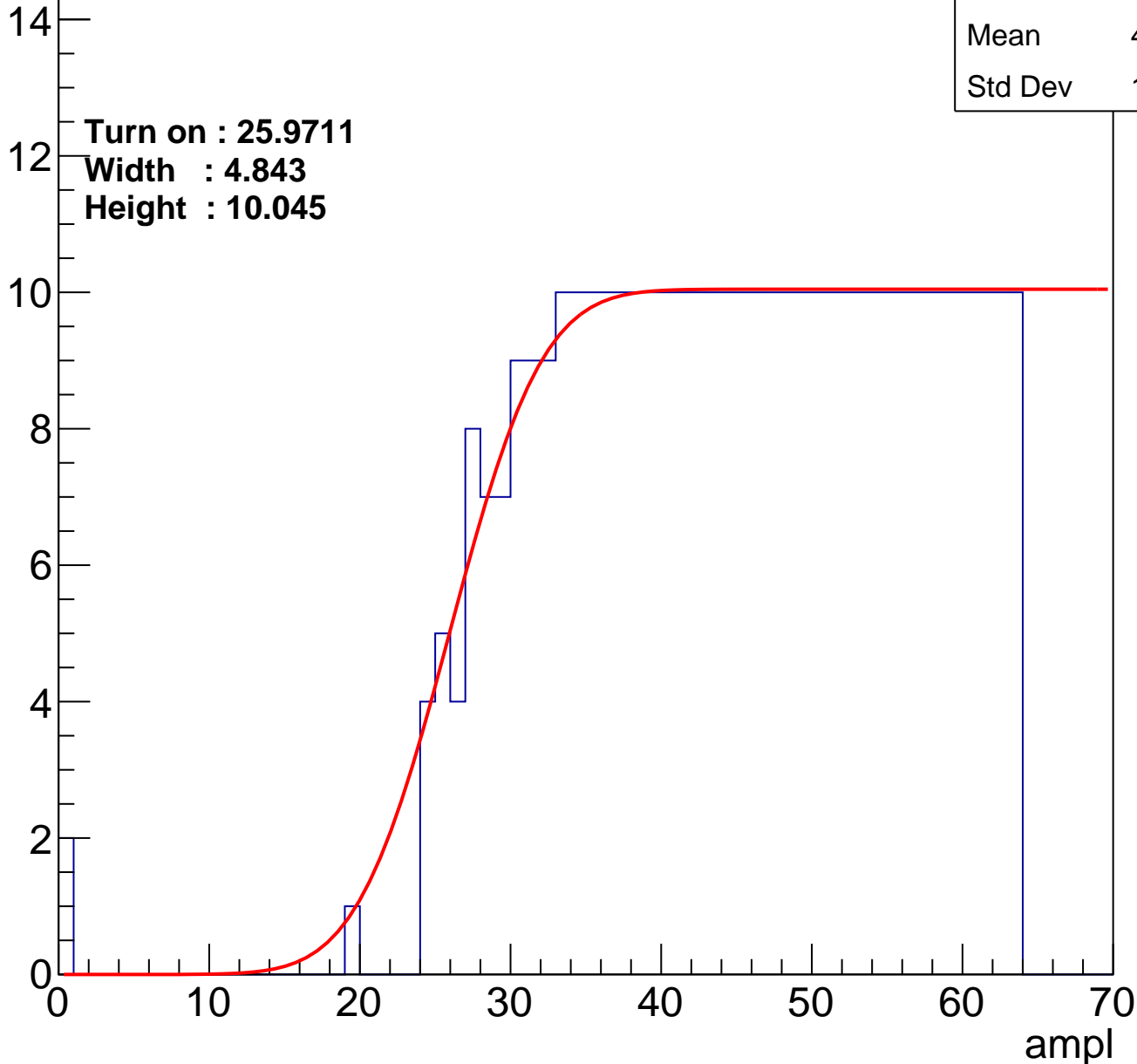
Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 25.9711

Width : 4.843

Height : 10.045

Entry



B1L103S, U16-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.61
Std Dev	12.09

Turn on : 25.5624

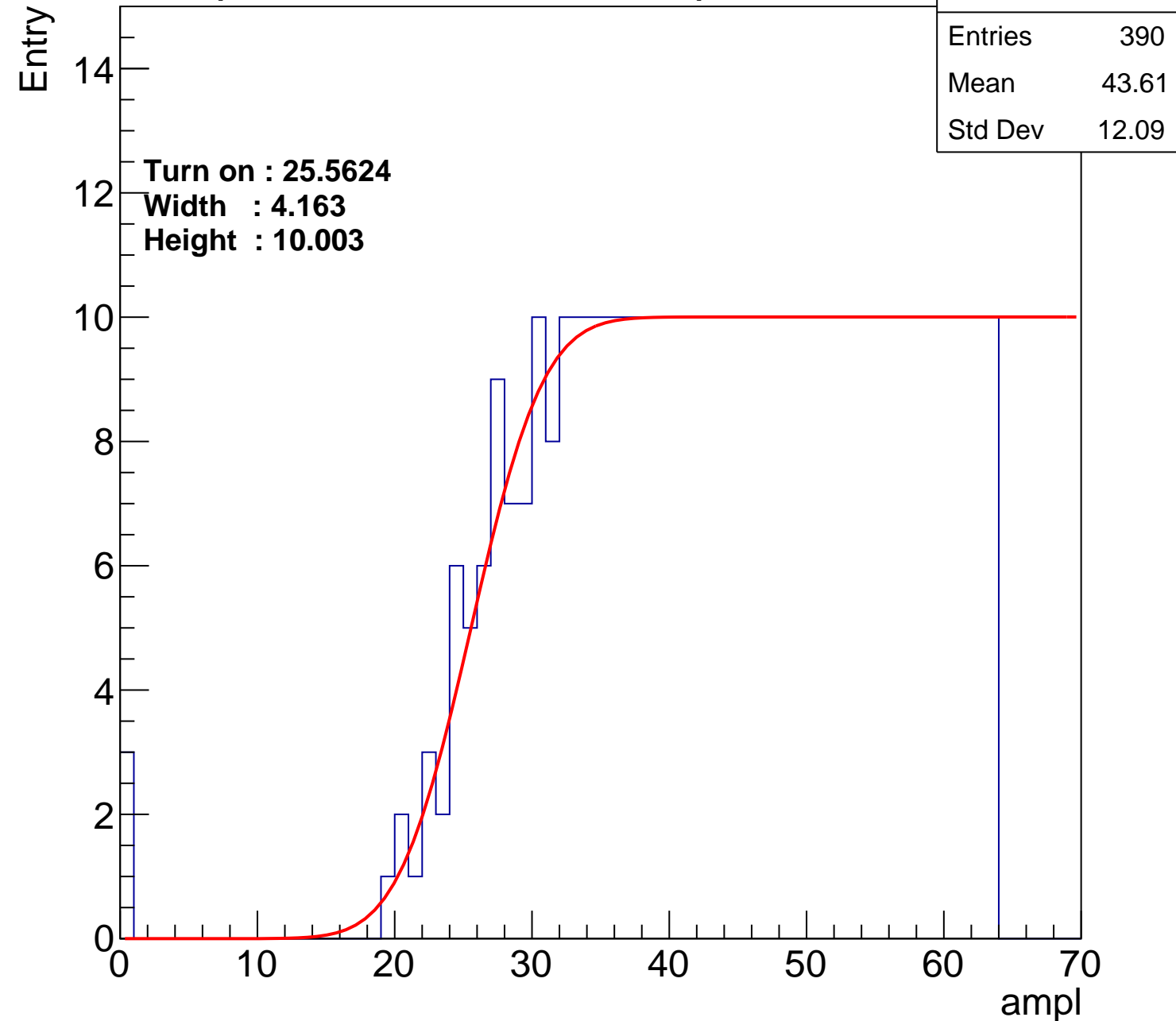
Width : 4.163

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.12
Std Dev	12.6

Turn on : 24.8125

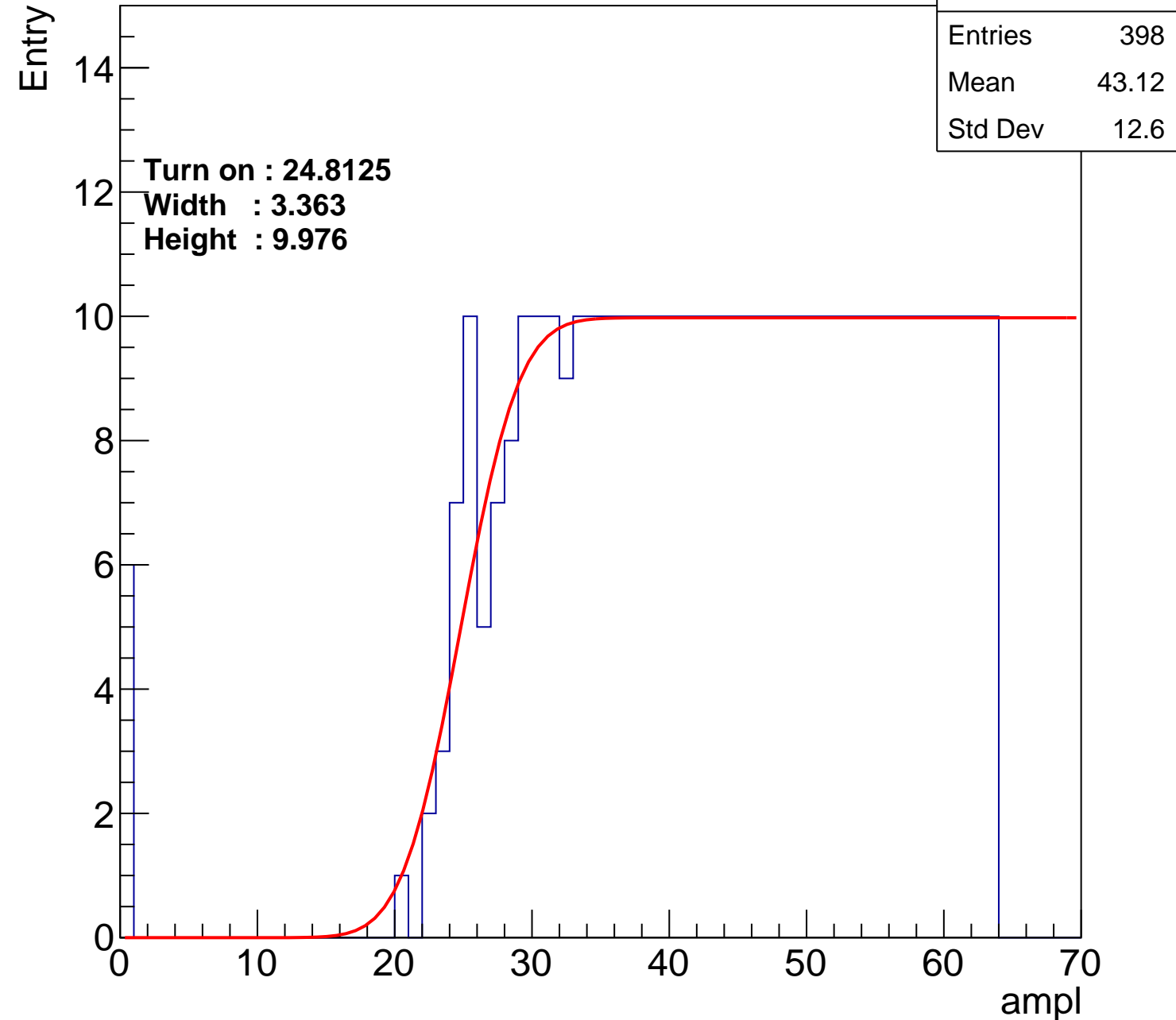
Width : 3.363

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.23
Std Dev	11.58

Turn on : 27.0589

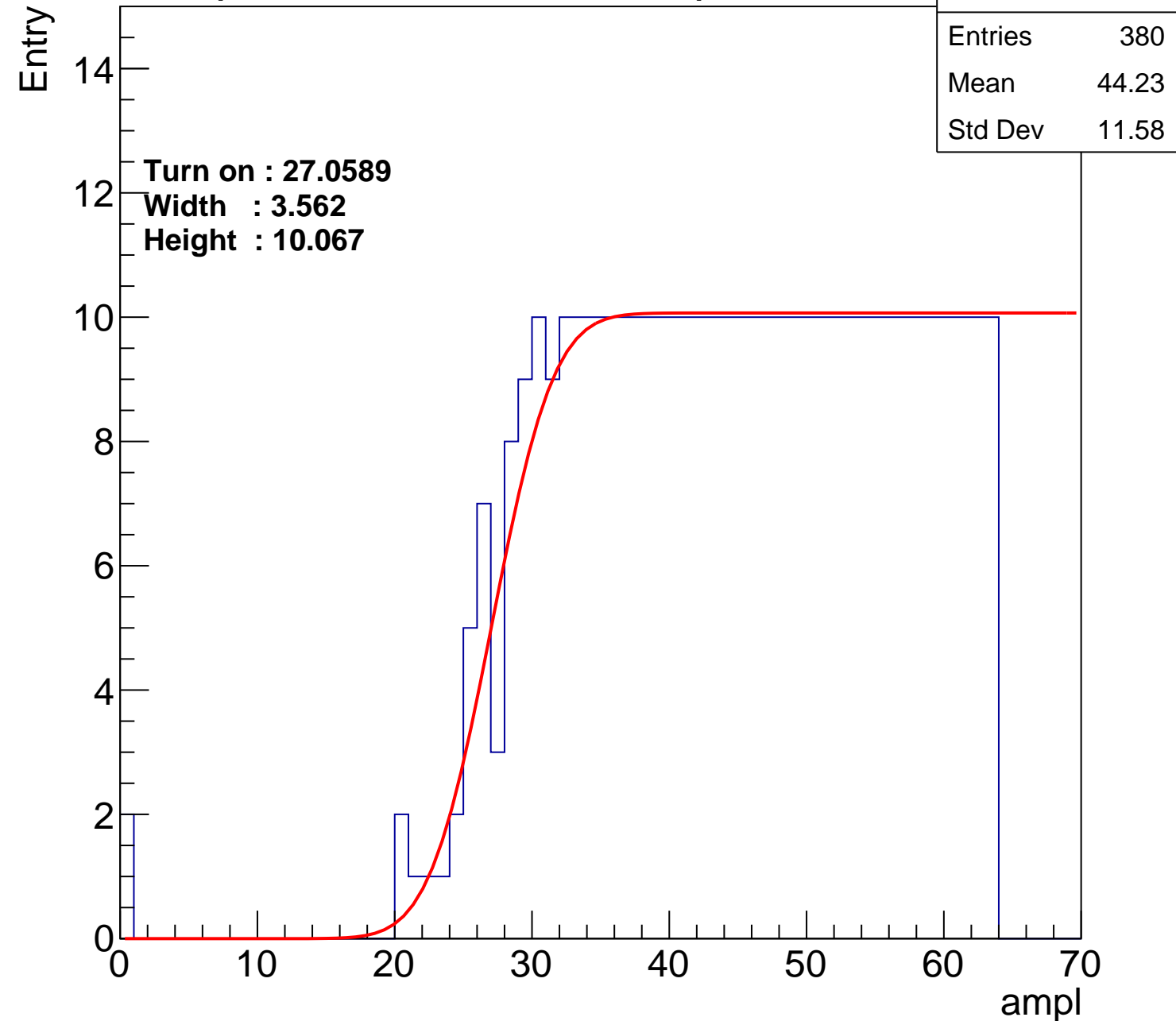
Width : 3.562

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.23
Std Dev	12

Turn on : 27.6227

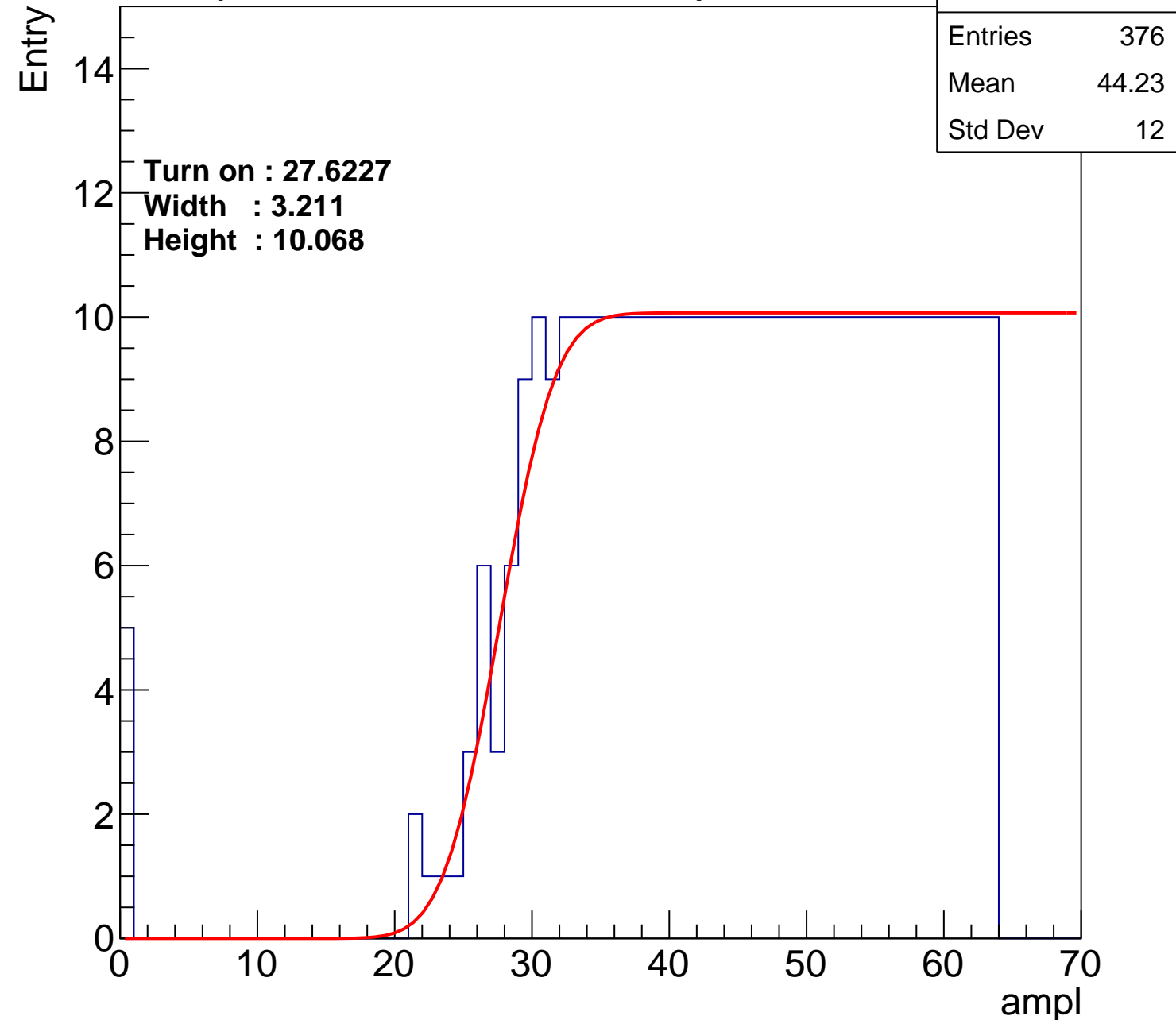
Width : 3.211

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.48
Std Dev	11.62

Turn on : 27.1418

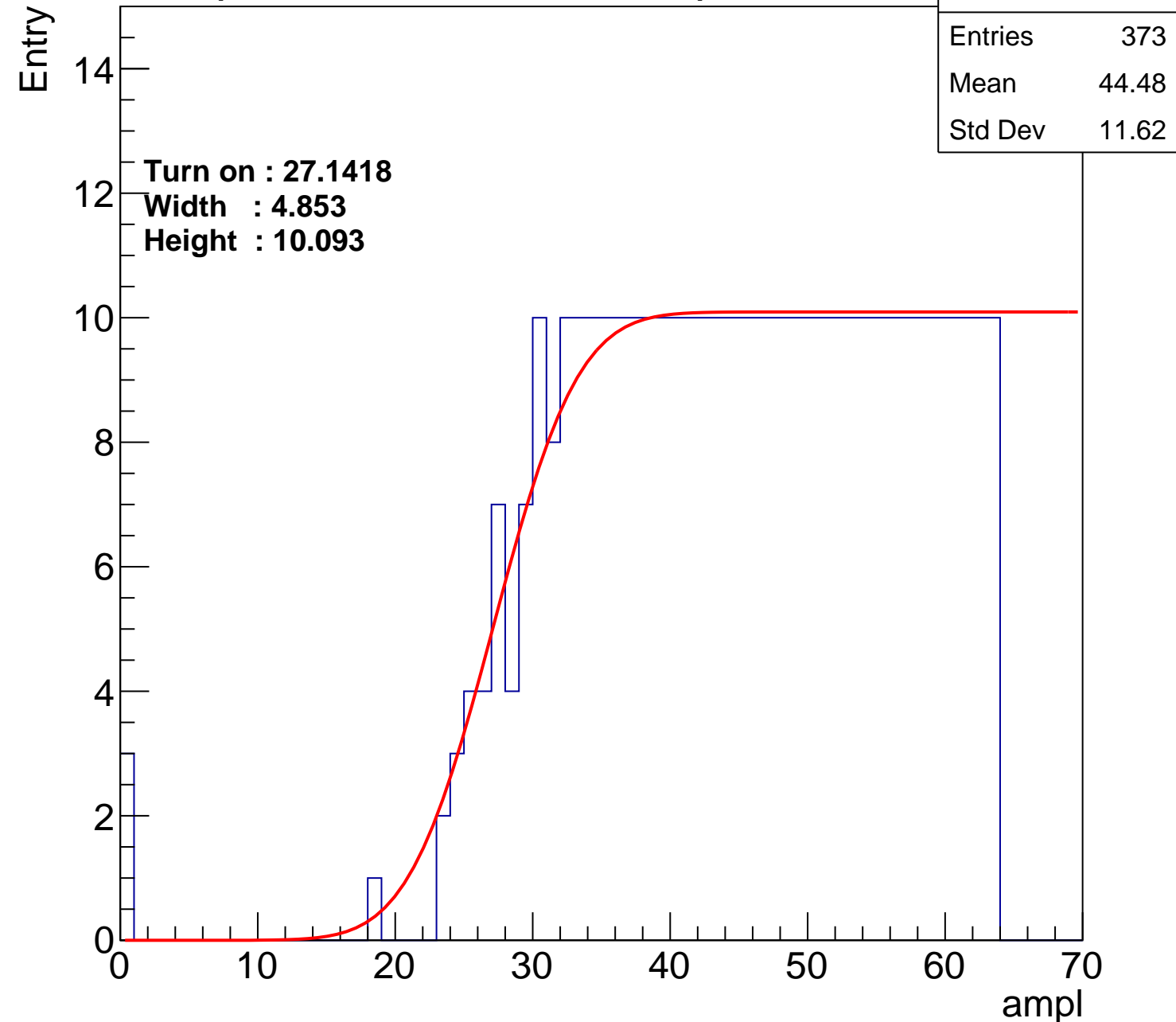
Width : 4.853

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.0867

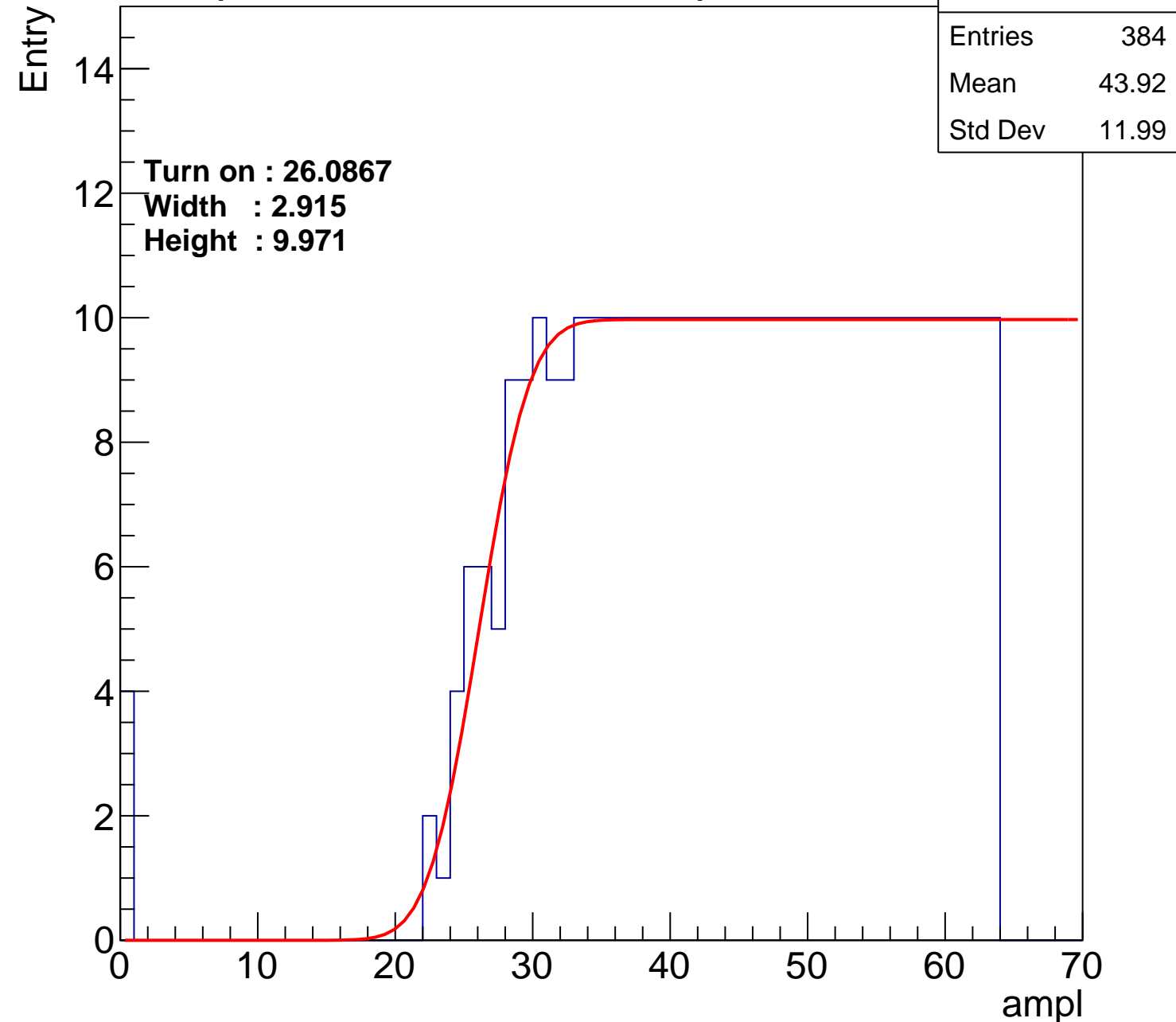
Width : 2.915

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.69
Std Dev	11.96

Turn on : 25.4197

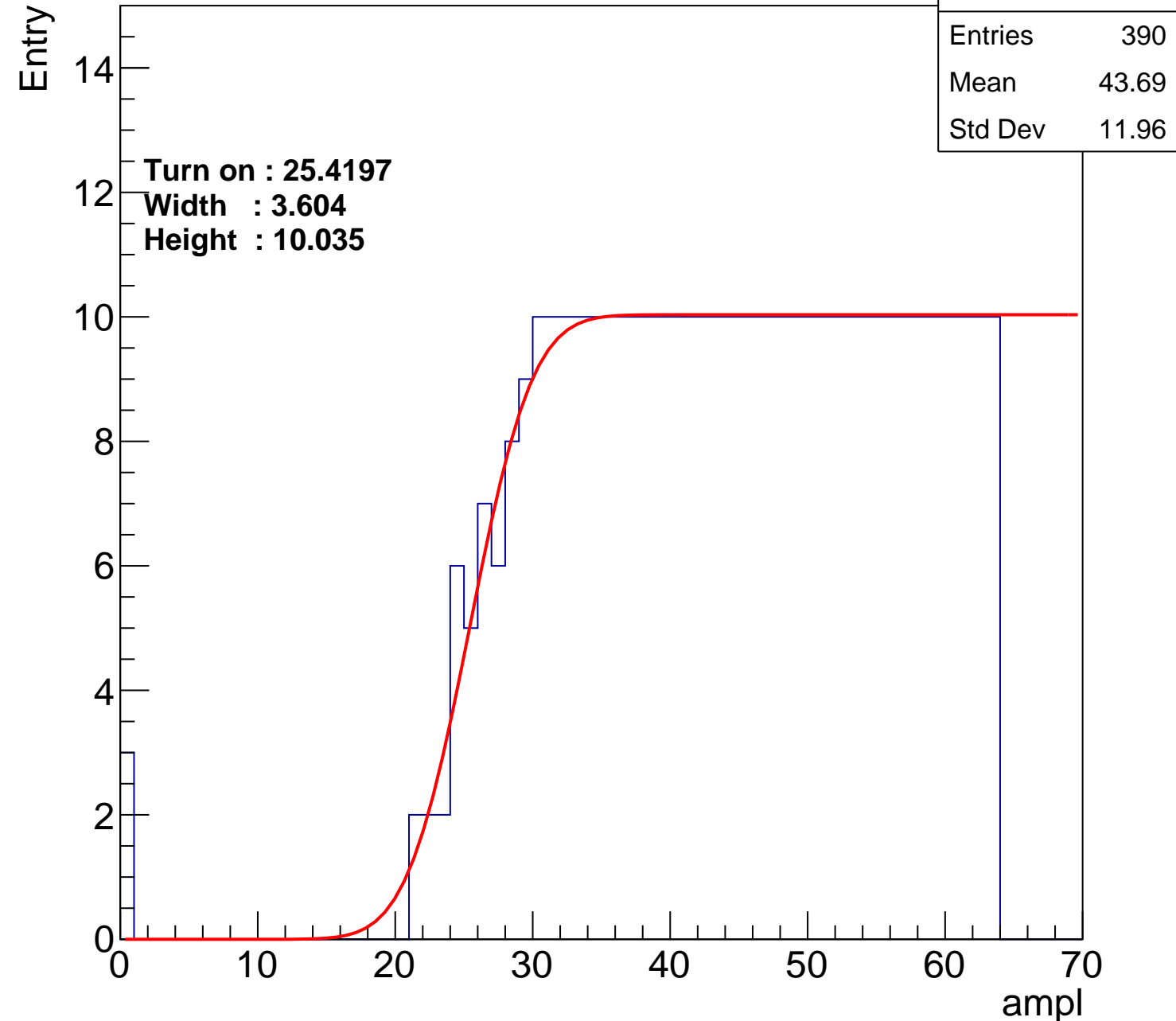
Width : 3.604

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch123

calib_packv5_042523_0143.root, FC#7, port C2

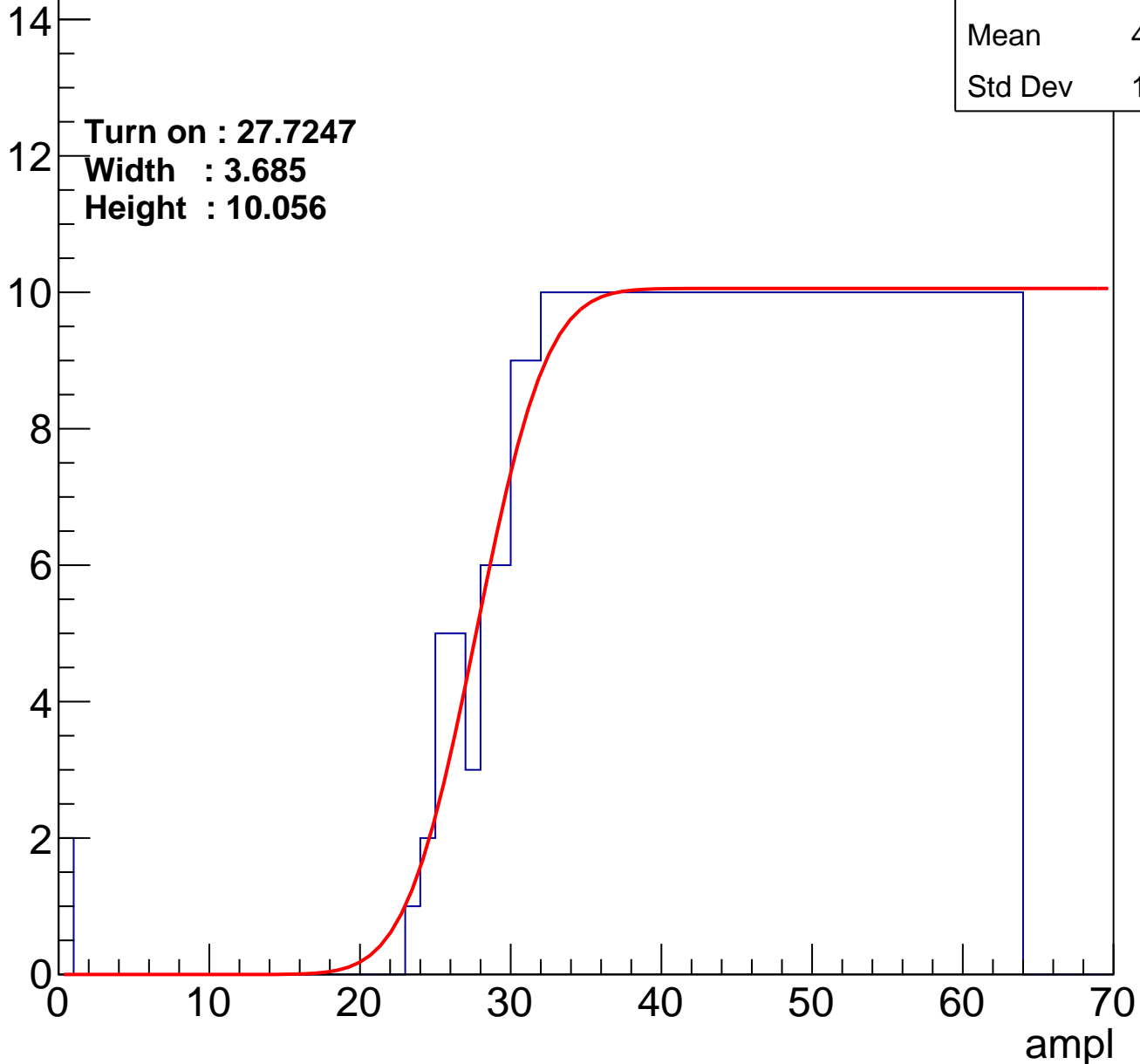
Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.7247

Width : 3.685

Height : 10.056

Entry



B1L103S, U16-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.37
Std Dev	12.39

Turn on : 25.5836

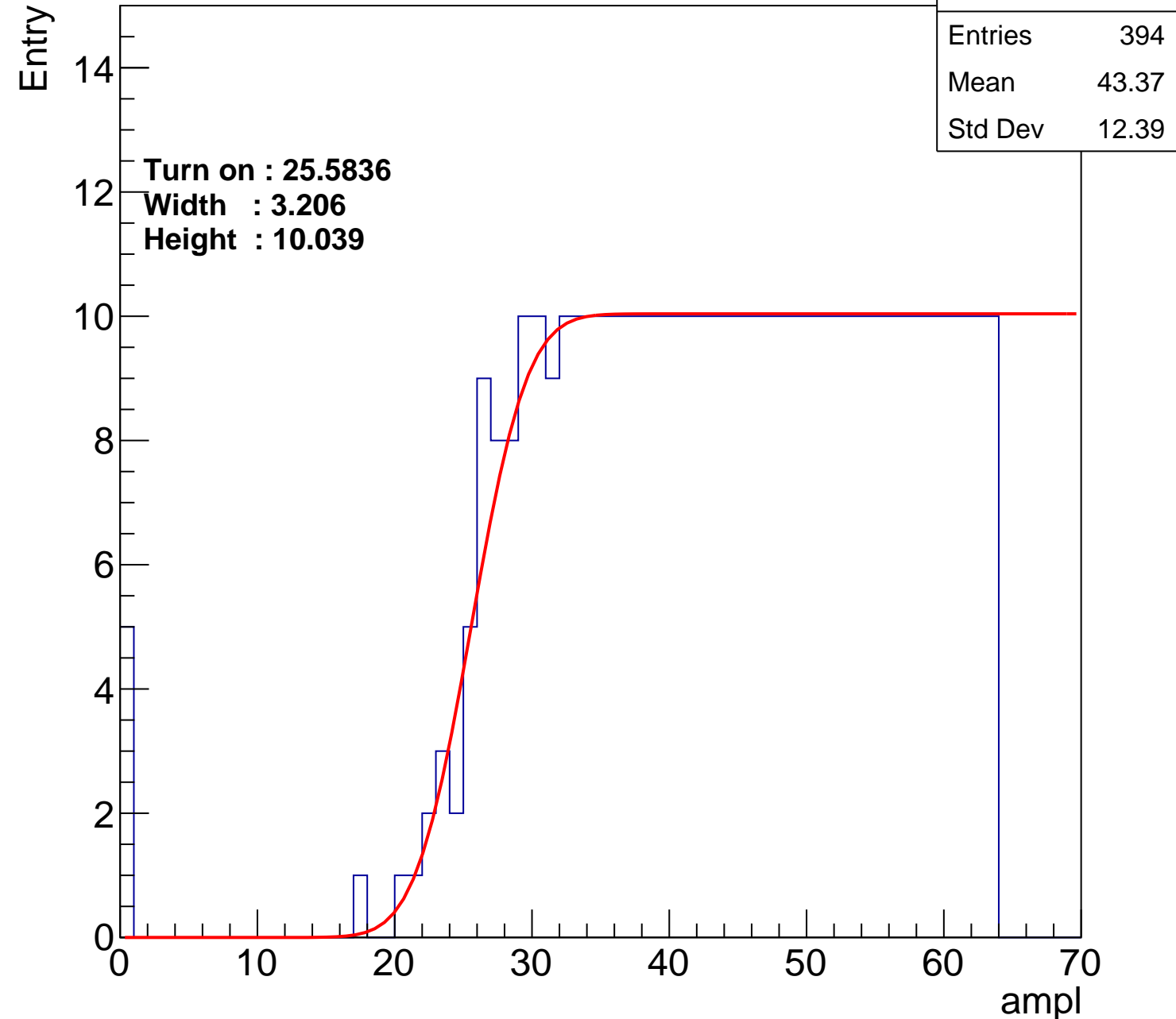
Width : 3.206

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.21
Std Dev	11.06

Turn on : 28.6038

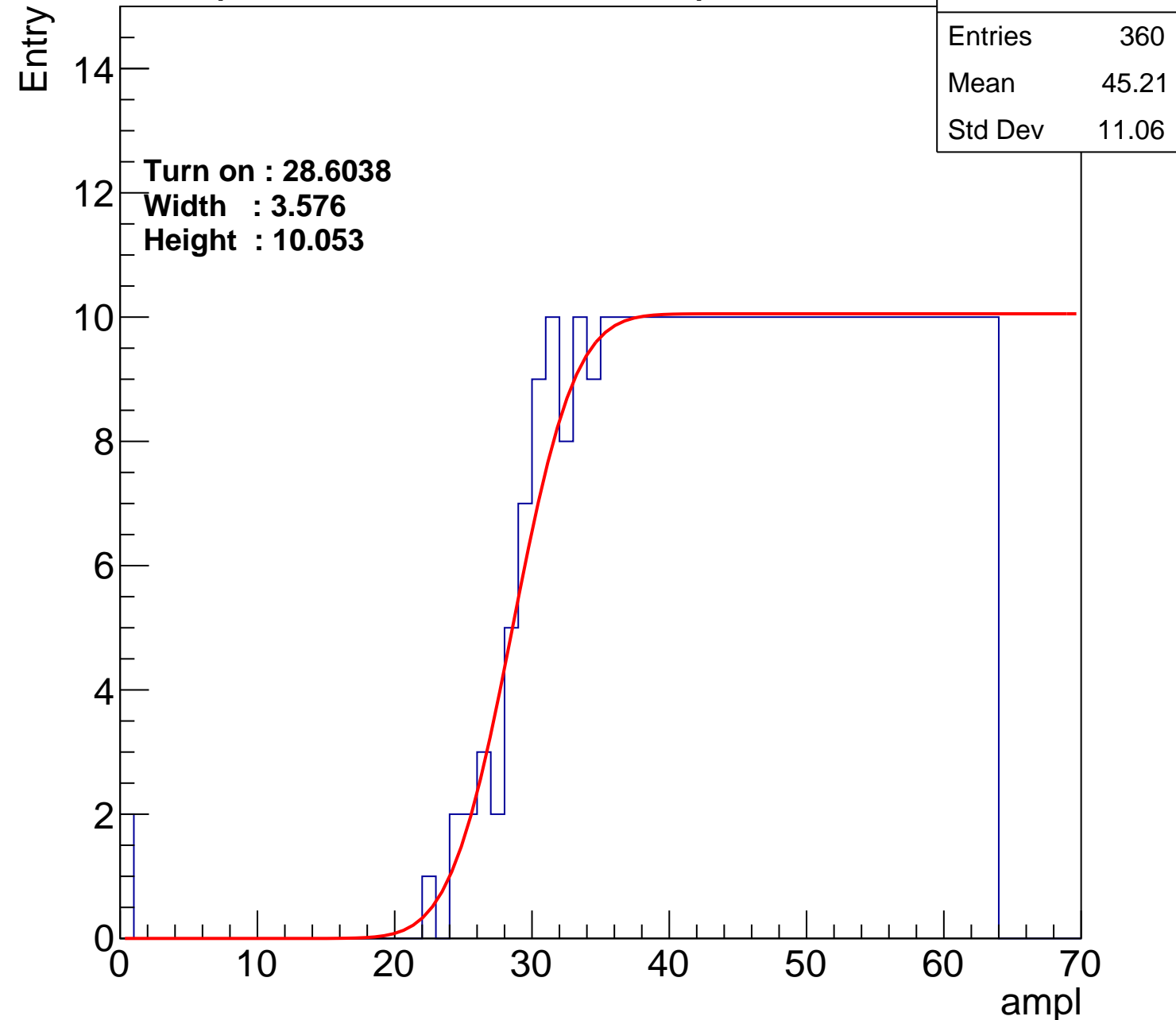
Width : 3.576

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch126

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.4
Std Dev	12.28

Turn on : 25.7524

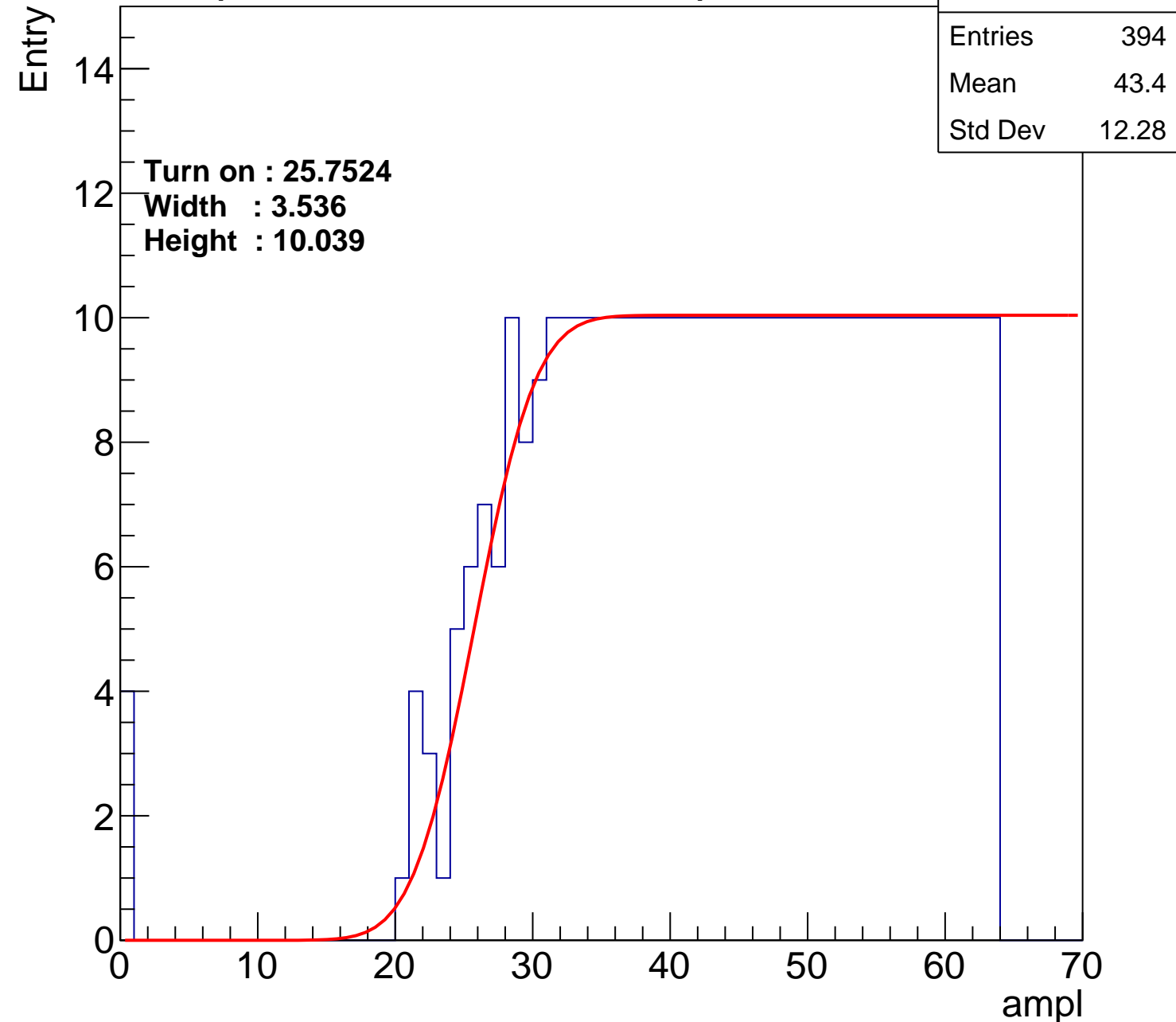
Width : 3.536

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.0852

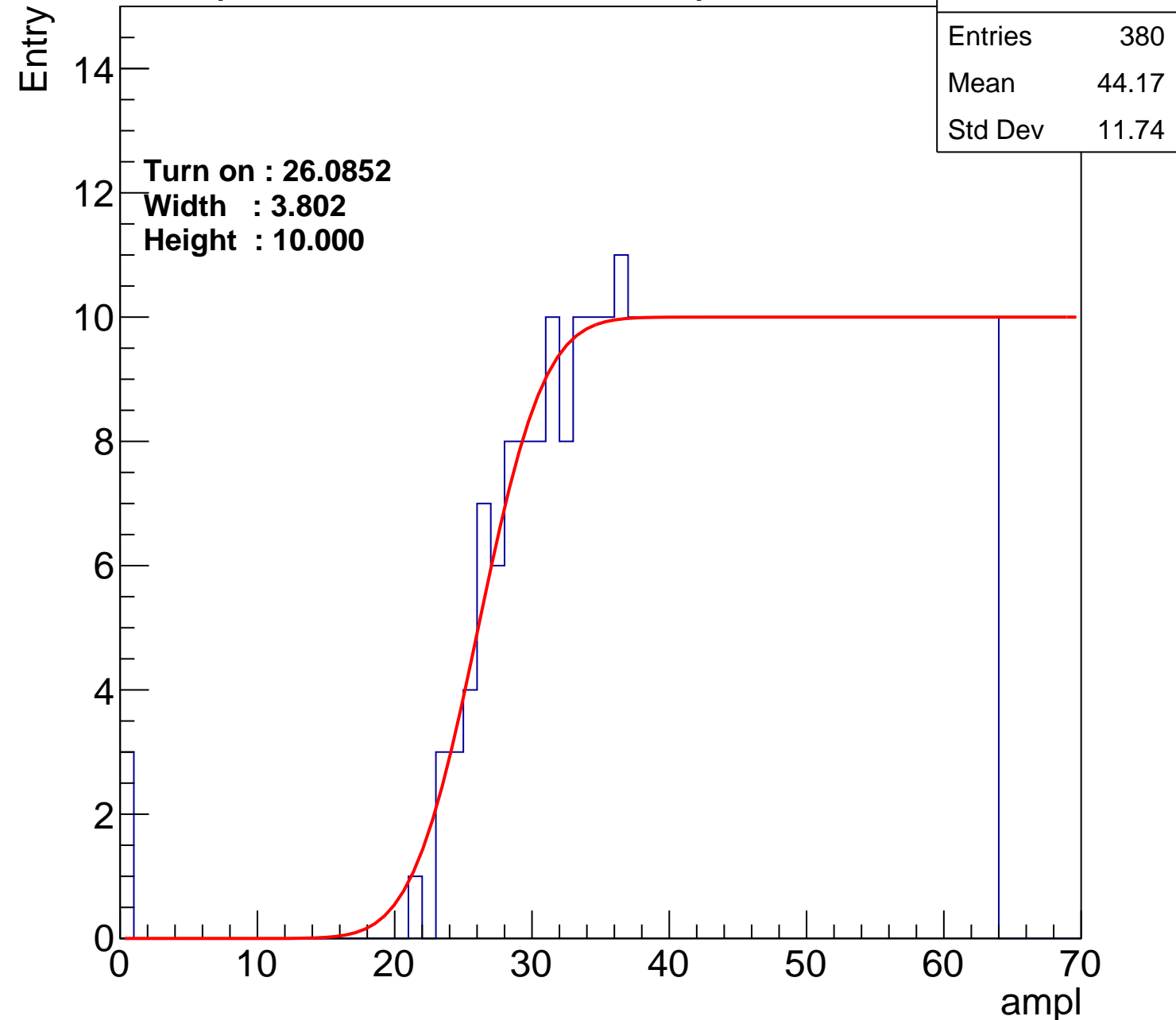
Width : 3.802

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U16-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.0852

Width : 3.802

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl

