

B1L101S, U12-ch0, adc0

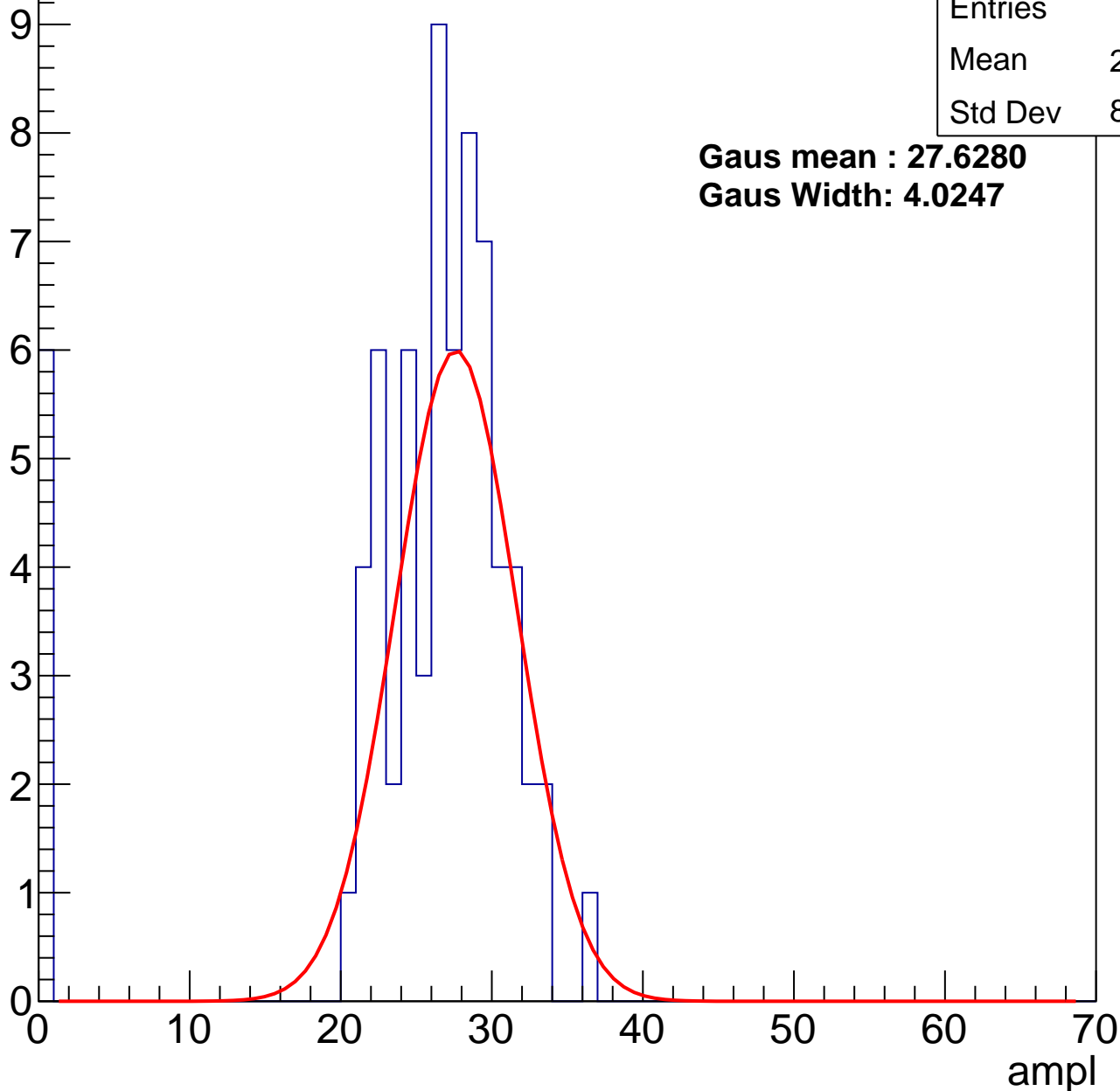
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	24.42
Std Dev	8.129

Gaus mean : 27.6280

Gaus Width: 4.0247



B1L101S, U12-ch0, adc1

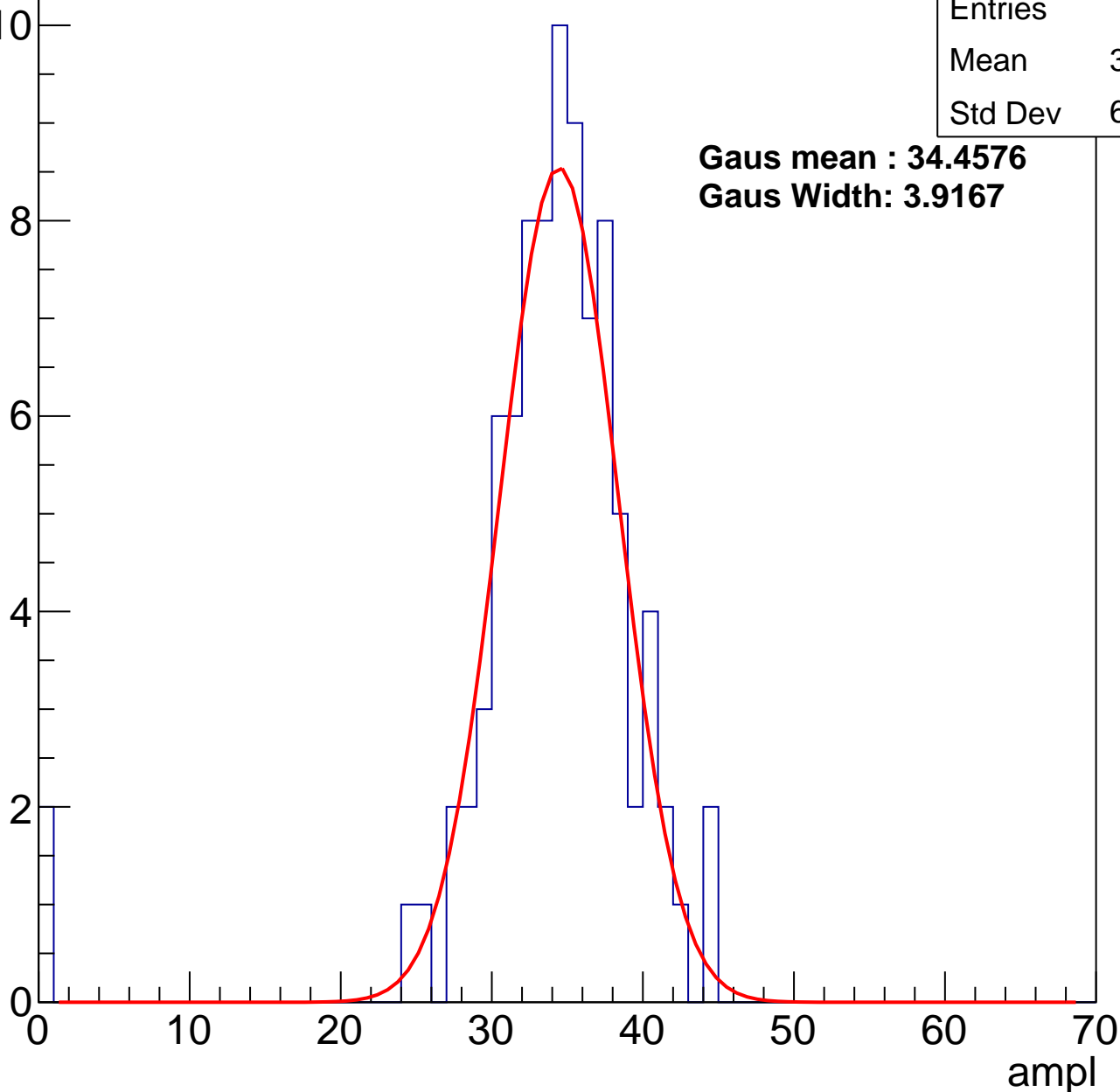
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	33.43
Std Dev	6.399

Gaus mean : 34.4576

Gaus Width: 3.9167



B1L101S, U12-ch0, adc2

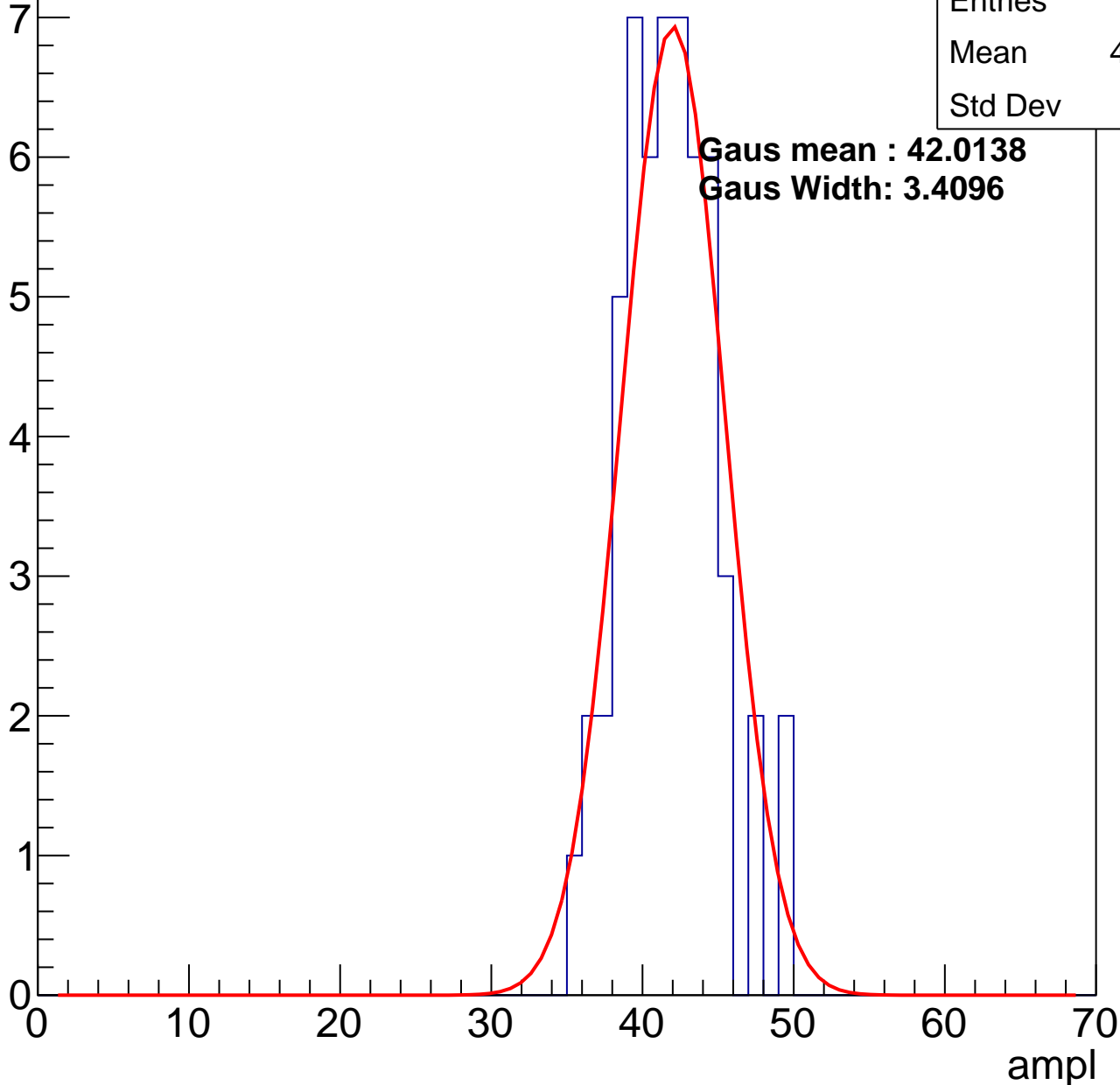
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.32
Std Dev	3.06

Gaus mean : 42.0138

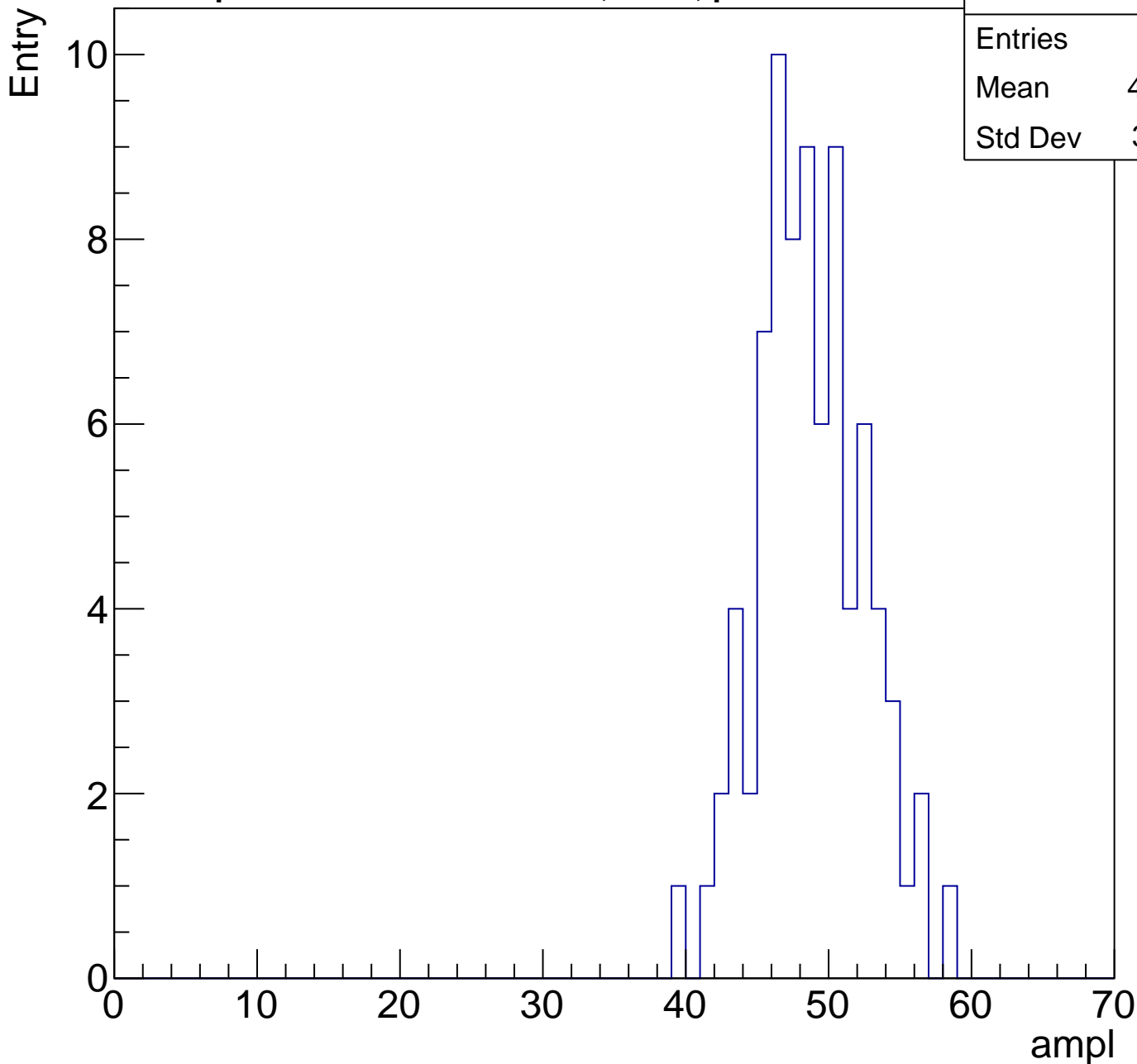
Gaus Width: 3.4096



B1L101S, U12-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	48.33
Std Dev	3.701



B1L101S, U12-ch0, adc4

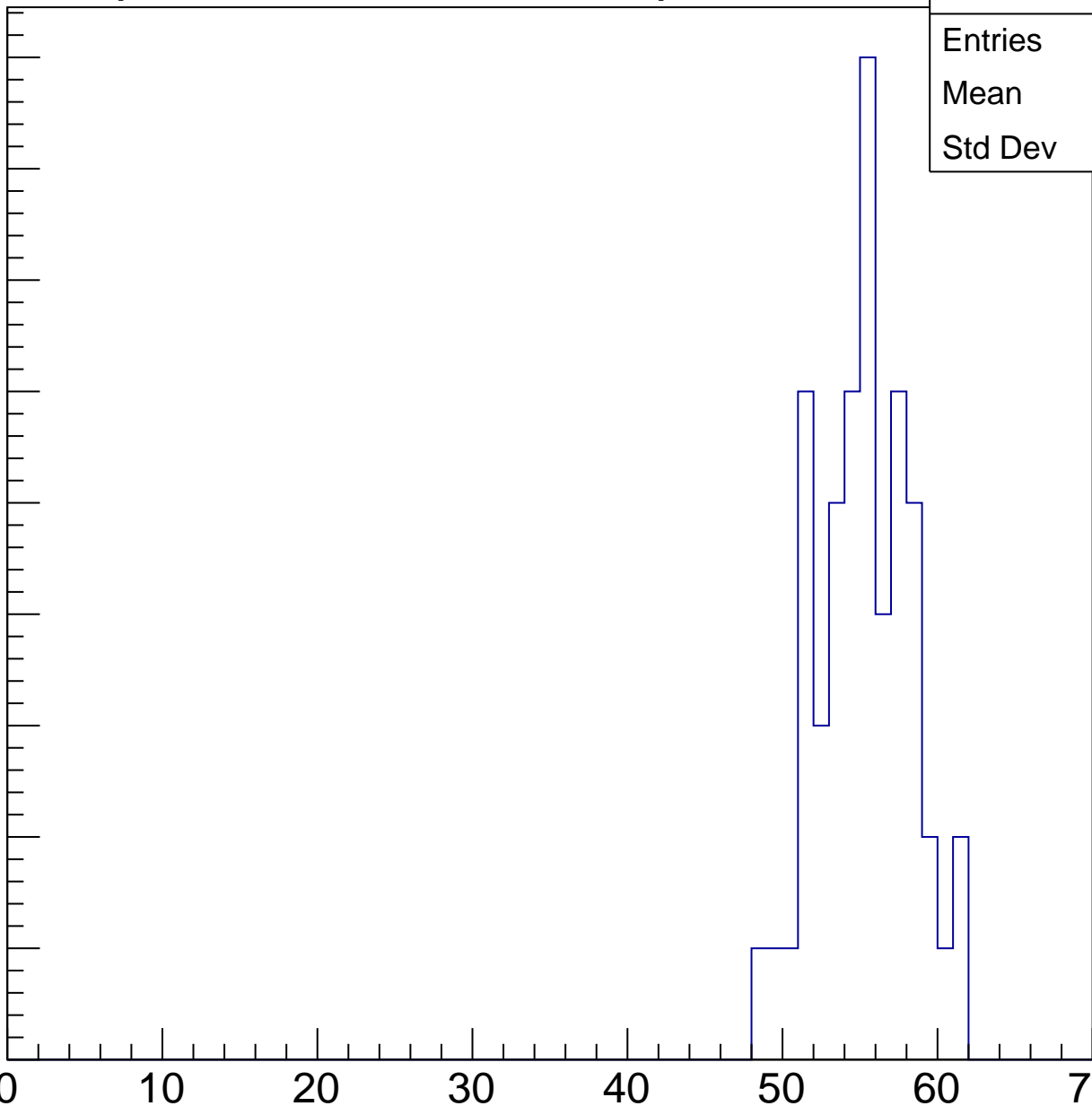
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	54.79
Std Dev	2.97

ampl

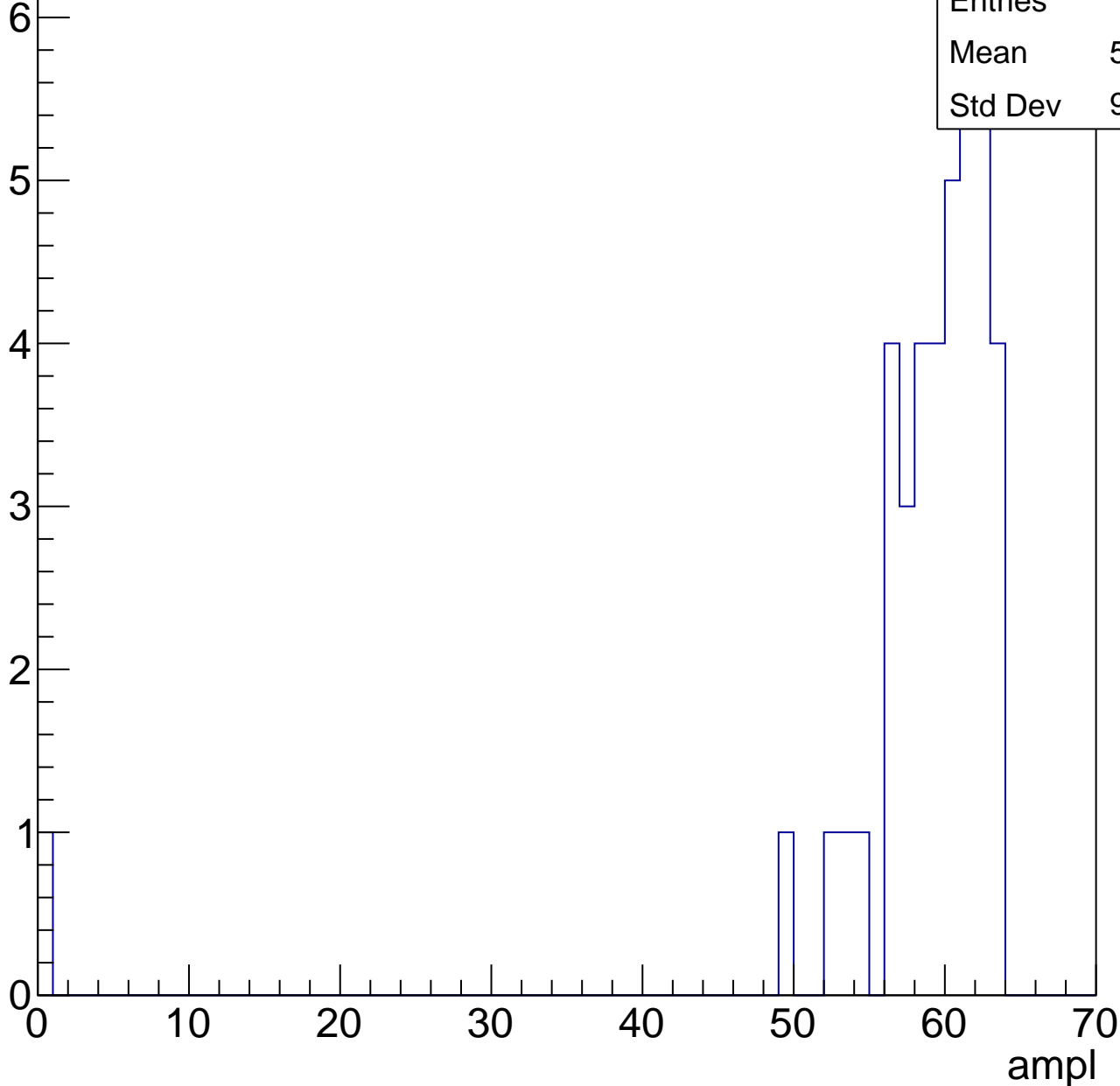


B1L101S, U12-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	57.59
Std Dev	9.637

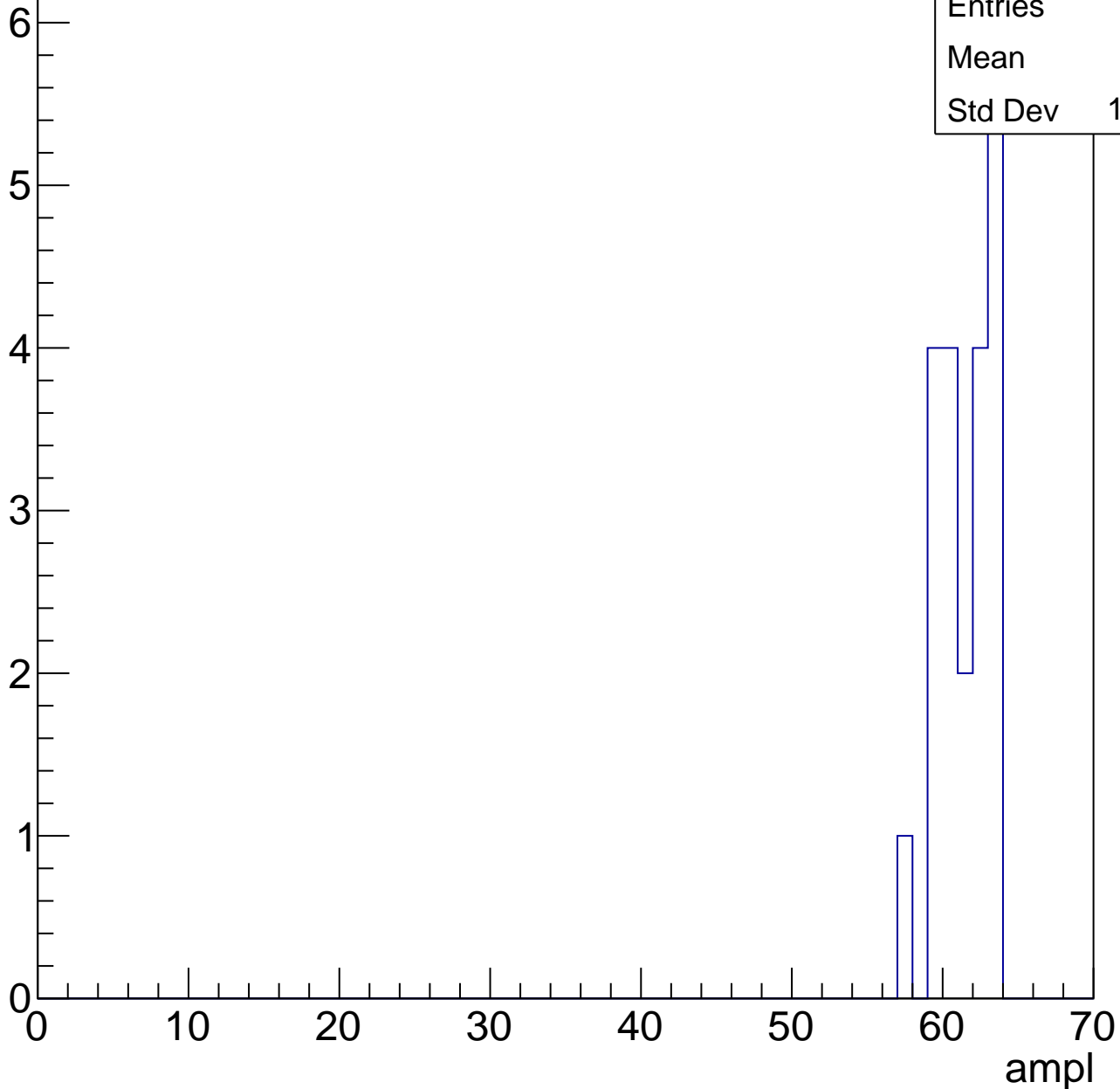


B1L101S, U12-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	61
Std Dev	1.746



B1L101S, U12-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch1, adc0

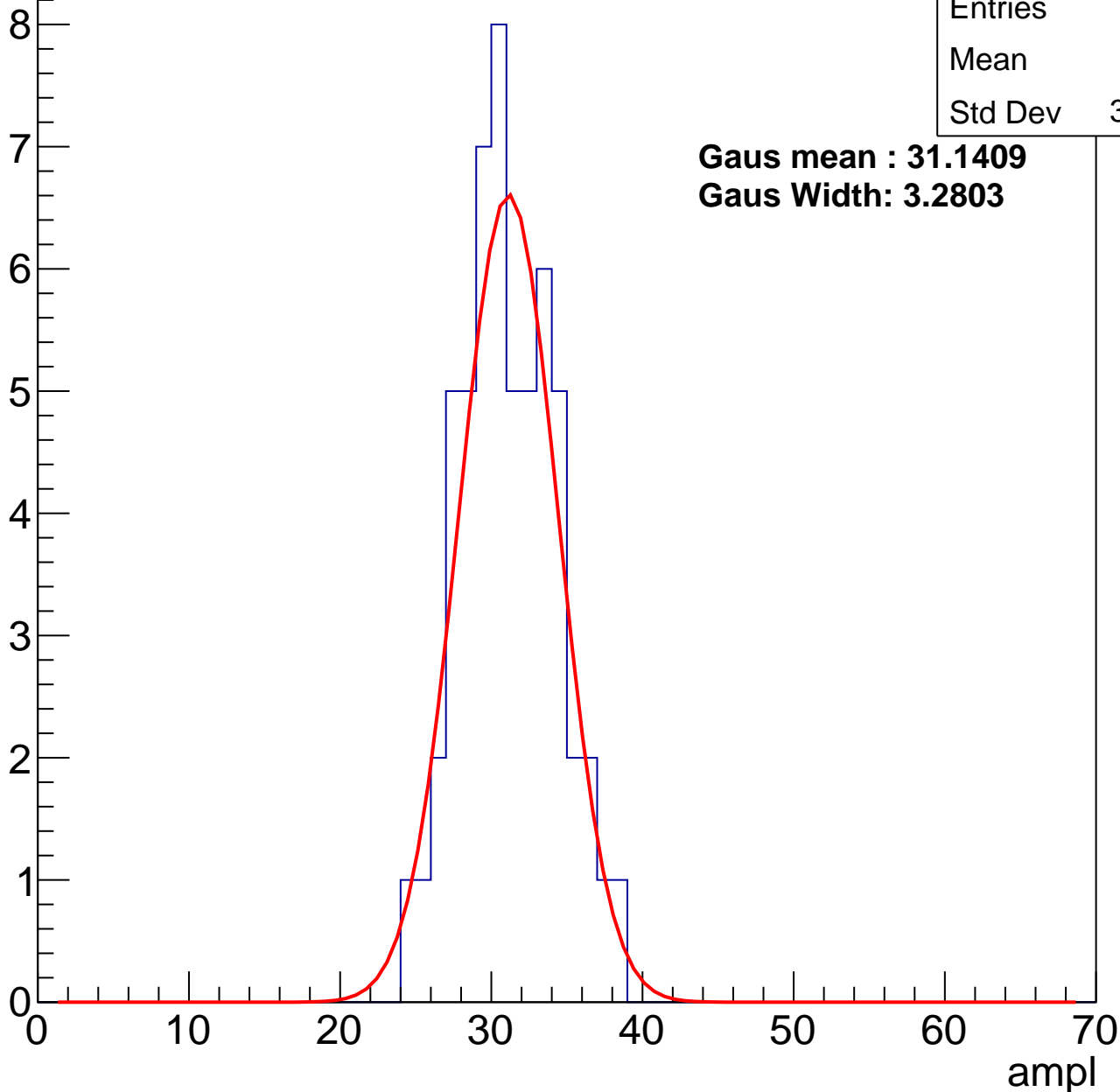
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	30.7
Std Dev	3.082

Gaus mean : 31.1409

Gaus Width: 3.2803



B1L101S, U12-ch1, adc1

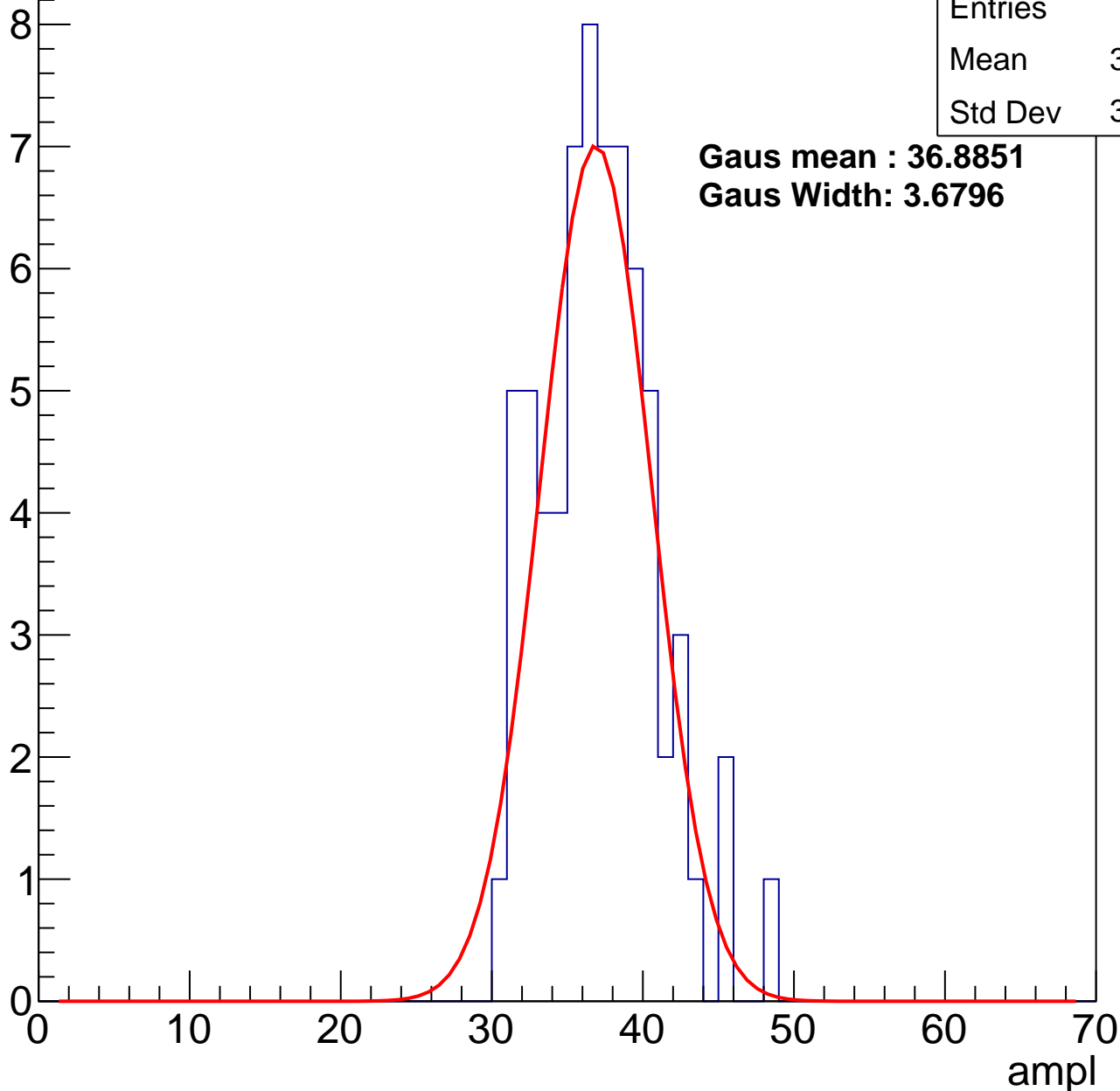
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.68
Std Dev	3.739

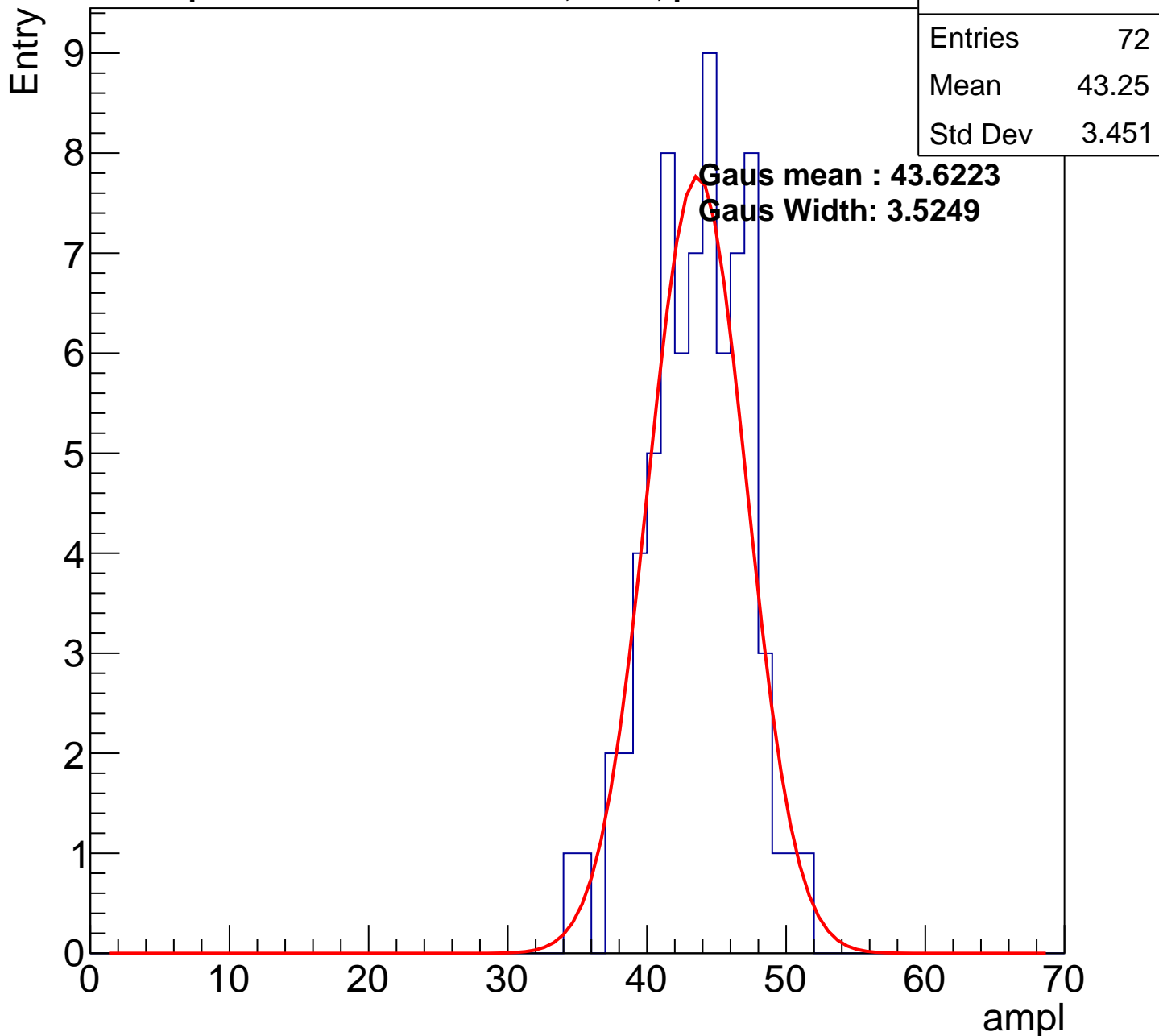
Gaus mean : 36.8851

Gaus Width: 3.6796



B1L101S, U12-ch1, adc2

calib_packv5_042523_0143.root, FC#0, port D2

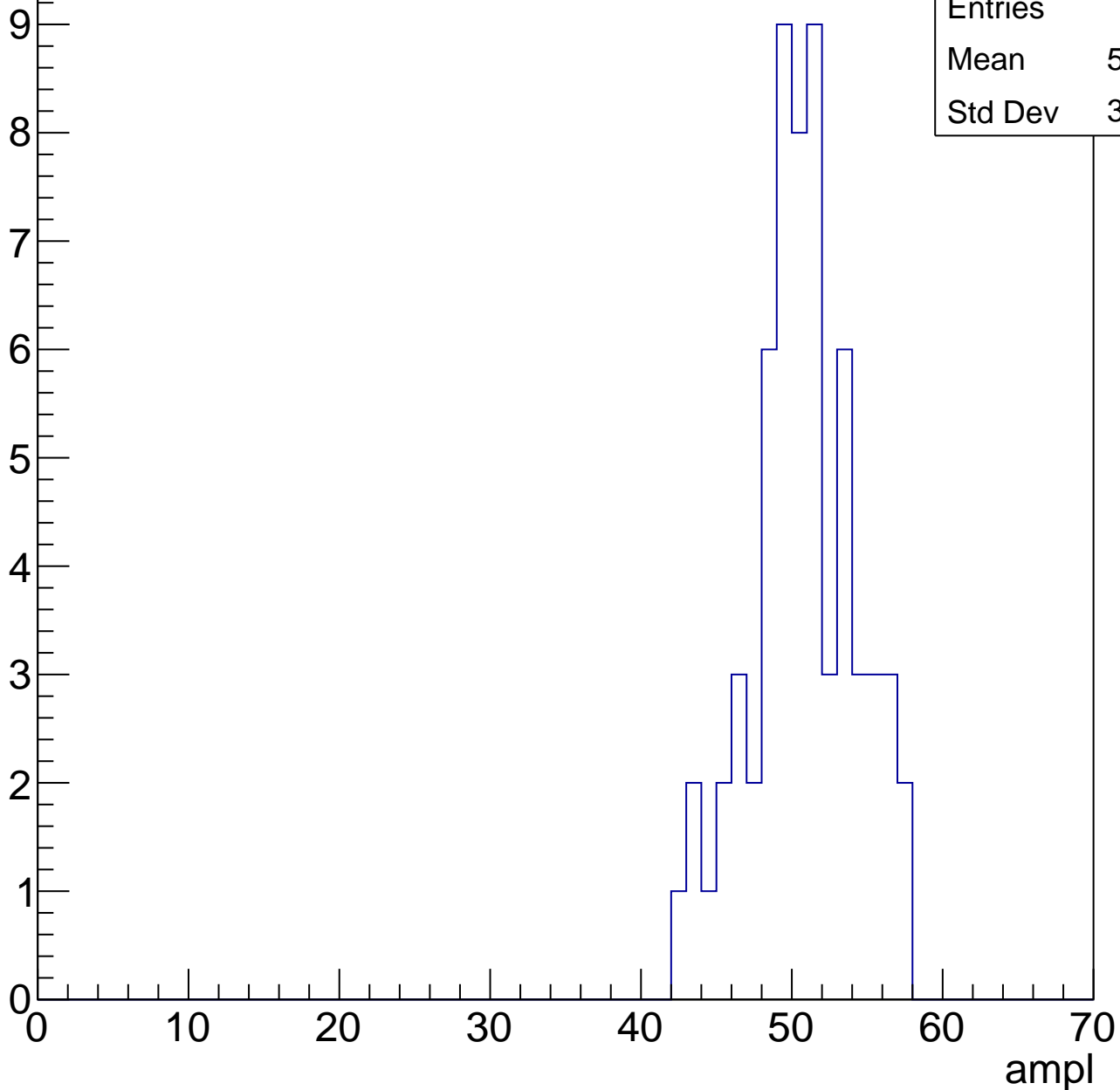


B1L101S, U12-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.24
Std Dev	3.444

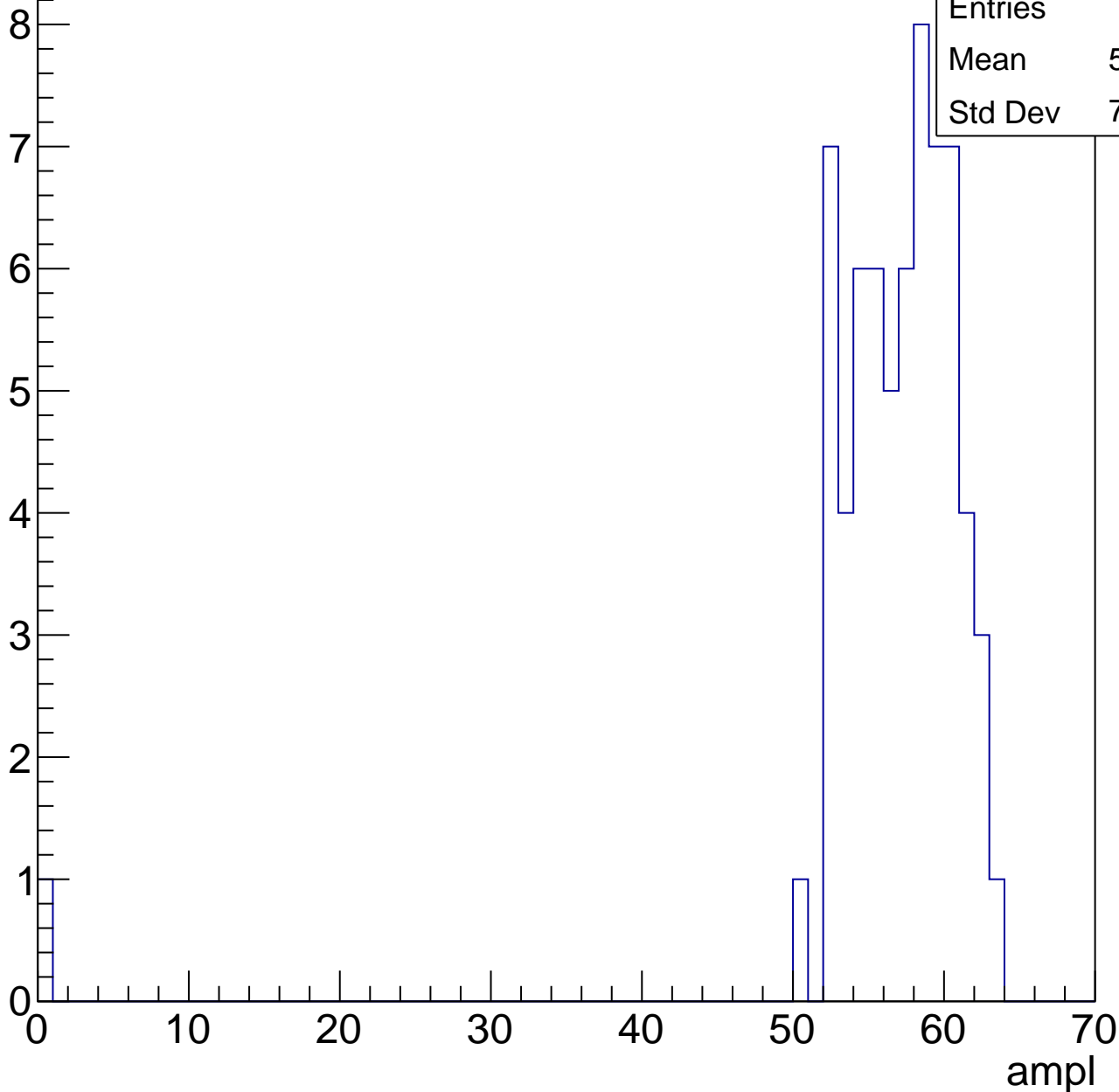


B1L101S, U12-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

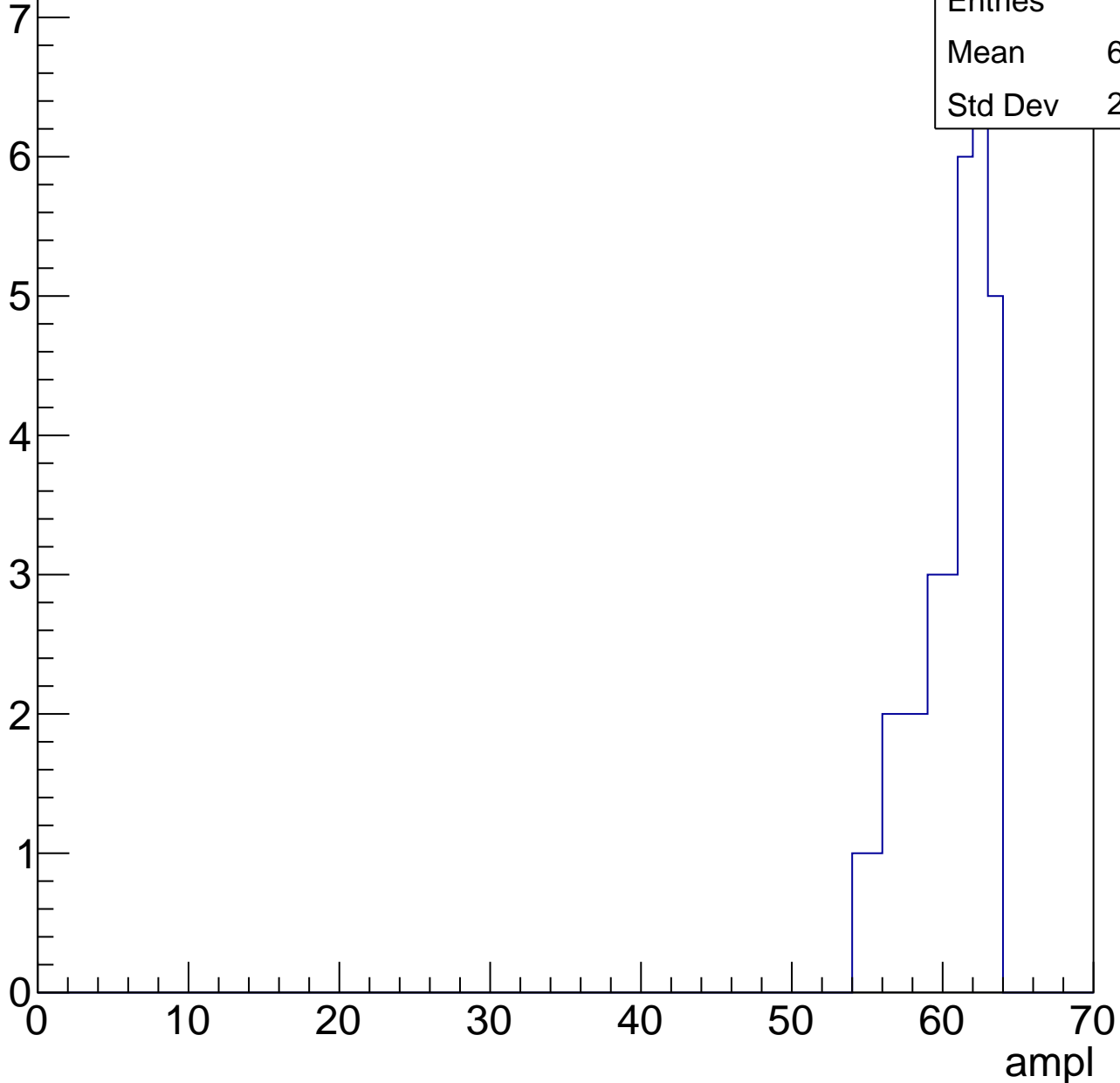
Entries	66
Mean	55.94
Std Dev	7.608



B1L101S, U12-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

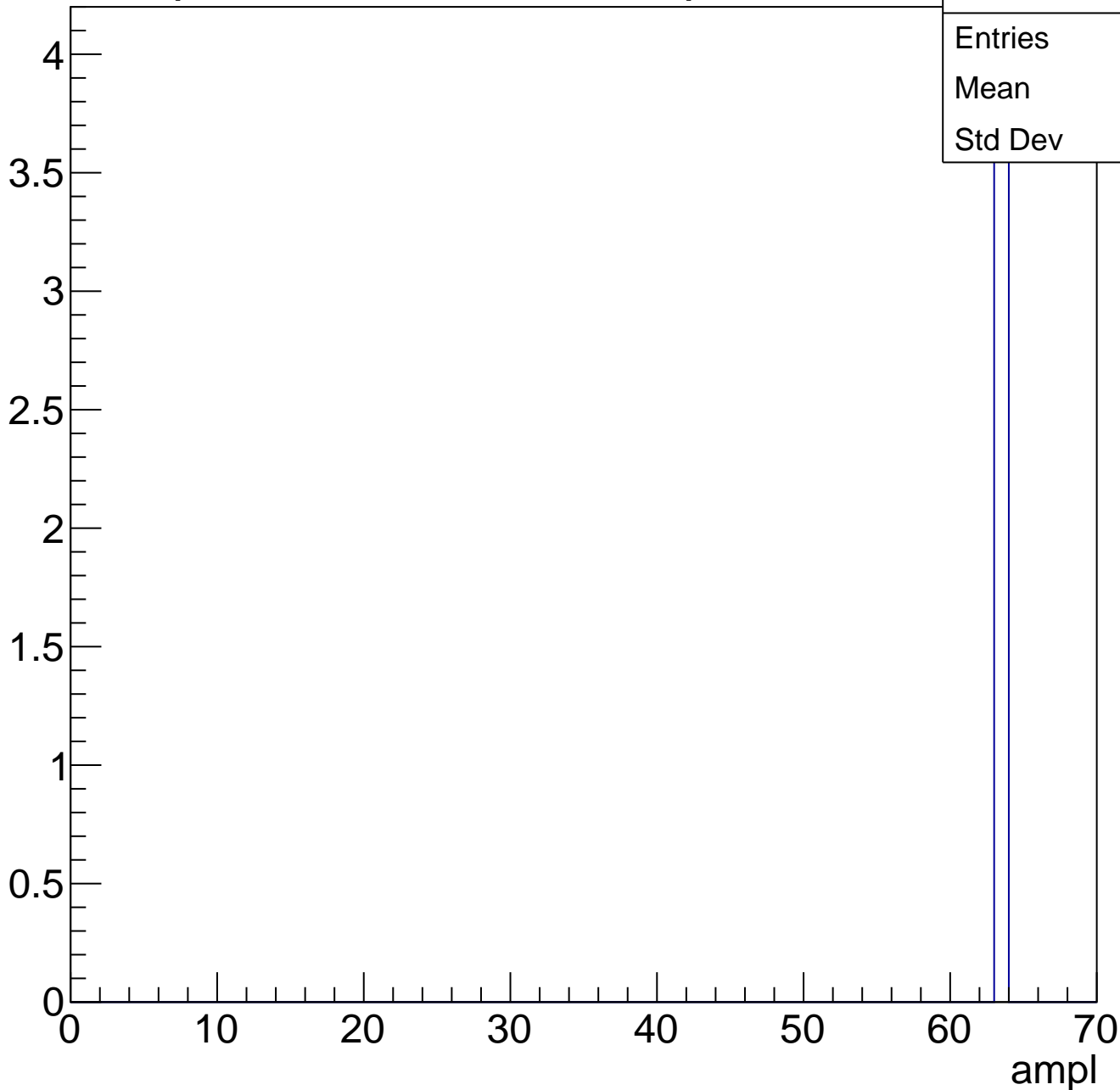
Entry



B1L101S, U12-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L101S, U12-ch2, adc0

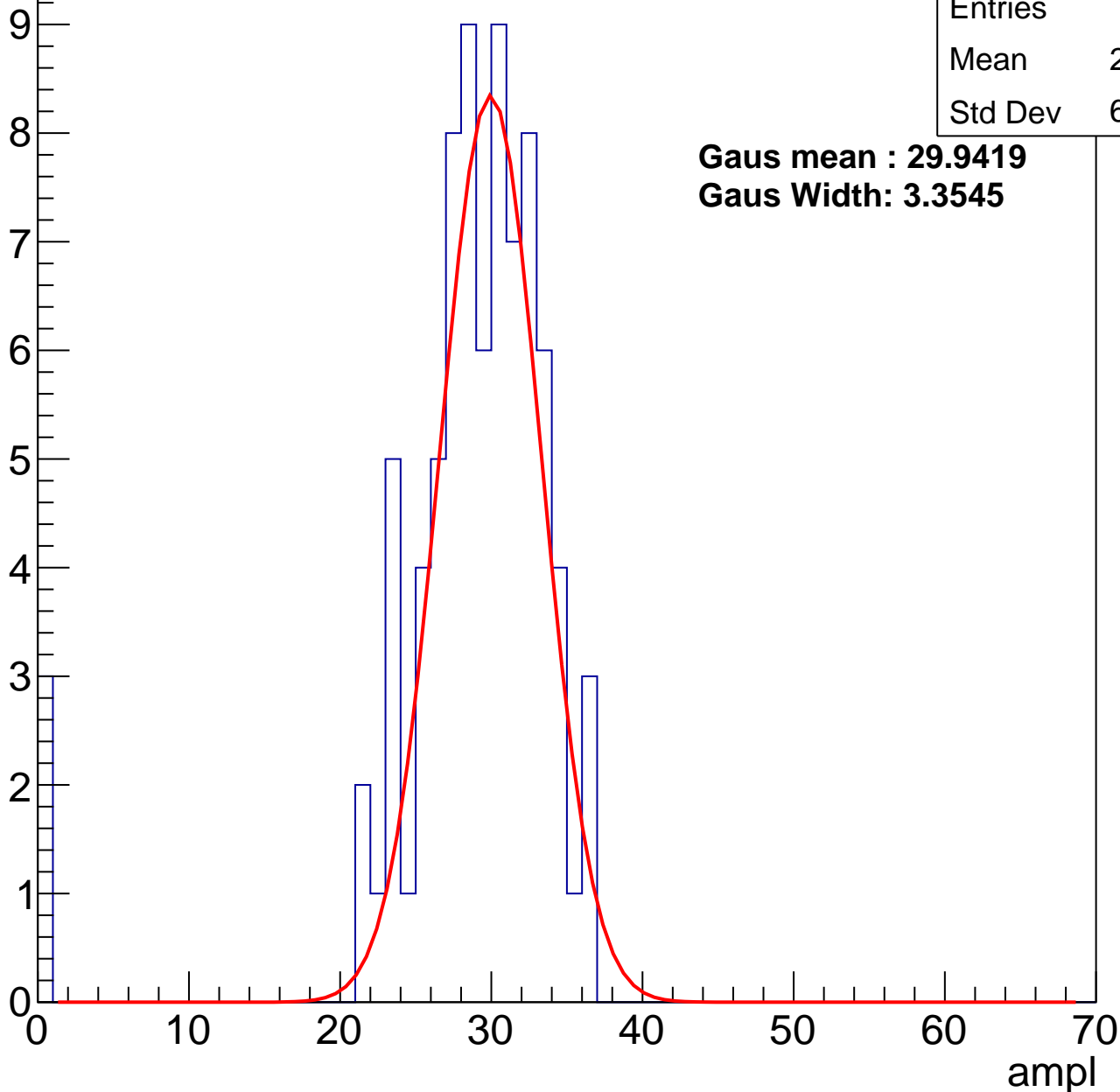
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	27.99
Std Dev	6.495

Gaus mean : 29.9419

Gaus Width: 3.3545



B1L101S, U12-ch2, adc1

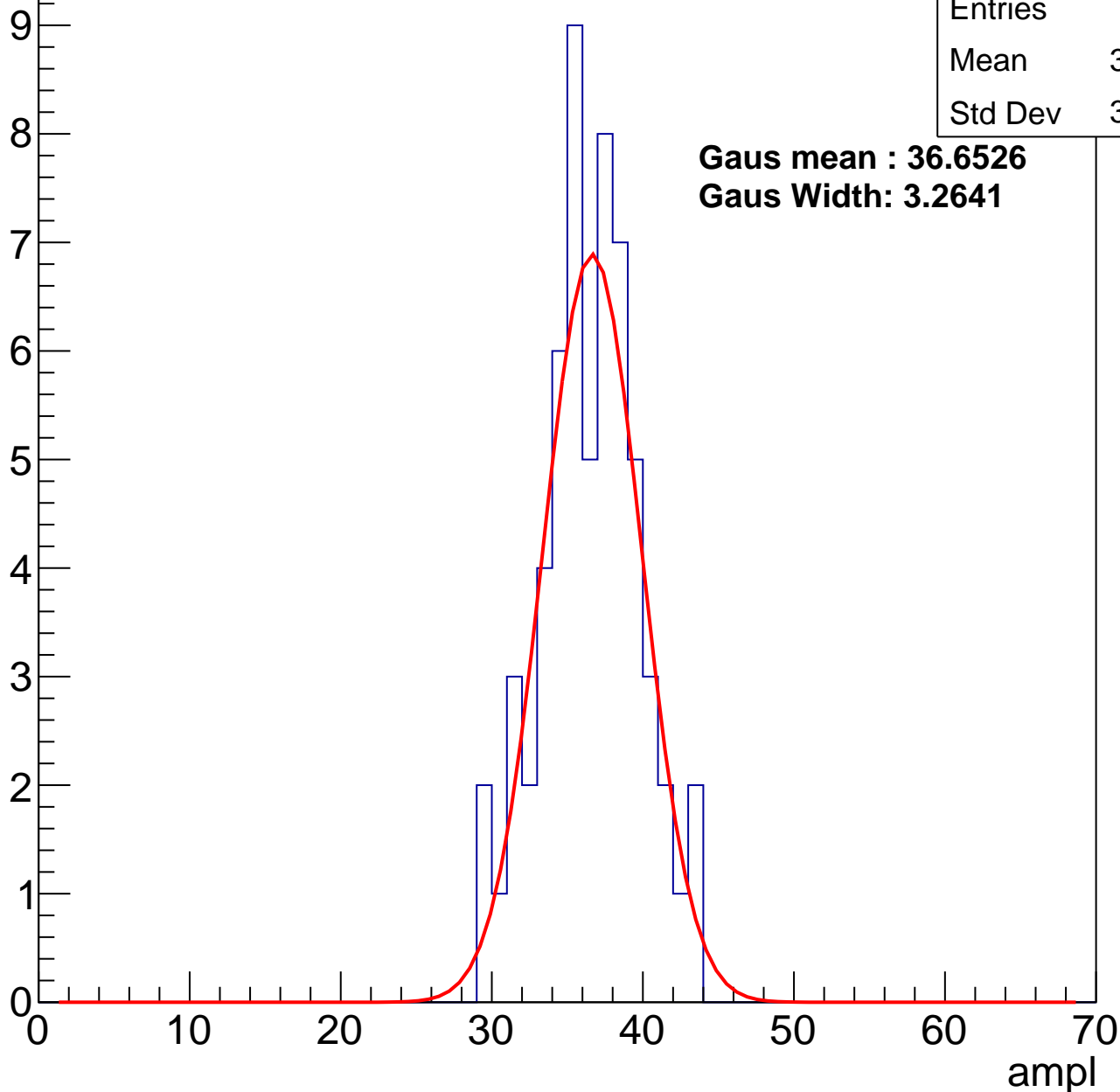
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.05
Std Dev	3.222

Gaus mean : 36.6526

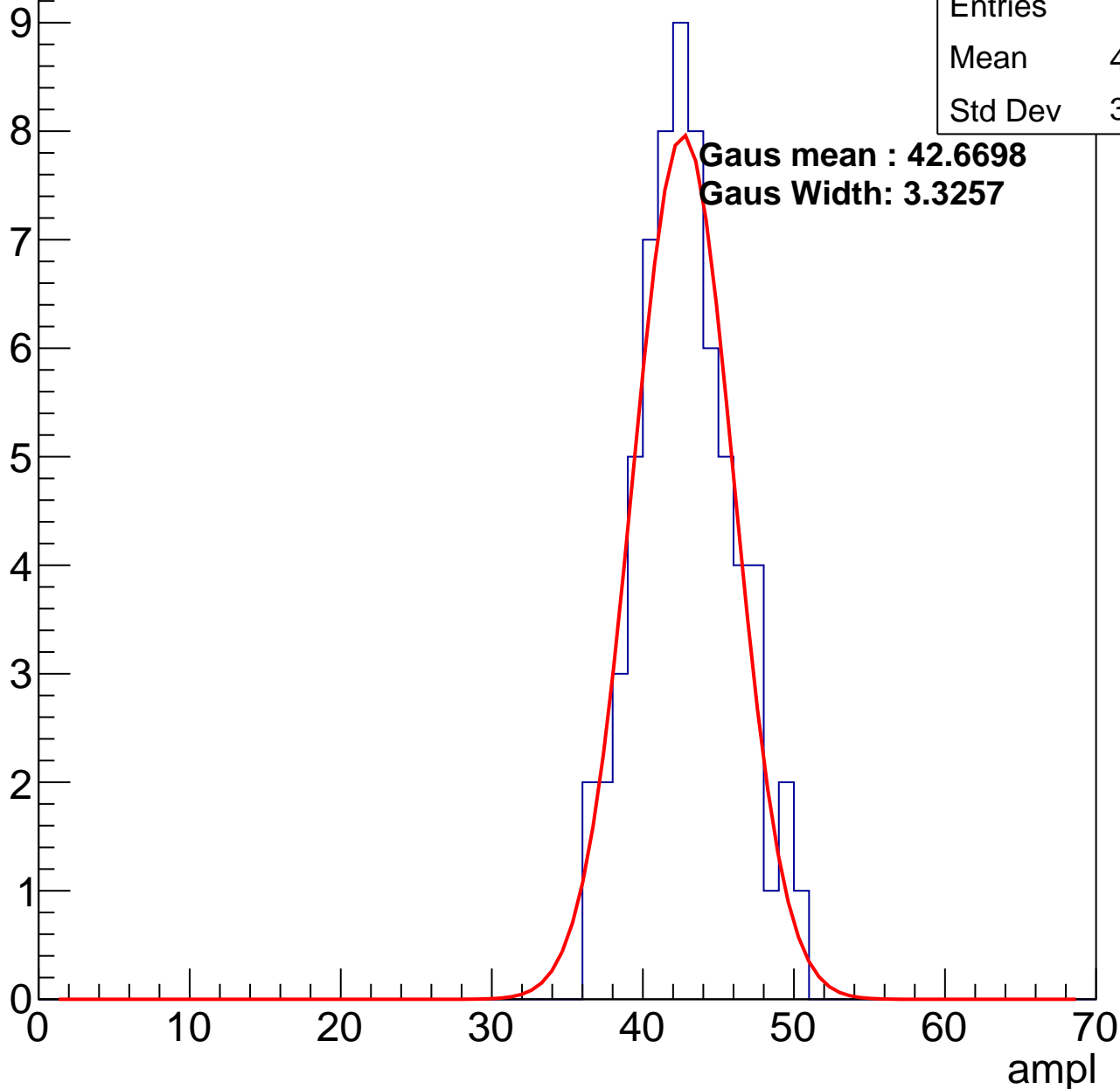
Gaus Width: 3.2641



B1L101S, U12-ch2, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch2, adc3

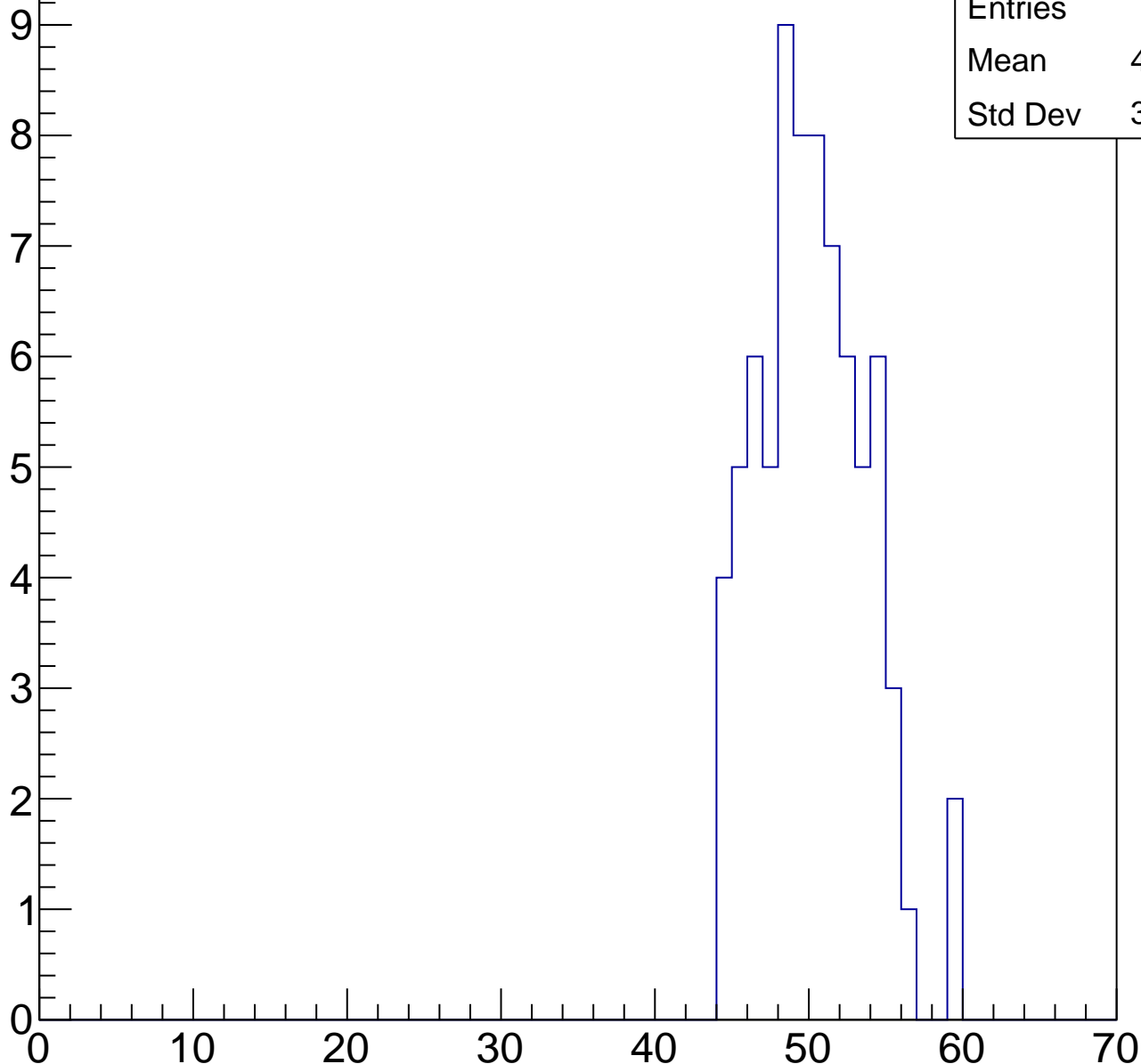
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

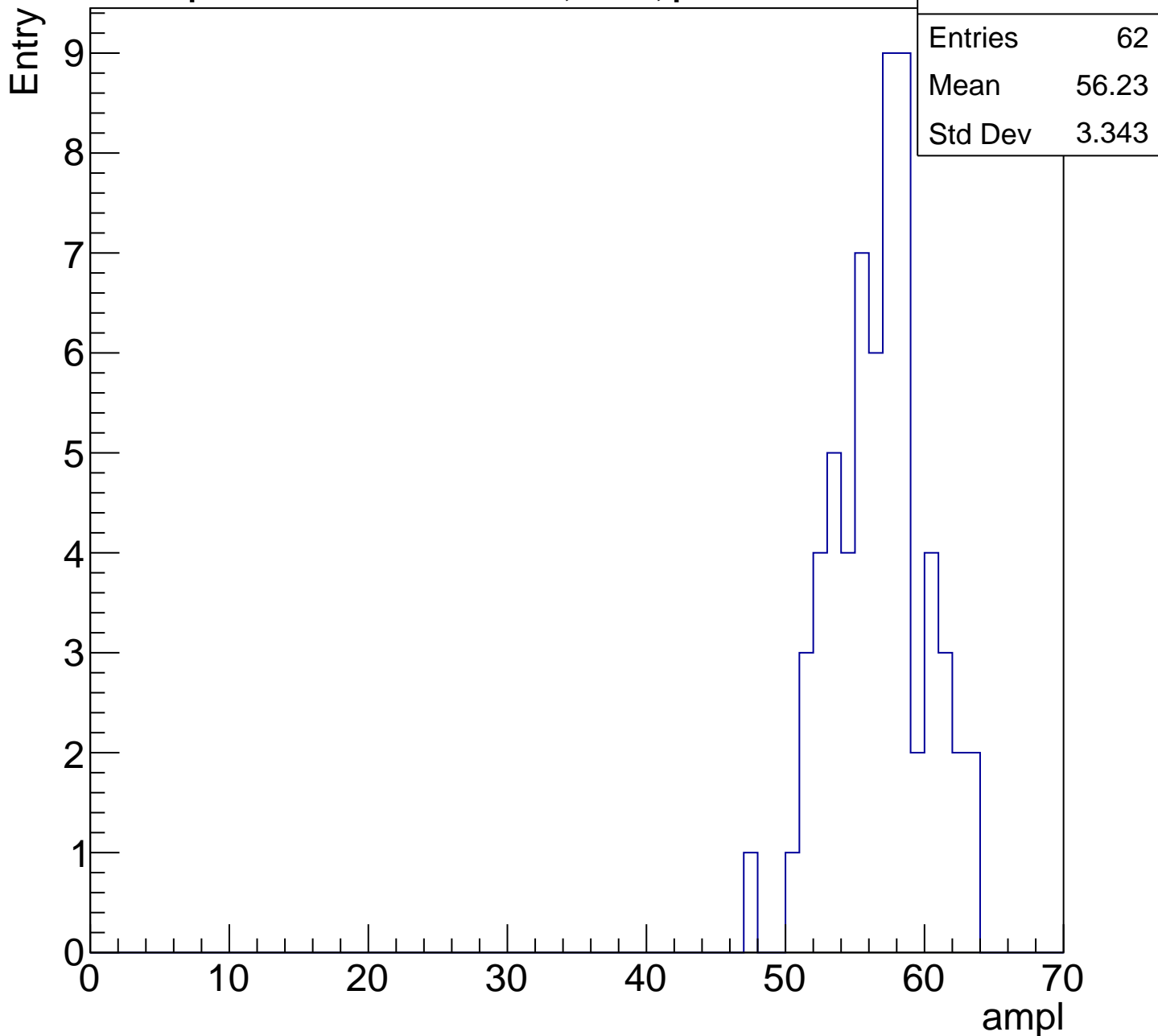
Entries	75
Mean	49.77
Std Dev	3.455

ampl



B1L101S, U12-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

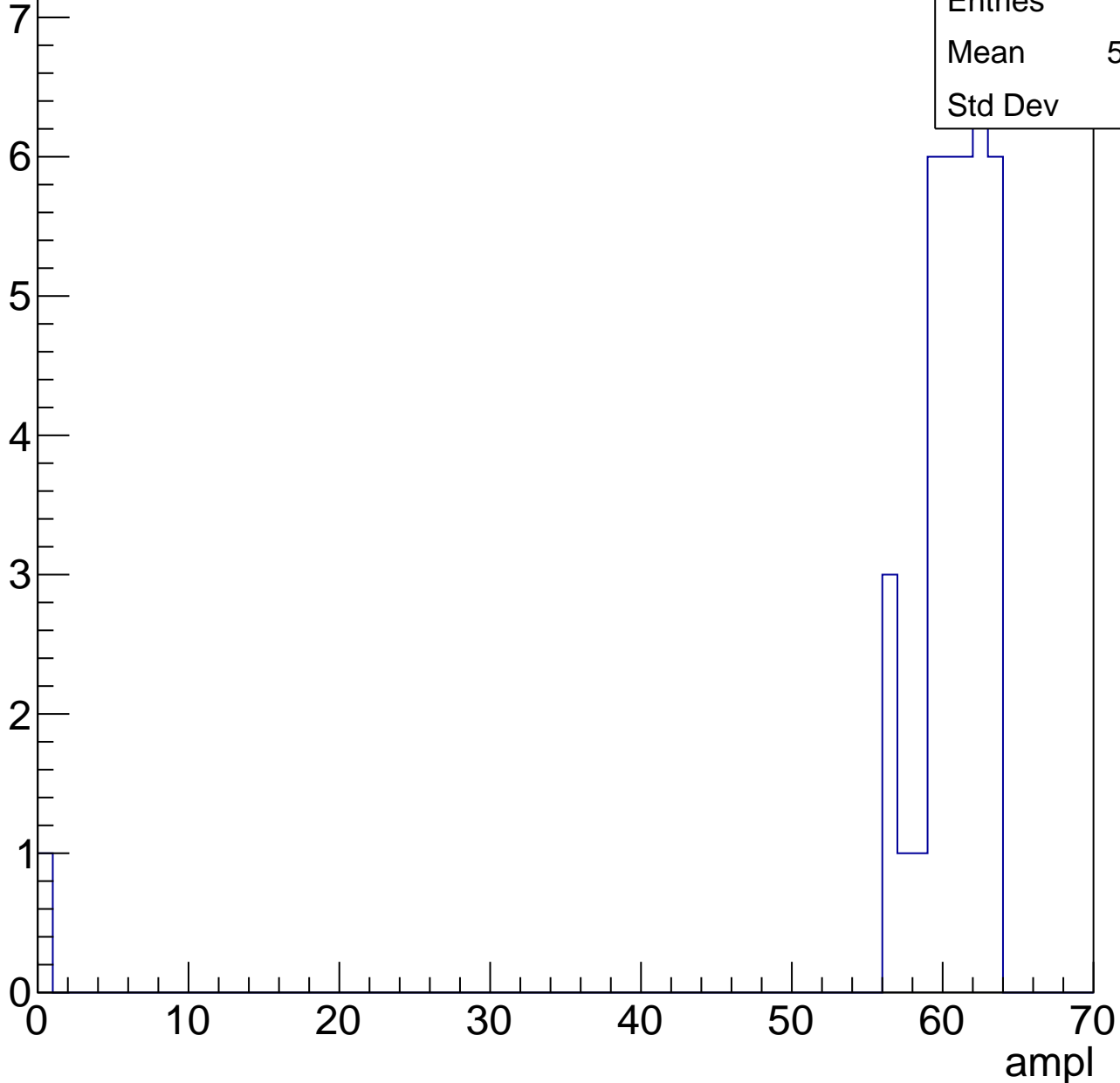


B1L101S, U12-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

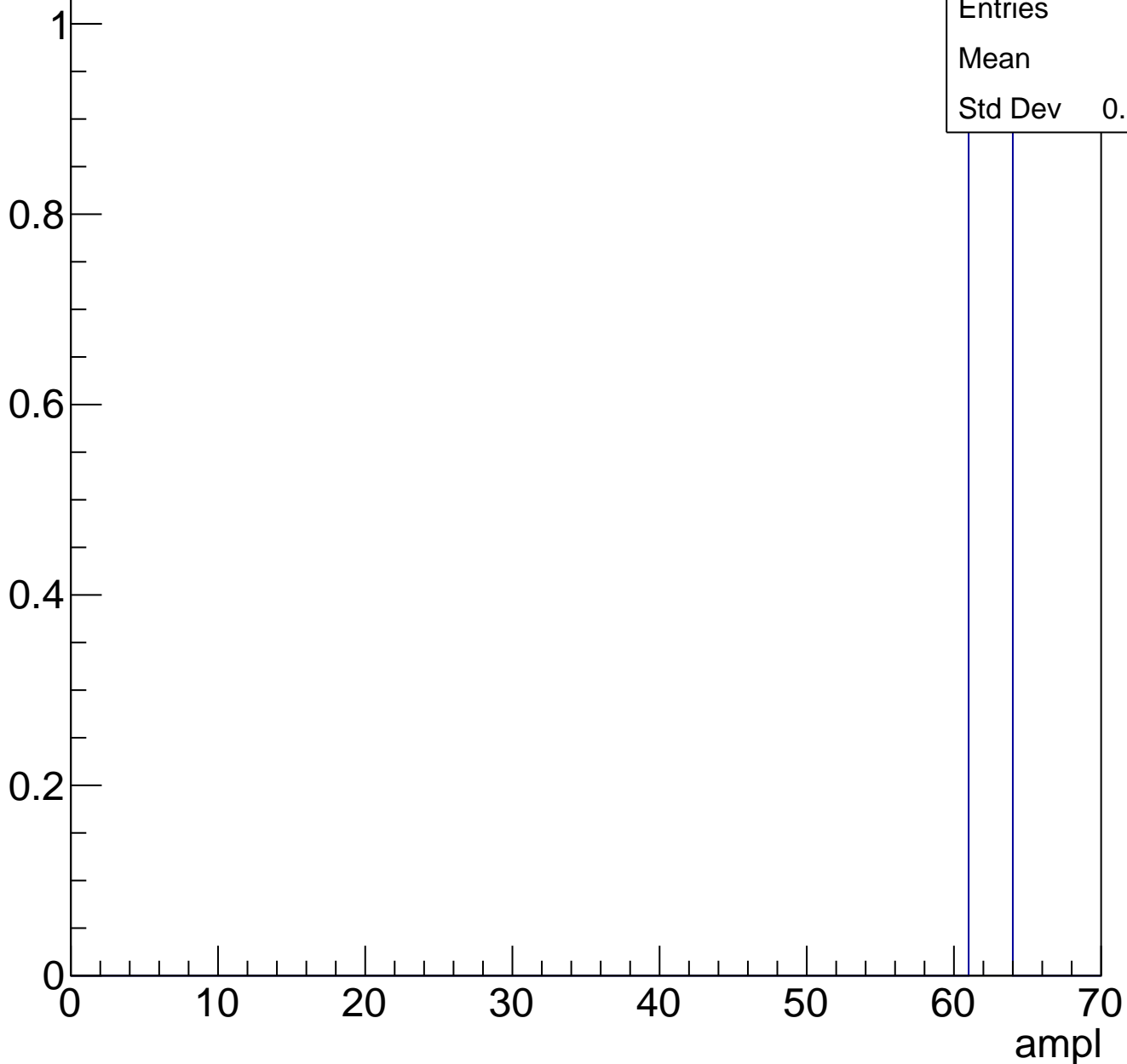
Entries	37
Mean	58.78
Std Dev	10



B1L101S, U12-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U12-ch3, adc0

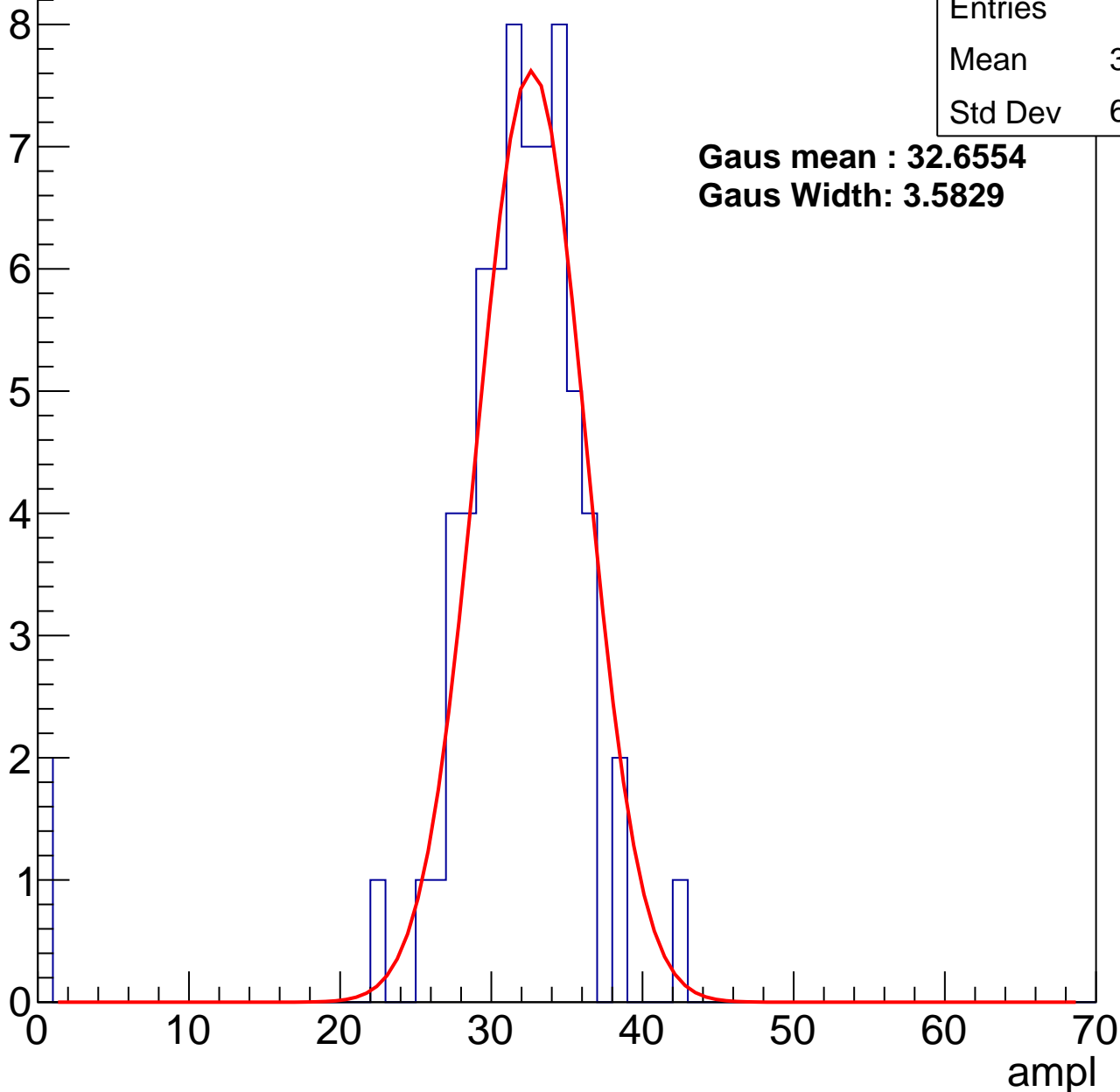
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.73
Std Dev	6.342

Gaus mean : 32.6554

Gaus Width: 3.5829



B1L101S, U12-ch3, adc1

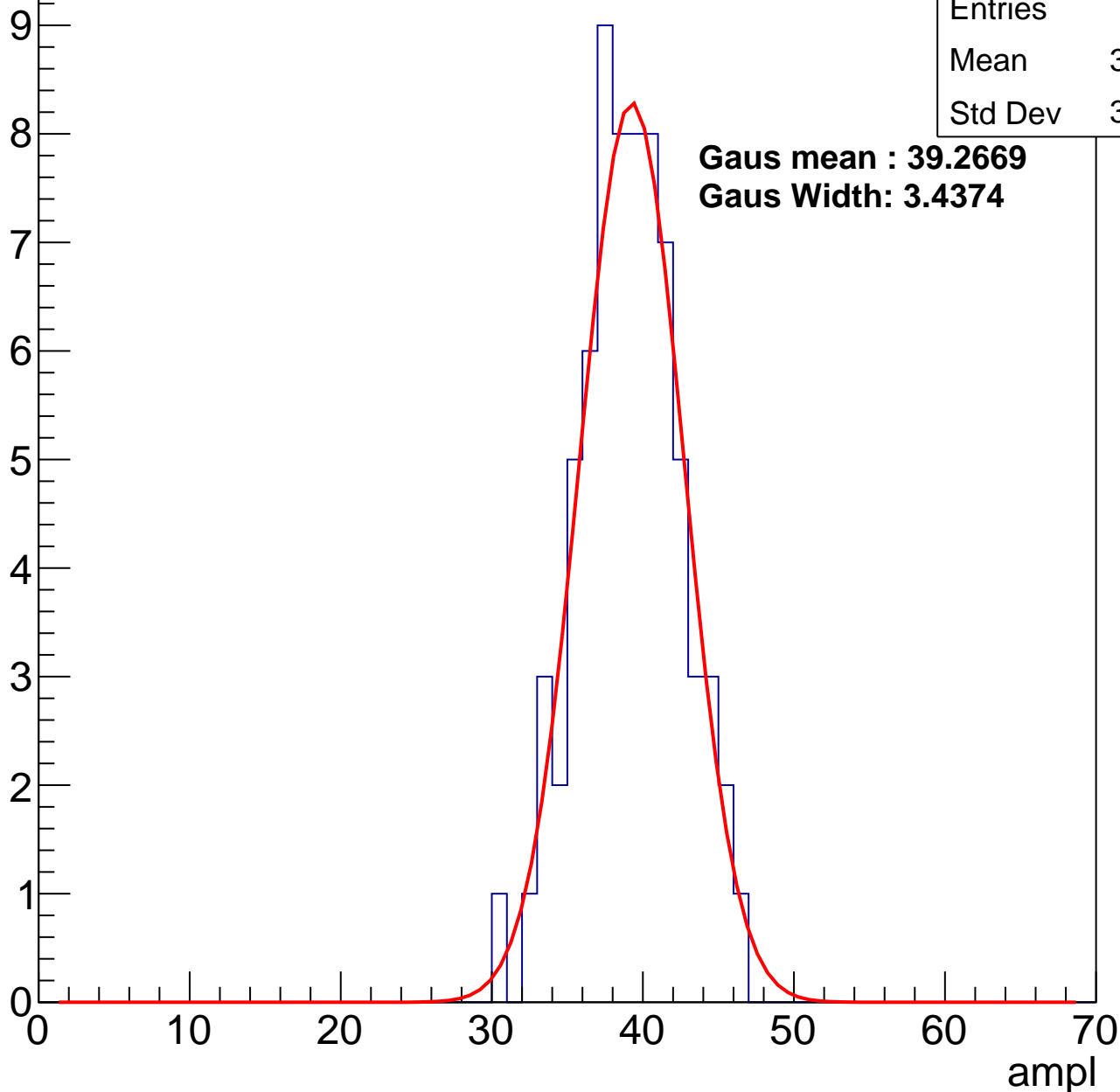
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	38.65
Std Dev	3.296

Gaus mean : 39.2669

Gaus Width: 3.4374



B1L101S, U12-ch3, adc2

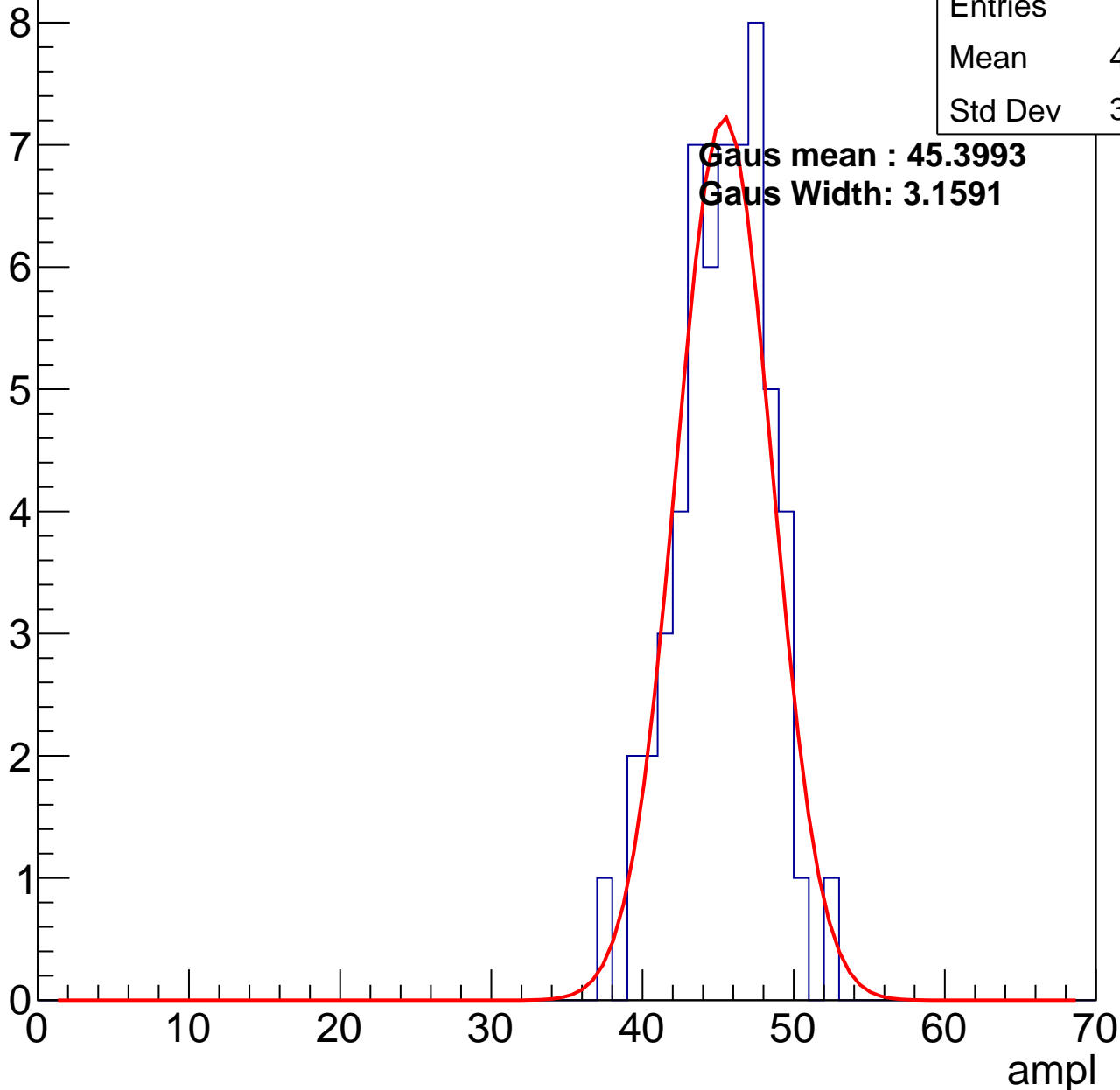
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.86
Std Dev	3.008

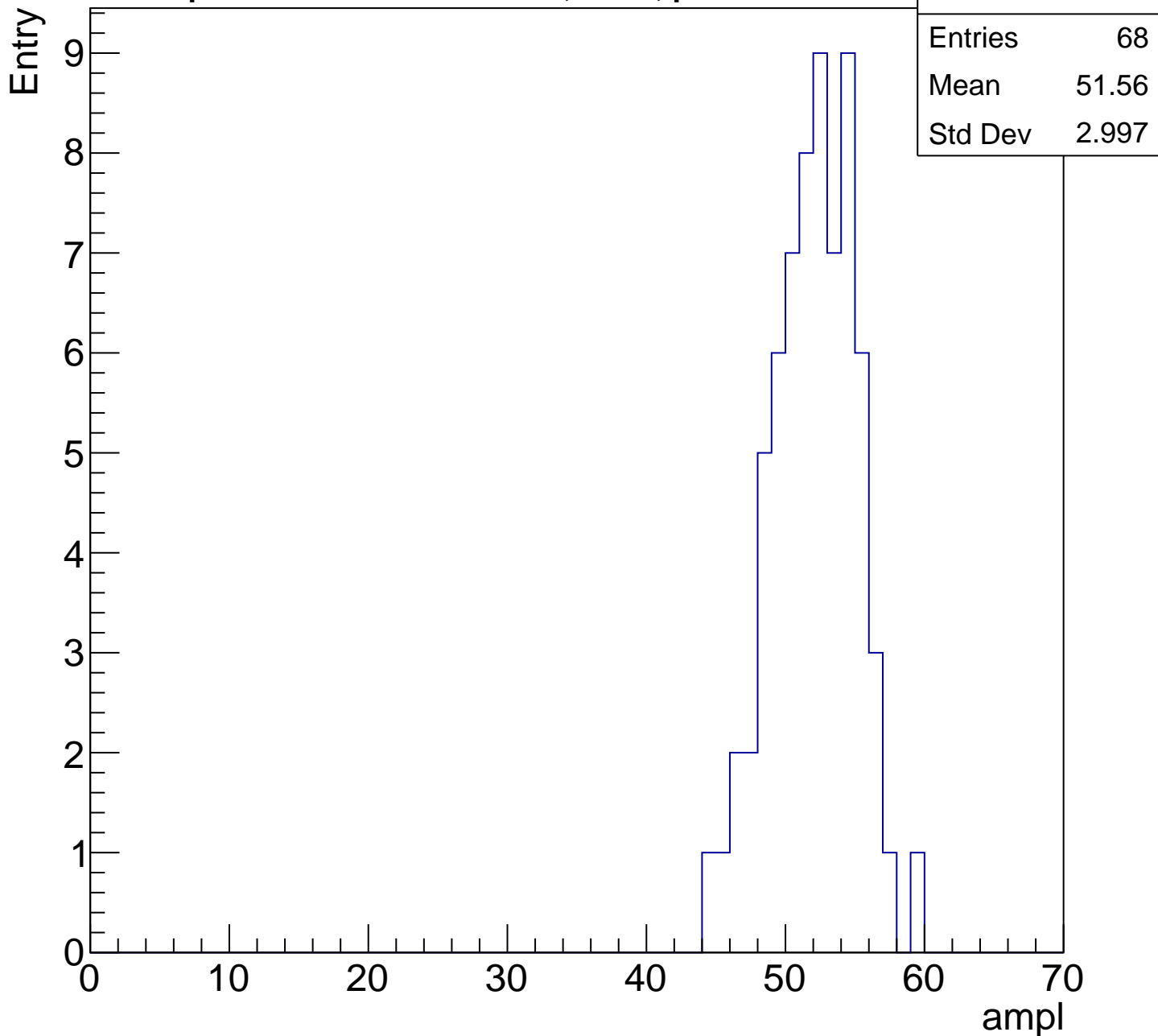
Gaus mean : 45.3993

Gaus Width: 3.1591



B1L101S, U12-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	57.95
Std Dev	3.229

Entry

10

8

6

4

2

0

0

10

20

30

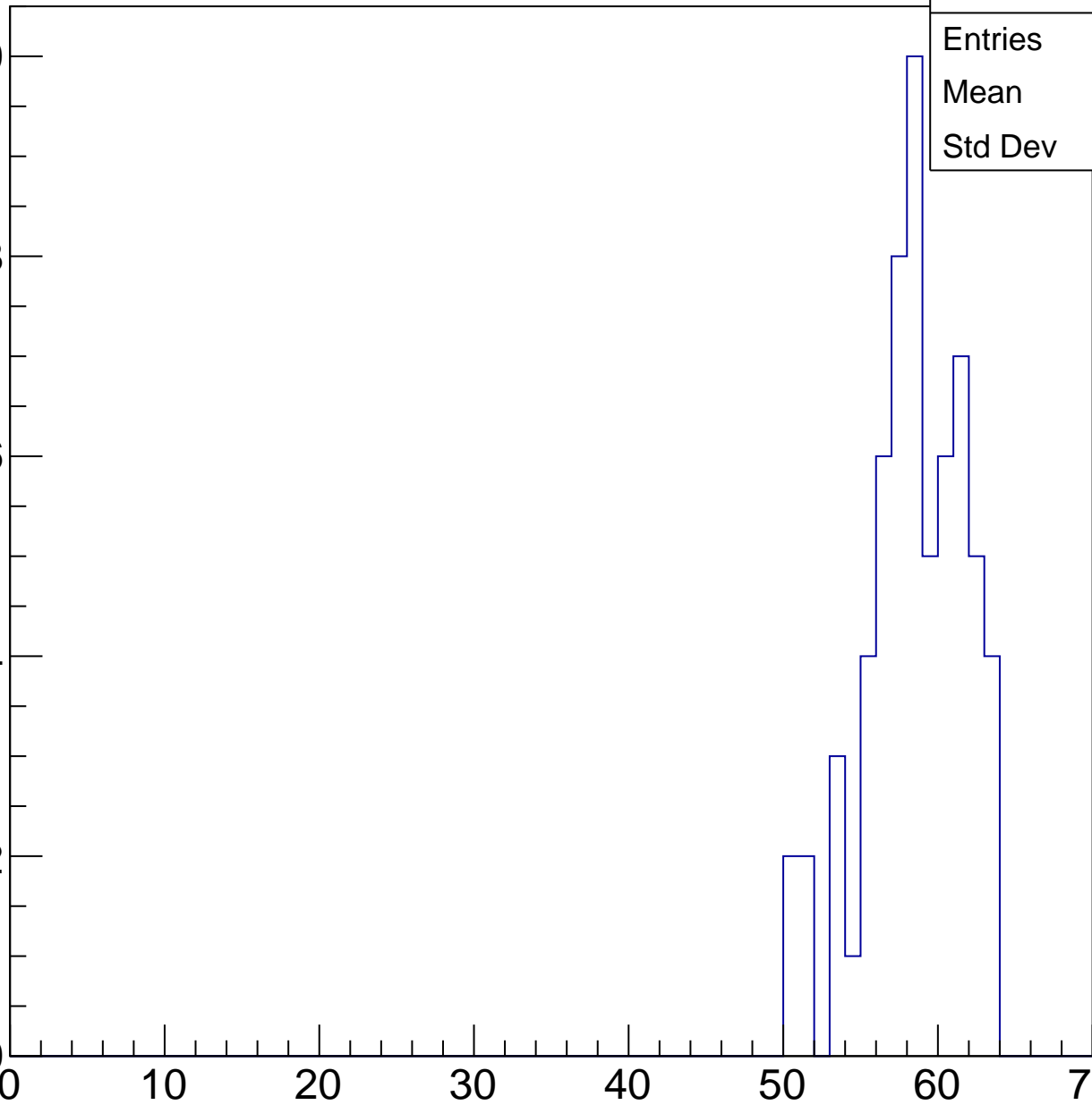
40

50

60

70

ampl

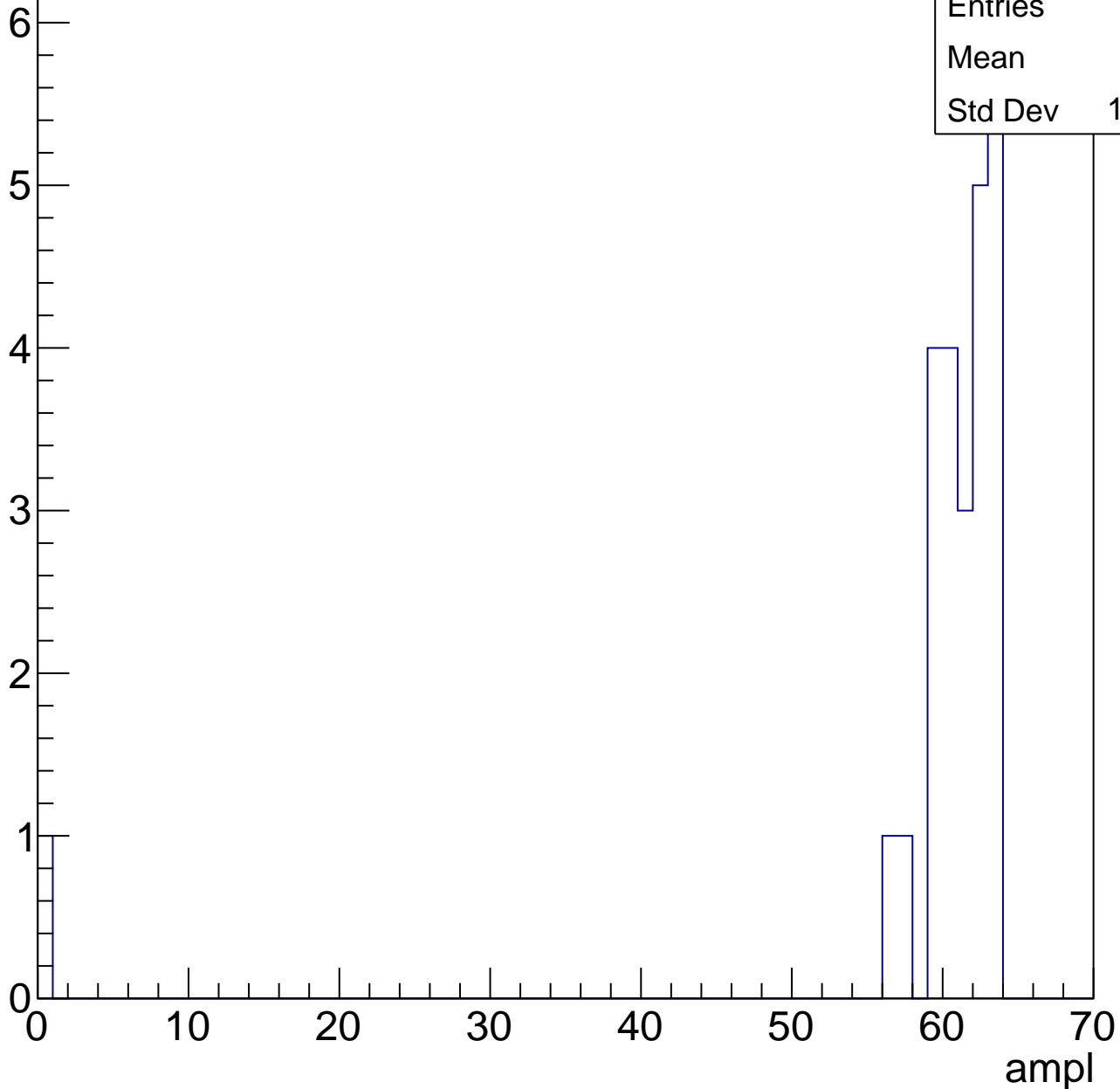


B1L101S, U12-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.4
Std Dev	12.07



B1L101S, U12-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch4, adc0

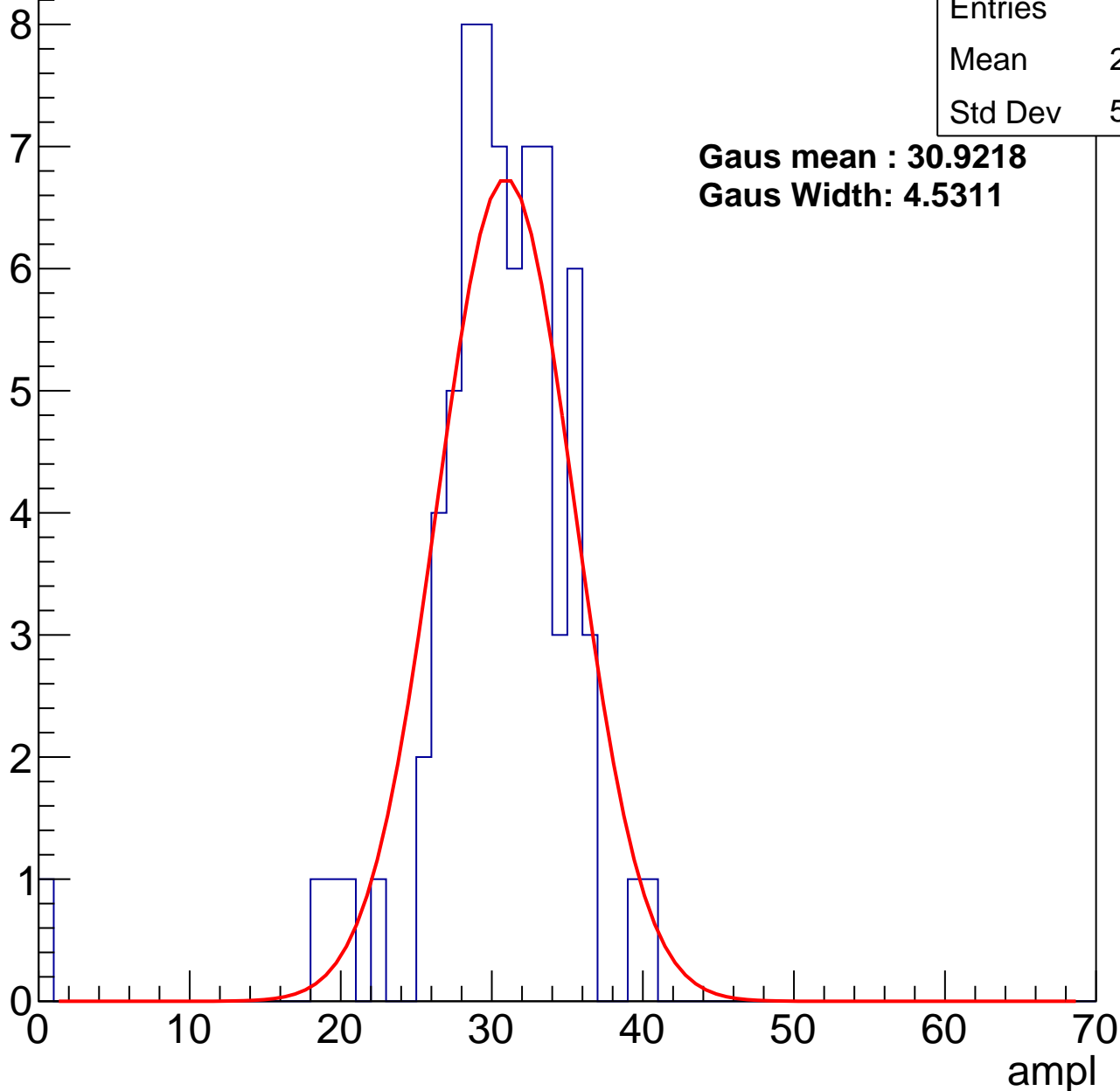
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.78
Std Dev	5.374

Gaus mean : 30.9218

Gaus Width: 4.5311



B1L101S, U12-ch4, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	37.11
Std Dev	3.725

Gaus mean : 37.6334

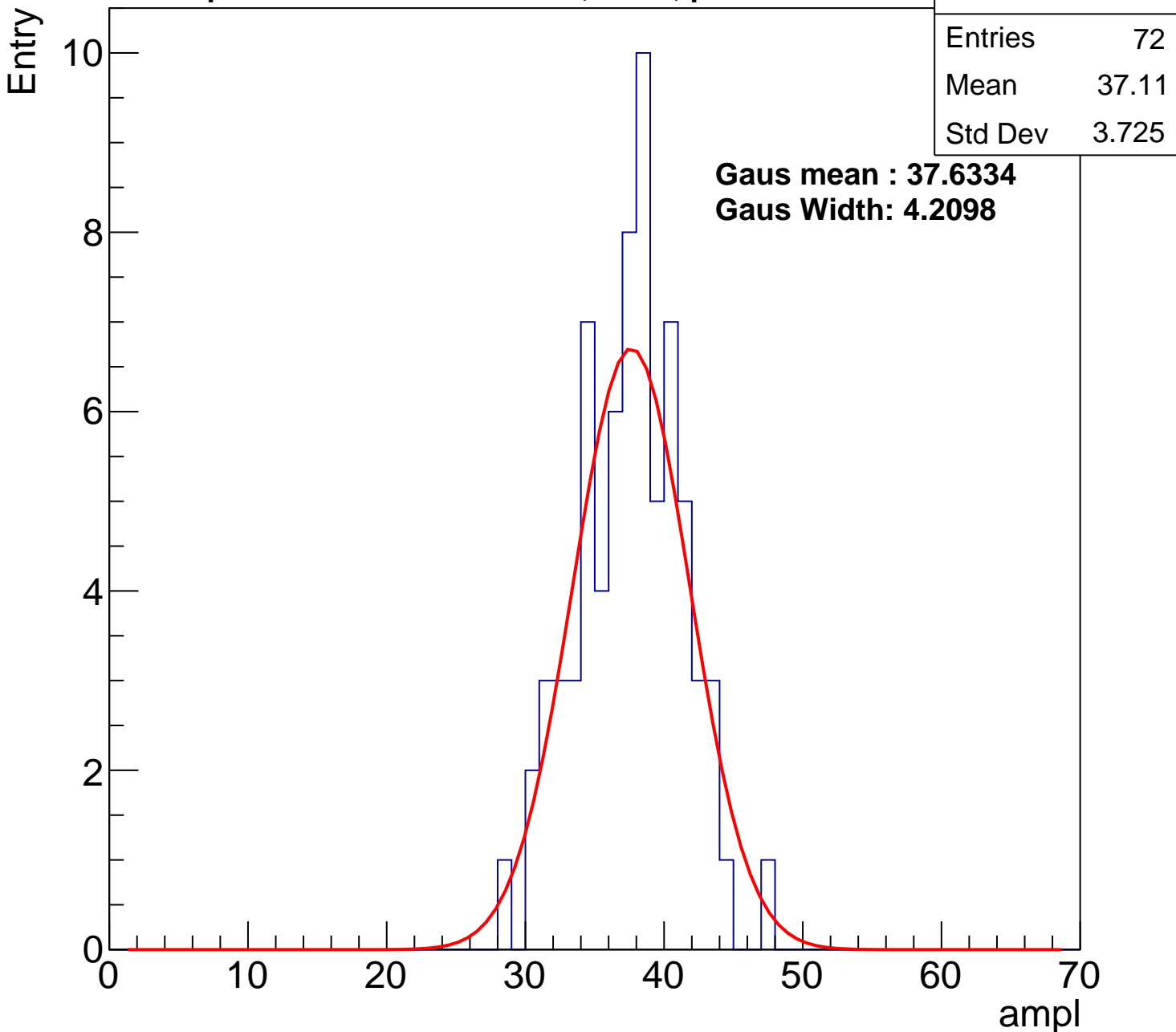
Gaus Width: 4.2098

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch4, adc2

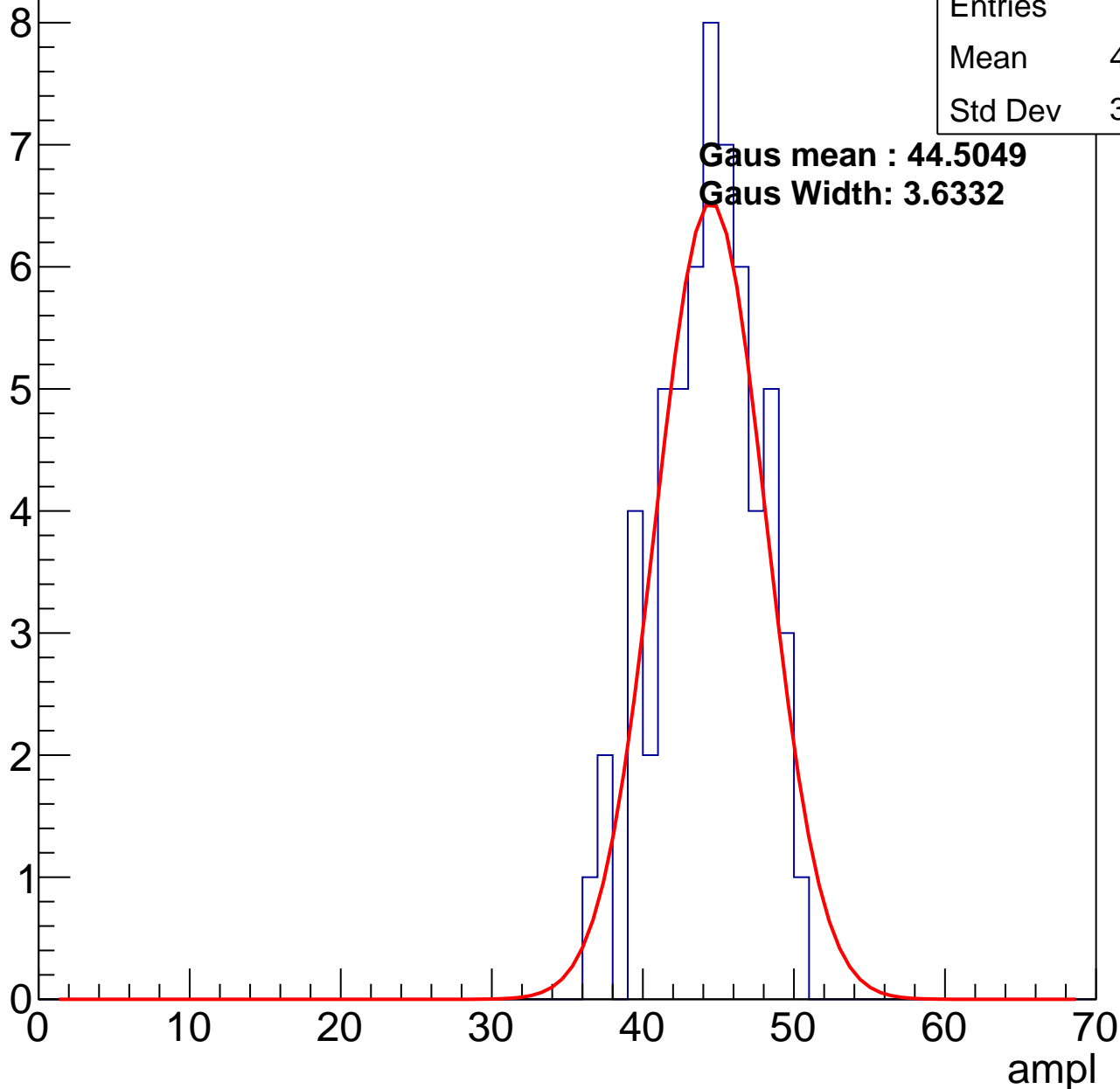
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.85
Std Dev	3.246

Gaus mean : 44.5049

Gaus Width: 3.6332

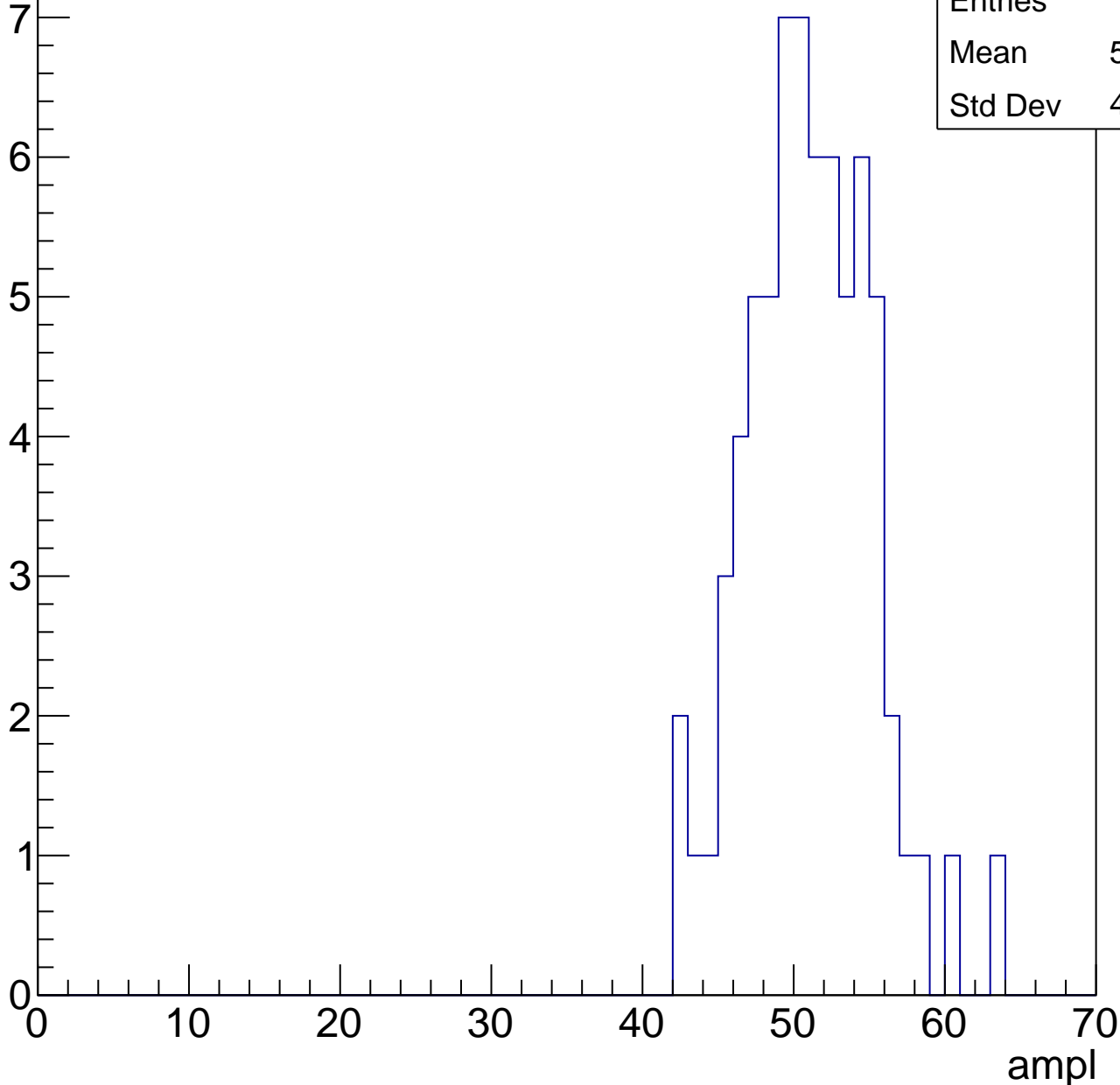


B1L101S, U12-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

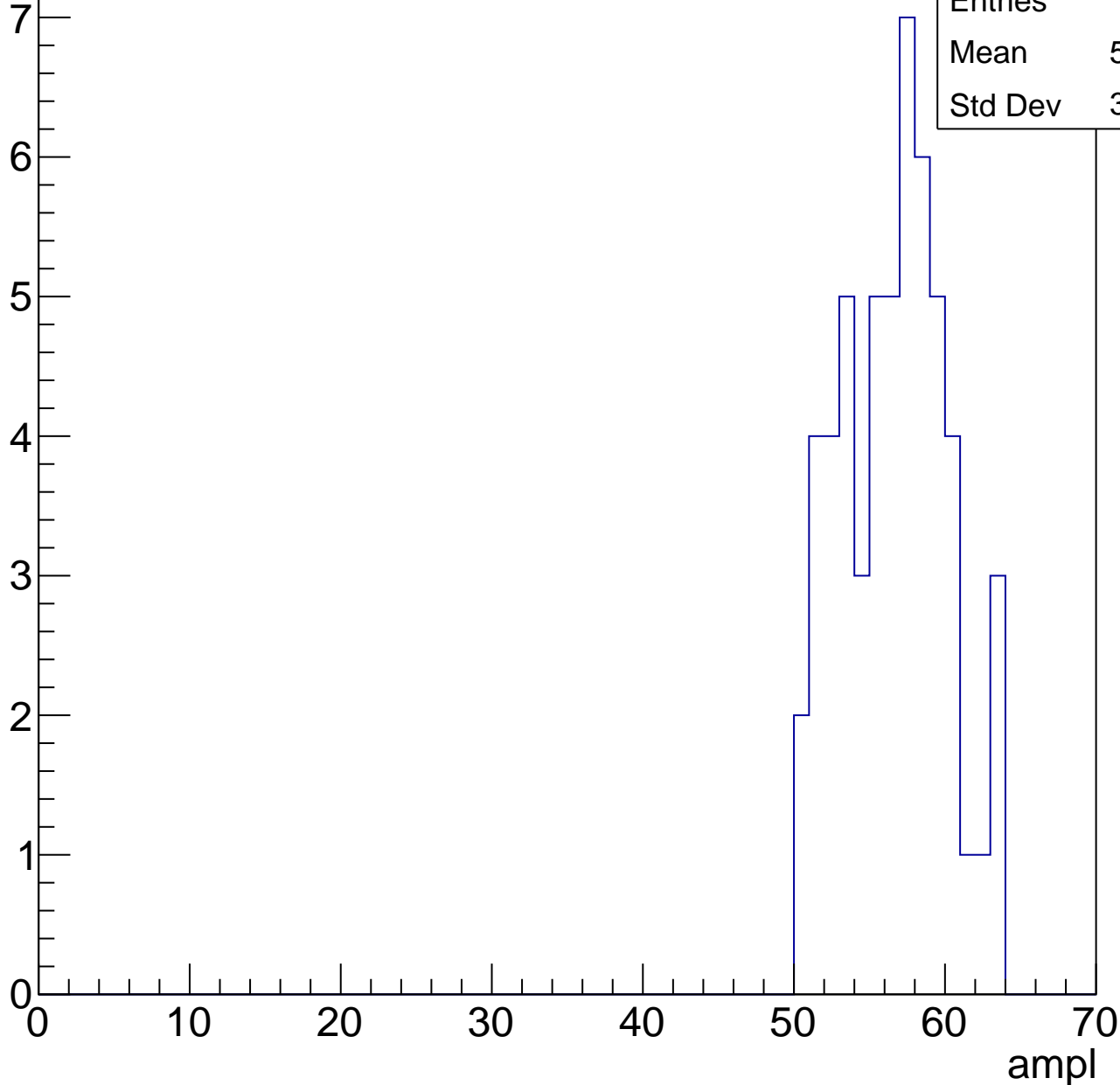
Entries	69
Mean	50.58
Std Dev	4.084



B1L101S, U12-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

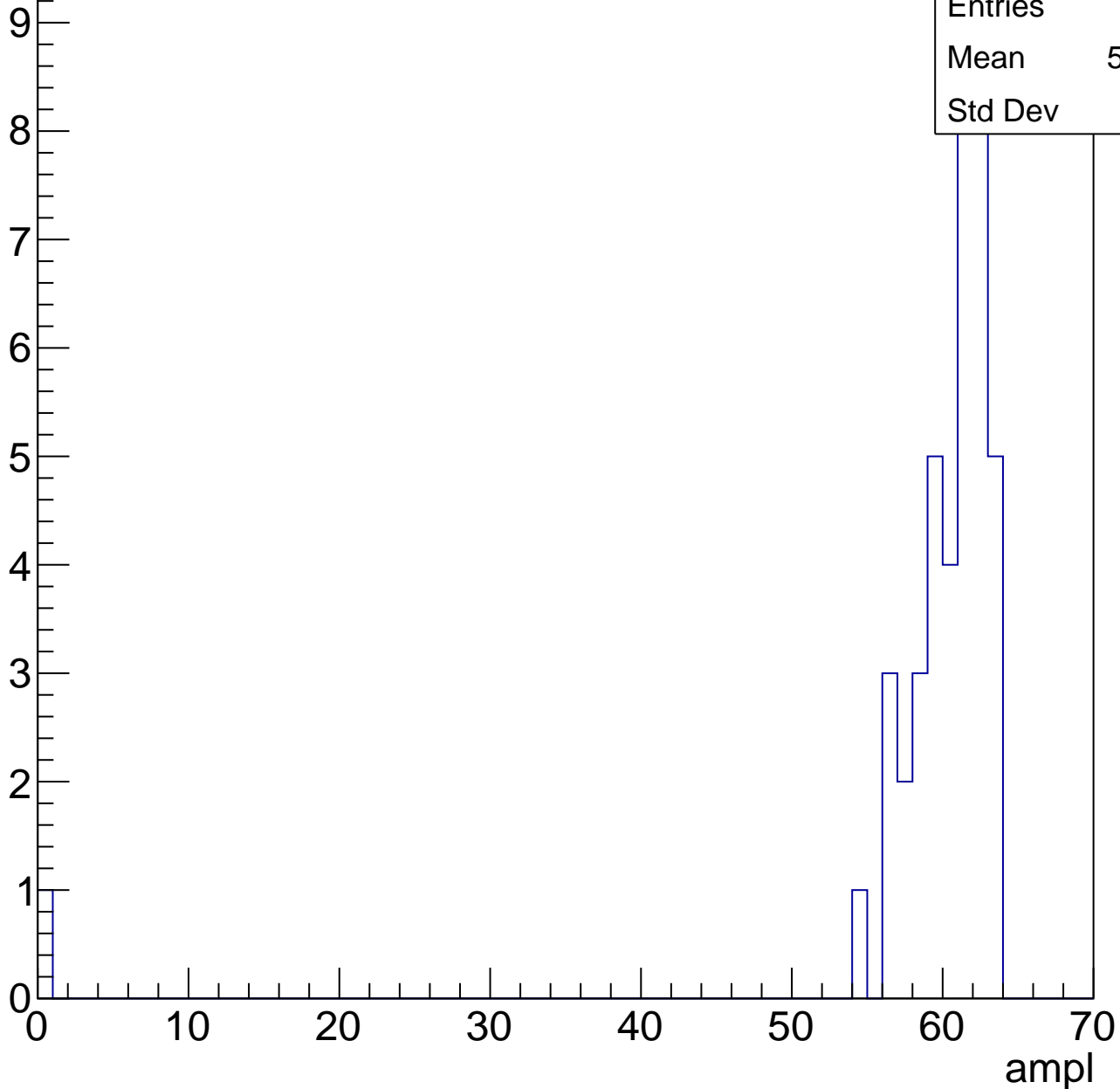


B1L101S, U12-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.66
Std Dev	9.54



B1L101S, U12-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch5, adc0

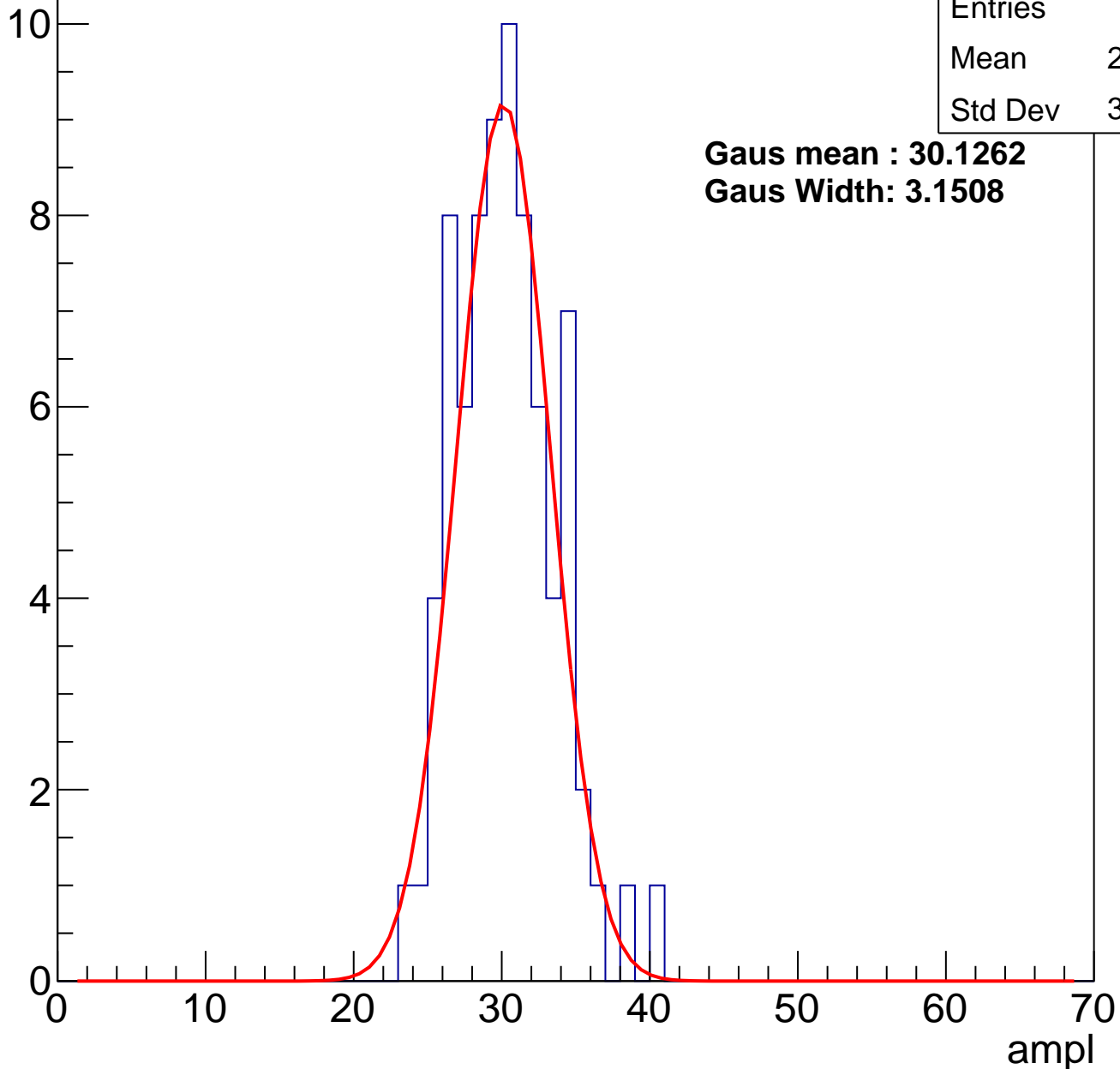
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	29.82
Std Dev	3.294

Gaus mean : 30.1262

Gaus Width: 3.1508

Entry



B1L101S, U12-ch5, adc1

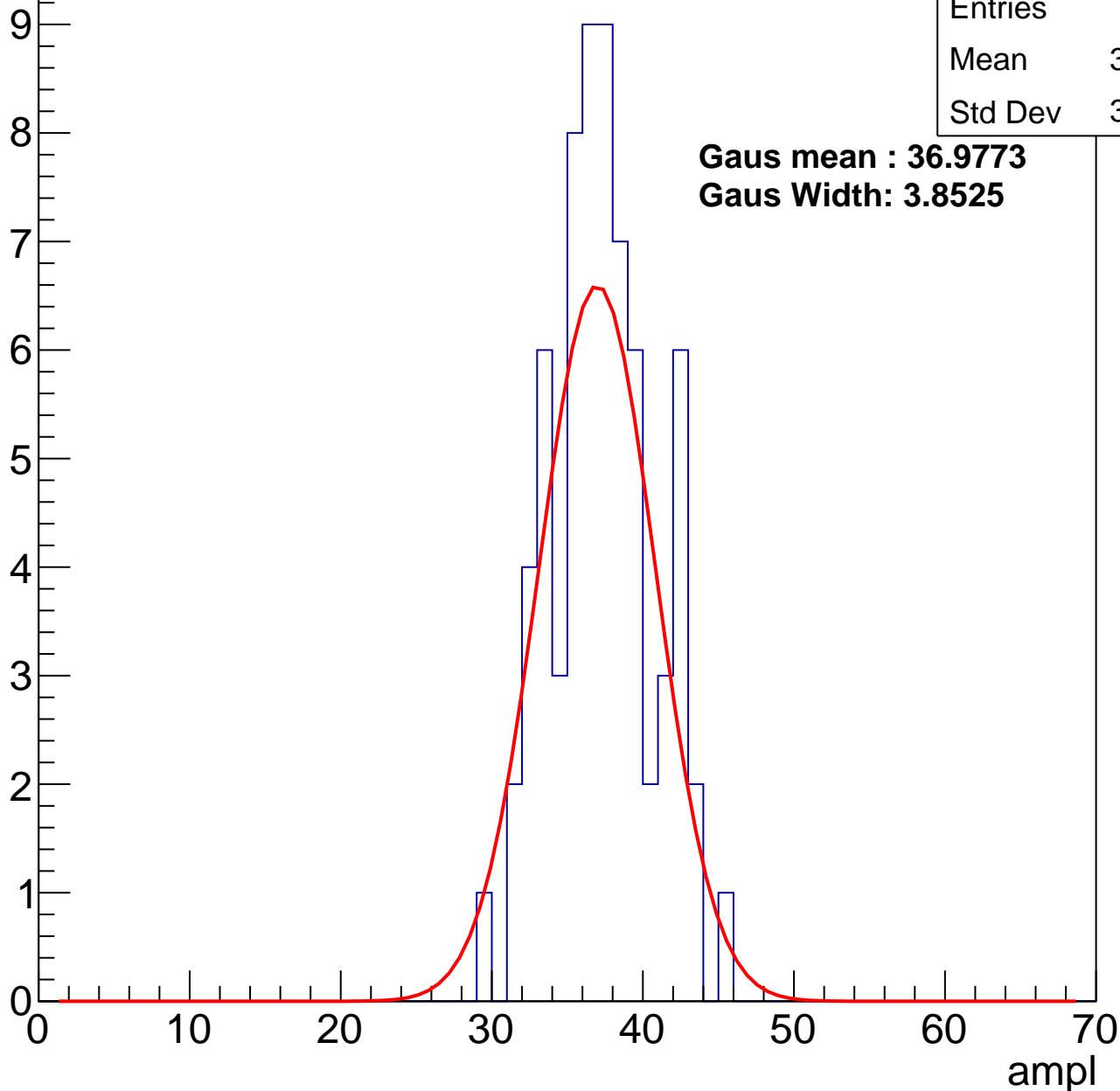
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.84
Std Dev	3.365

Gaus mean : 36.9773

Gaus Width: 3.8525



B1L101S, U12-ch5, adc2

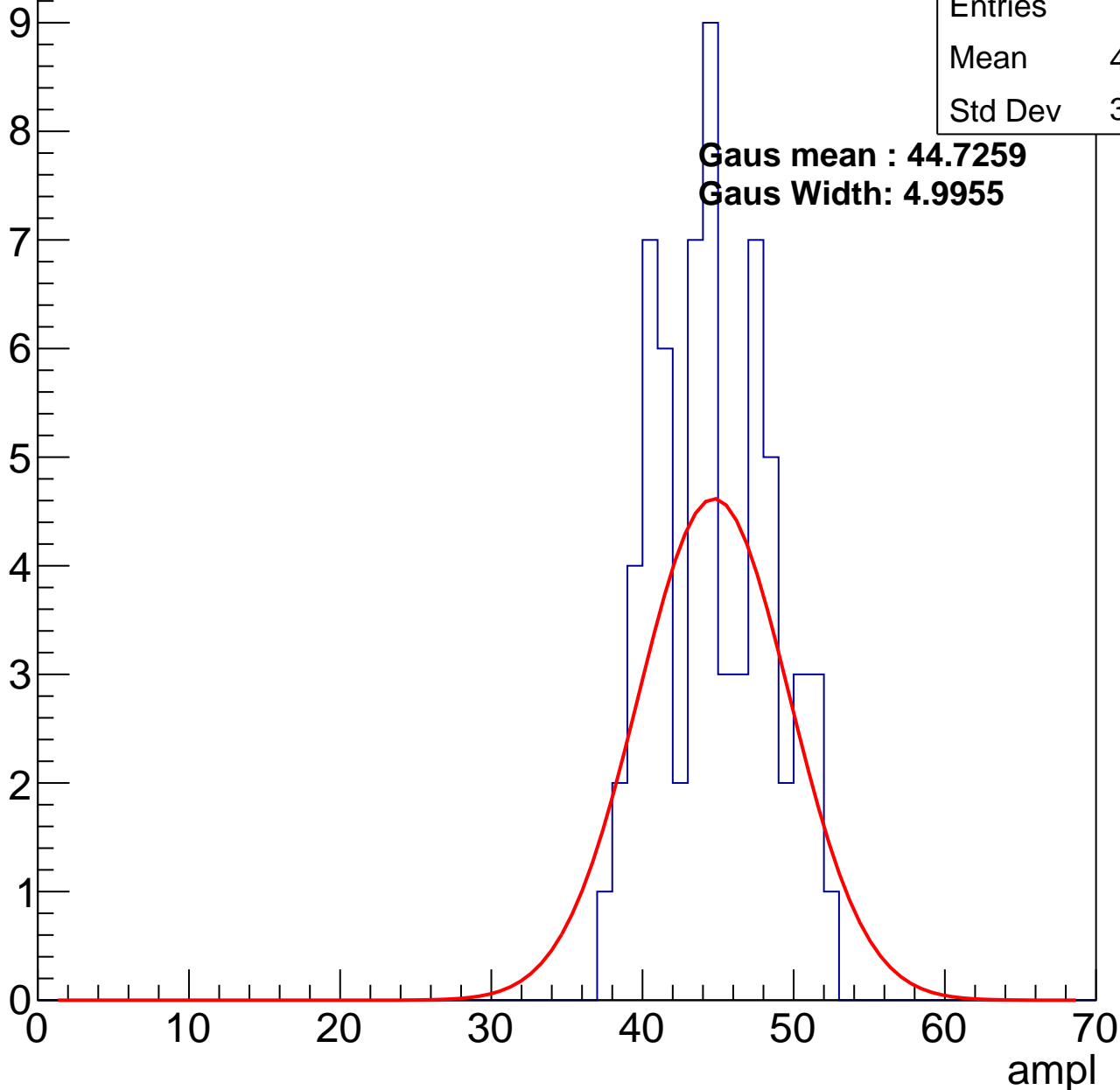
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.17
Std Dev	3.777

Gaus mean : 44.7259

Gaus Width: 4.9955

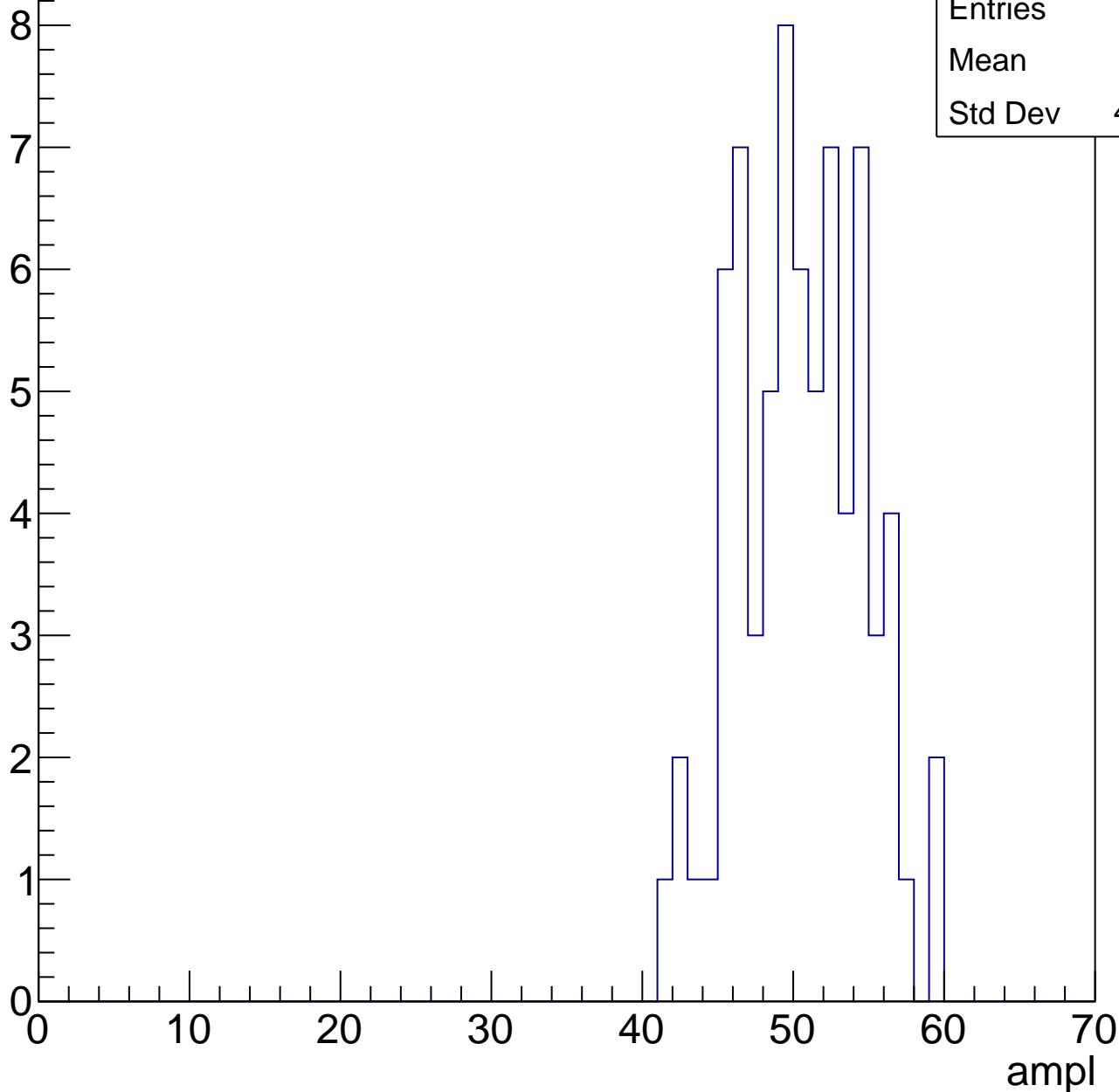


B1L101S, U12-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	50
Std Dev	4.091

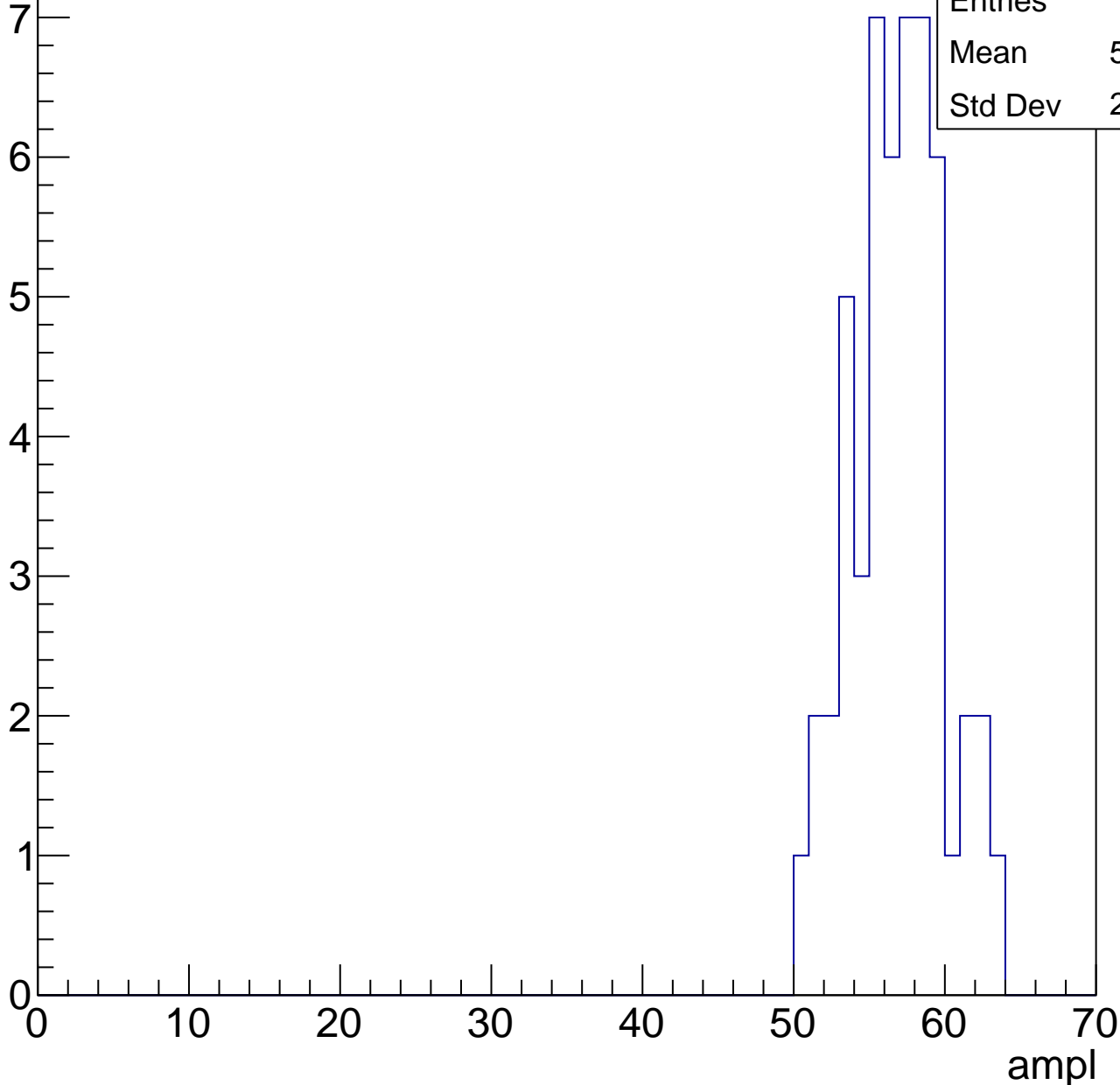


B1L101S, U12-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.38
Std Dev	2.943

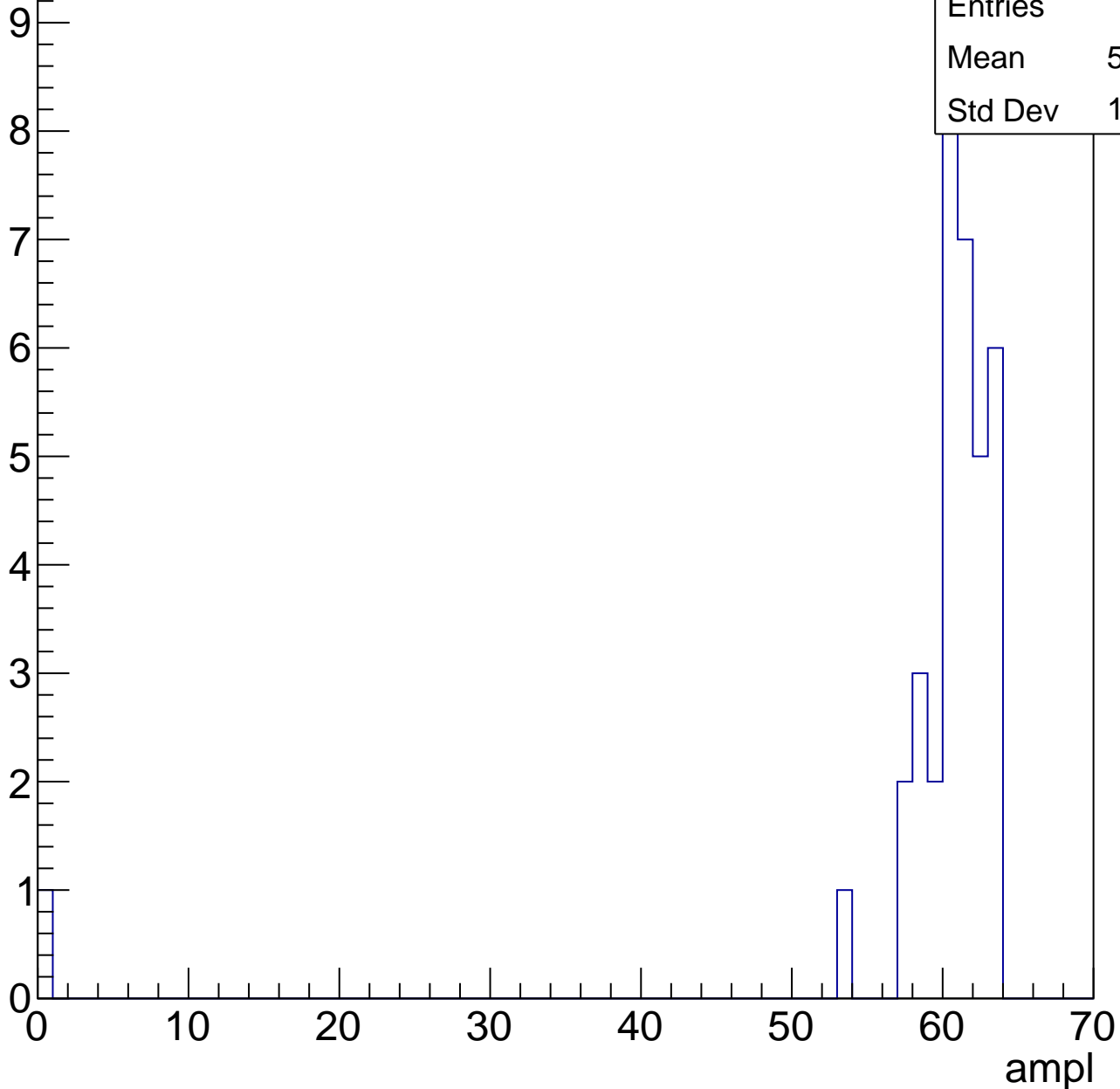


B1L101S, U12-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.72
Std Dev	10.14



B1L101S, U12-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch6, adc0

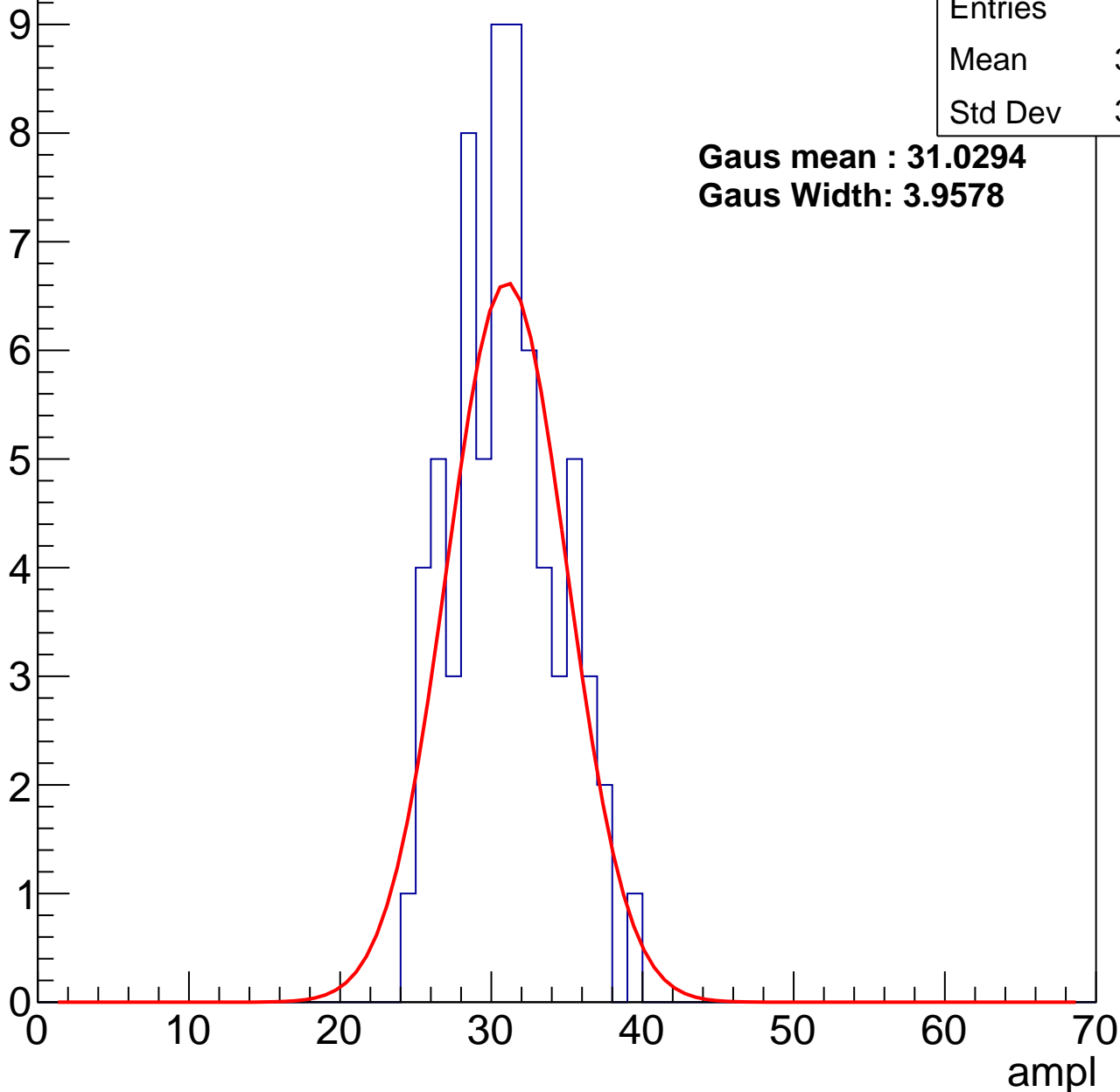
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.51
Std Dev	3.411

Gaus mean : 31.0294

Gaus Width: 3.9578



B1L101S, U12-ch6, adc1

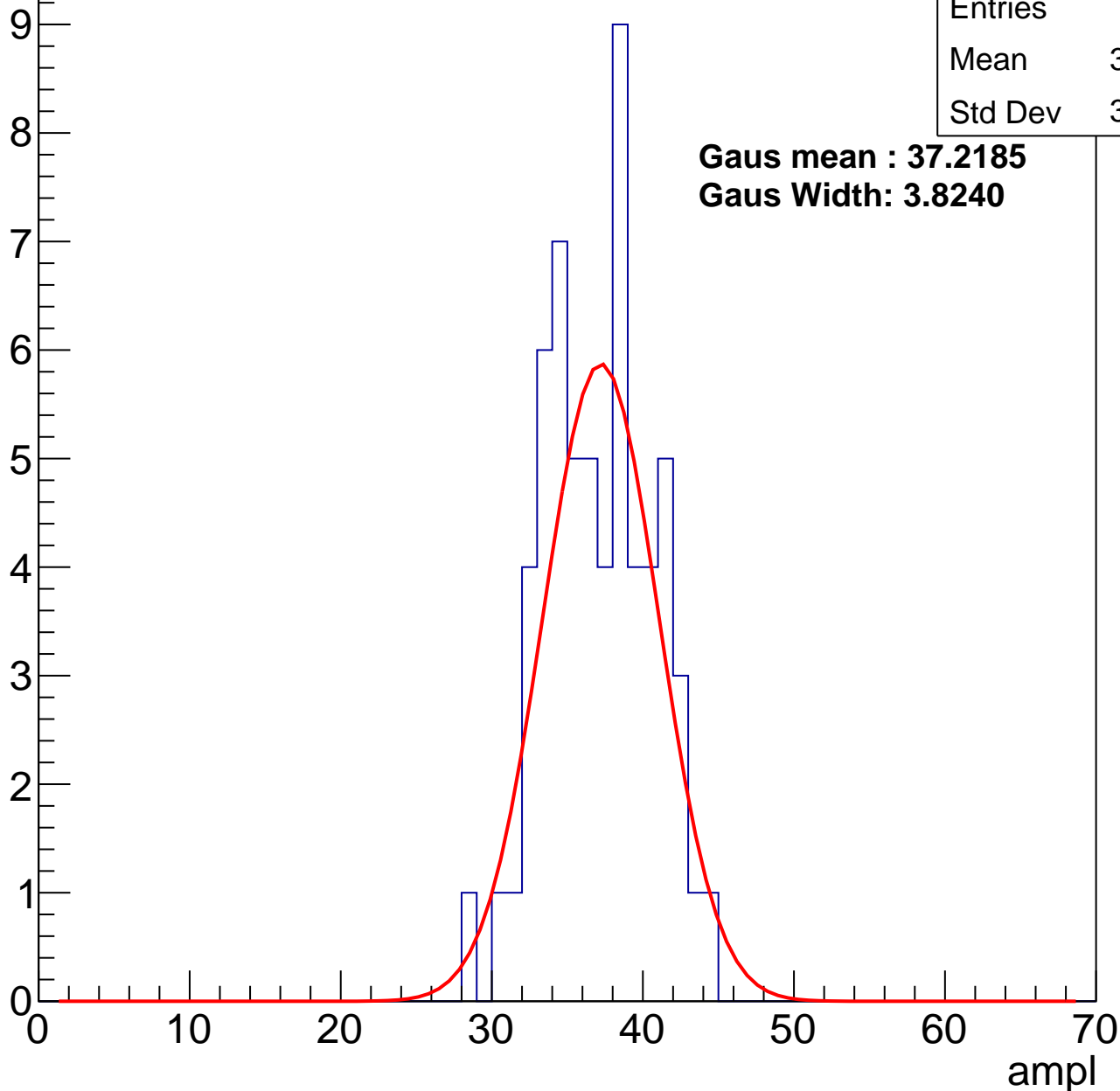
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.59
Std Dev	3.485

Gaus mean : 37.2185

Gaus Width: 3.8240



B1L101S, U12-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

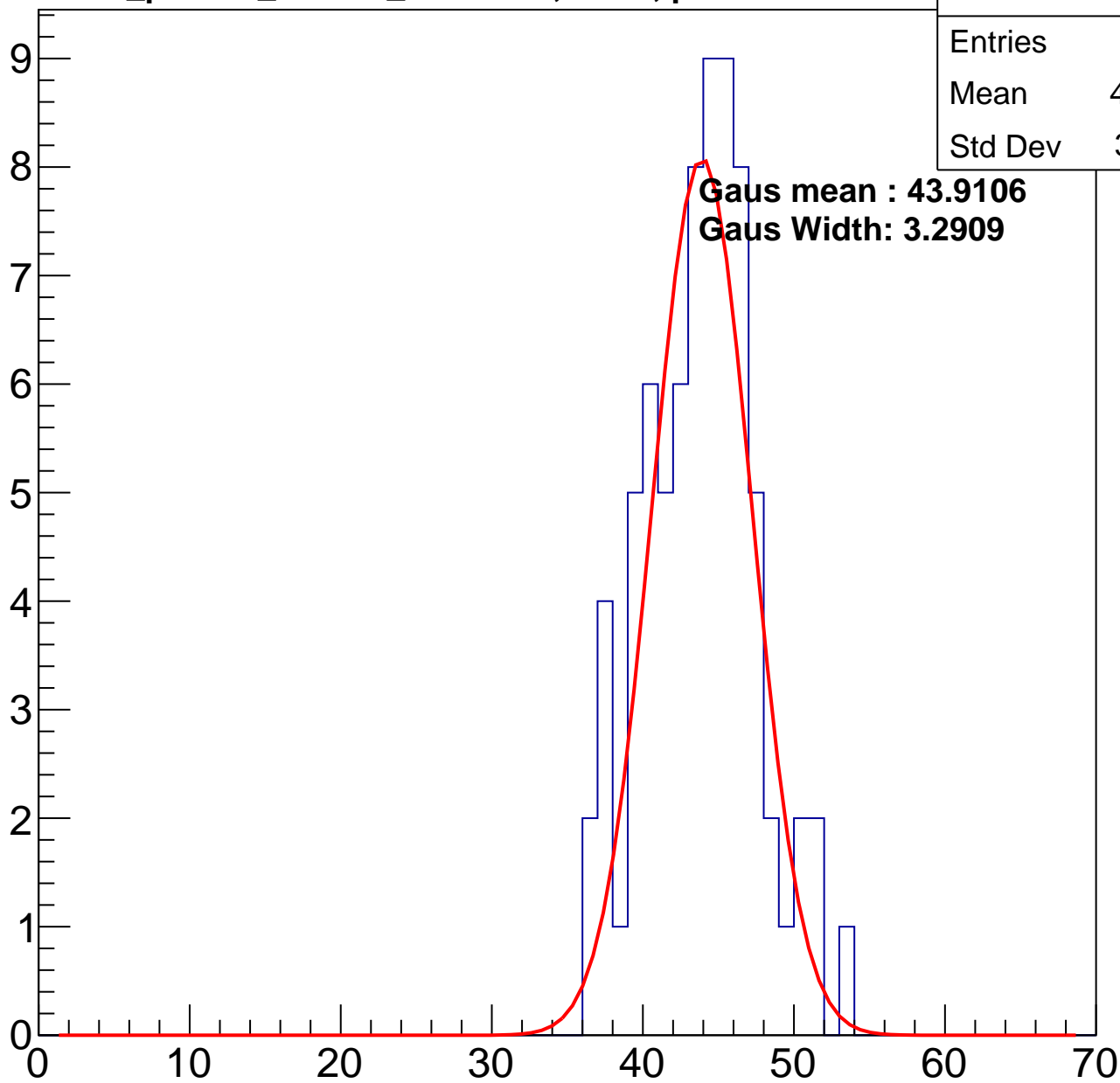
9
8
7
6
5
4
3
2
1
0

Entries	76
Mean	43.39
Std Dev	3.671

Gaus mean : 43.9106

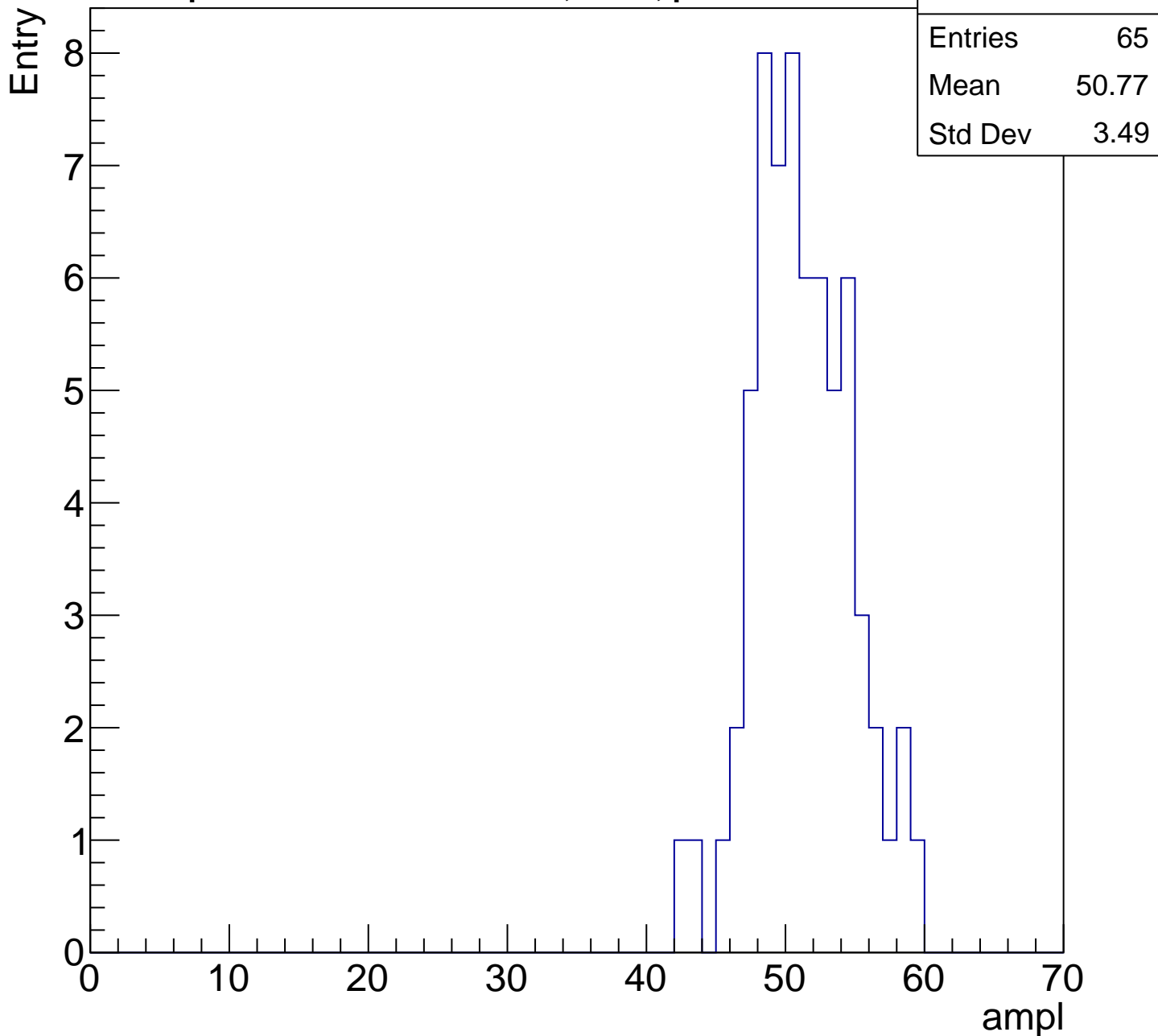
Gaus Width: 3.2909

ampl



B1L101S, U12-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

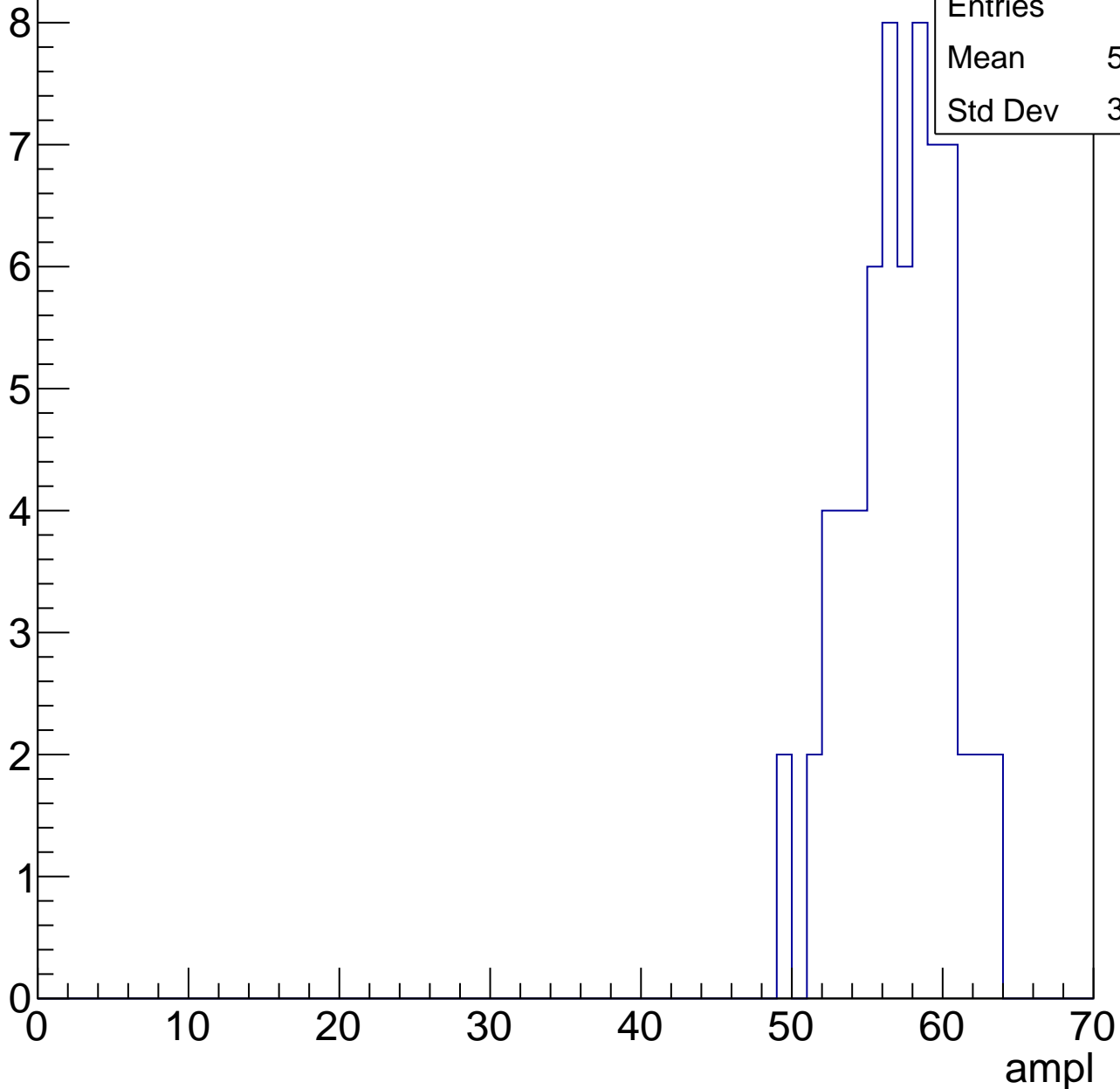


B1L101S, U12-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

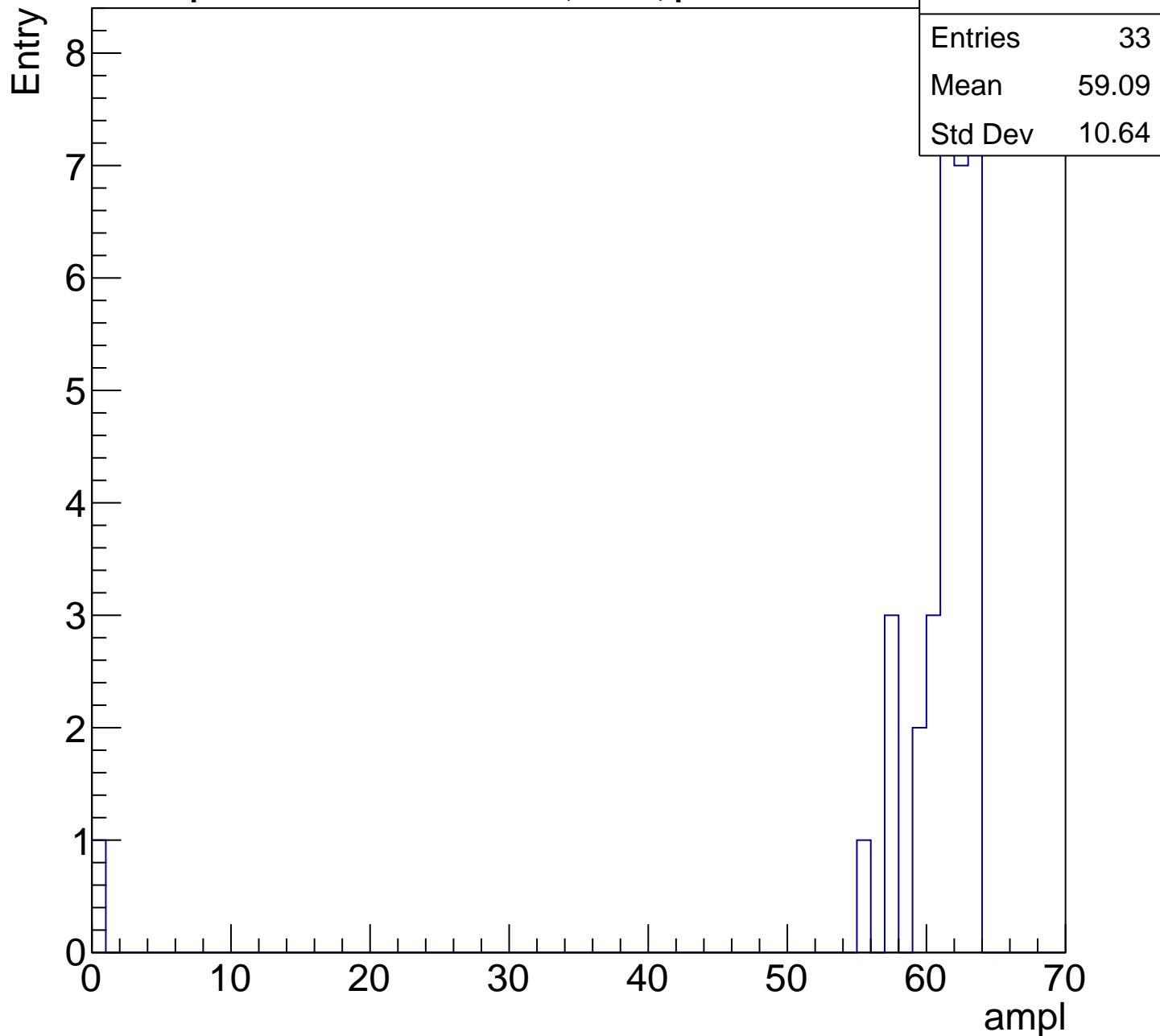
Entry

Entries	64
Mean	56.64
Std Dev	3.252



B1L101S, U12-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L101S, U12-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch7, adc0

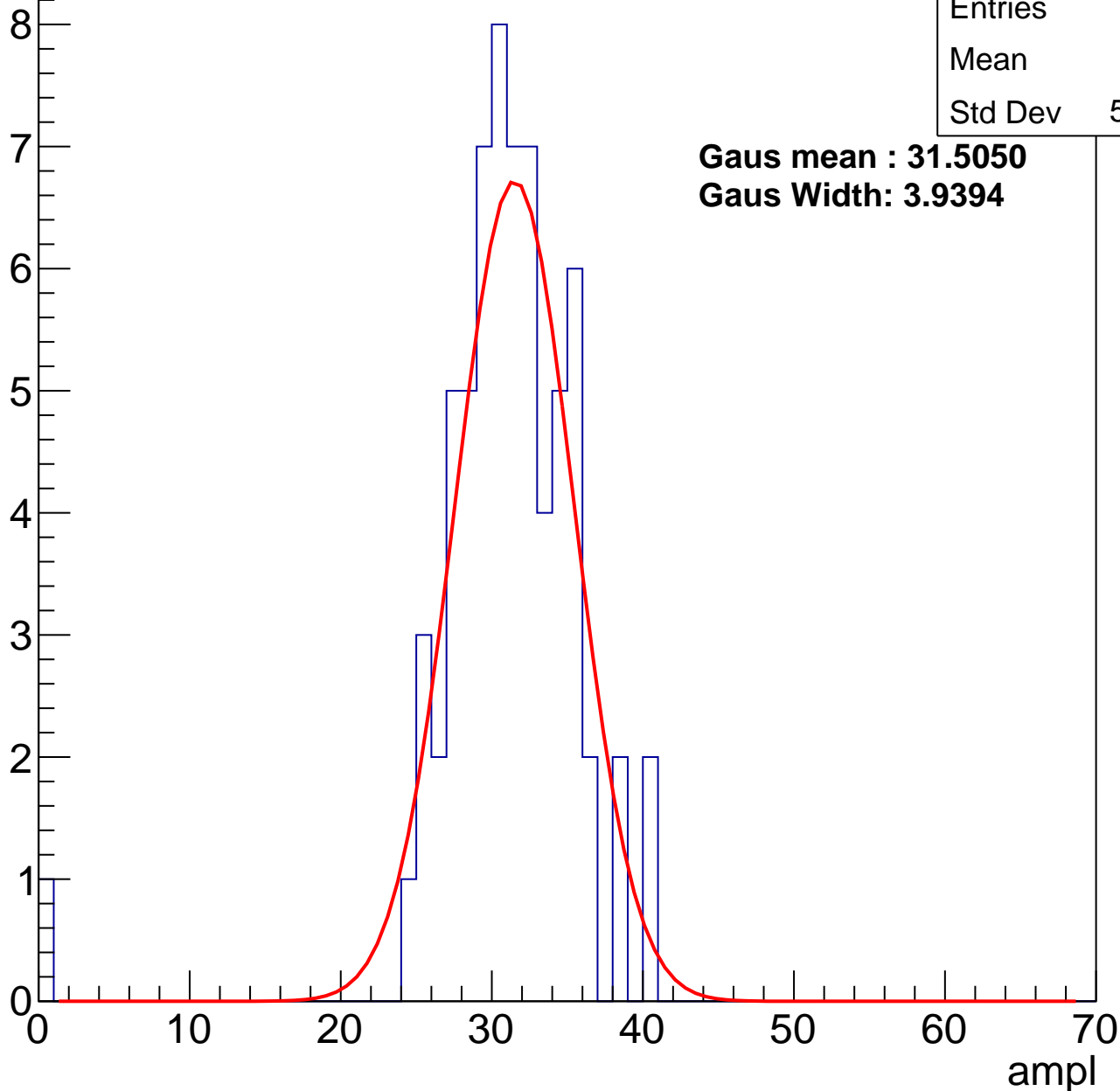
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.6
Std Dev	5.172

Gaus mean : 31.5050

Gaus Width: 3.9394



B1L101S, U12-ch7, adc1

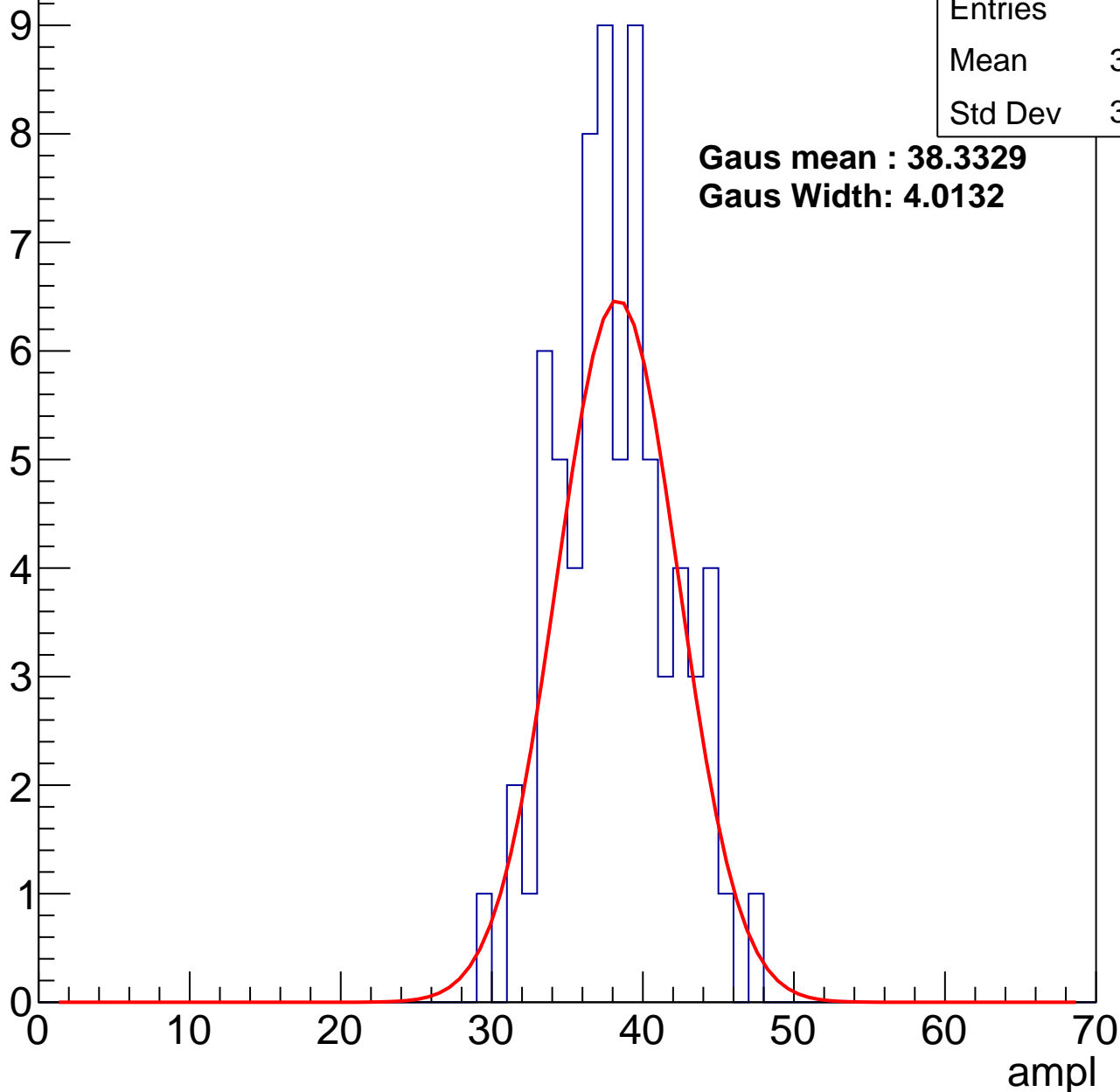
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.76
Std Dev	3.728

Gaus mean : 38.3329

Gaus Width: 4.0132



B1L101S, U12-ch7, adc2

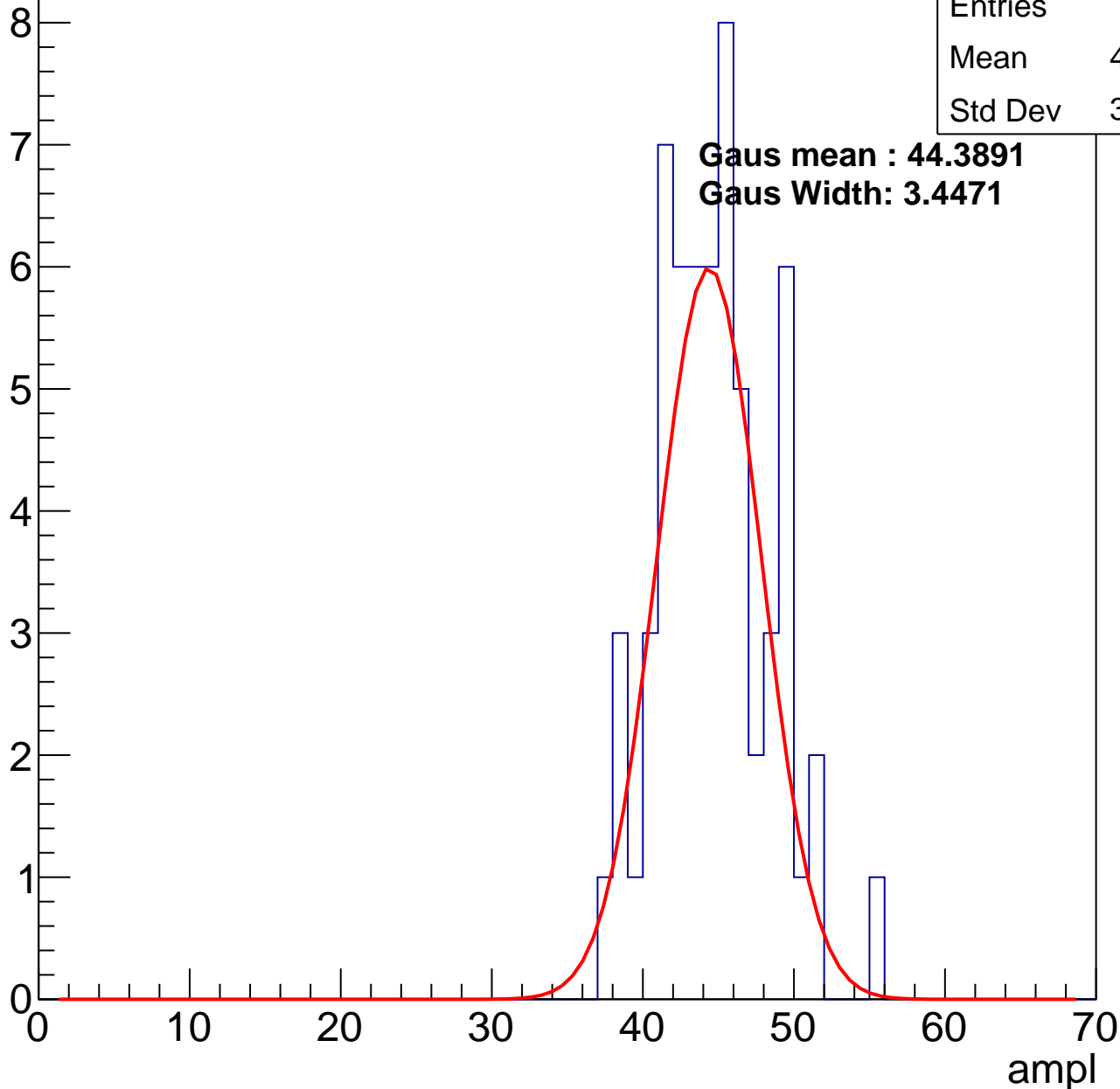
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	44.26
Std Dev	3.662

Gaus mean : 44.3891

Gaus Width: 3.4471

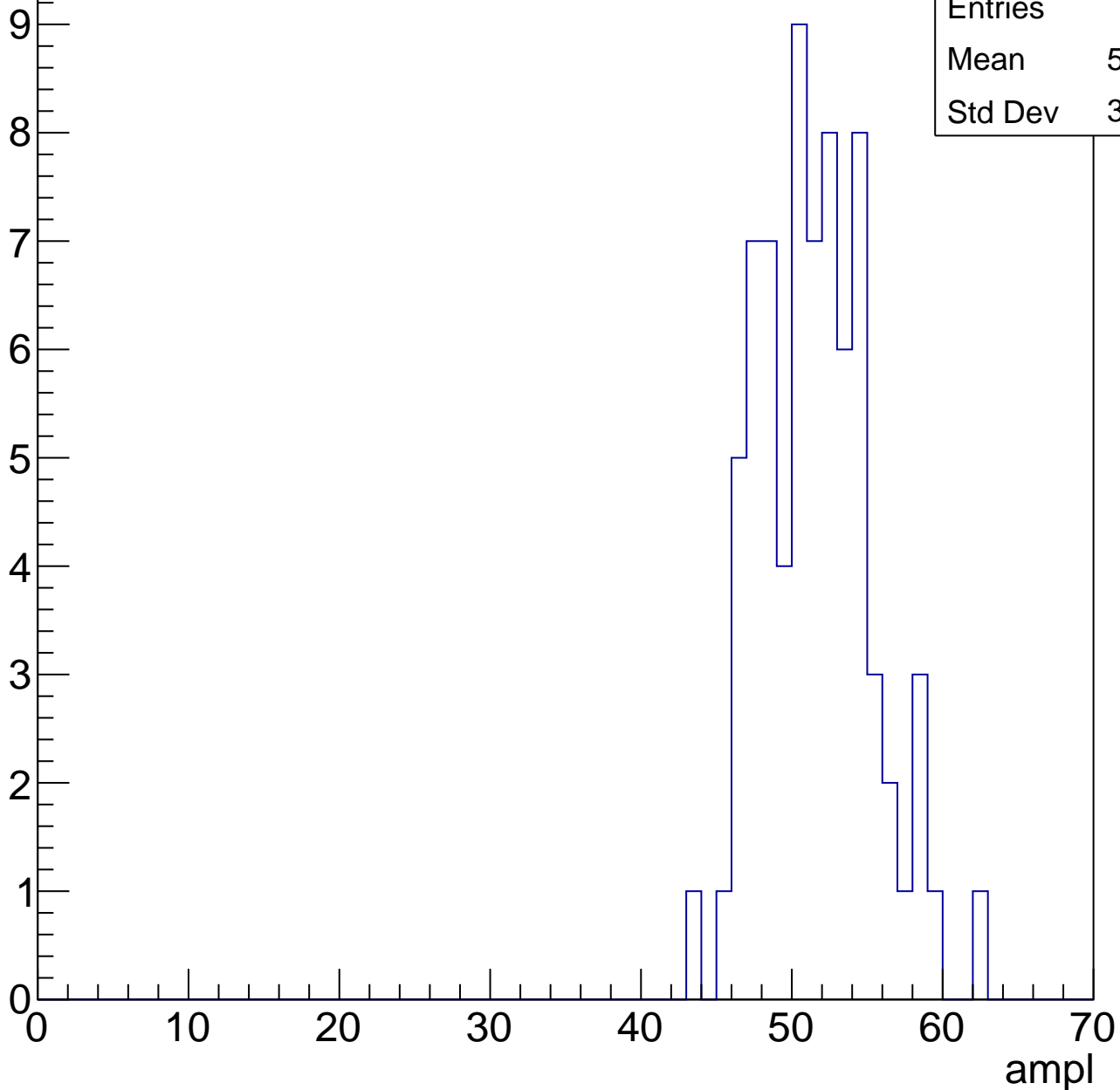


B1L101S, U12-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

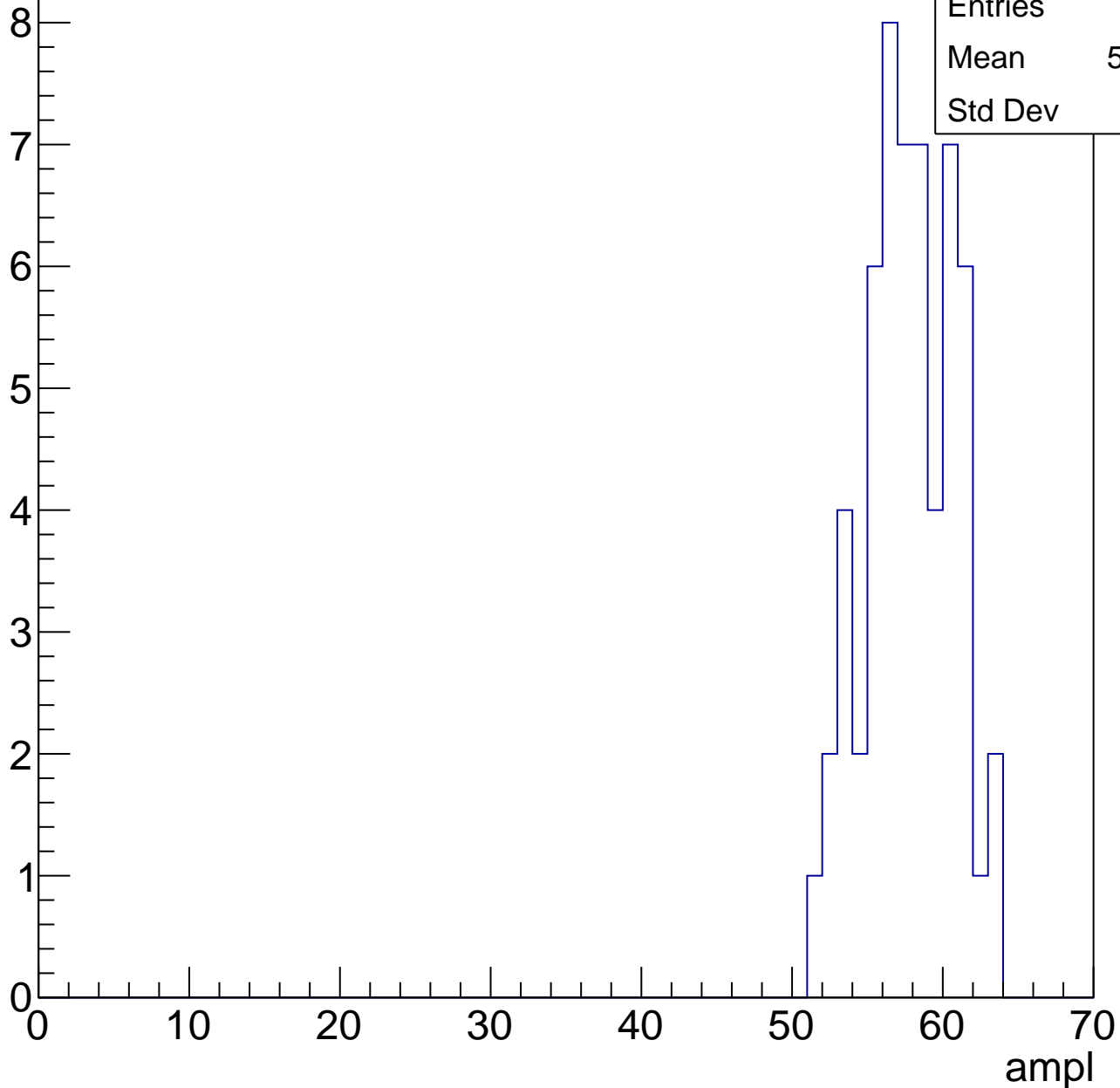
Entries	74
Mean	51.09
Std Dev	3.666



B1L101S, U12-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

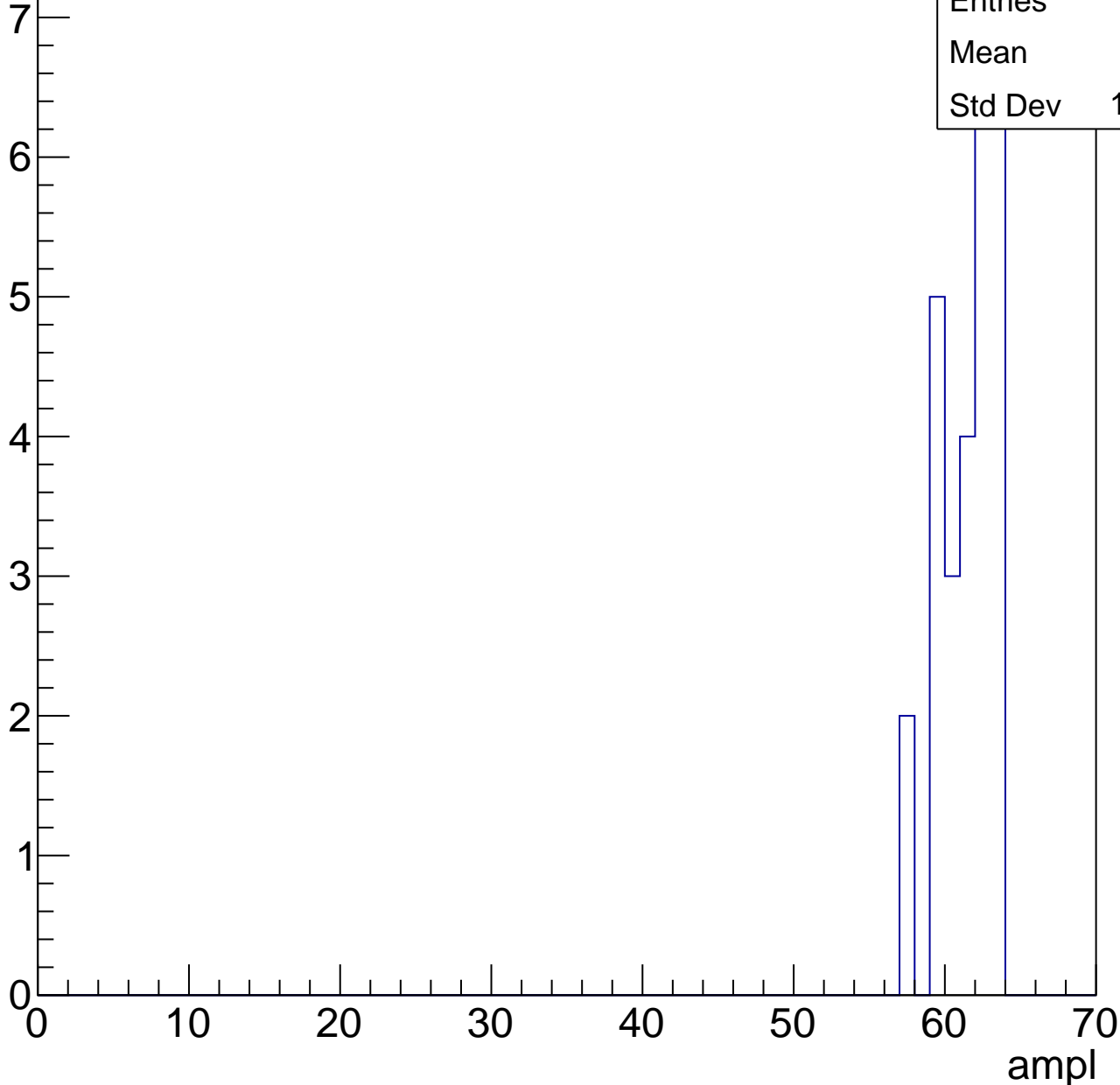


B1L101S, U12-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

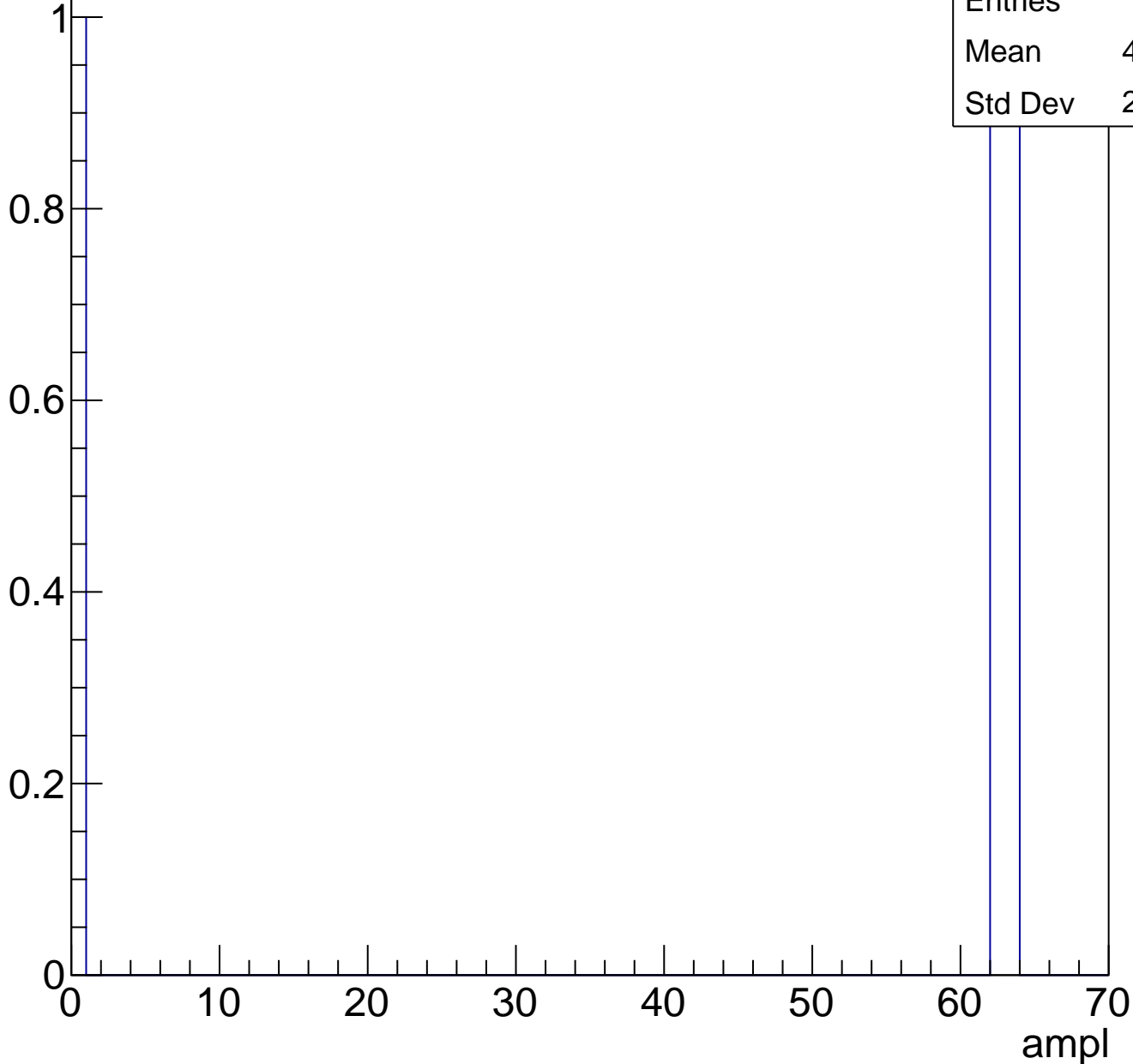
Entries	28
Mean	61
Std Dev	1.793



B1L101S, U12-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L101S, U12-ch8, adc0

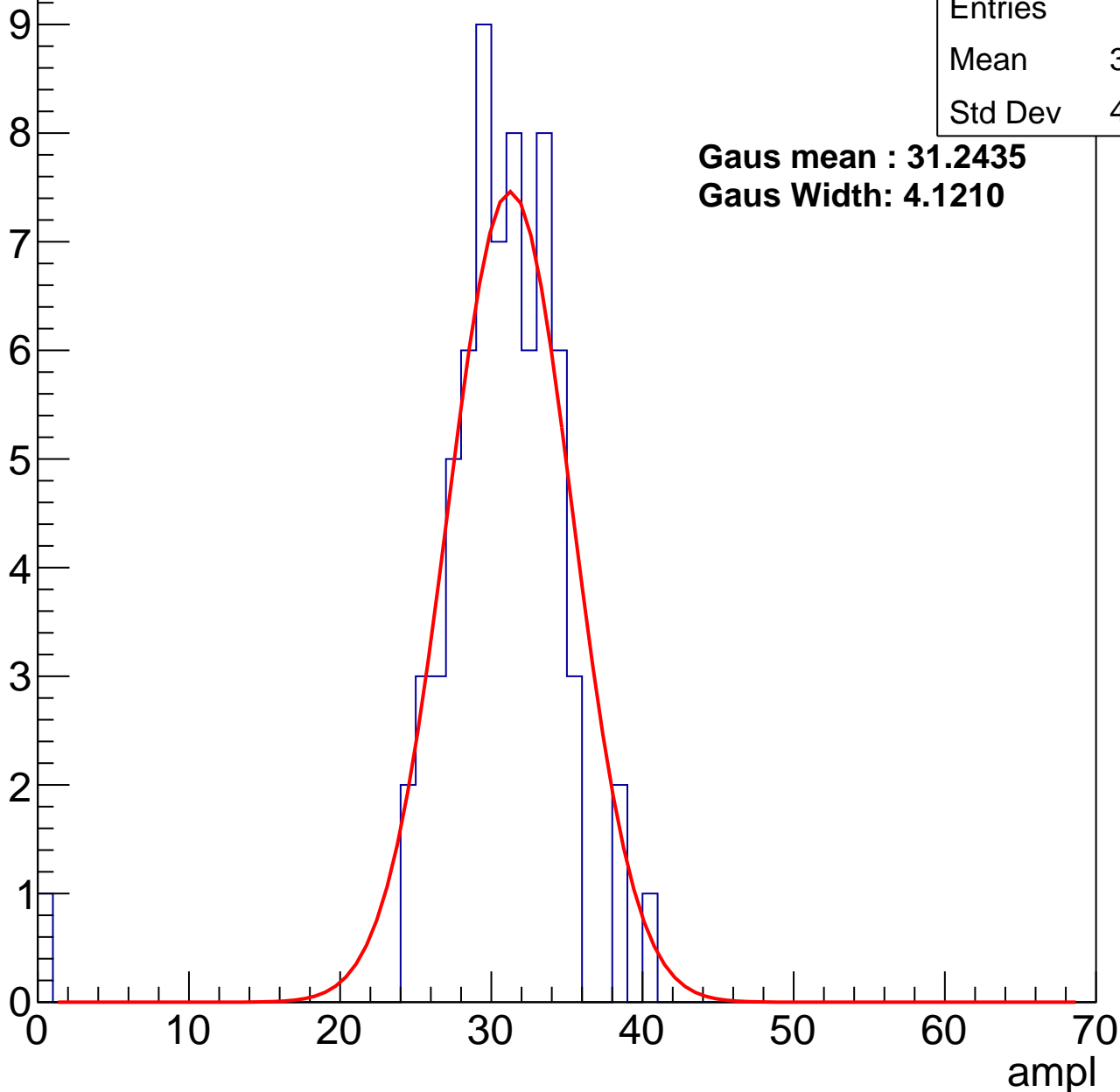
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.06
Std Dev	4.893

Gaus mean : 31.2435

Gaus Width: 4.1210



B1L101S, U12-ch8, adc1

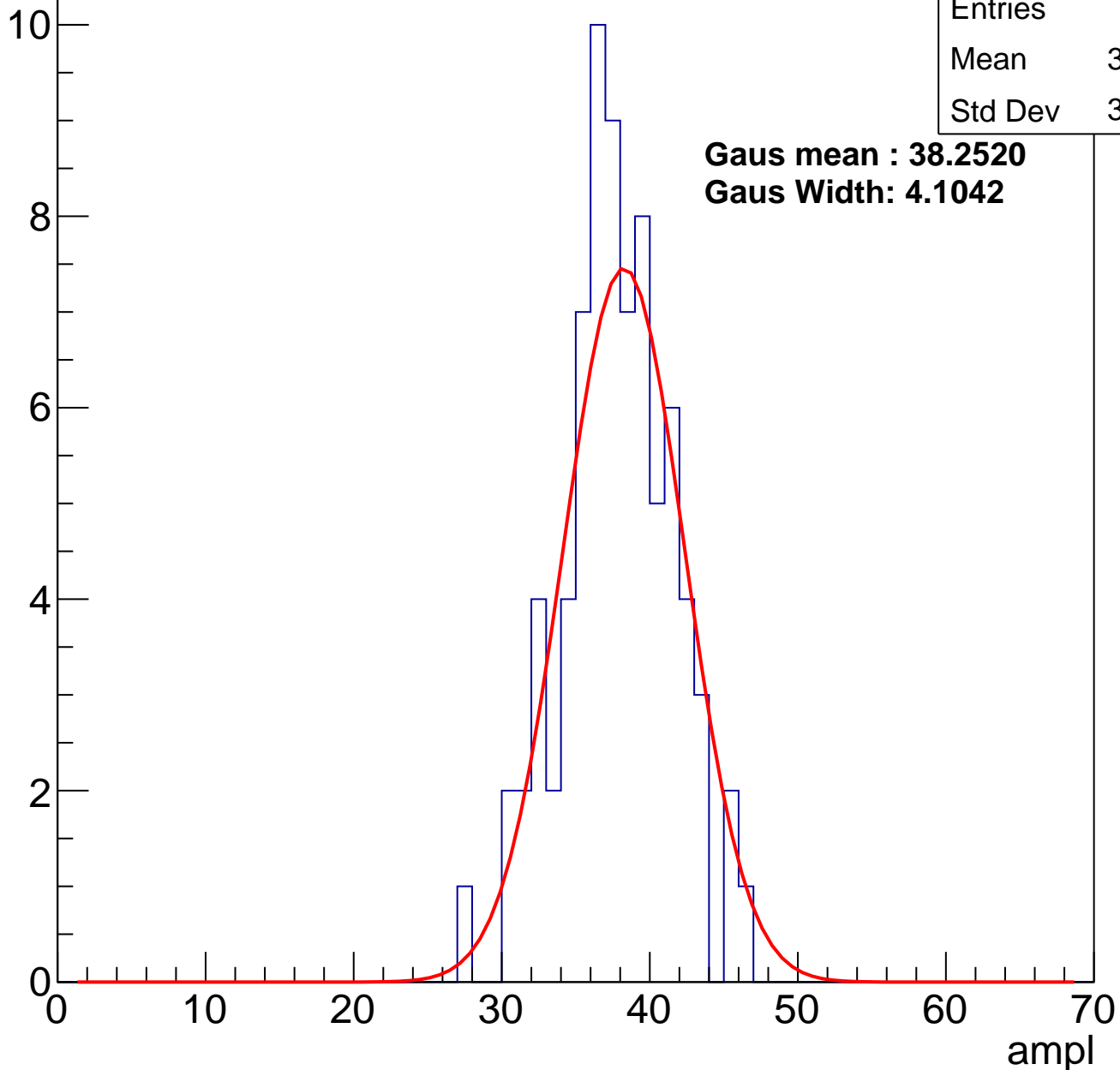
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.32
Std Dev	3.719

Gaus mean : 38.2520

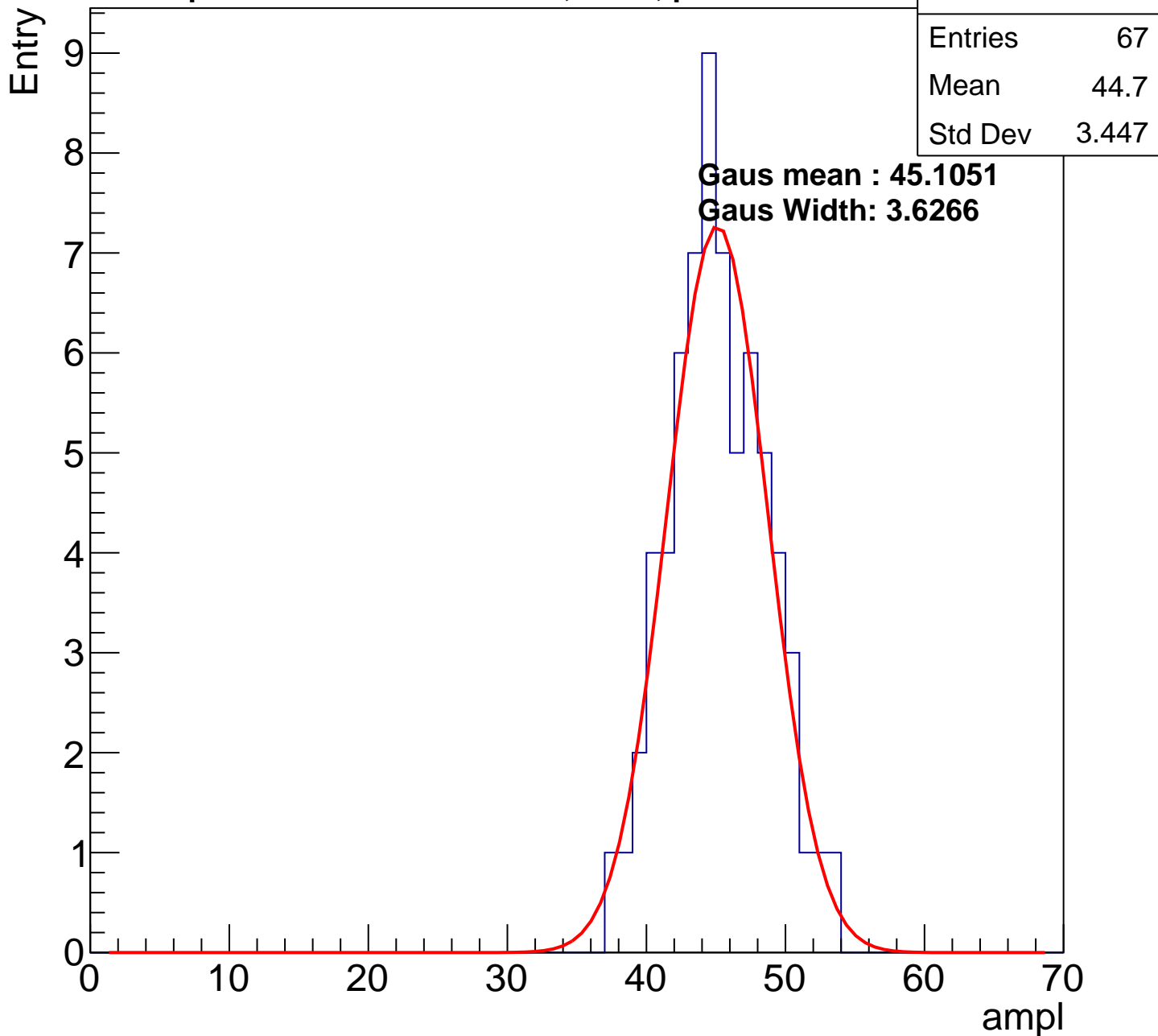
Gaus Width: 4.1042

Entry



B1L101S, U12-ch8, adc2

calib_packv5_042523_0143.root, FC#0, port D2

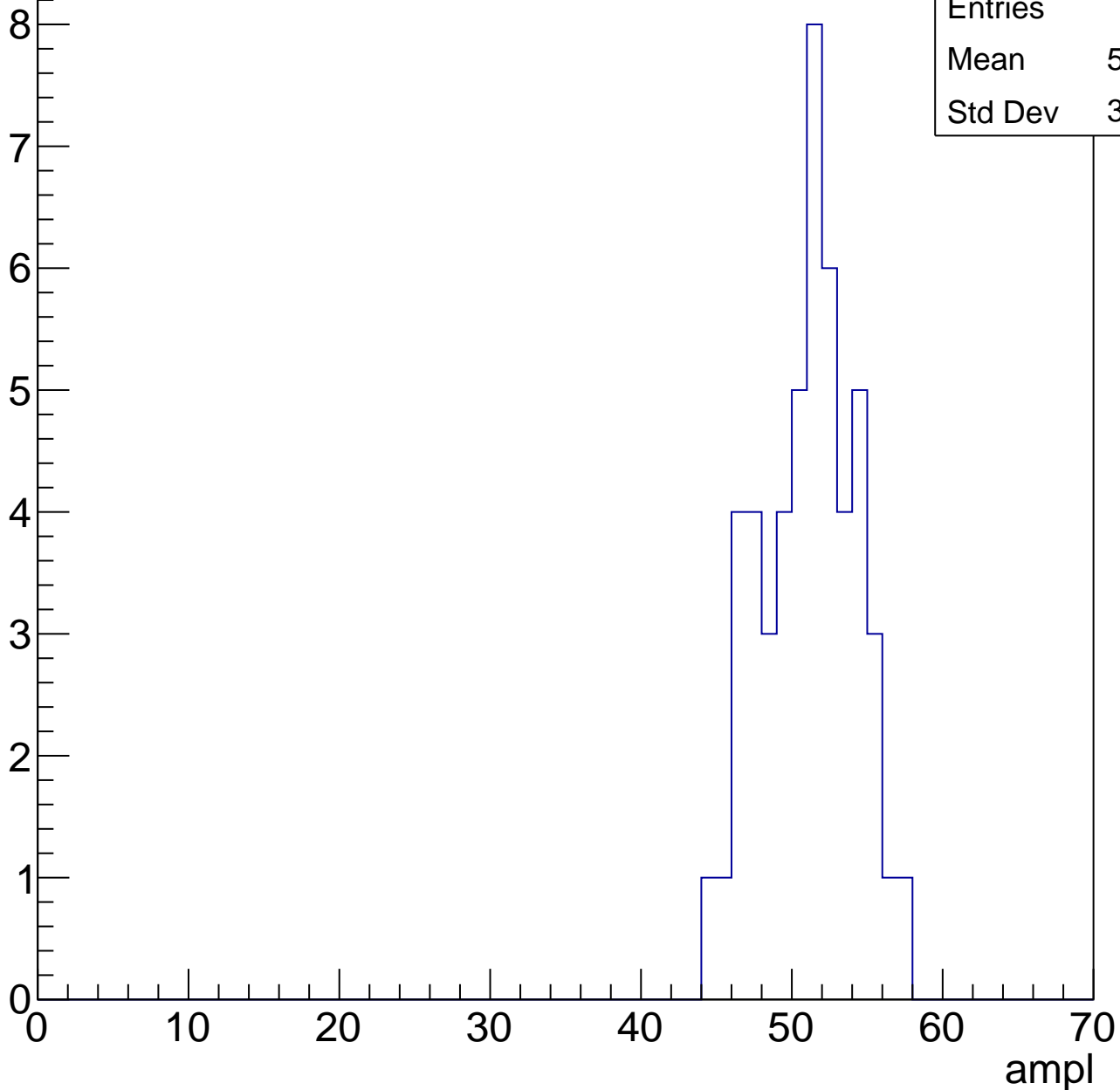


B1L101S, U12-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

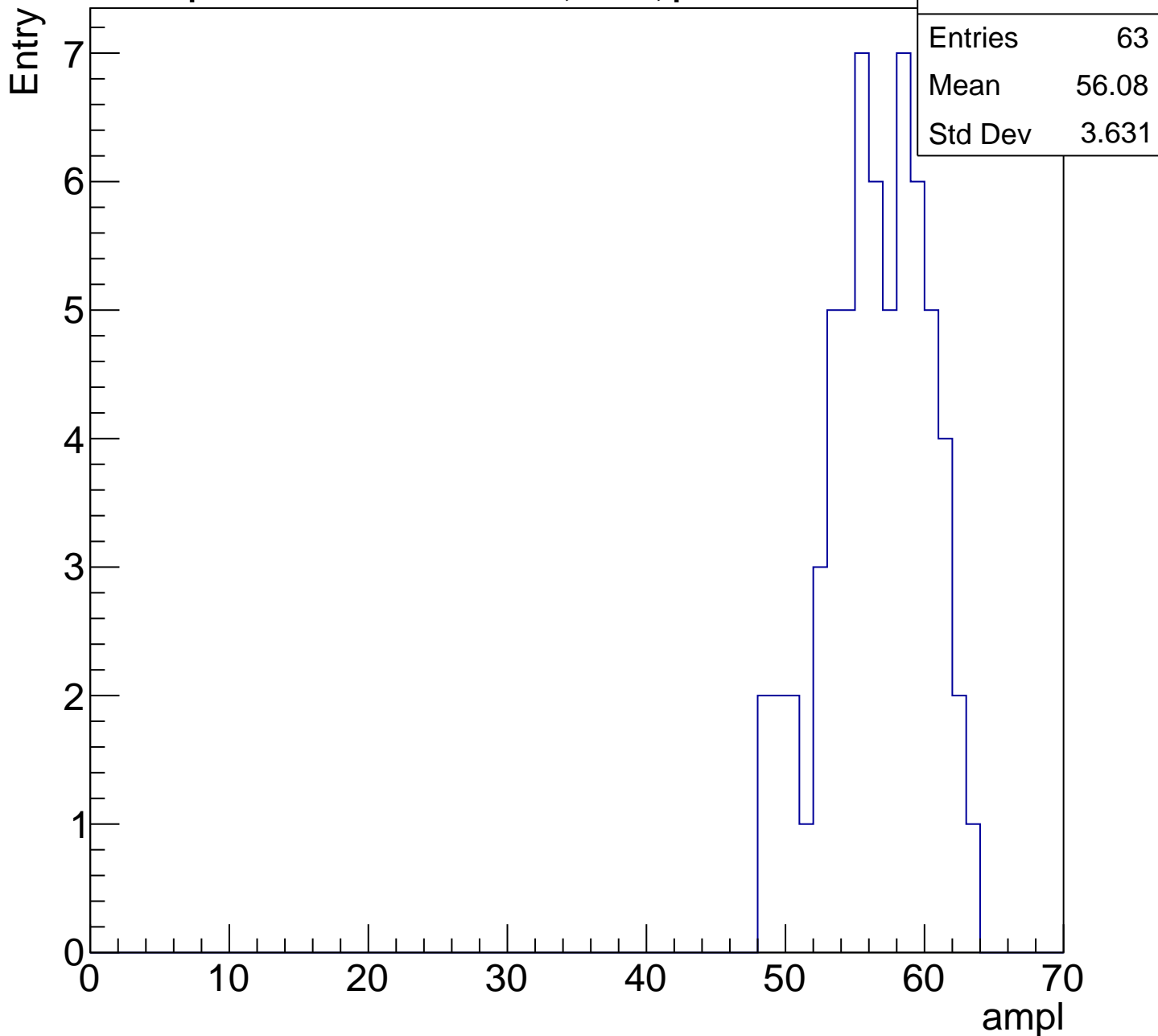
Entry

Entries	50
Mean	50.62
Std Dev	3.052



B1L101S, U12-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

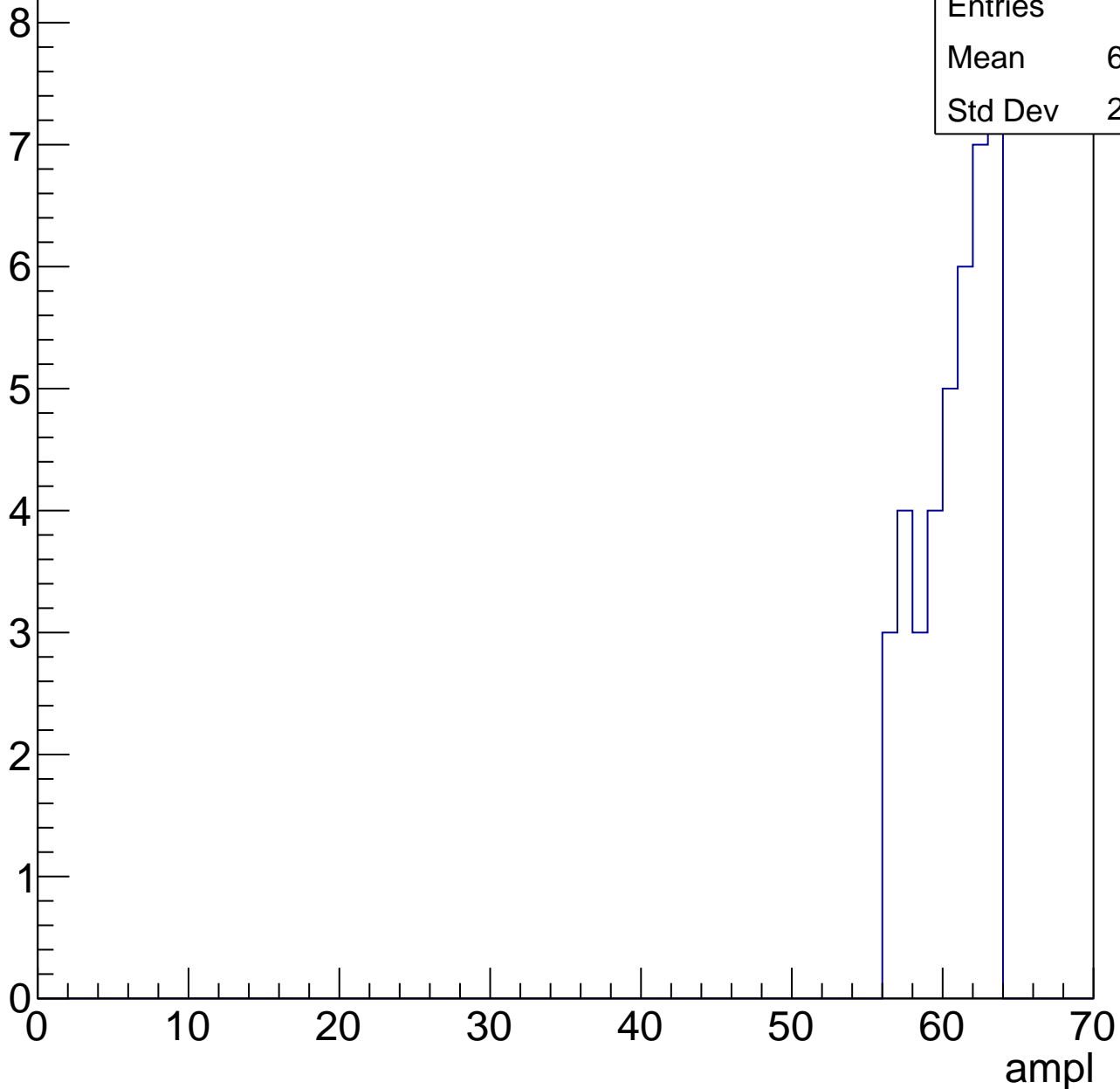


B1L101S, U12-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

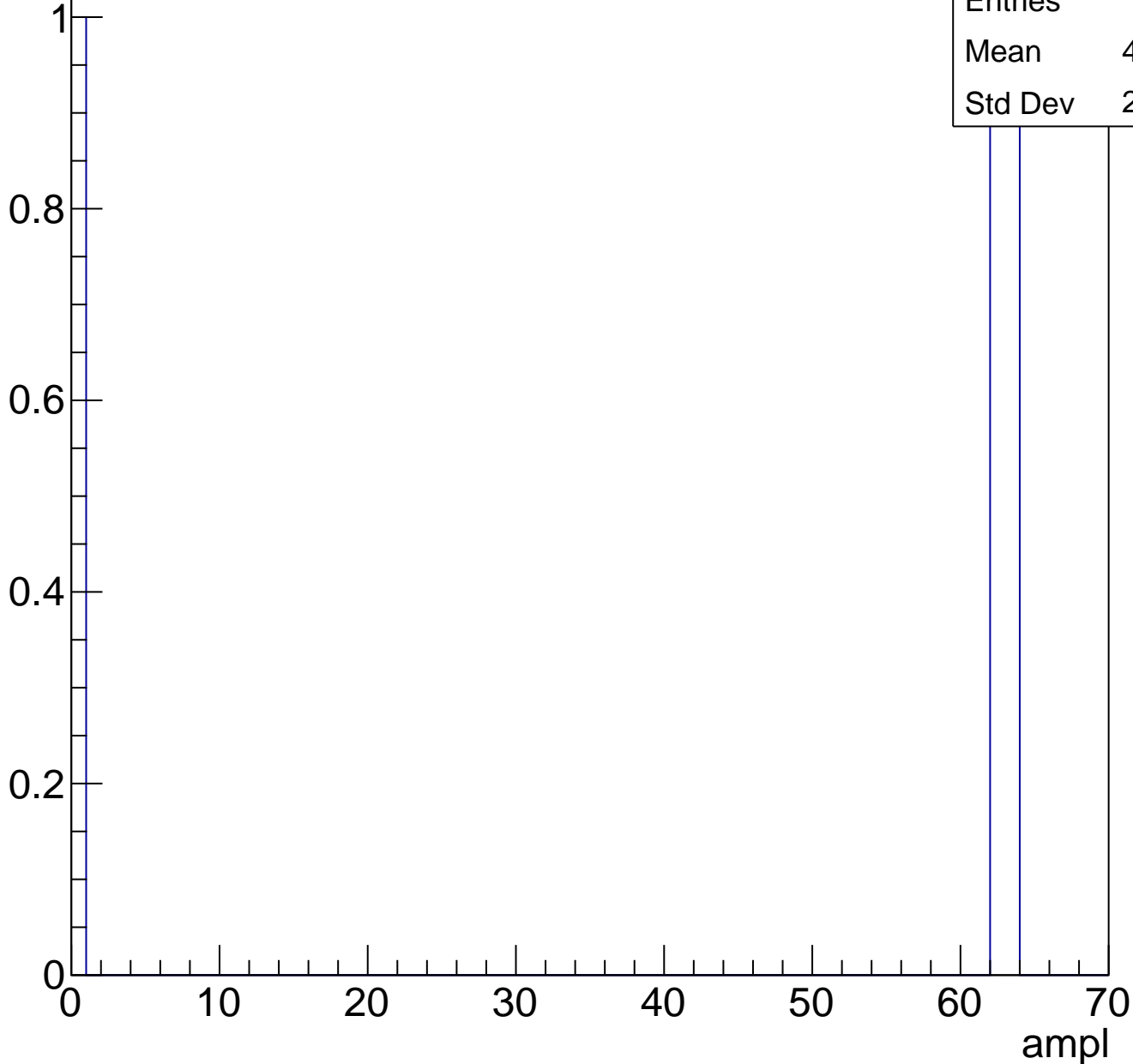
Entries	40
Mean	60.25
Std Dev	2.256



B1L101S, U12-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L101S, U12-ch9, adc0

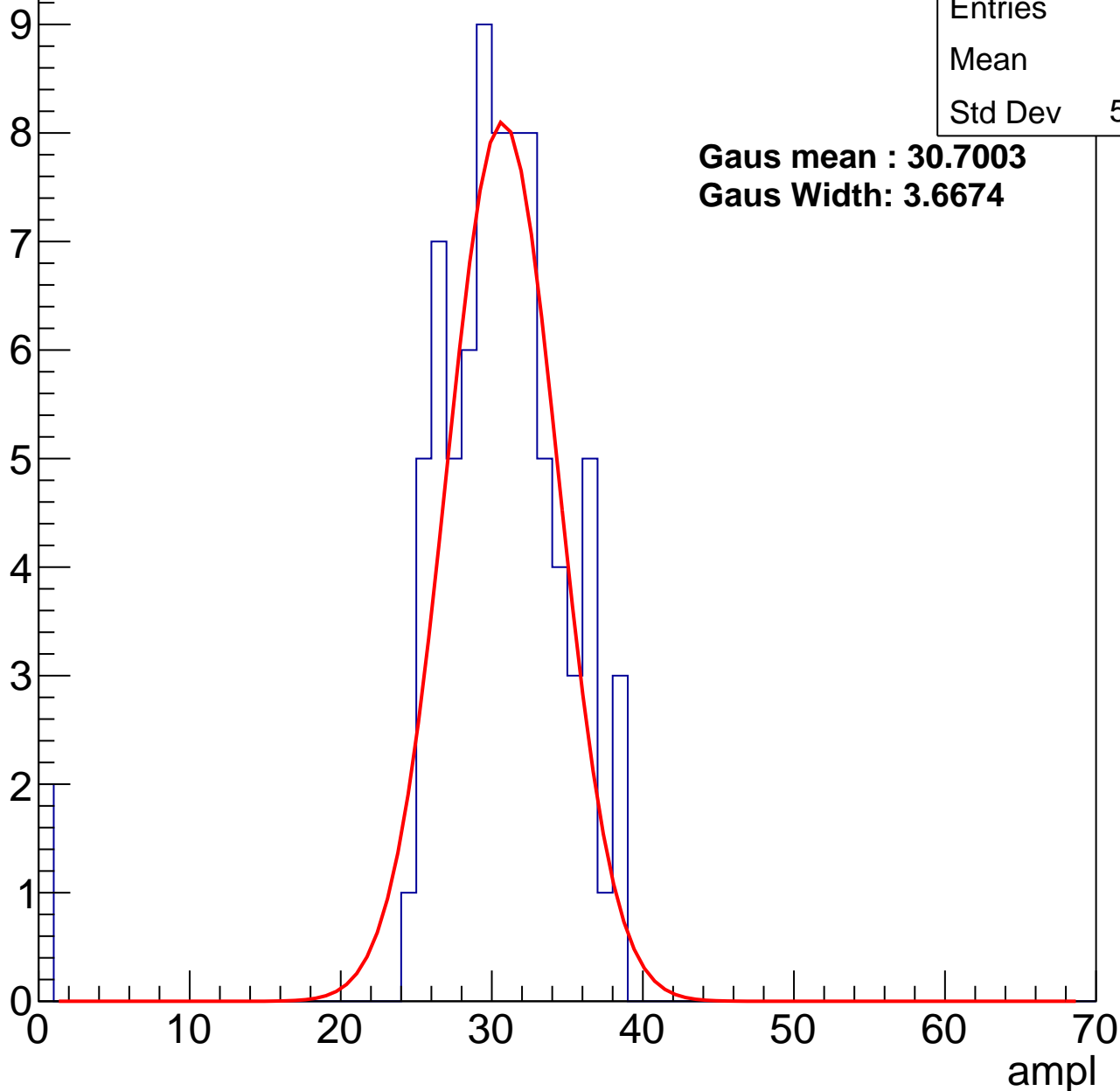
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	29.7
Std Dev	5.904

Gaus mean : 30.7003

Gaus Width: 3.6674



B1L101S, U12-ch9, adc1

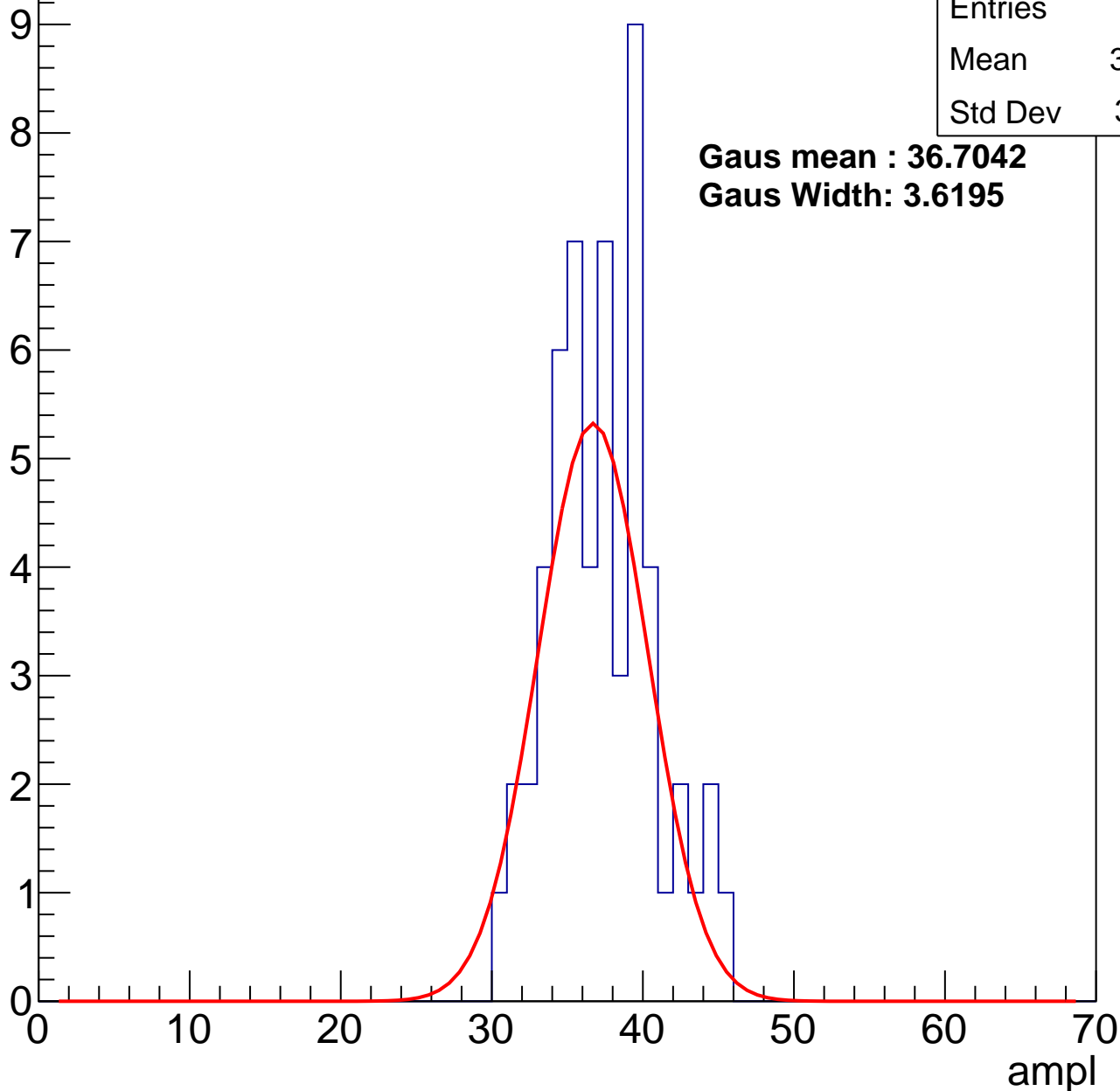
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	36.89
Std Dev	3.431

Gaus mean : 36.7042

Gaus Width: 3.6195



B1L101S, U12-ch9, adc2

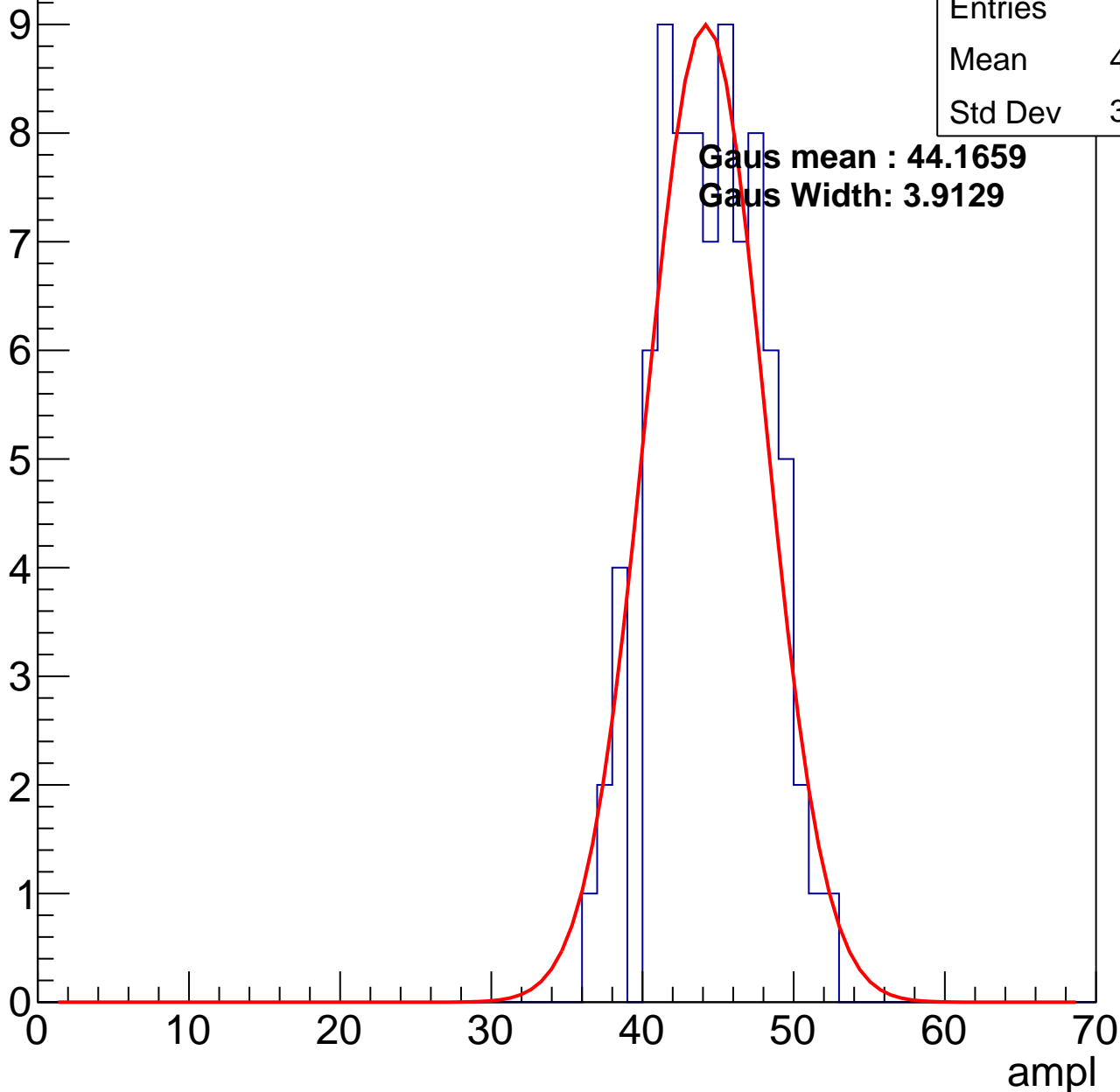
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	44.02
Std Dev	3.515

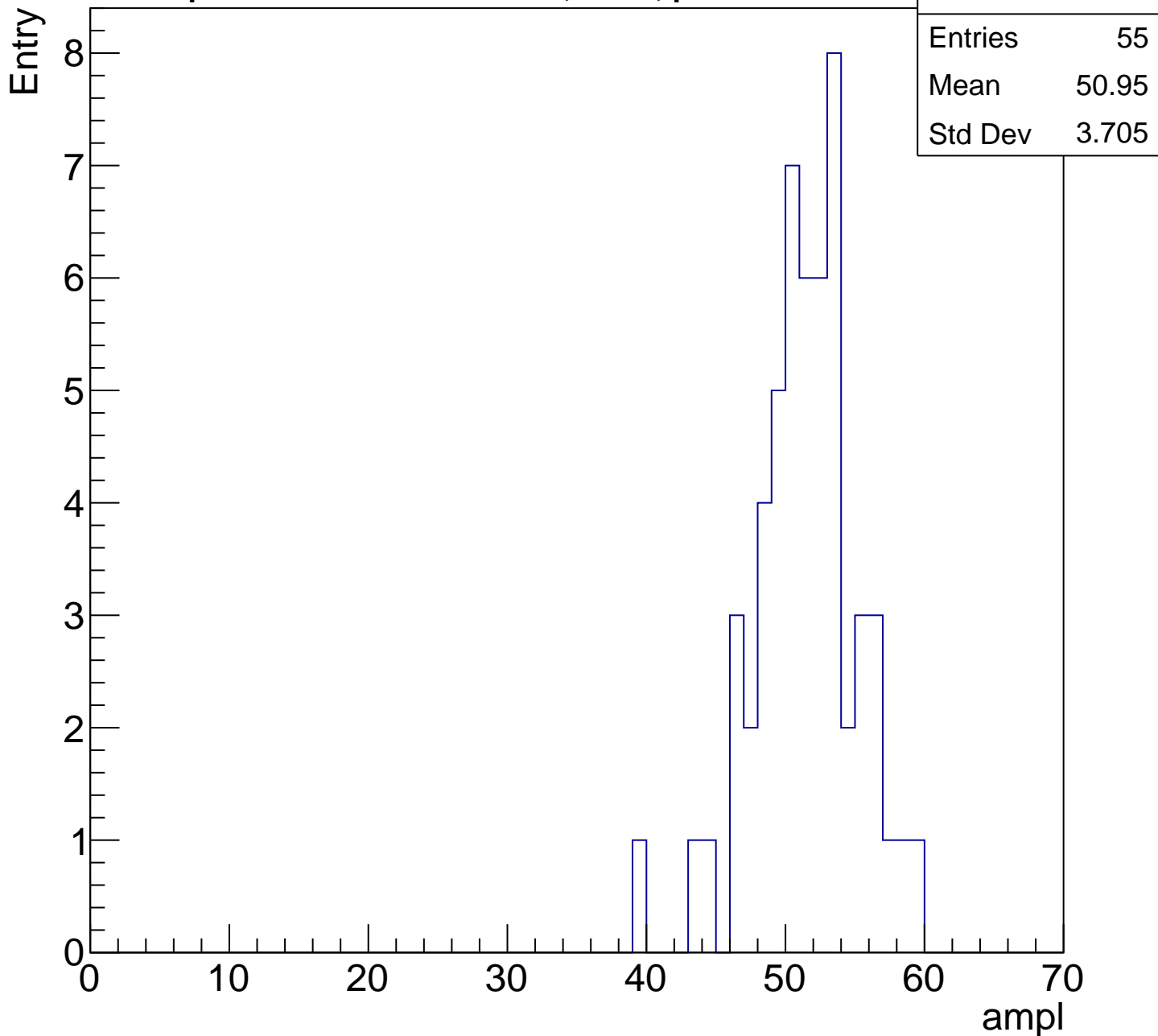
Gaus mean : 44.1659

Gaus Width: 3.9129



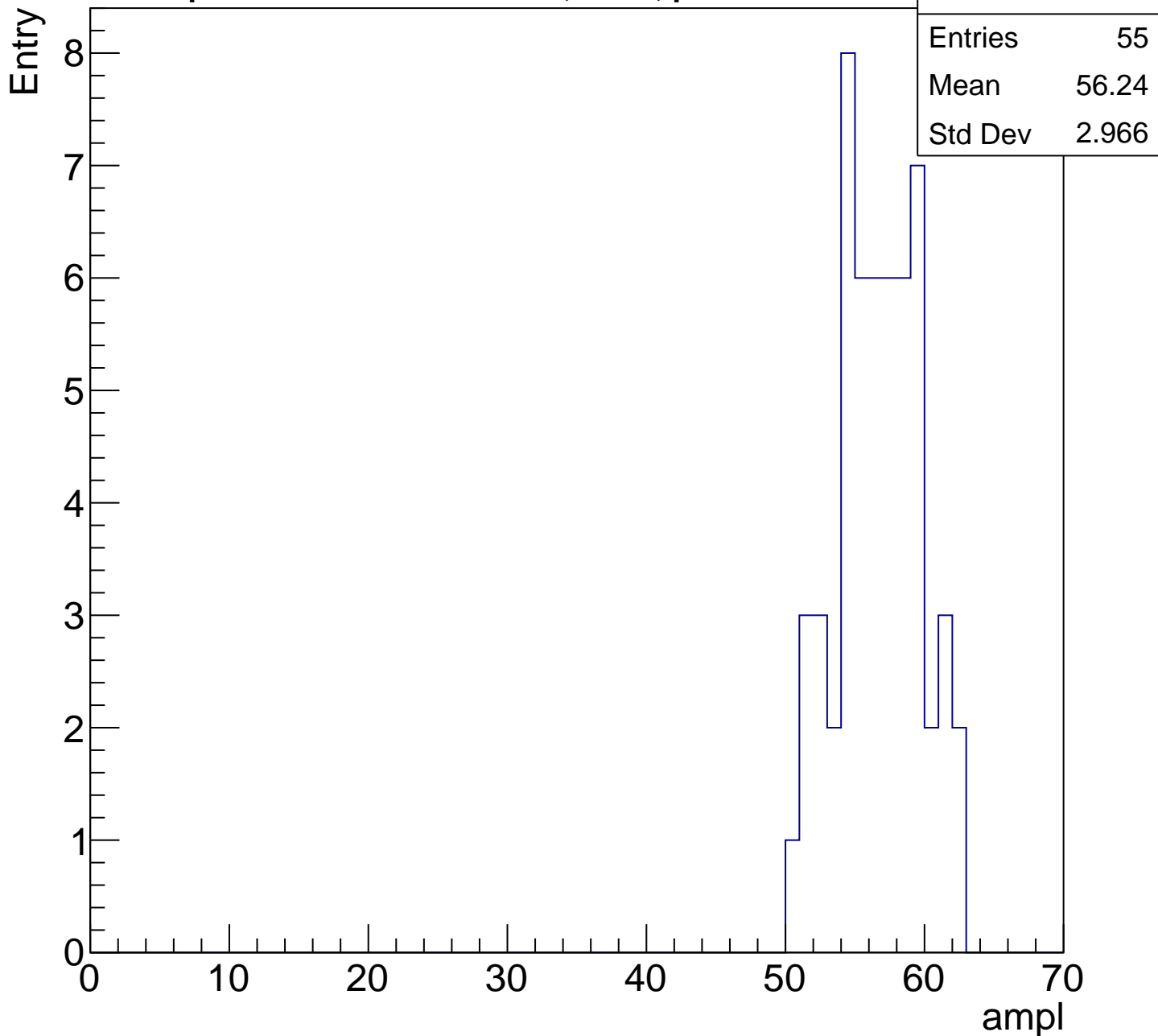
B1L101S, U12-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.02
Std Dev	9.567

ampl

0

10

20

30

40

50

60

70

B1L101S, U12-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch10, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

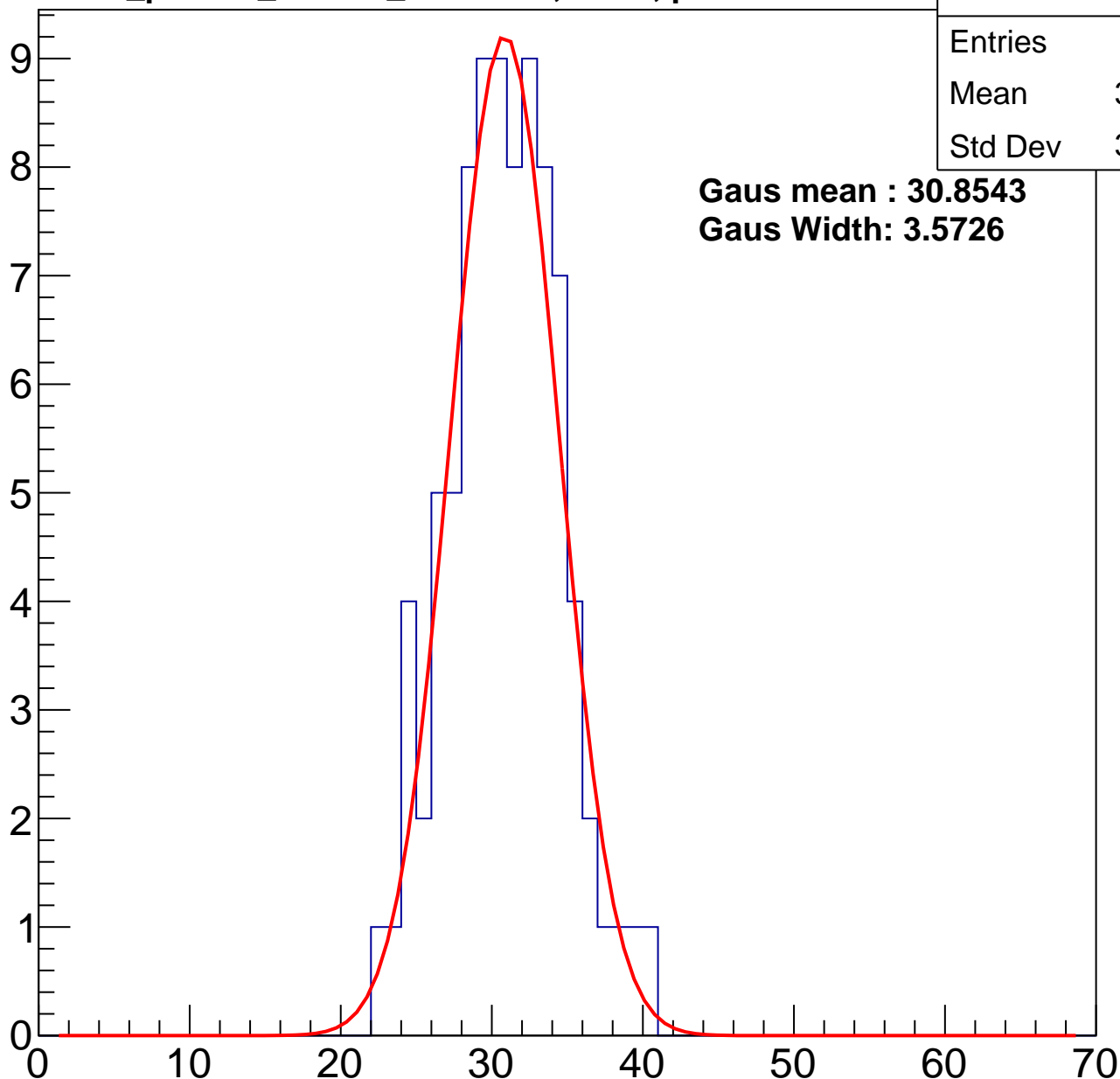
9
8
7
6
5
4
3
2
1
0

Entries	86
Mean	30.41
Std Dev	3.661

Gaus mean : 30.8543

Gaus Width: 3.5726

ampl



B1L101S, U12-ch10, adc1

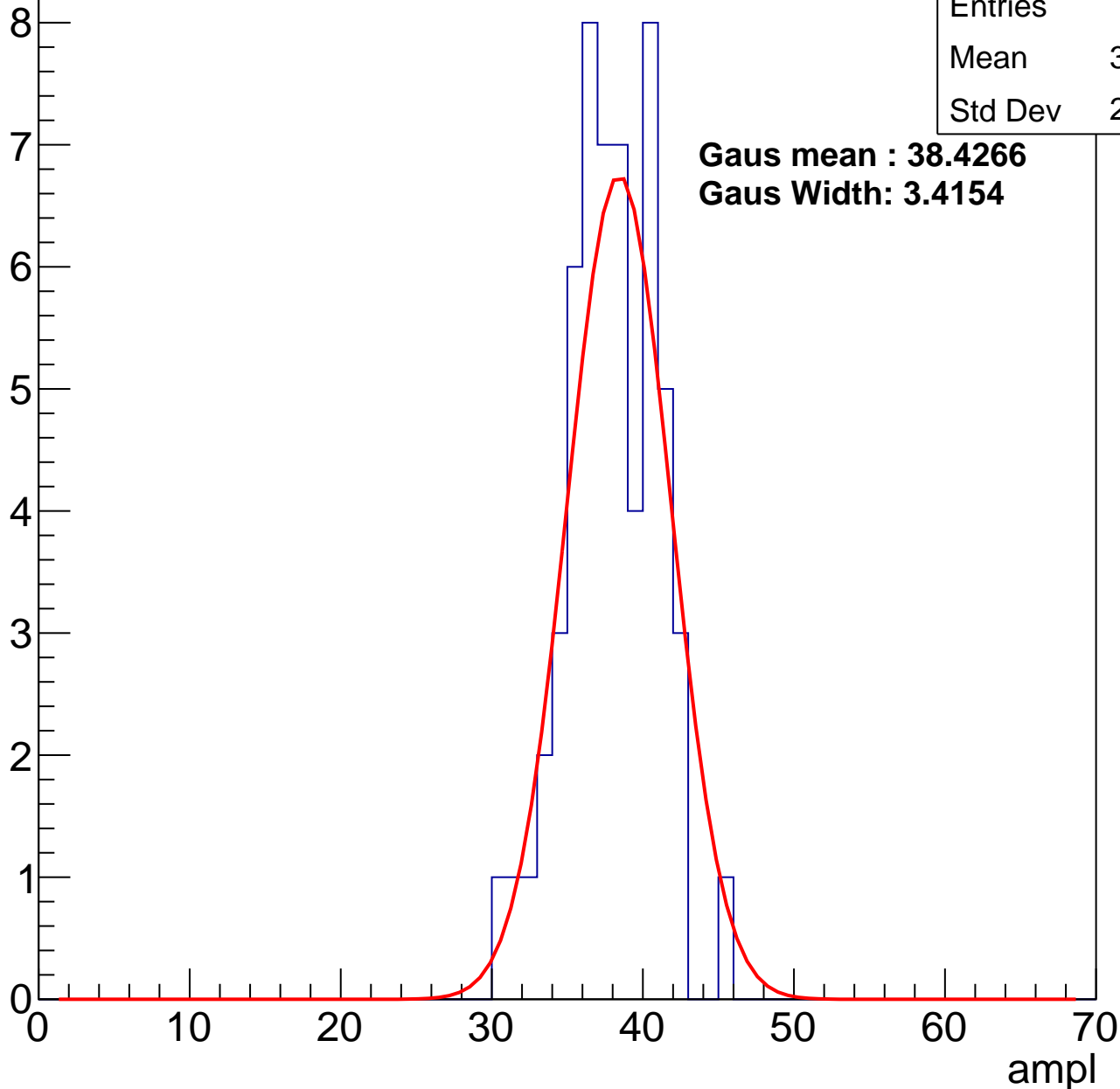
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.47
Std Dev	2.968

Gaus mean : 38.4266

Gaus Width: 3.4154



B1L101S, U12-ch10, adc2

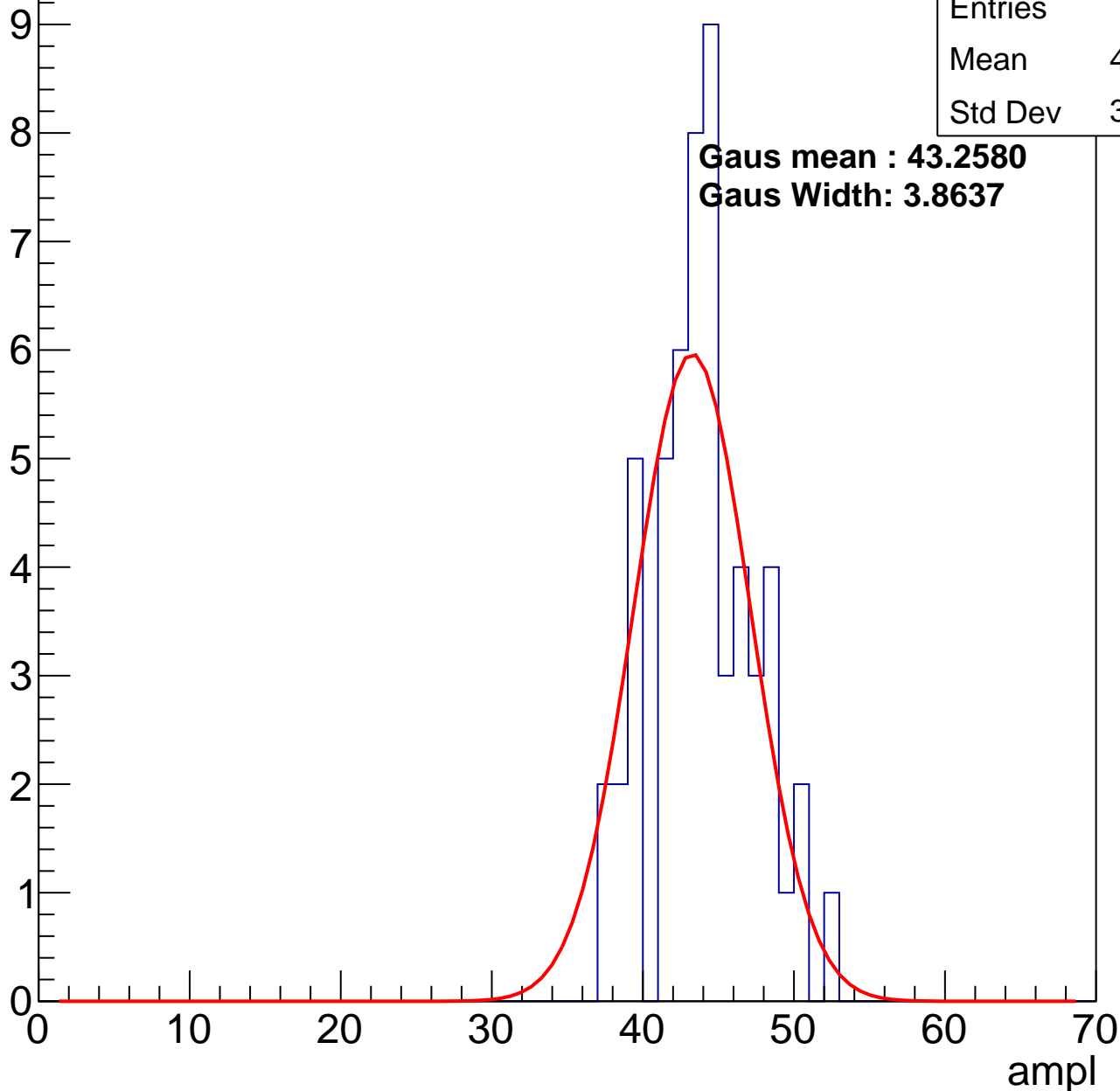
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.55
Std Dev	3.389

Gaus mean : 43.2580

Gaus Width: 3.8637

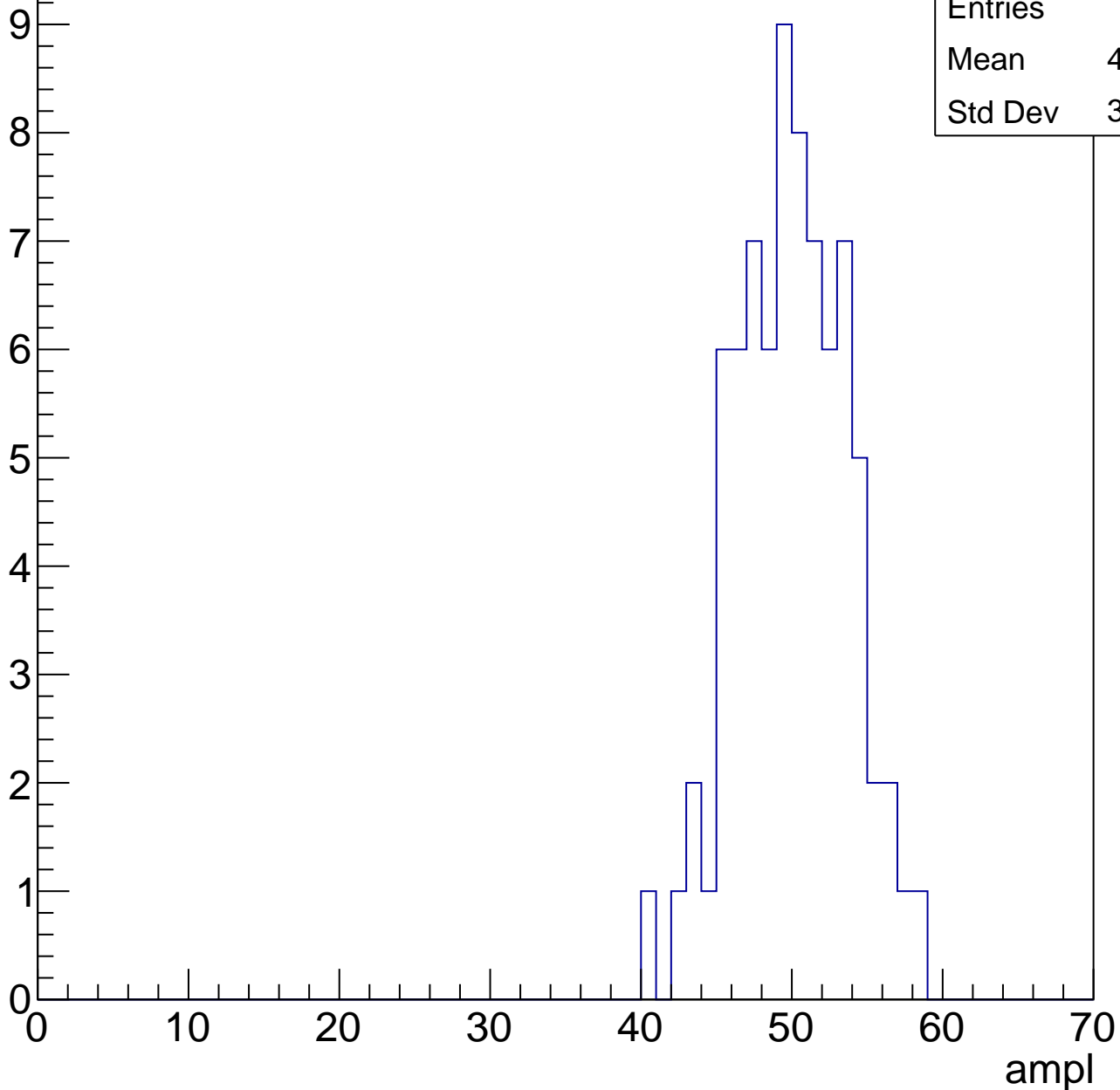


B1L101S, U12-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

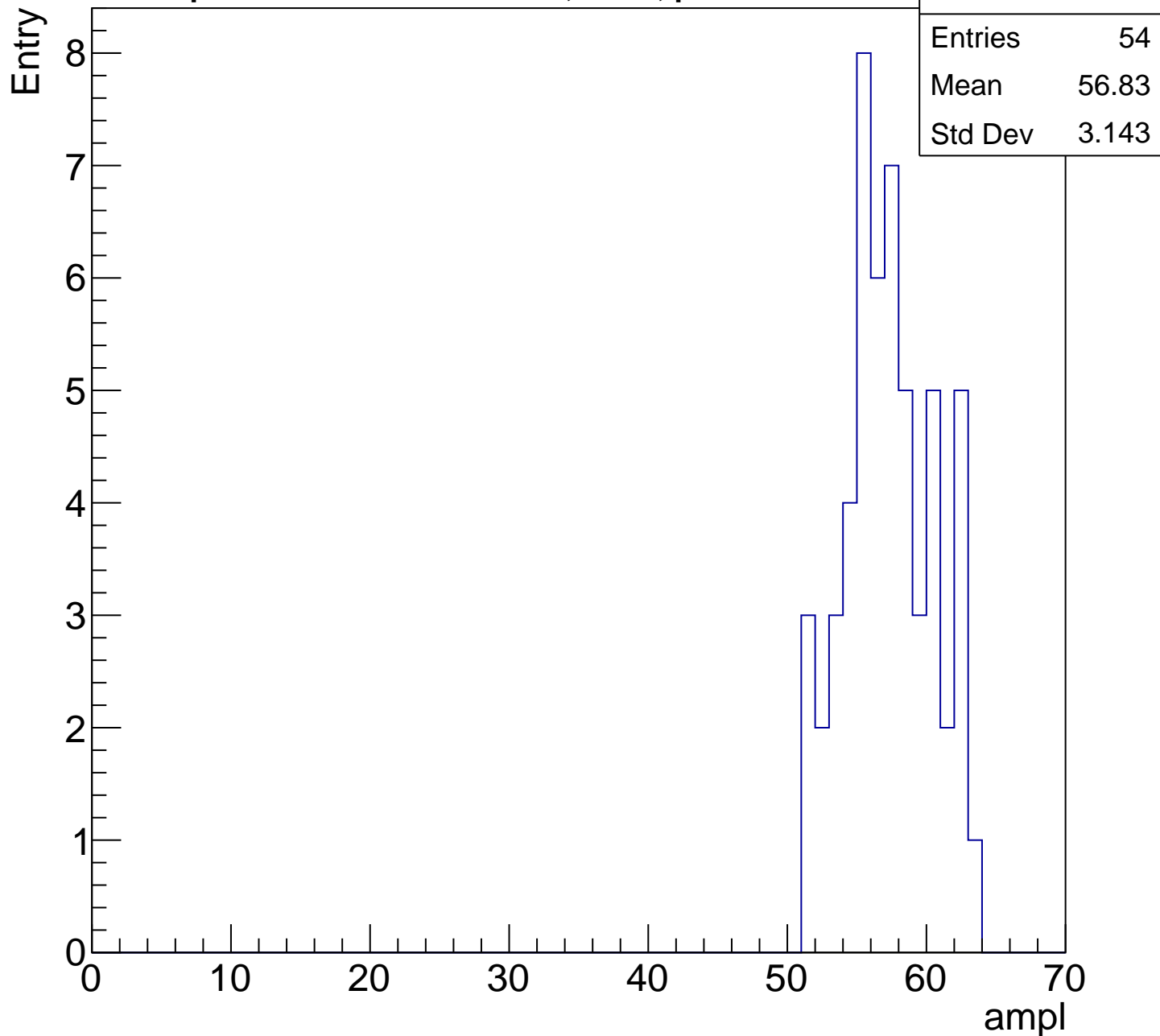
Entry

Entries	78
Mean	49.53
Std Dev	3.636



B1L101S, U12-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 38

Mean 58.21

Std Dev 9.9

ampl

0

10

20

30

40

50

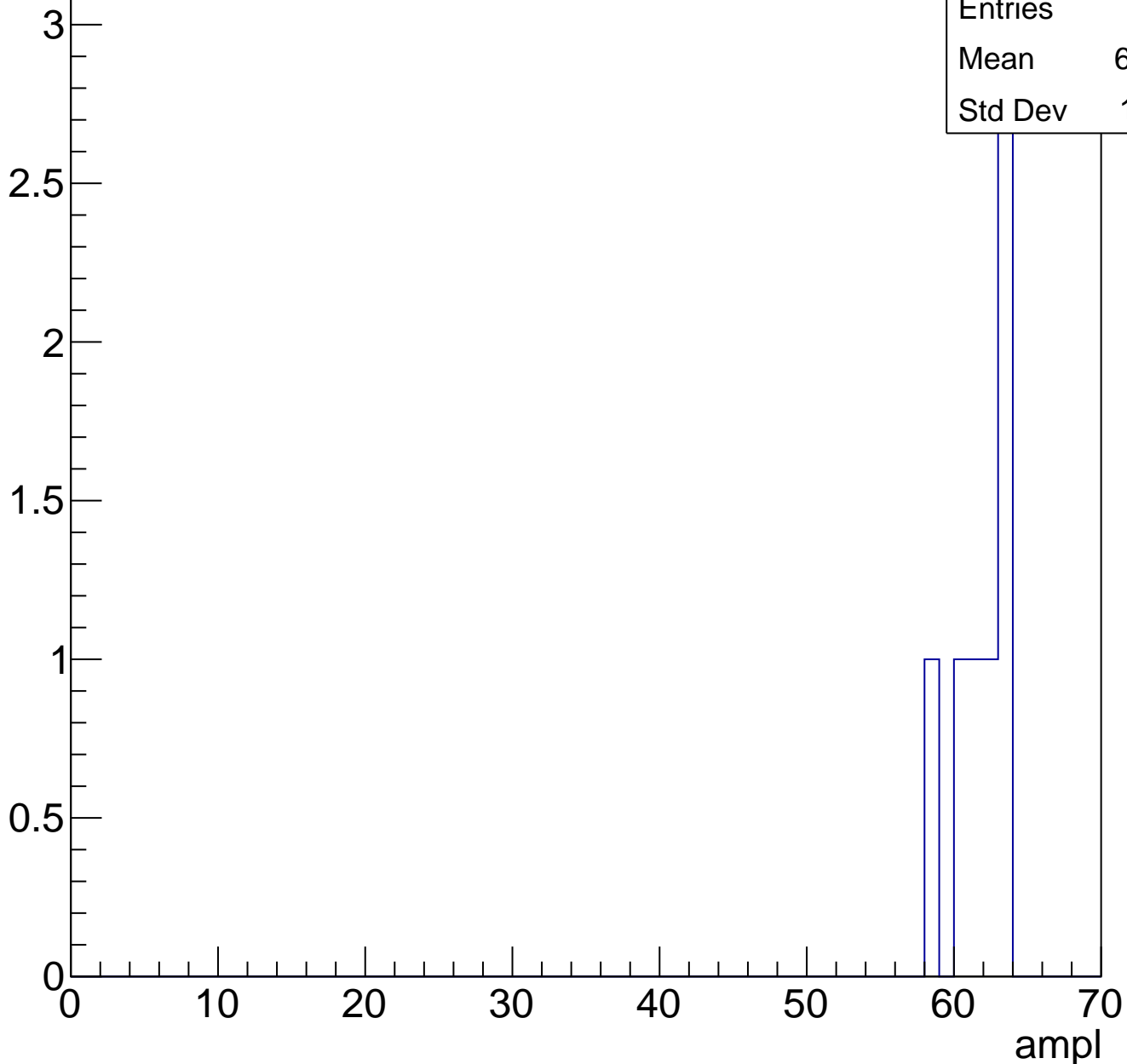
60

70

B1L101S, U12-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch11, adc0

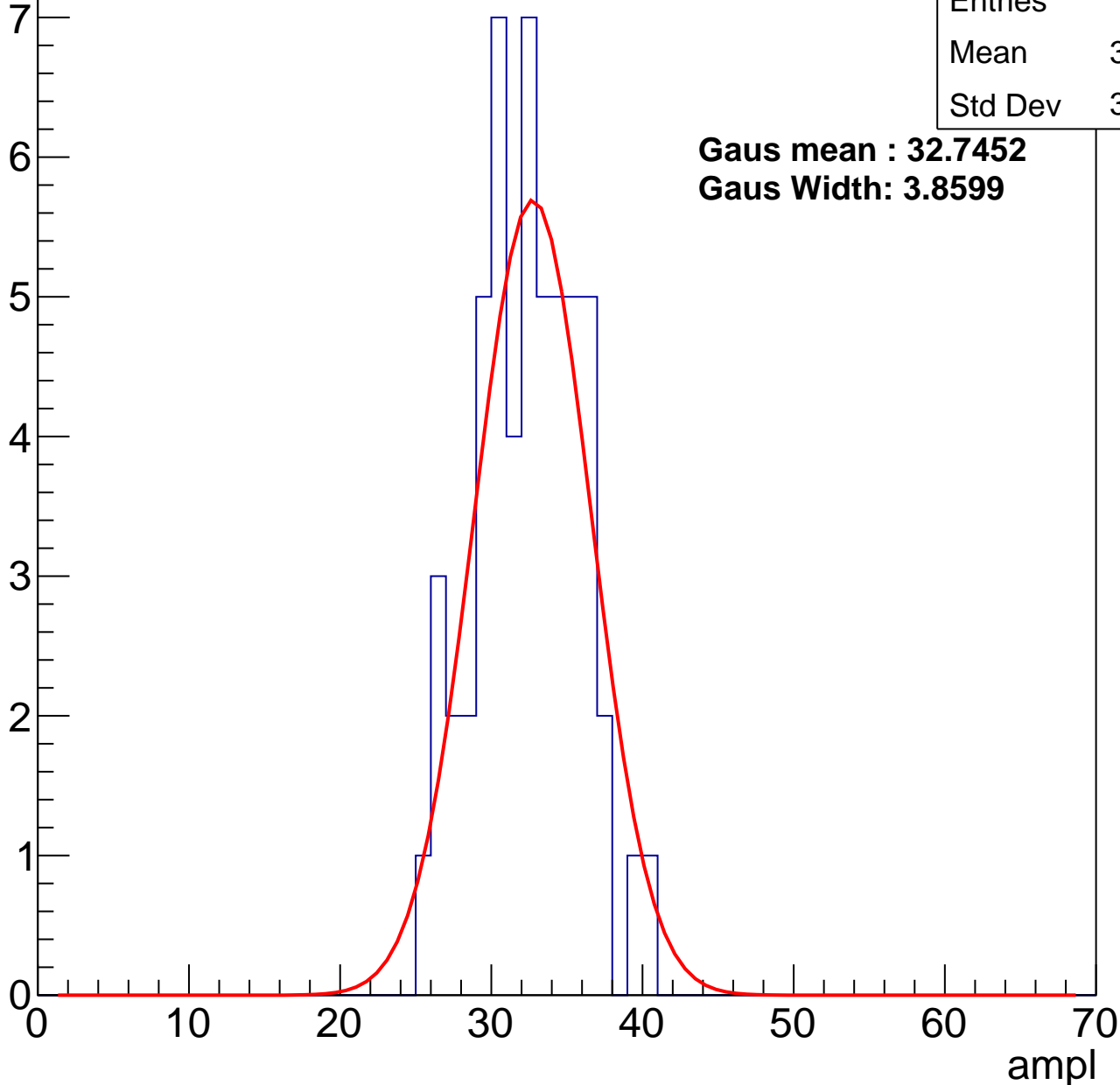
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	31.98
Std Dev	3.382

Gaus mean : 32.7452

Gaus Width: 3.8599



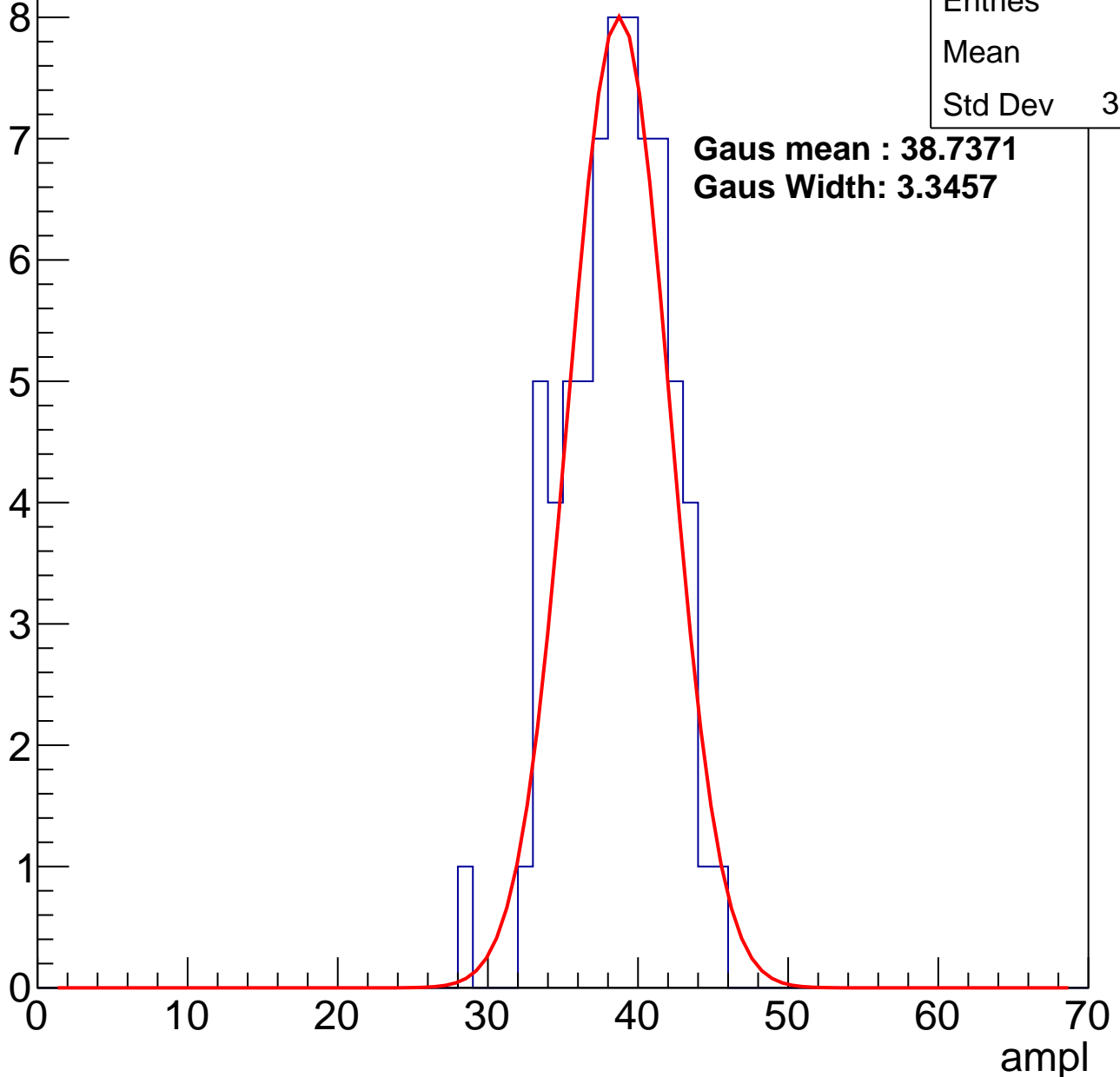
B1L101S, U12-ch11, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.1
Std Dev	3.319

Gaus mean : 38.7371
Gaus Width: 3.3457



B1L101S, U12-ch11, adc2

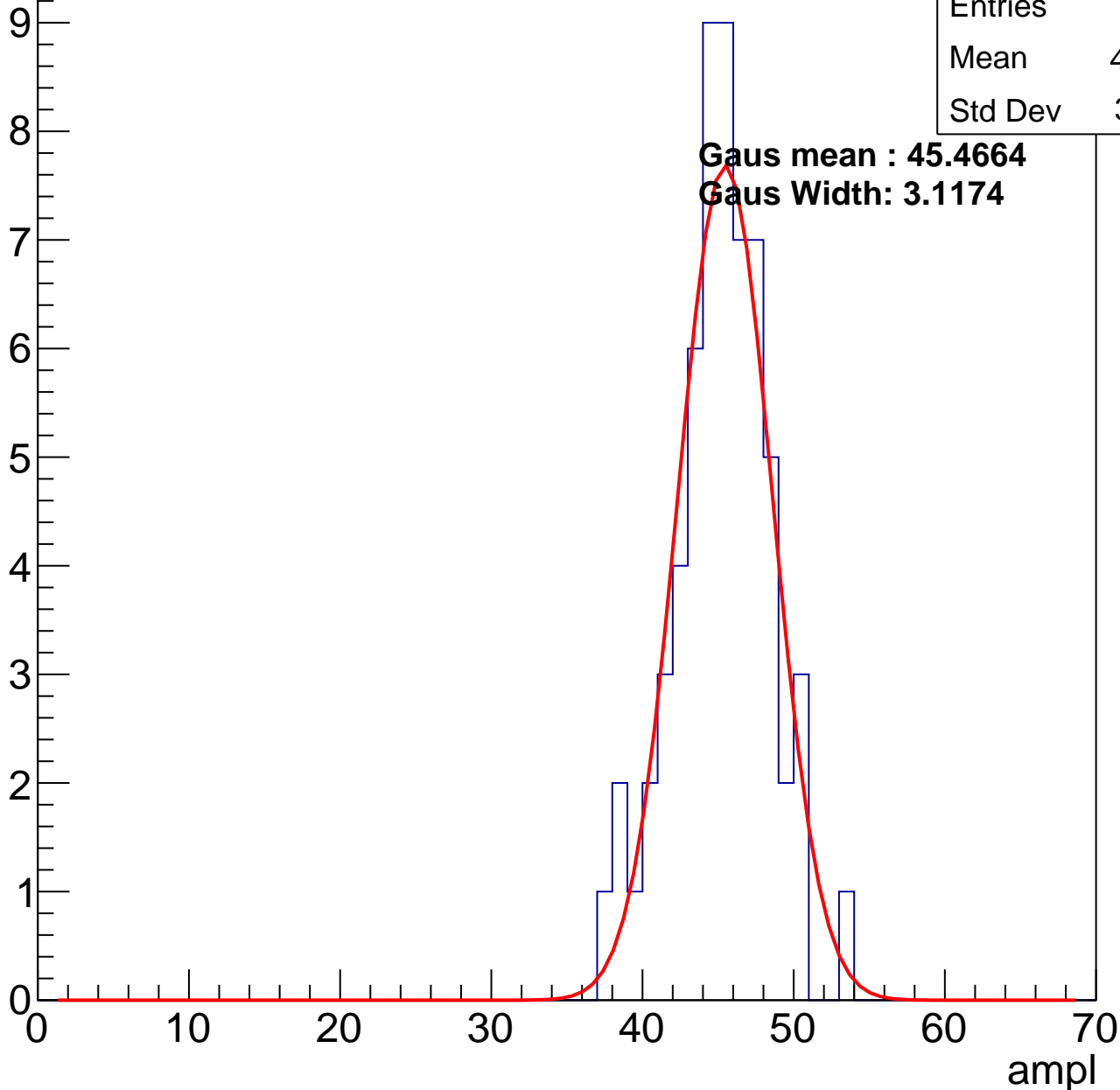
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.74
Std Dev	3.141

Gaus mean : 45.4664

Gaus Width: 3.1174

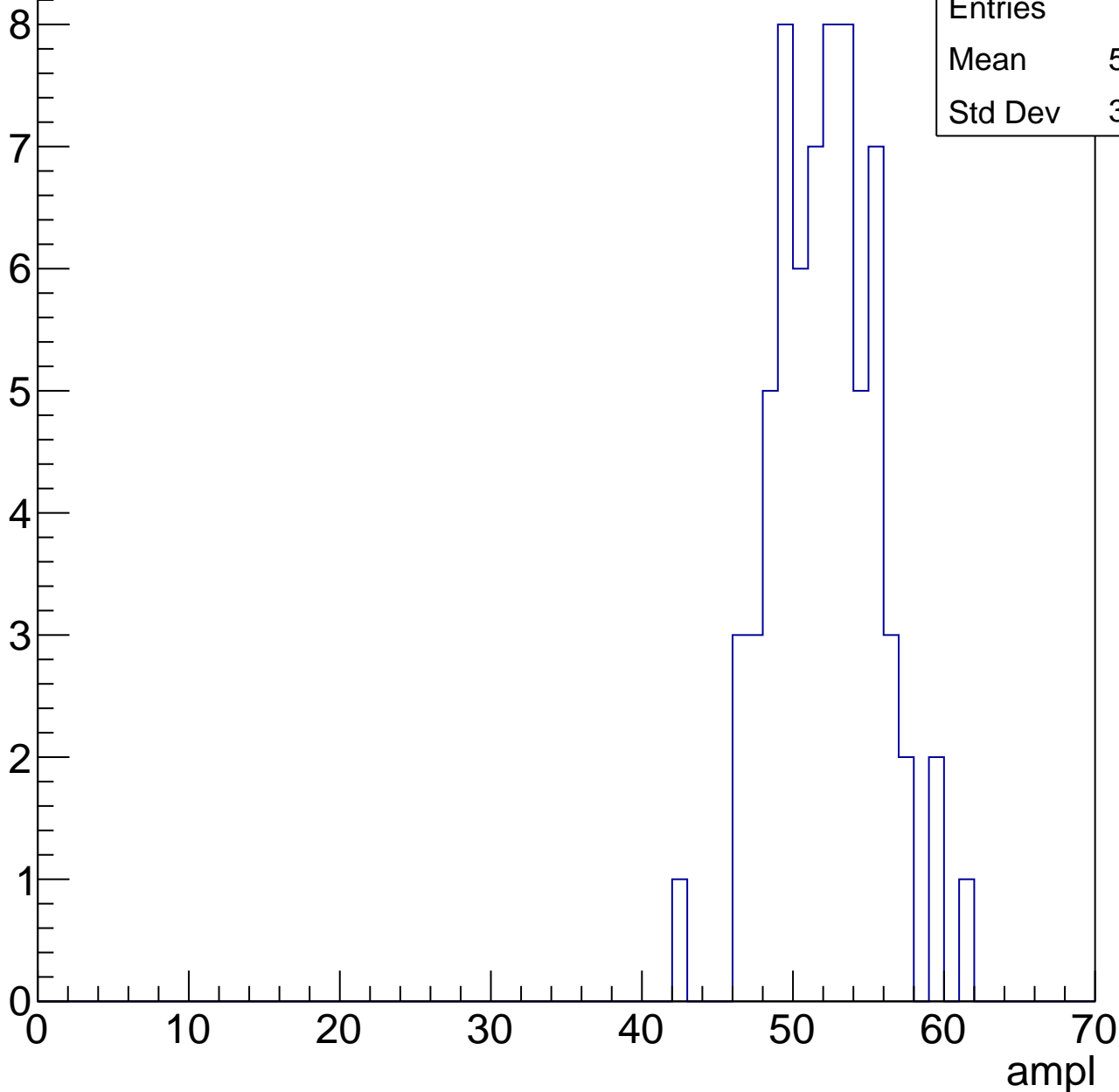


B1L101S, U12-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	51.68
Std Dev	3.462

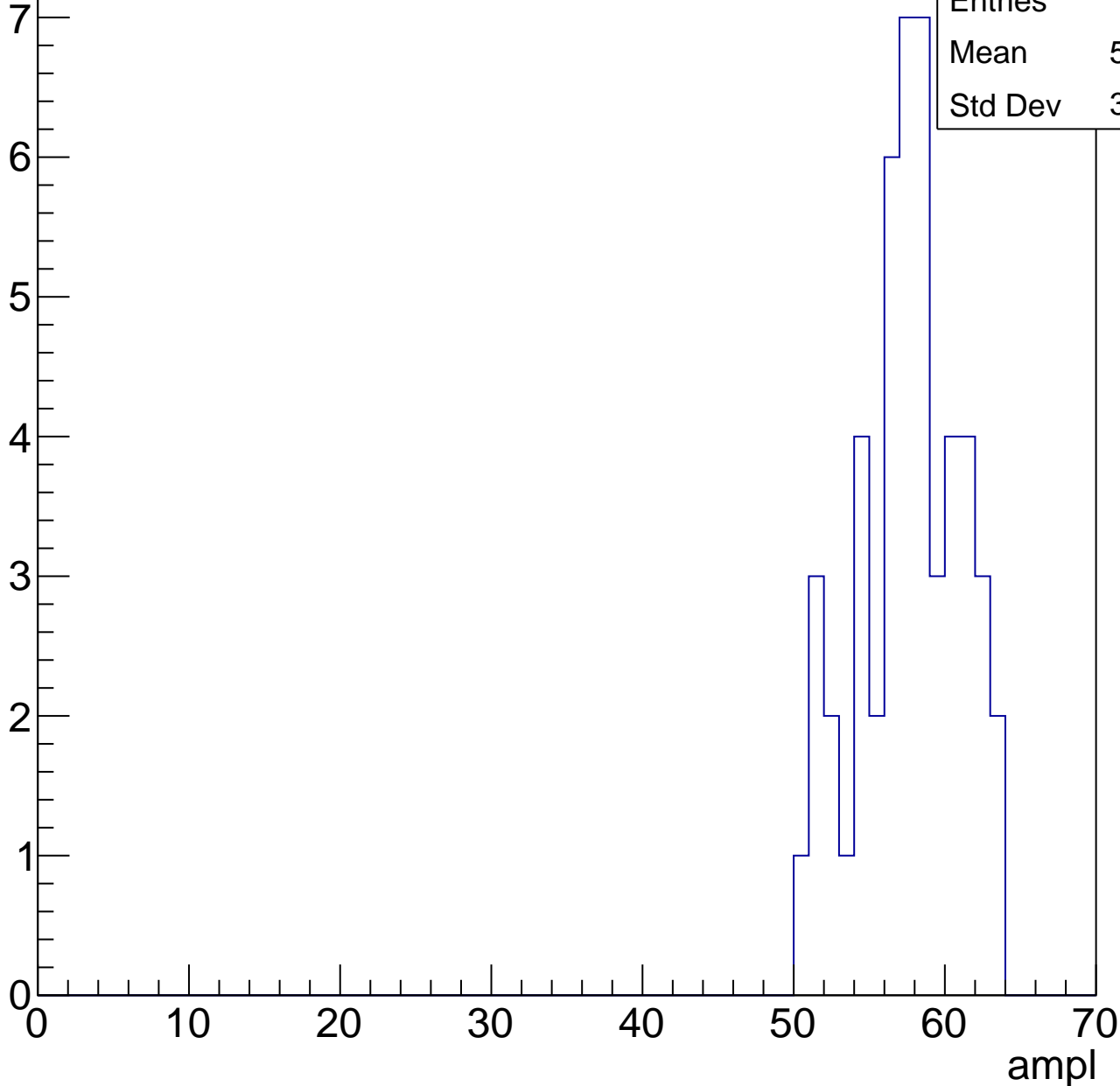


B1L101S, U12-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.14
Std Dev	3.314

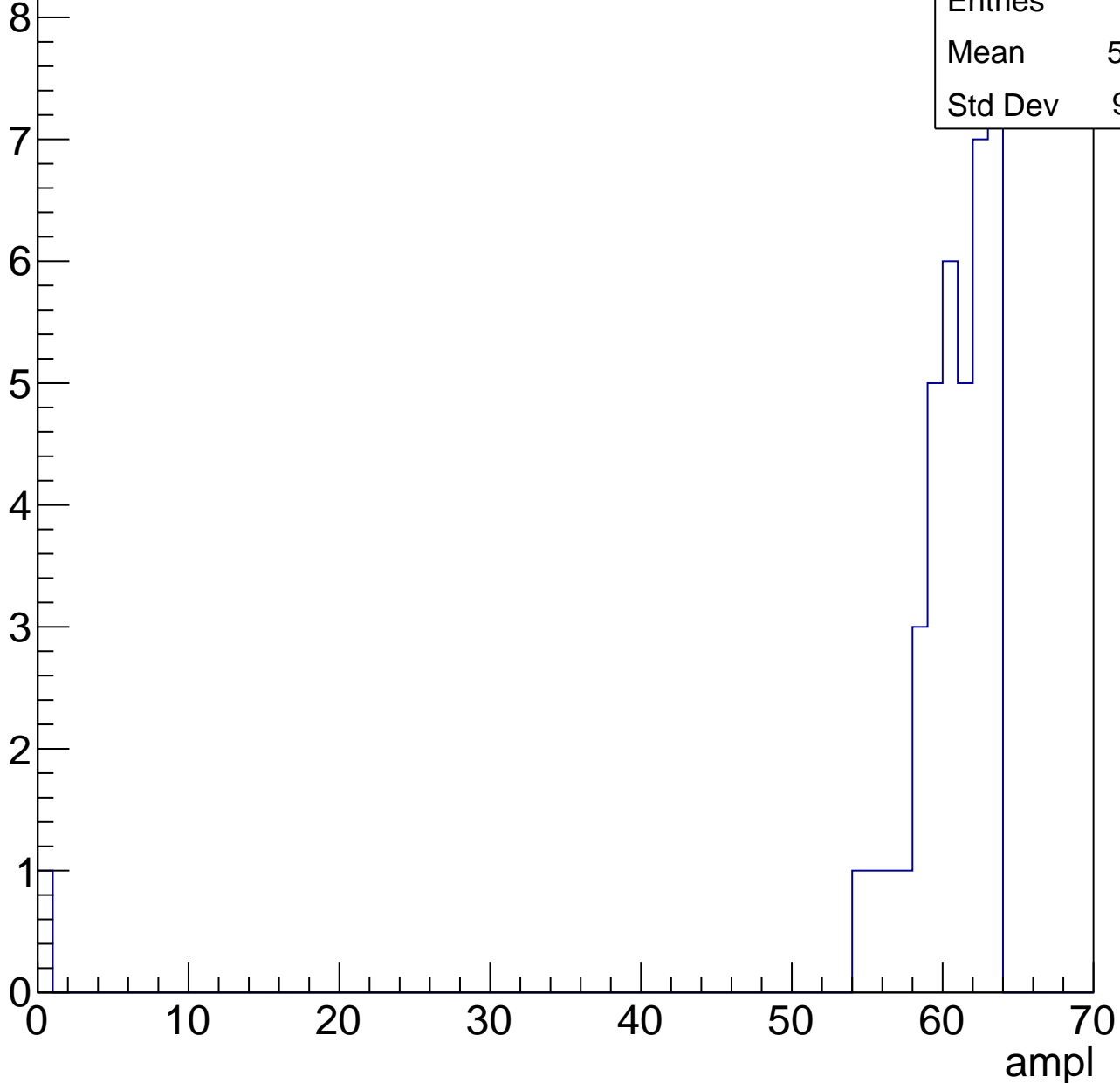


B1L101S, U12-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.82
Std Dev	9.811



B1L101S, U12-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch12, adc0

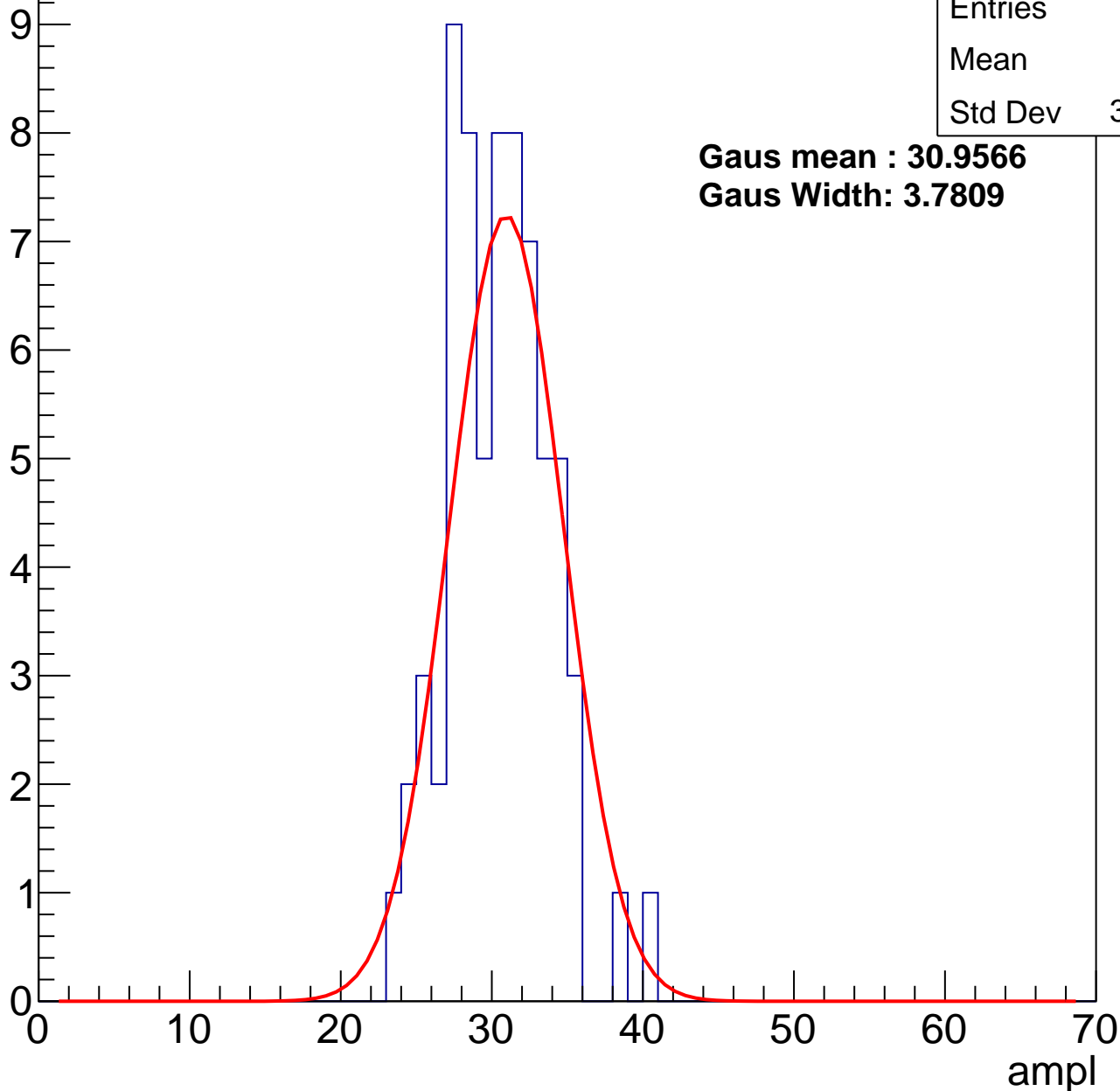
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30
Std Dev	3.312

Gaus mean : 30.9566

Gaus Width: 3.7809



B1L101S, U12-ch12, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	36.58
Std Dev	3.36

Gaus mean : 37.2211

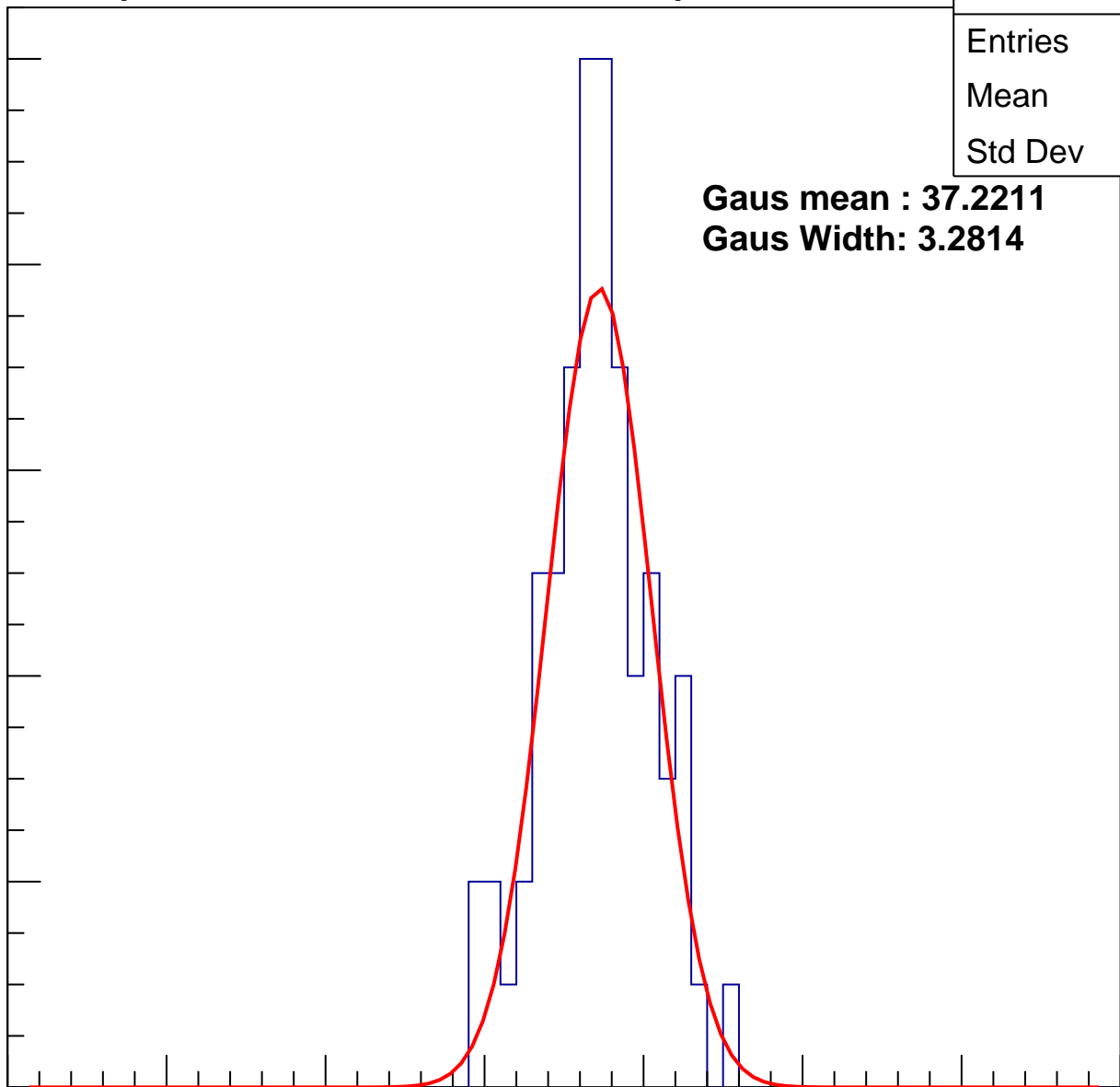
Gaus Width: 3.2814

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch12, adc2

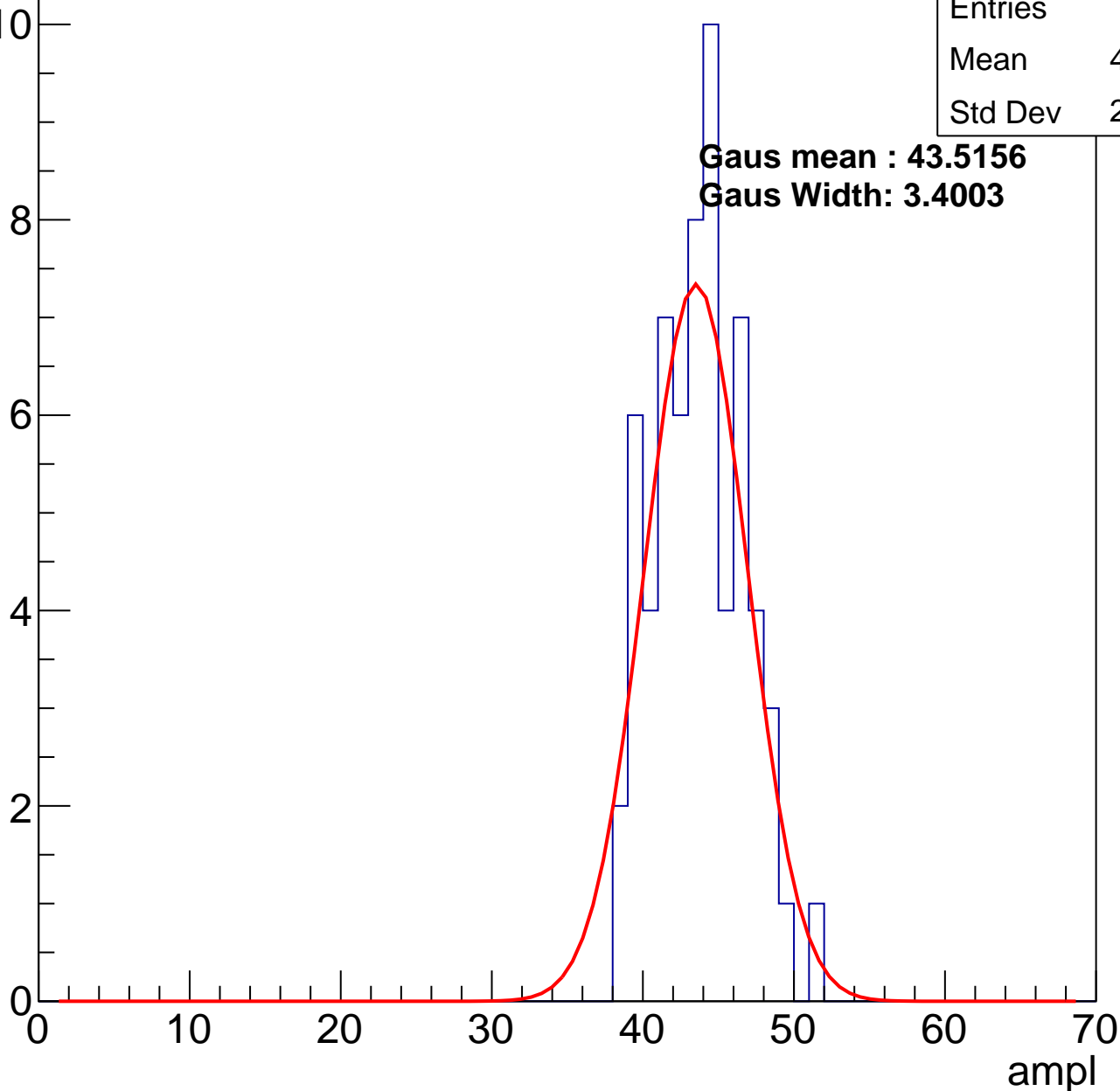
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.29
Std Dev	2.925

Gaus mean : 43.5156

Gaus Width: 3.4003

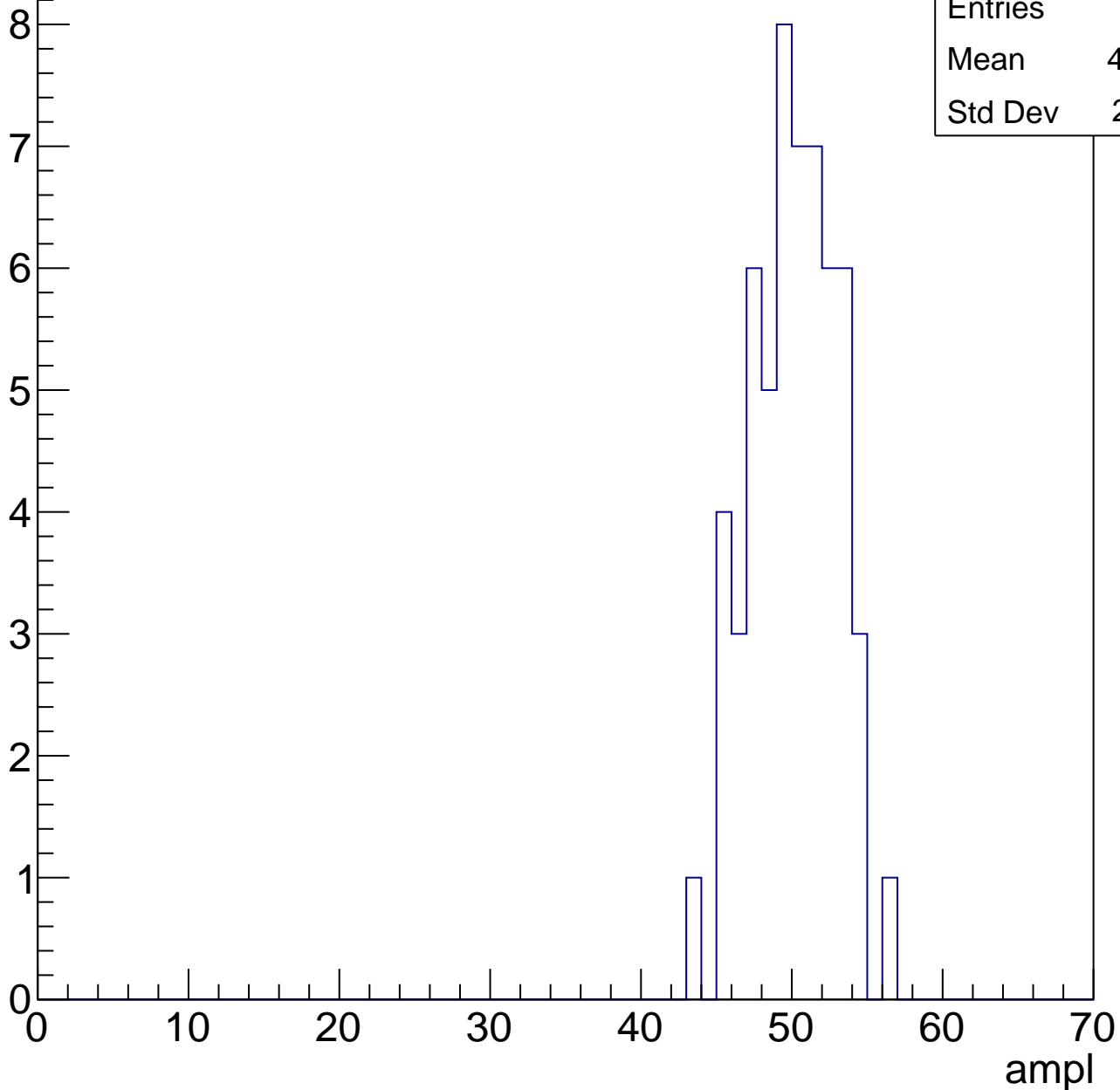


B1L101S, U12-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	49.65
Std Dev	2.781



B1L101S, U12-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	55.64
Std Dev	3.629

Entry

10

8

6

4

2

0

0

10

20

30

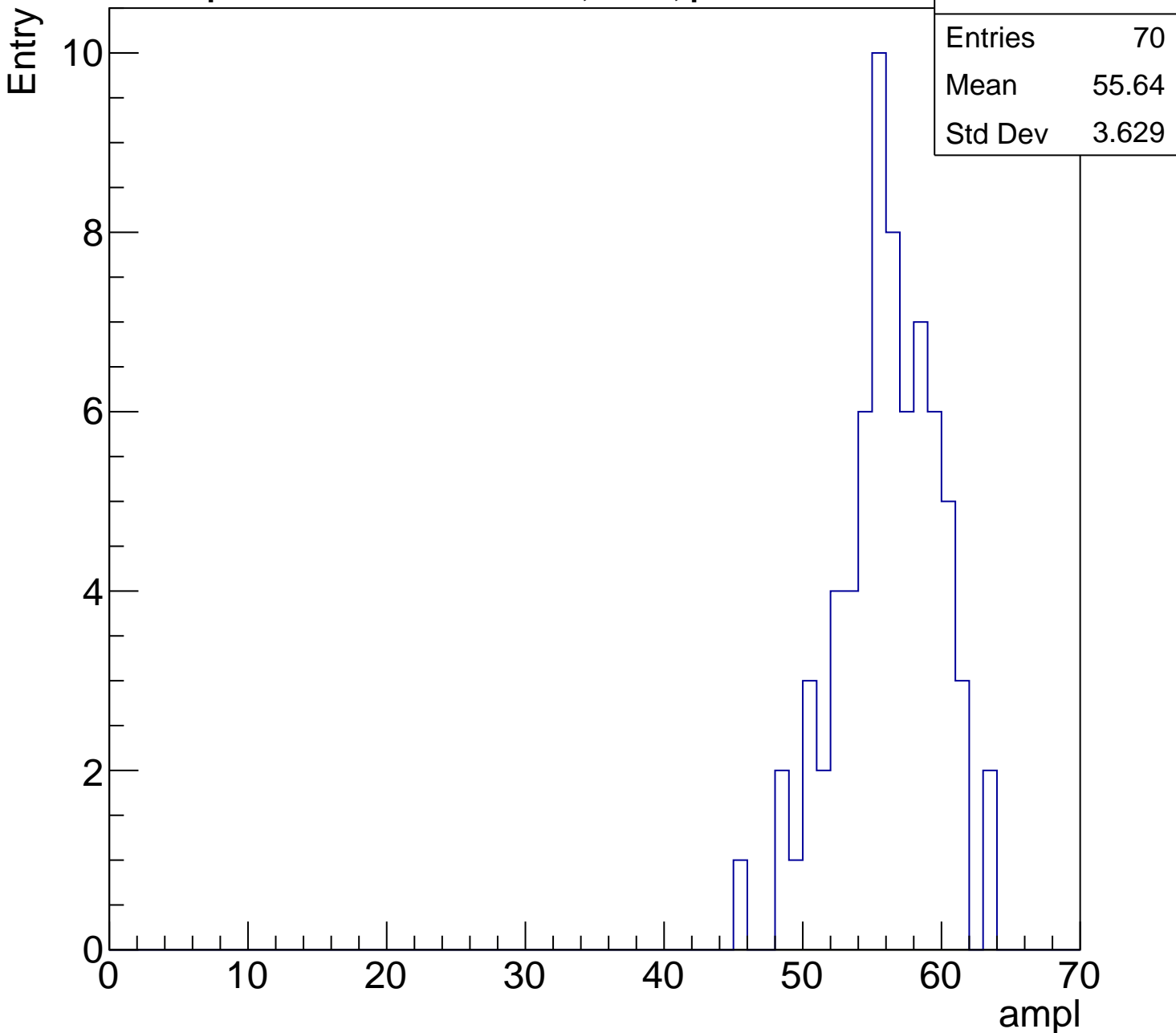
40

50

60

70

ampl

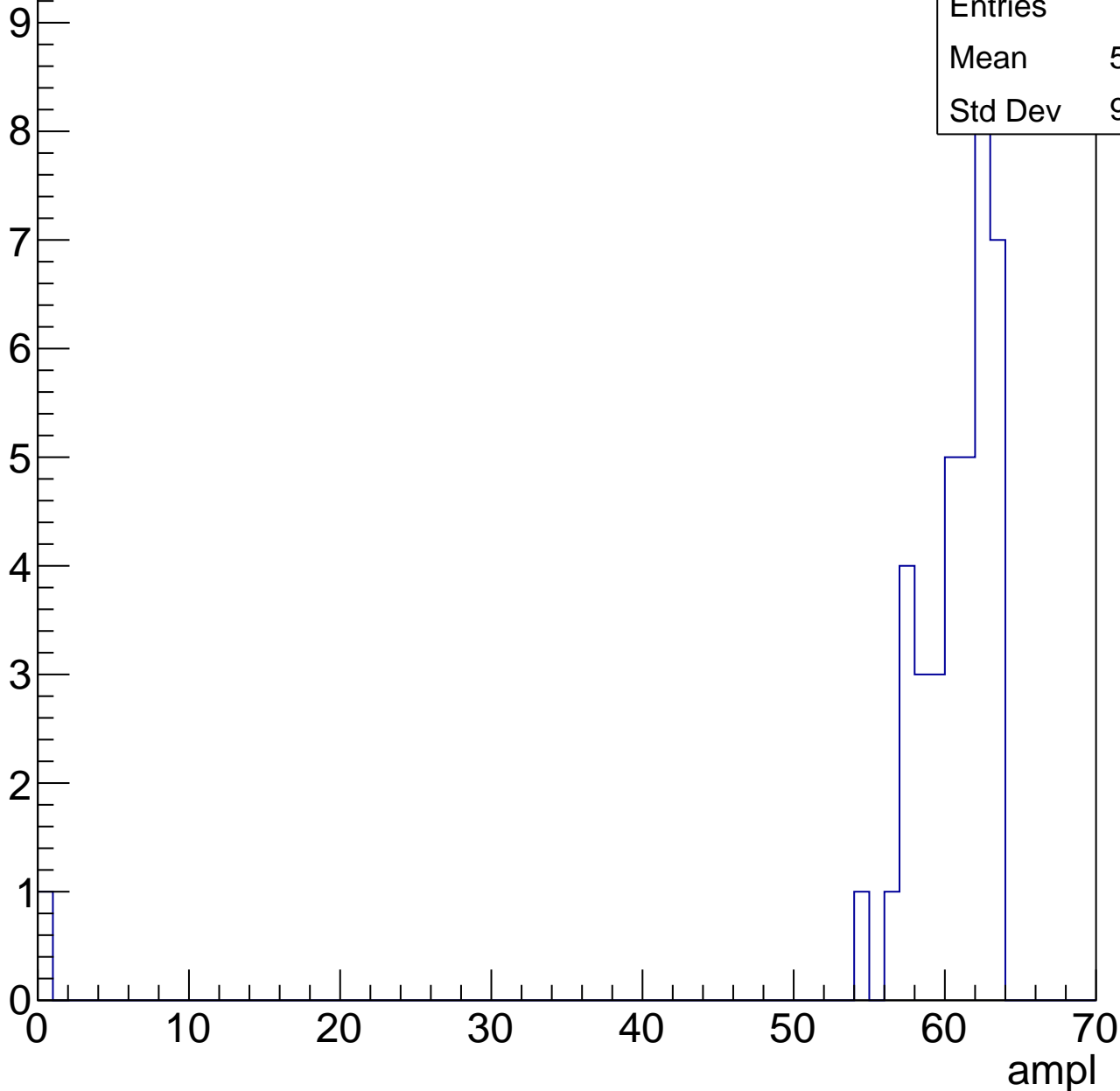


B1L101S, U12-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

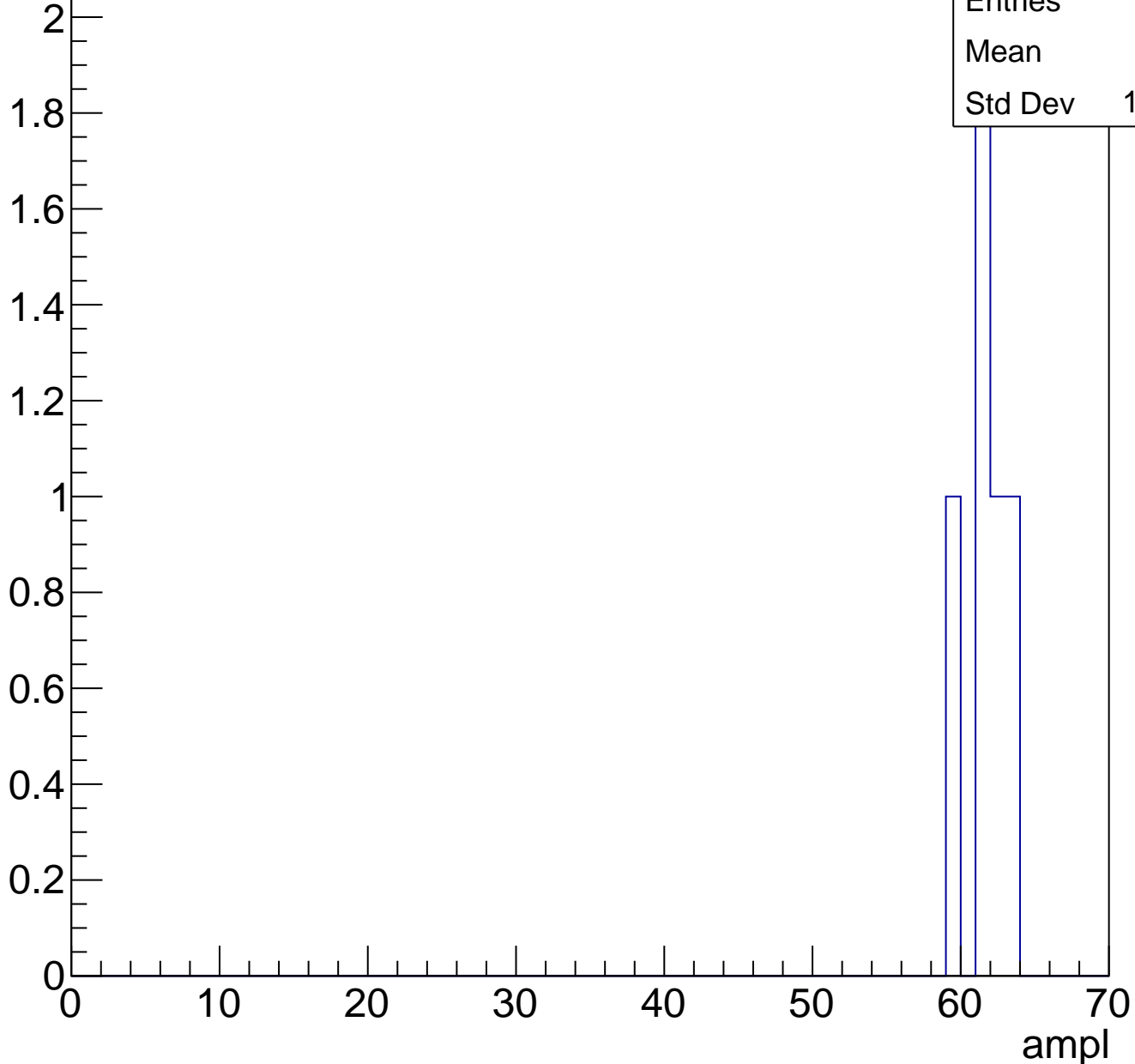
Entries	39
Mean	58.79
Std Dev	9.806



B1L101S, U12-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch13, adc0

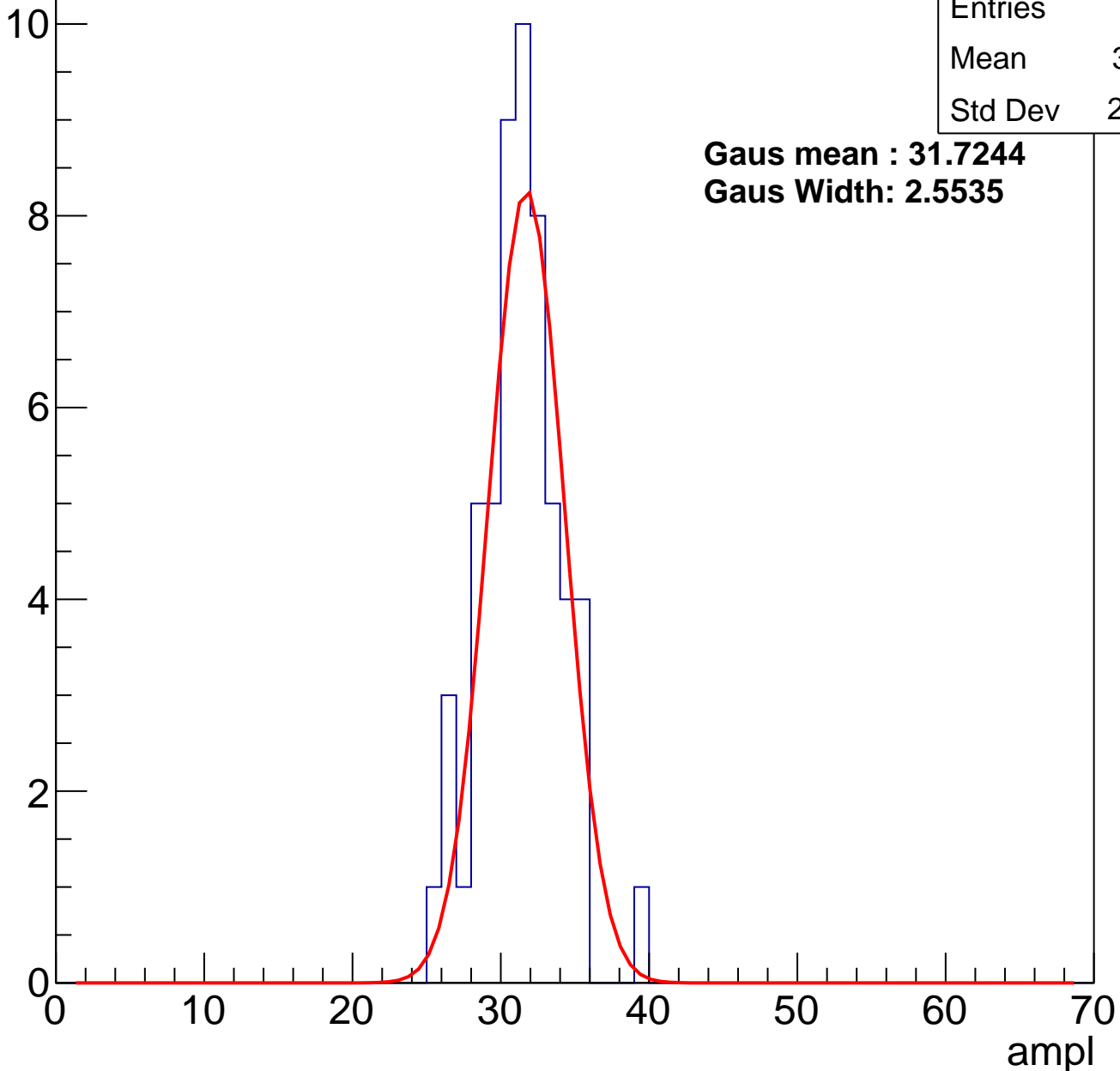
calib_packv5_042523_0143.root, FC#0, port D2

Entries	56
Mean	30.91
Std Dev	2.648

Gaus mean : 31.7244

Gaus Width: 2.5535

Entry



B1L101S, U12-ch13, adc1

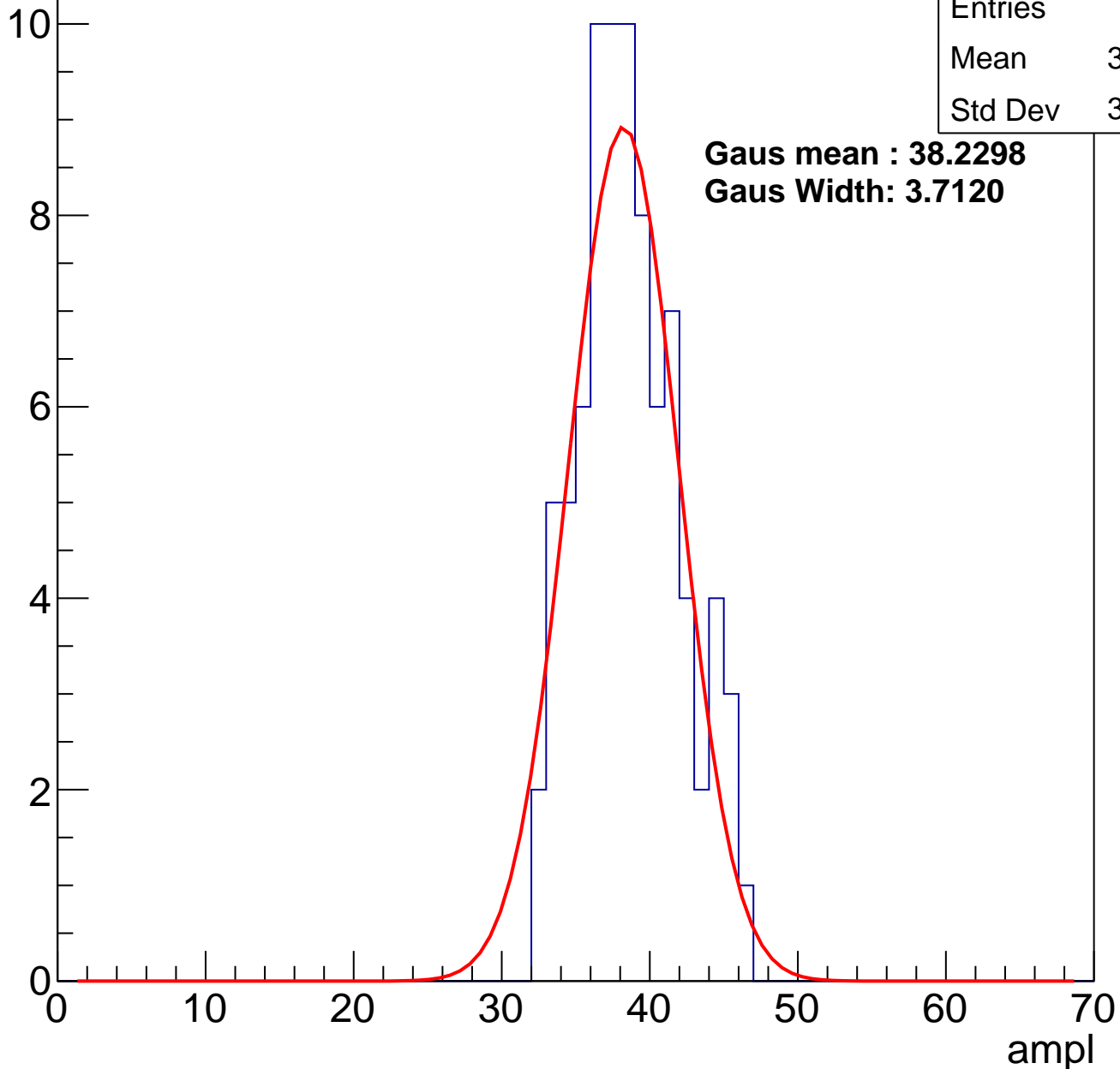
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	38.18
Std Dev	3.369

Gaus mean : 38.2298

Gaus Width: 3.7120

Entry



B1L101S, U12-ch13, adc2

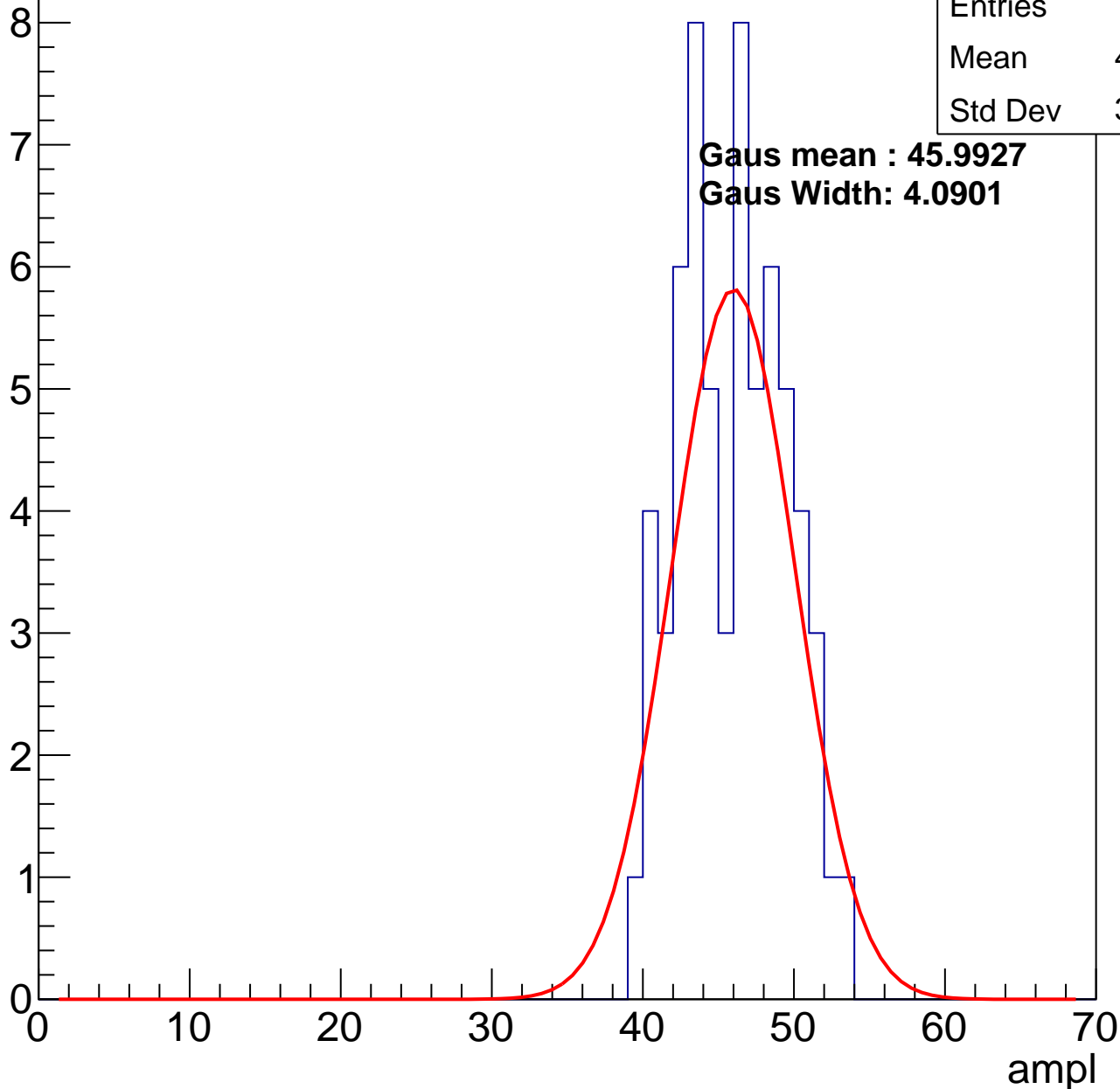
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	45.51
Std Dev	3.431

Gaus mean : 45.9927

Gaus Width: 4.0901

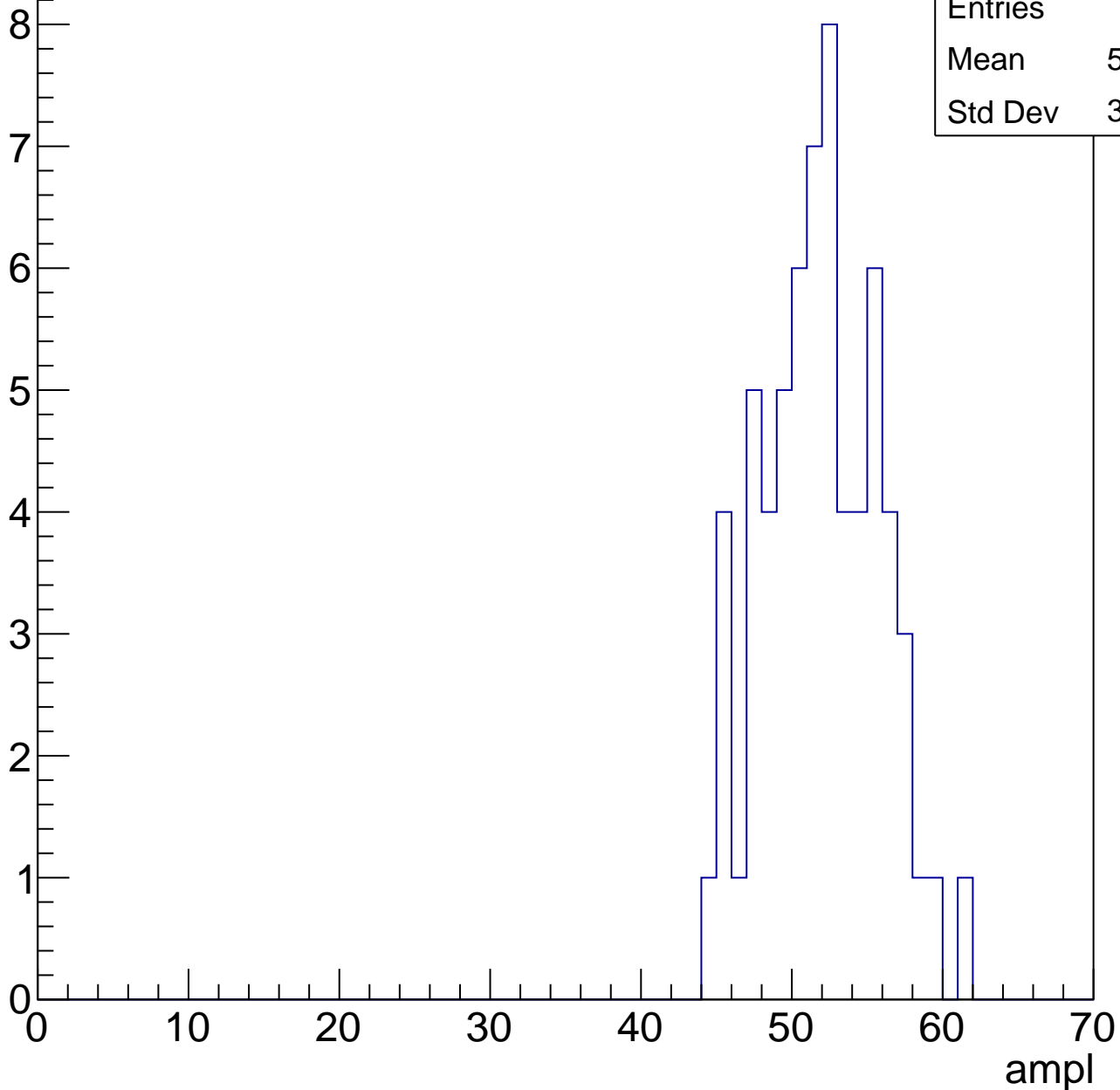


B1L101S, U12-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	51.48
Std Dev	3.767

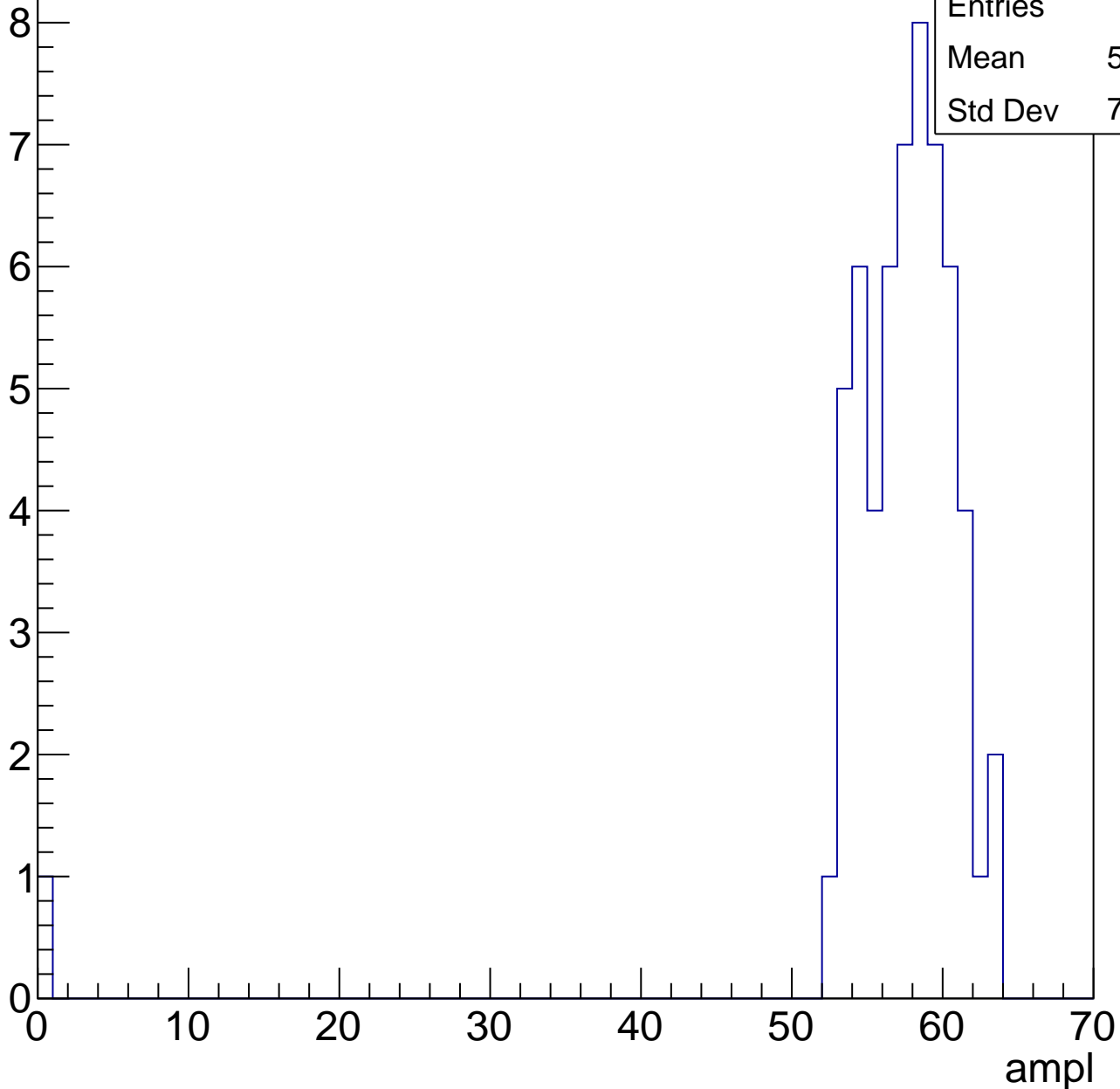


B1L101S, U12-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

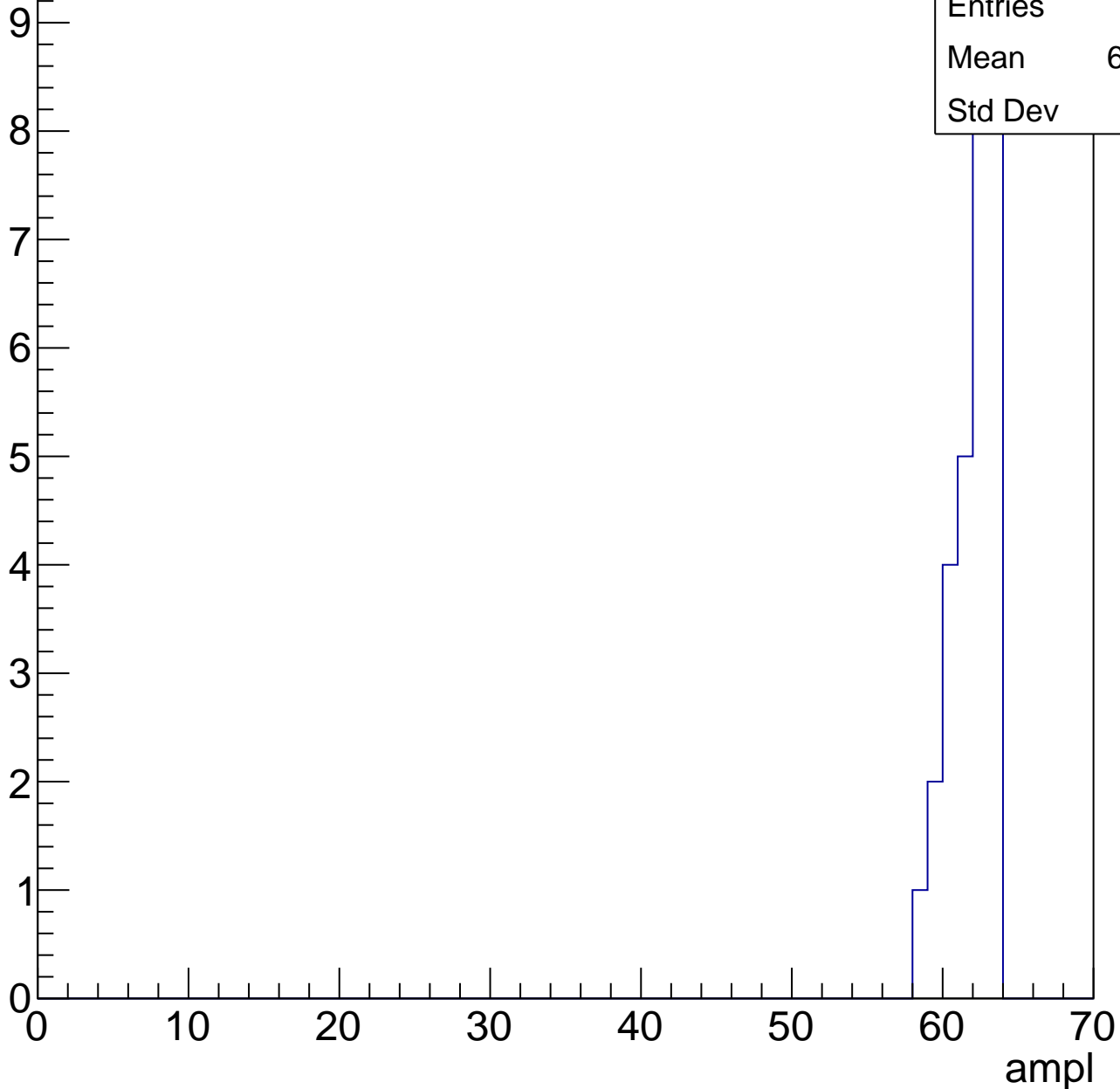
Entries	58
Mean	56.29
Std Dev	7.935



B1L101S, U12-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch14, adc0

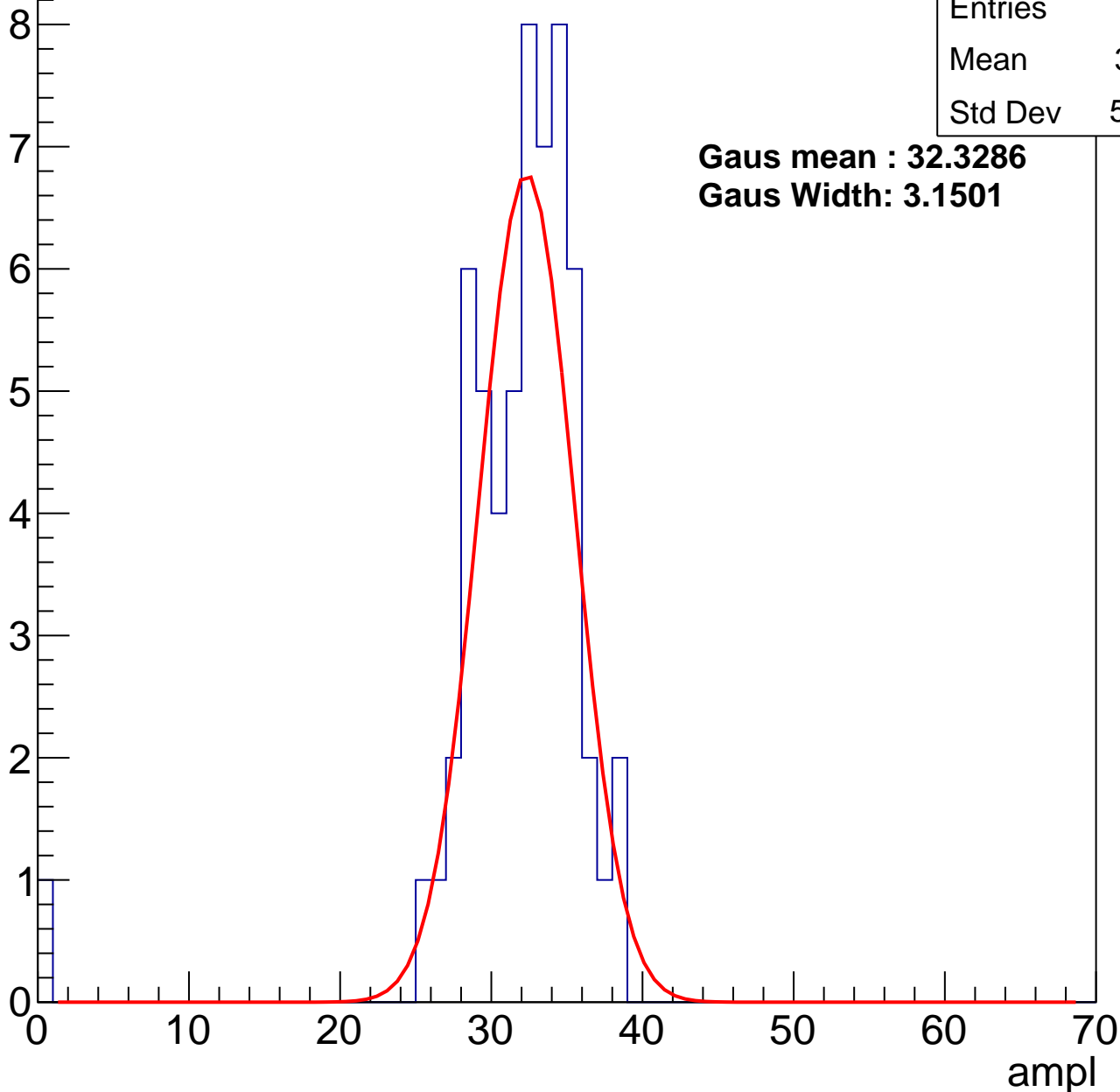
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	31.31
Std Dev	5.067

Gaus mean : 32.3286

Gaus Width: 3.1501



B1L101S, U12-ch14, adc1

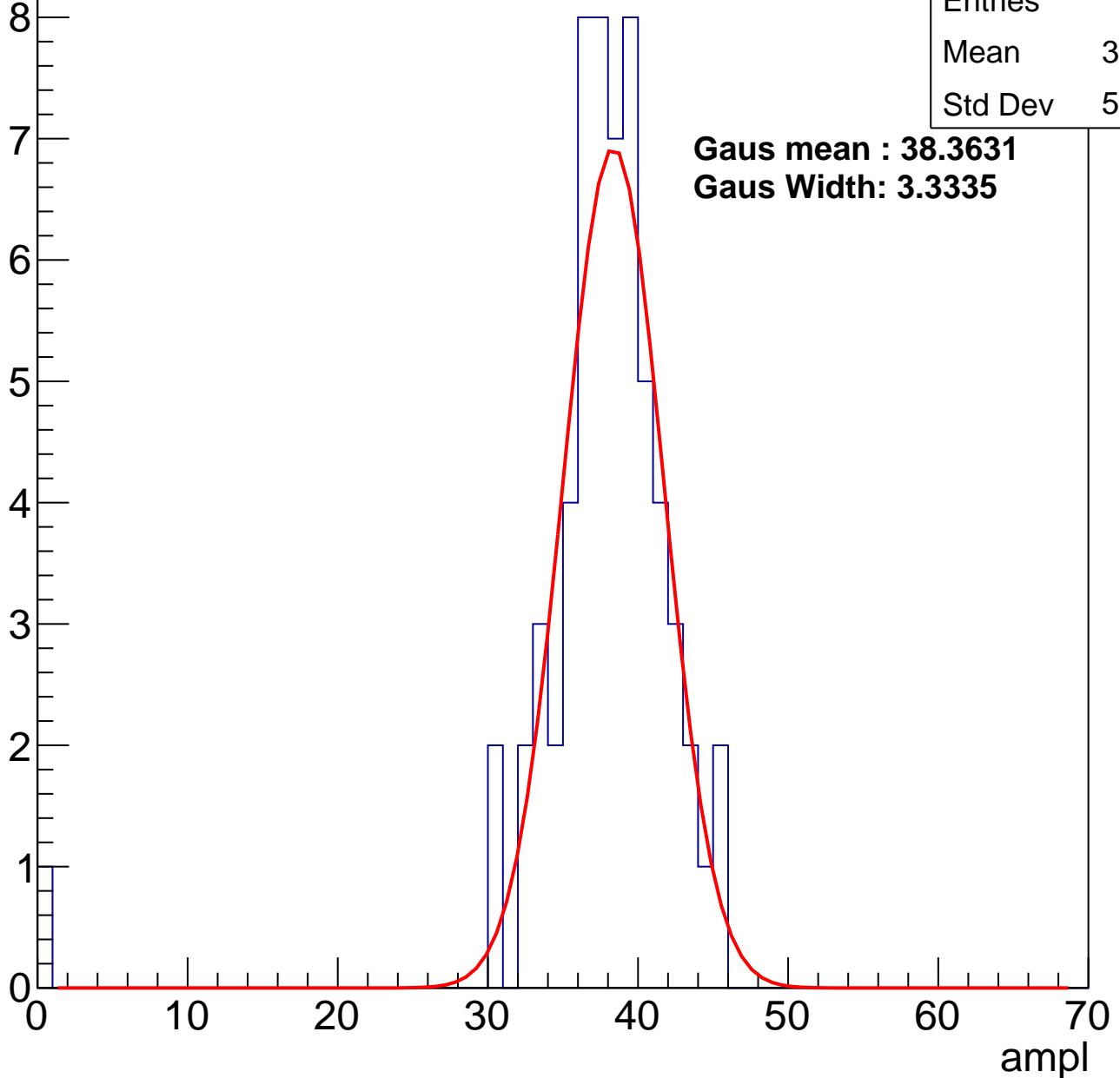
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.15
Std Dev	5.789

Gaus mean : 38.3631

Gaus Width: 3.3335



B1L101S, U12-ch14, adc2

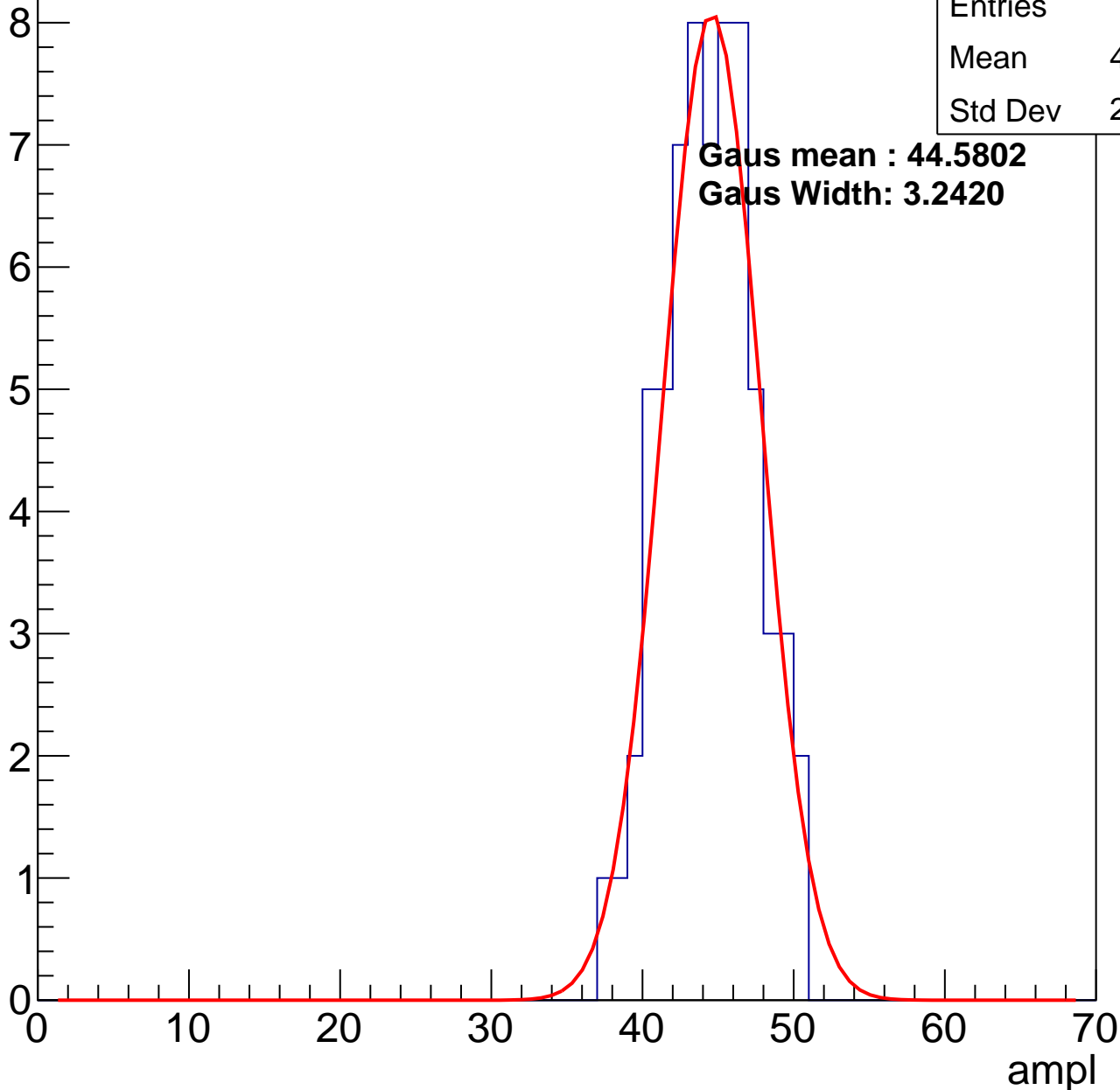
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.97
Std Dev	2.977

Gaus mean : 44.5802

Gaus Width: 3.2420

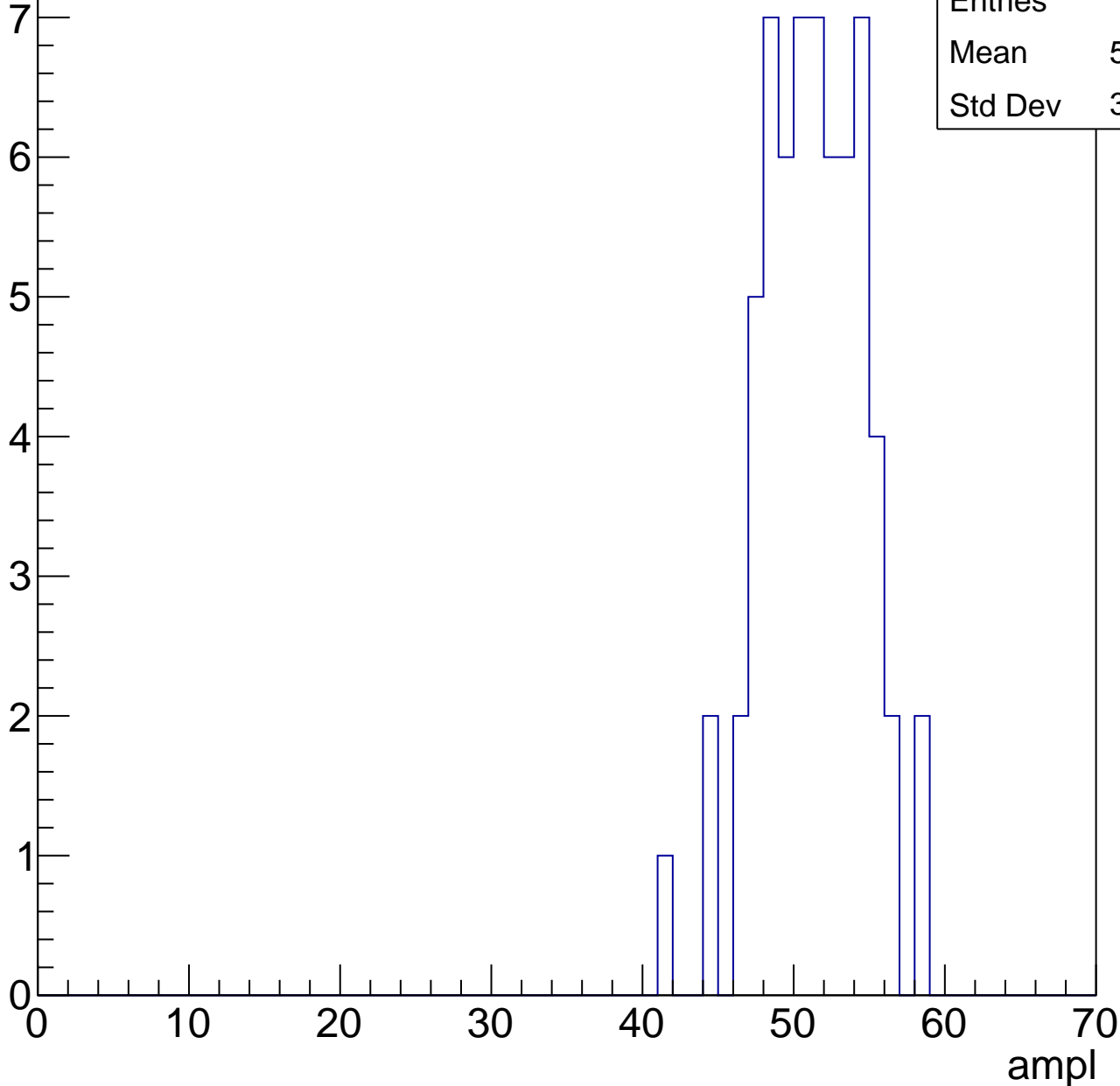


B1L101S, U12-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.77
Std Dev	3.362



B1L101S, U12-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

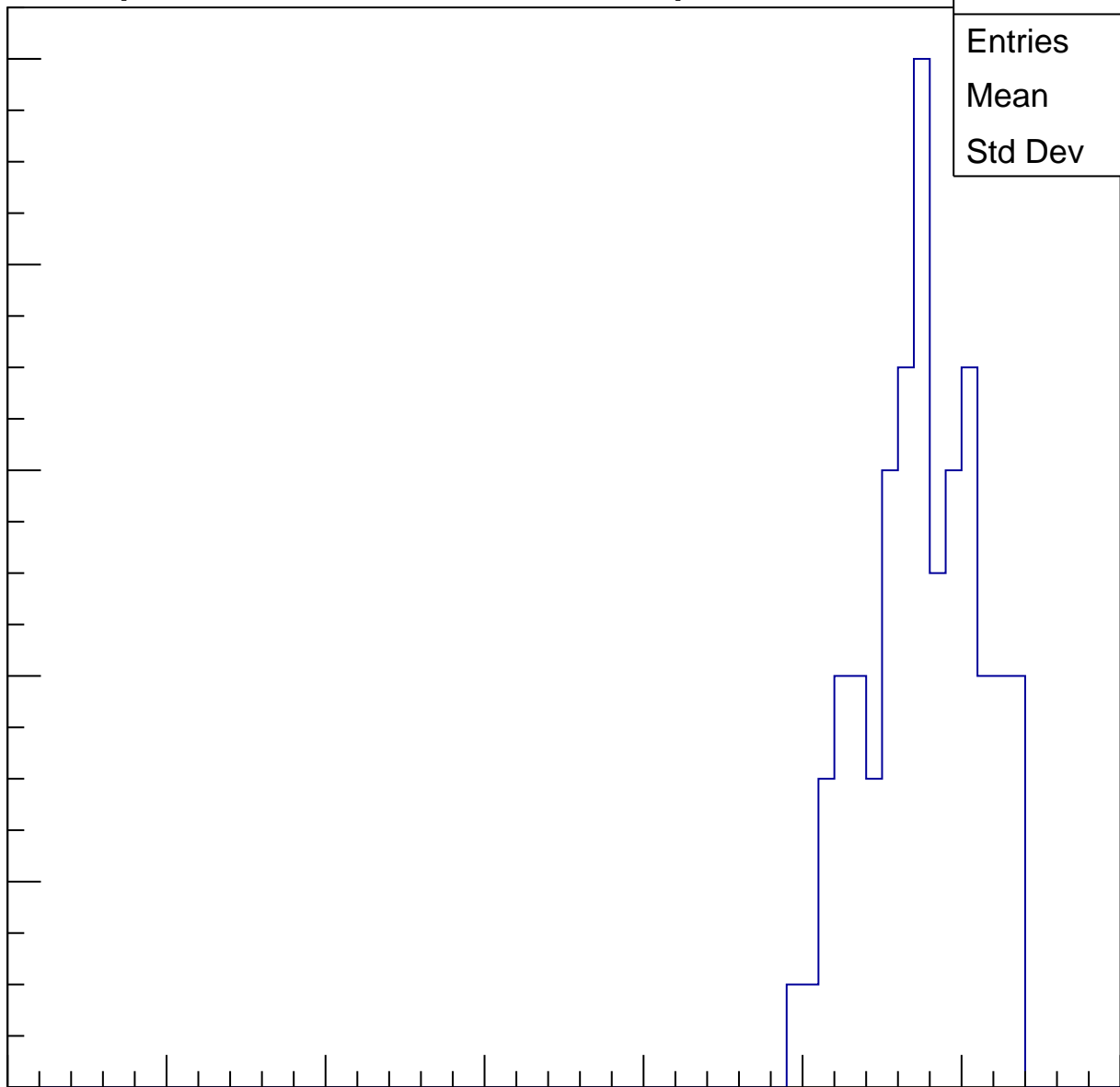
Entries	69
Mean	57.01
Std Dev	3.495

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

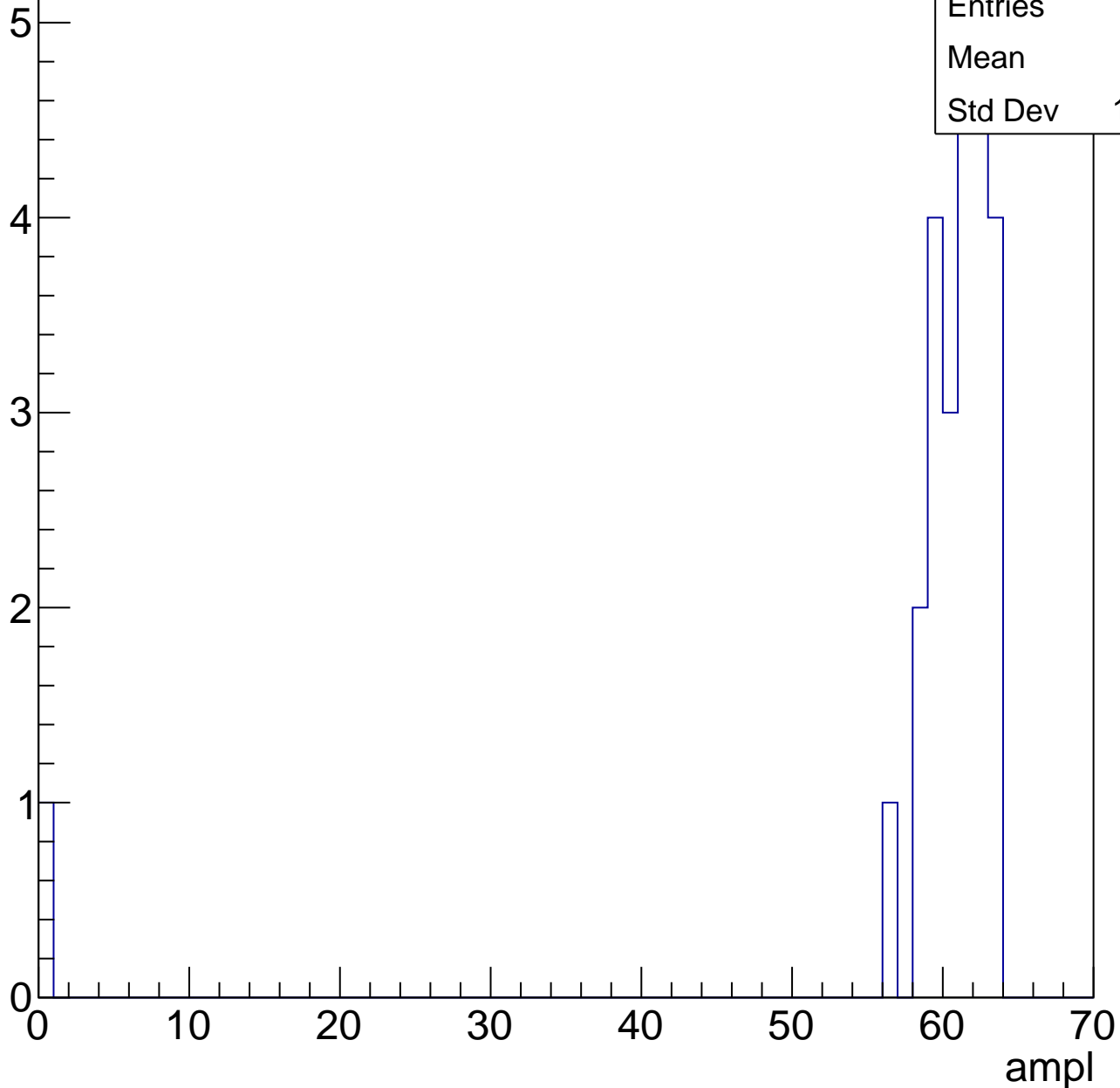


B1L101S, U12-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

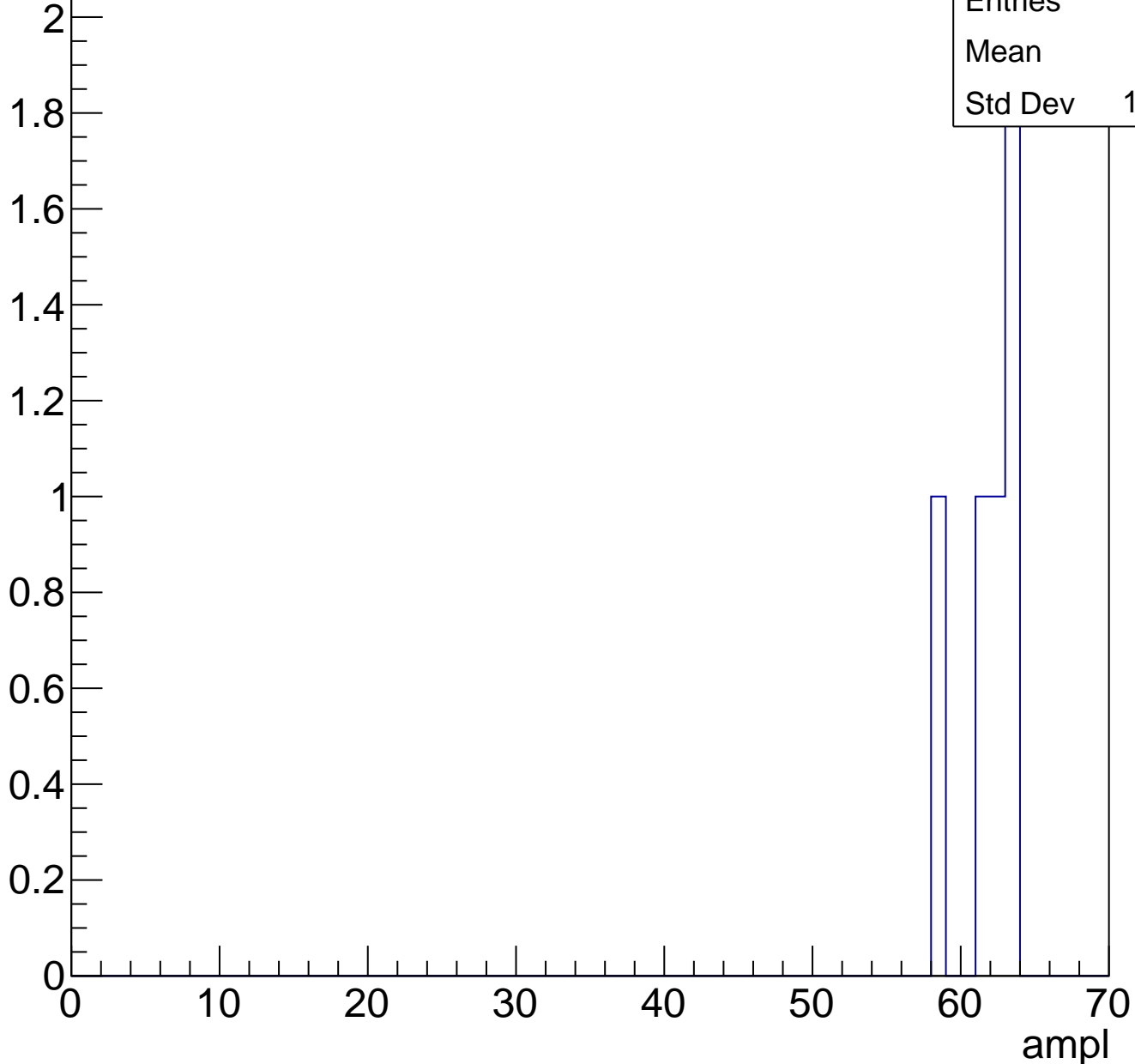
Entries	25
Mean	58.2
Std Dev	12.01



B1L101S, U12-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	61.4
Std Dev	1.855

B1L101S, U12-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch15, adc0

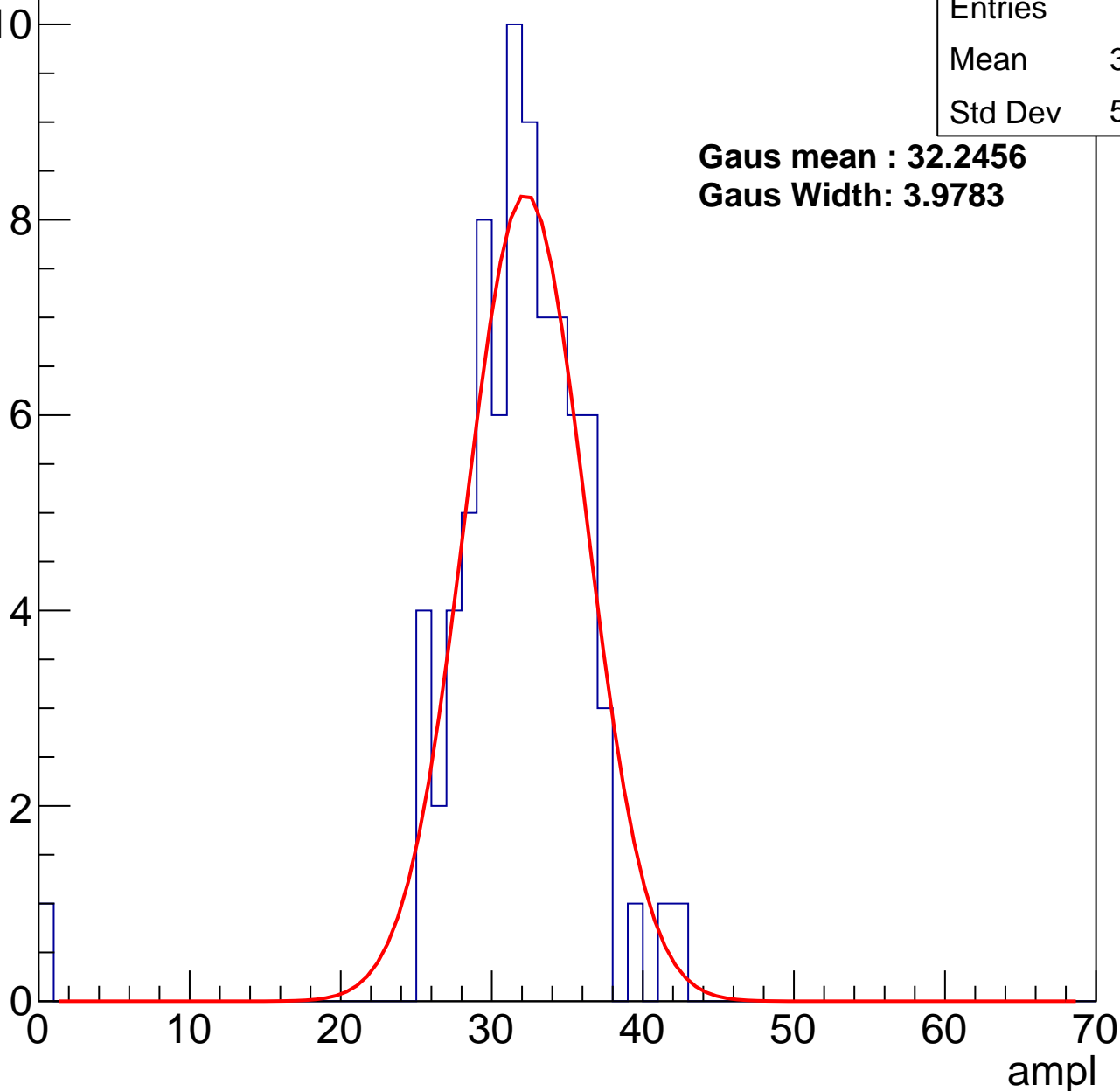
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	31.33
Std Dev	5.009

Gaus mean : 32.2456

Gaus Width: 3.9783



B1L101S, U12-ch15, adc1

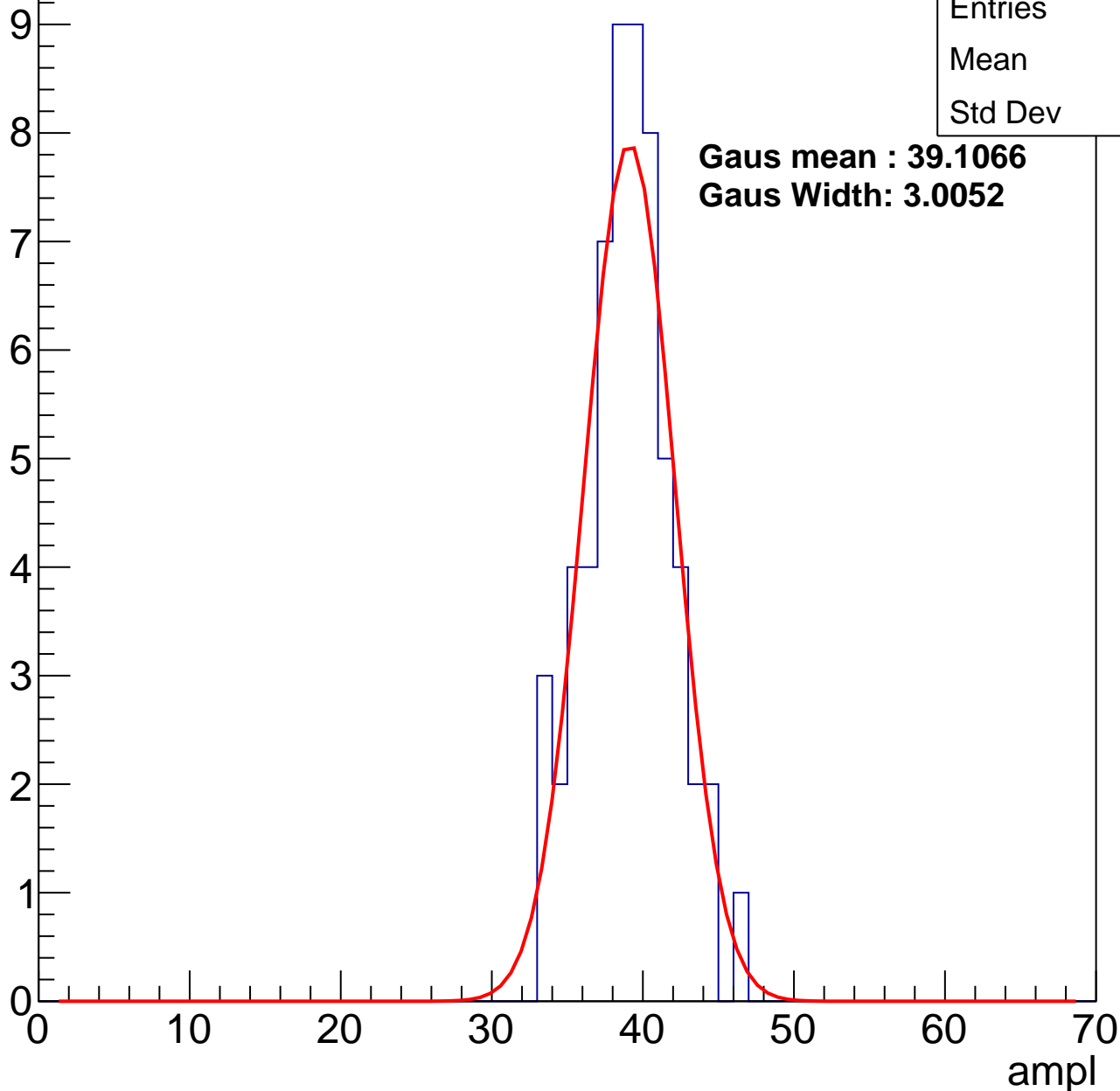
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.6
Std Dev	2.83

Gaus mean : 39.1066

Gaus Width: 3.0052



B1L101S, U12-ch15, adc2

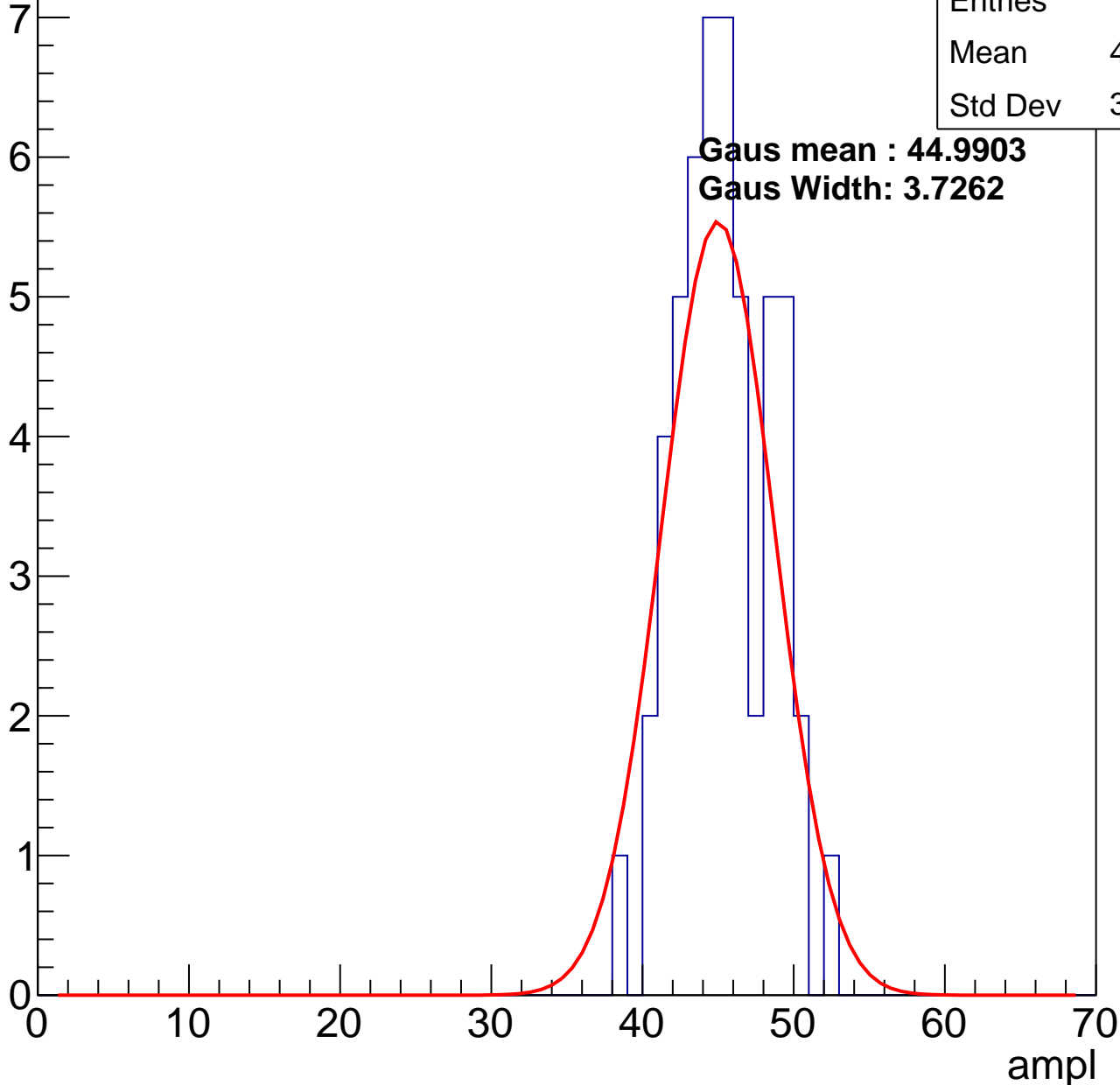
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.88
Std Dev	3.023

Gaus mean : 44.9903

Gaus Width: 3.7262

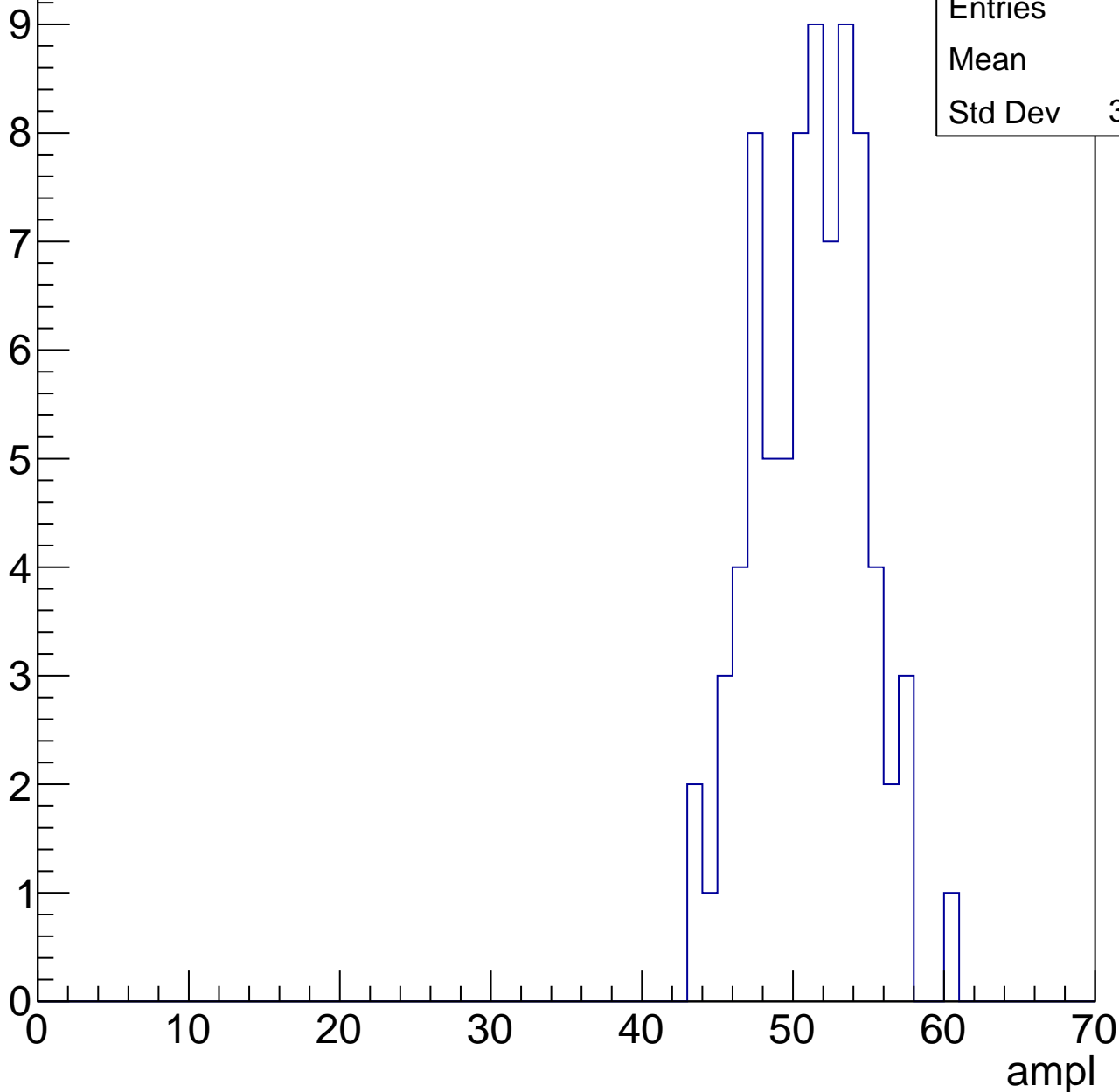


B1L101S, U12-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

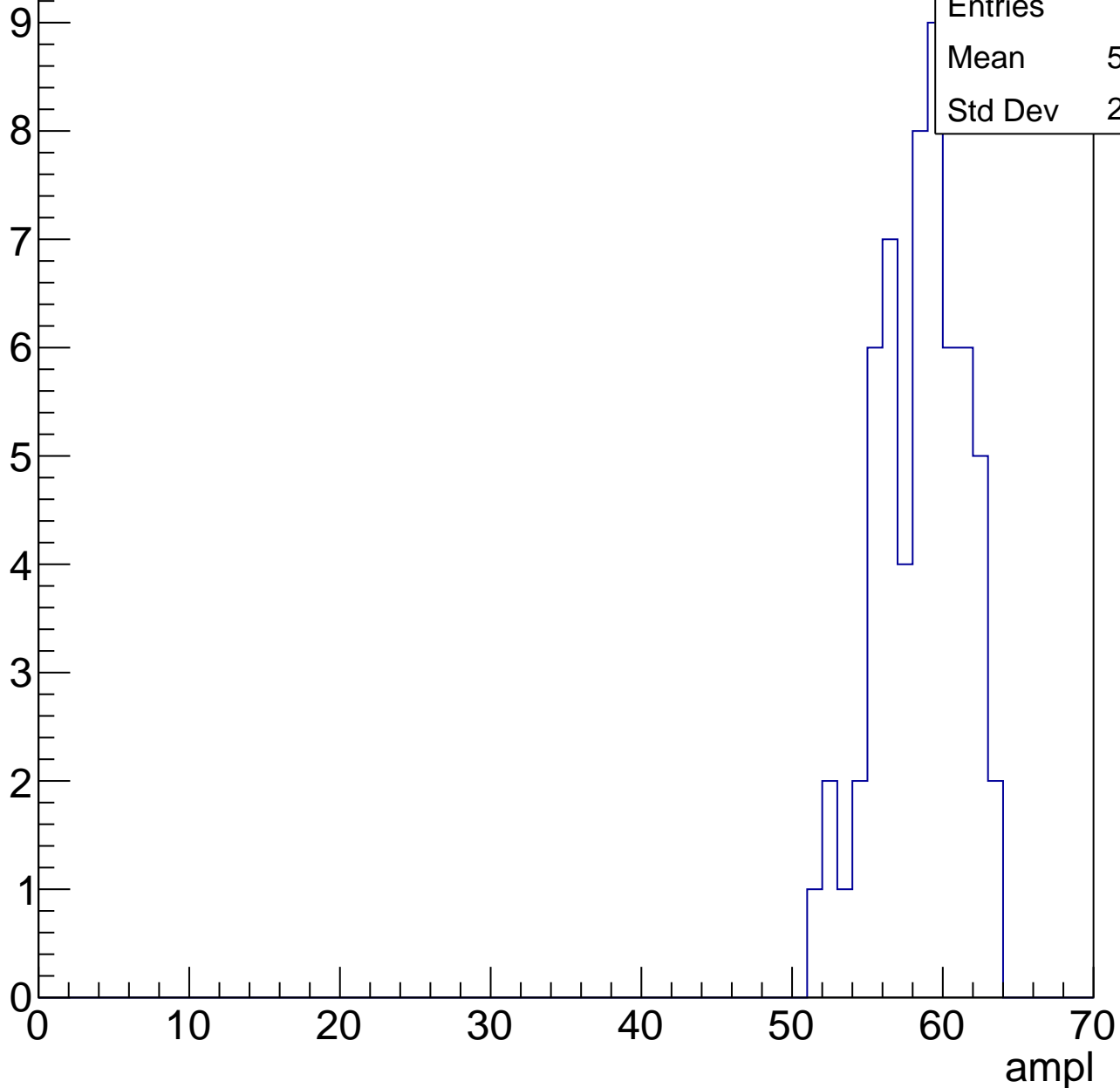
Entries	79
Mean	50.7
Std Dev	3.552



B1L101S, U12-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

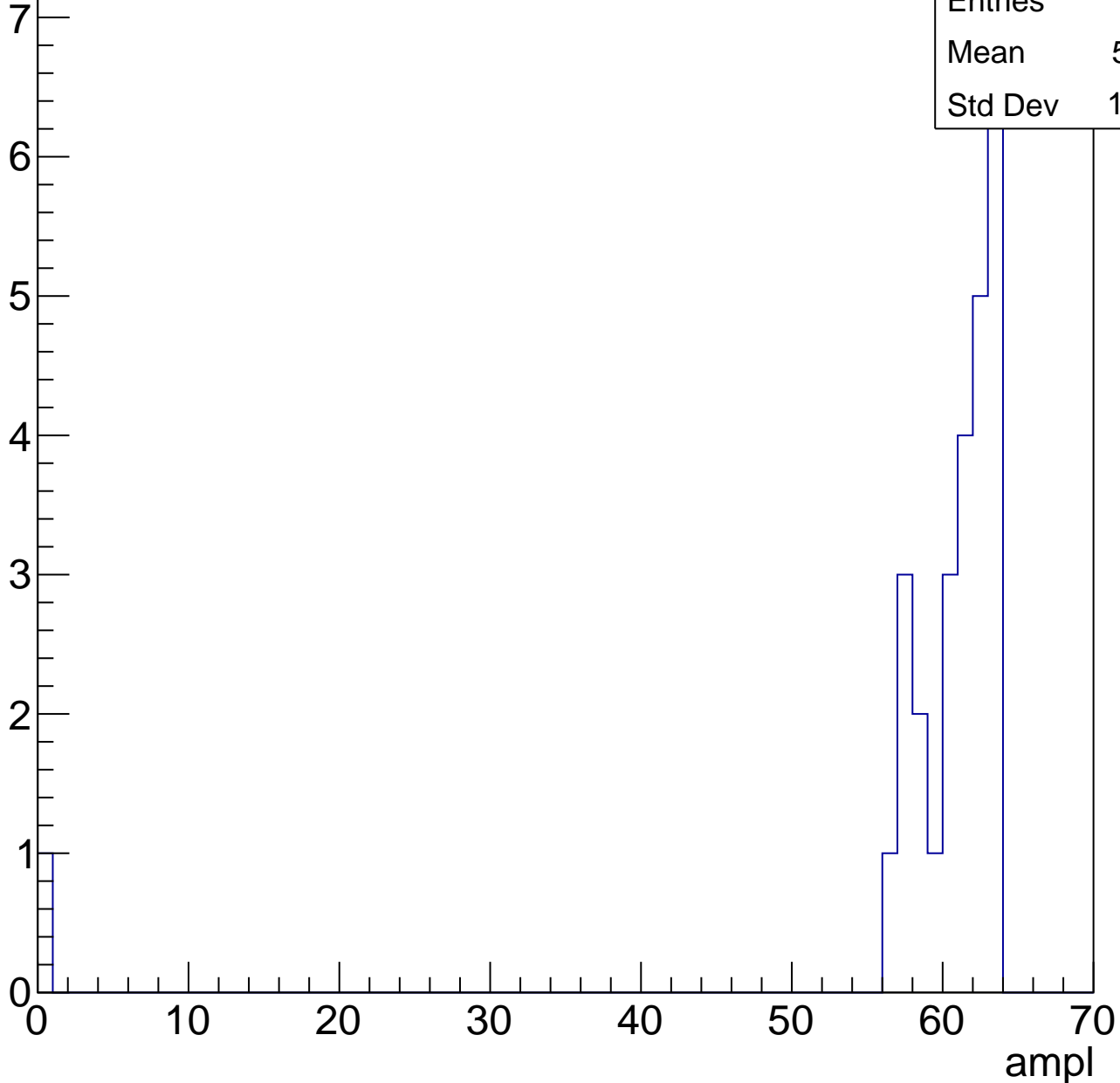


B1L101S, U12-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.41
Std Dev	11.66



B1L101S, U12-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch16, adc0

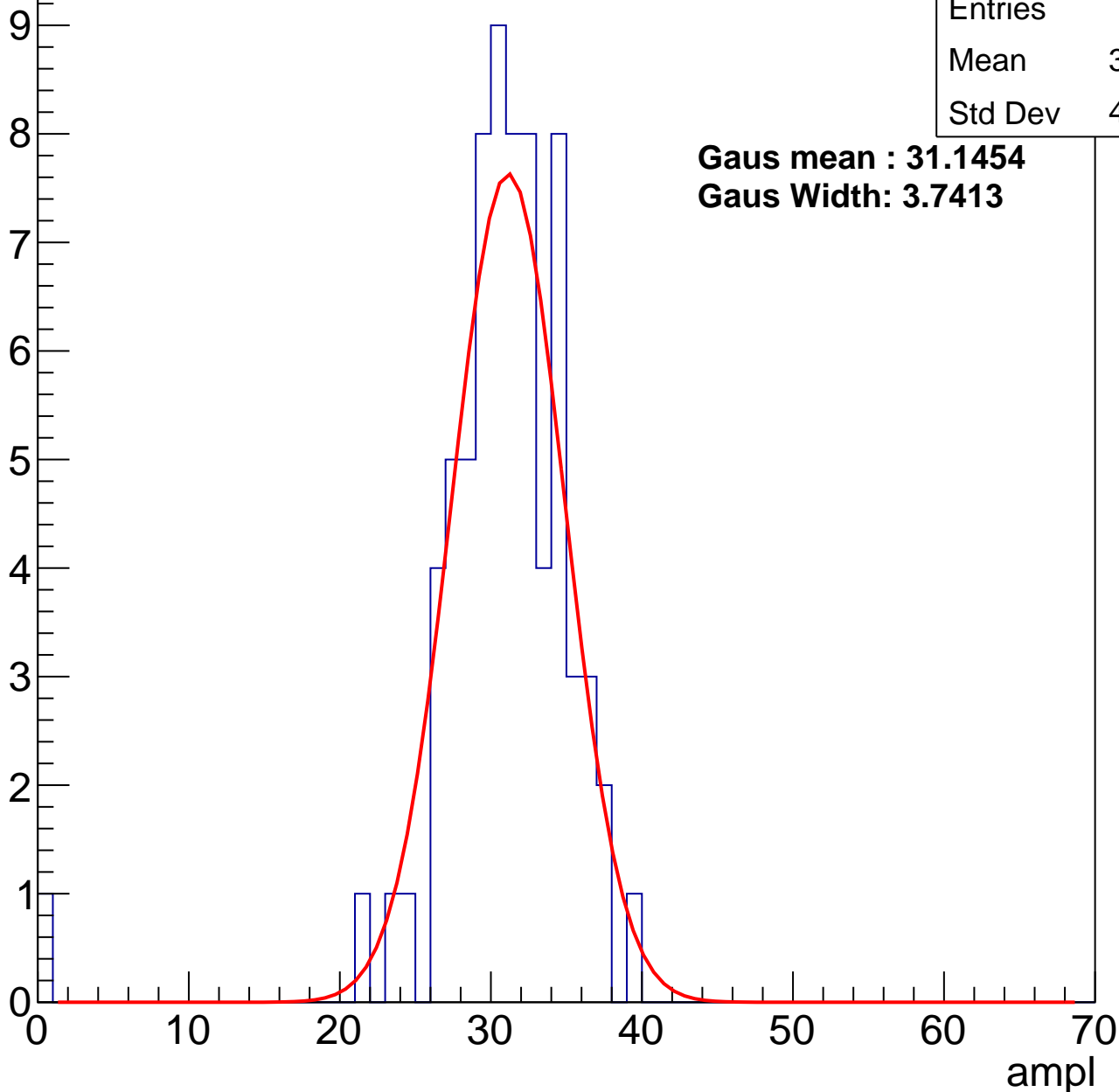
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.32
Std Dev	4.946

Gaus mean : 31.1454

Gaus Width: 3.7413



B1L101S, U12-ch16, adc1

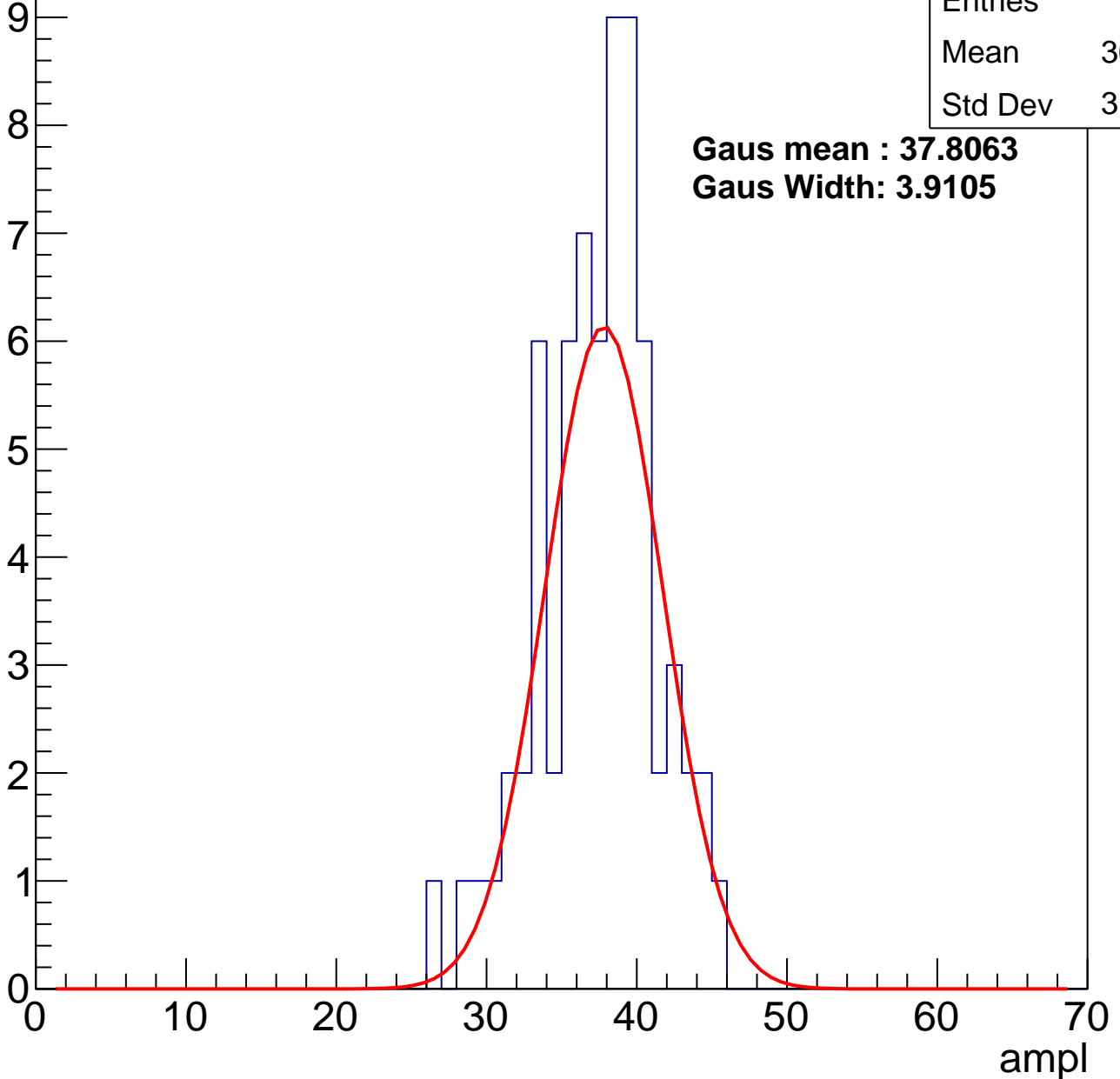
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.94
Std Dev	3.848

Gaus mean : 37.8063

Gaus Width: 3.9105



B1L101S, U12-ch16, adc2

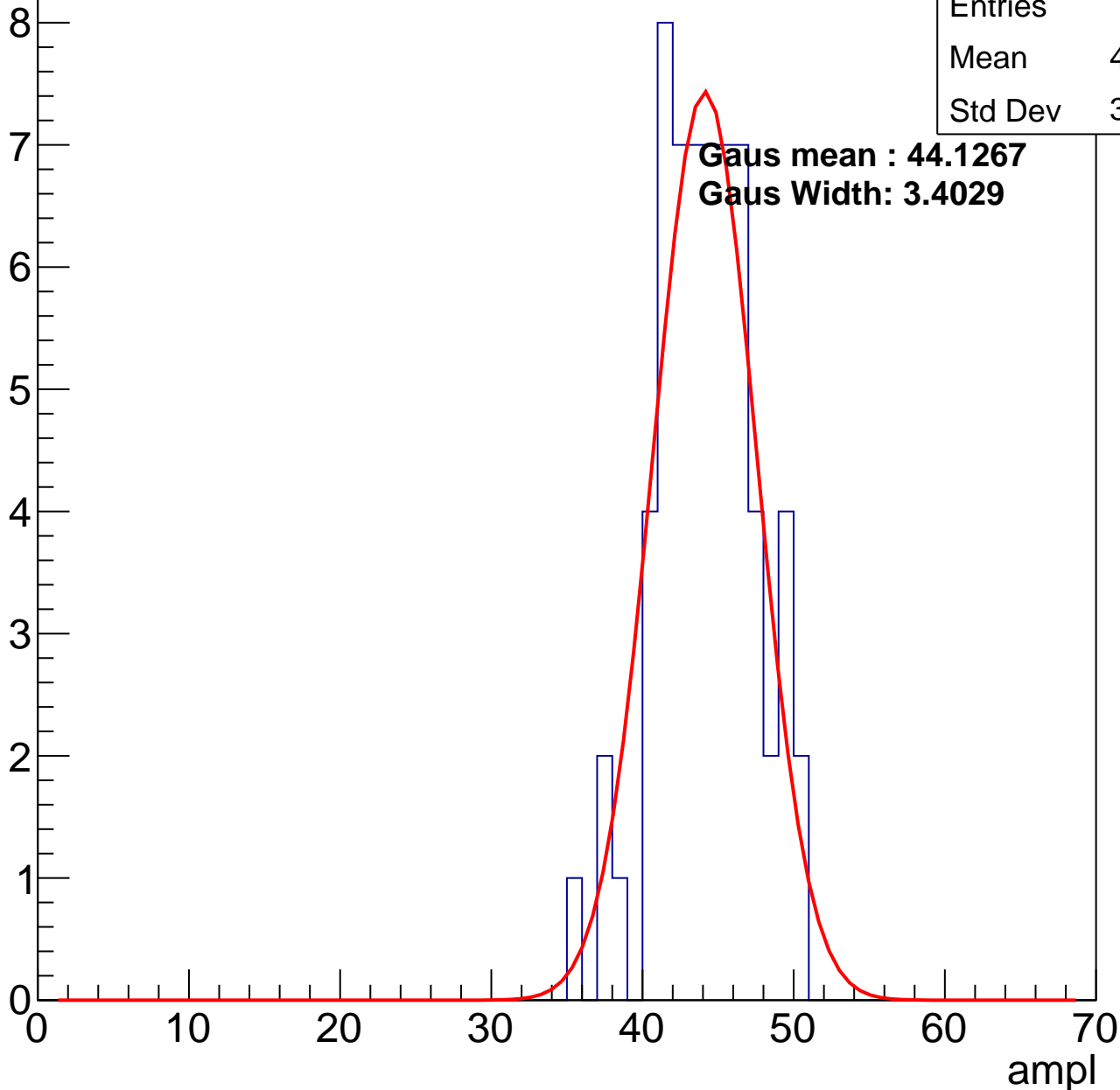
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.73
Std Dev	3.228

Gaus mean : 44.1267

Gaus Width: 3.4029

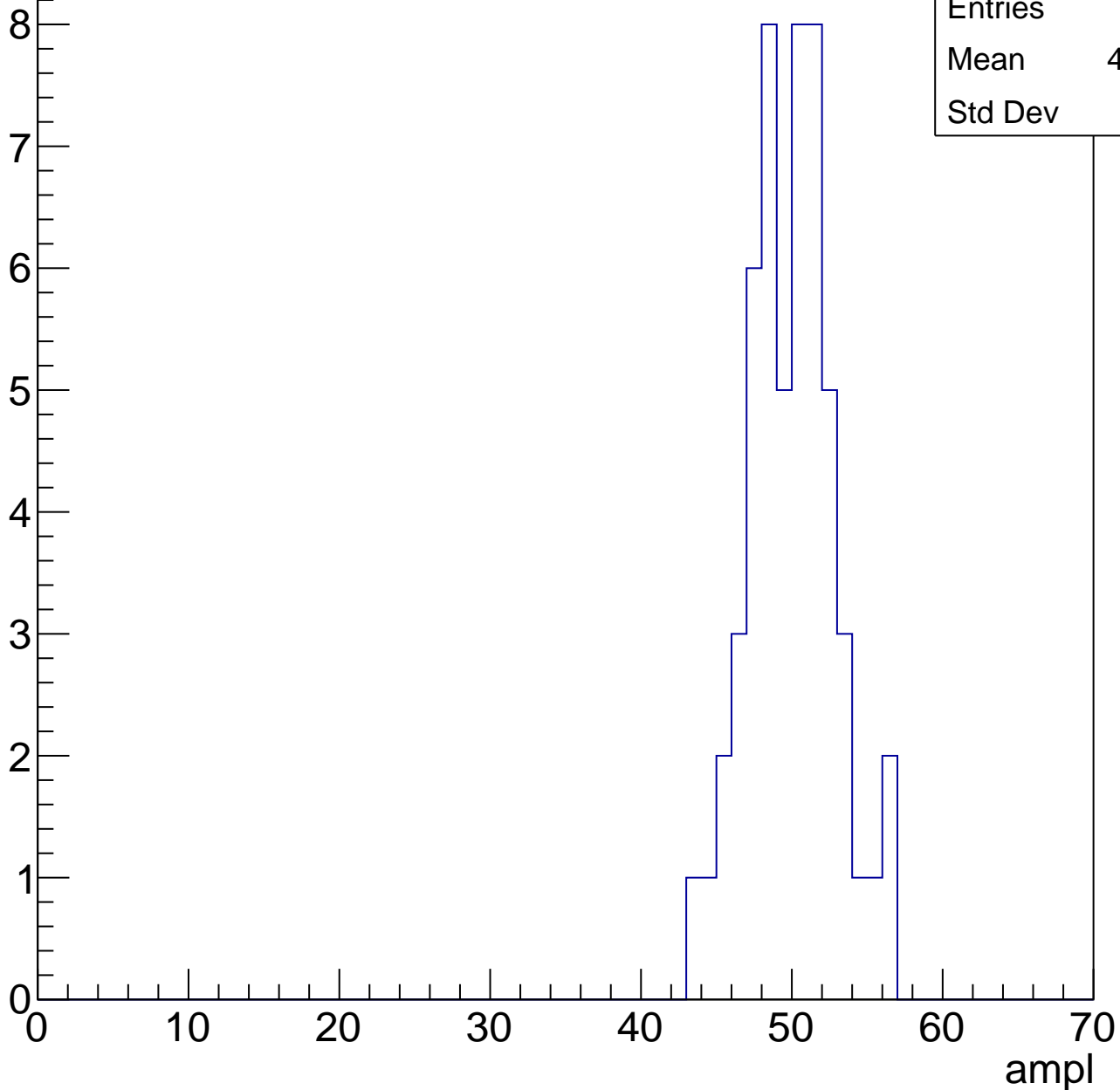


B1L101S, U12-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	49.52
Std Dev	2.82

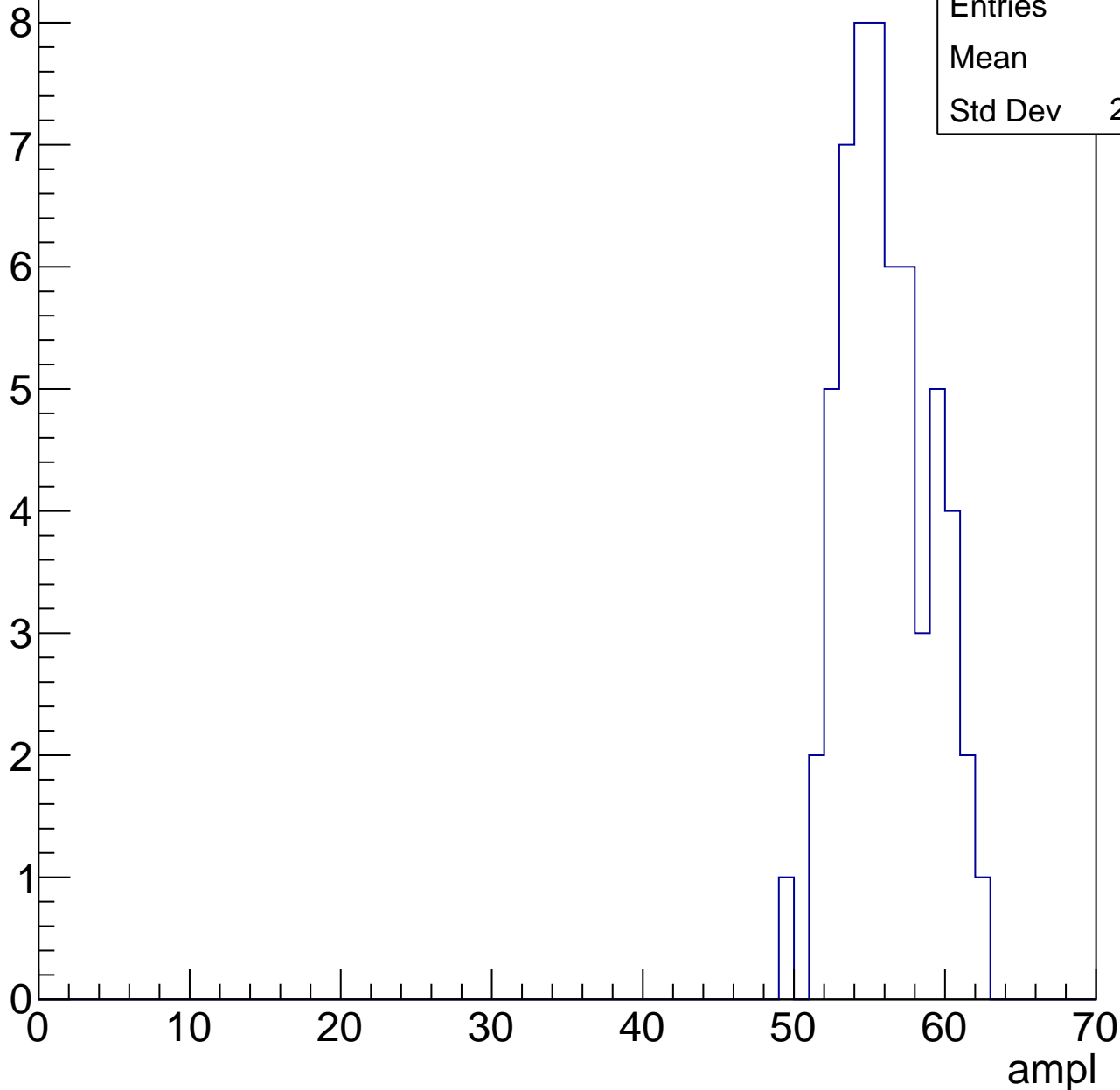


B1L101S, U12-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.6
Std Dev	2.894



B1L101S, U12-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 52

Mean 58.96

Std Dev 8.584

8

6

4

2

0

0

10

20

30

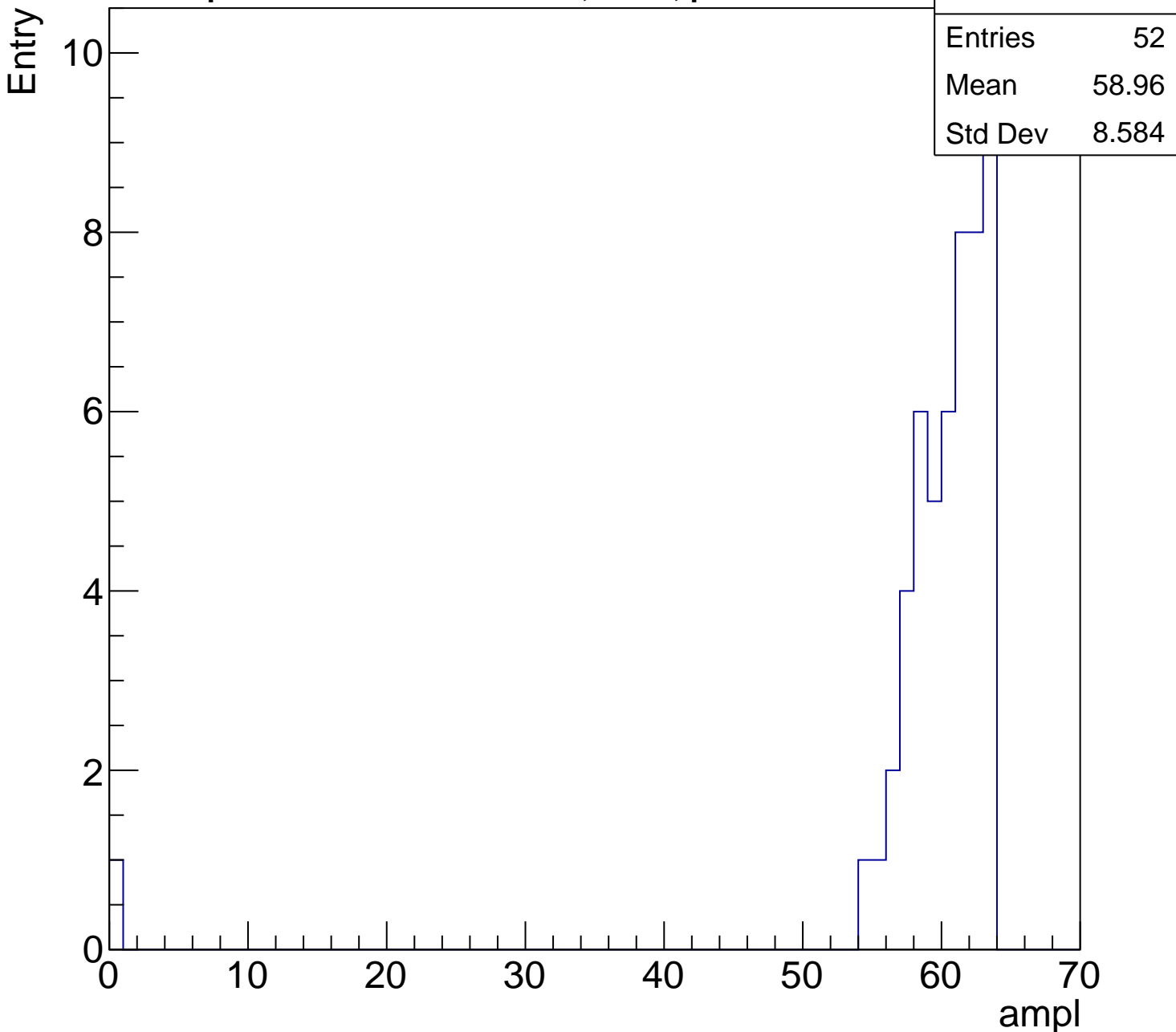
40

50

60

ampl

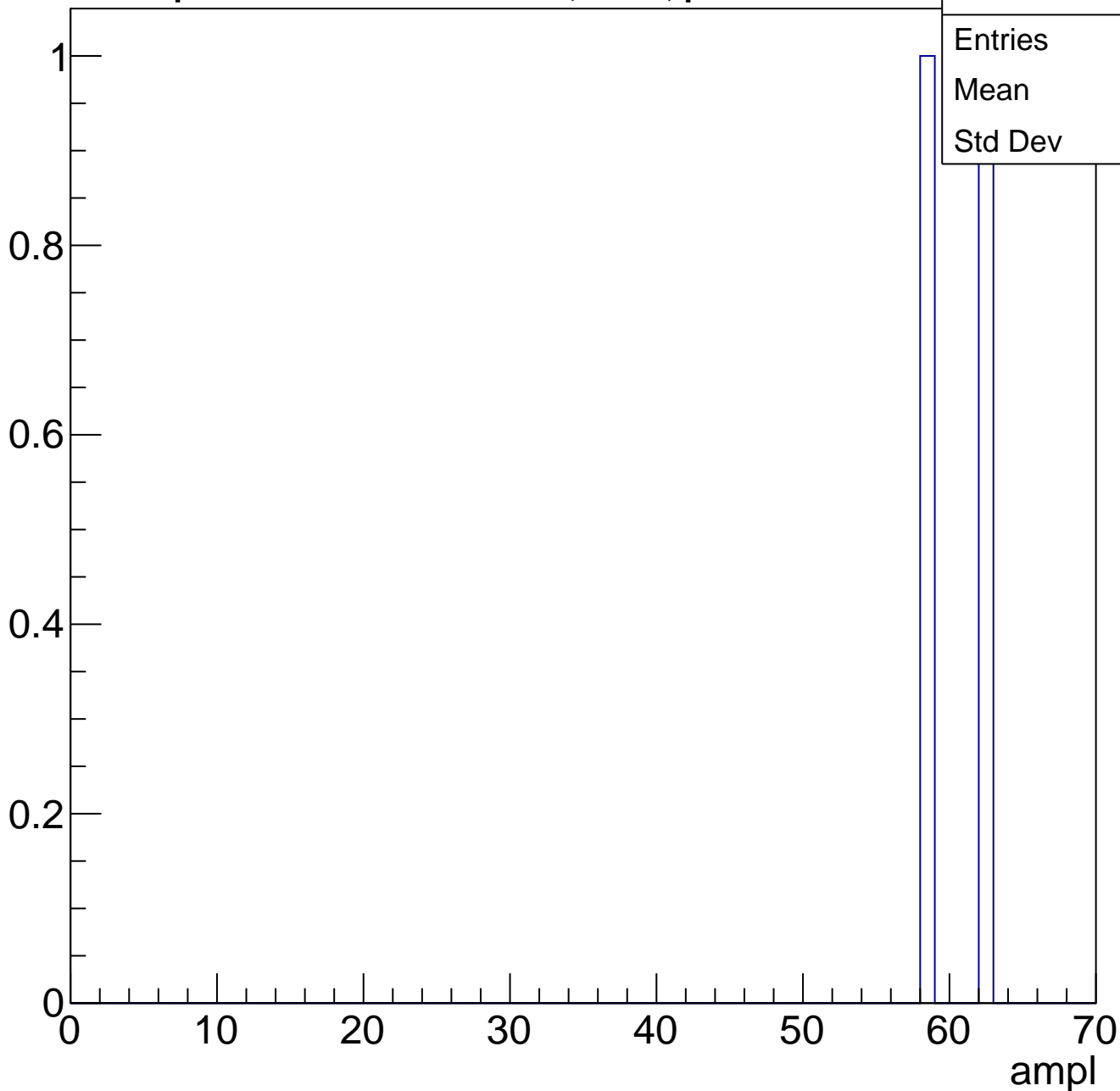
70



B1L101S, U12-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch17, adc0

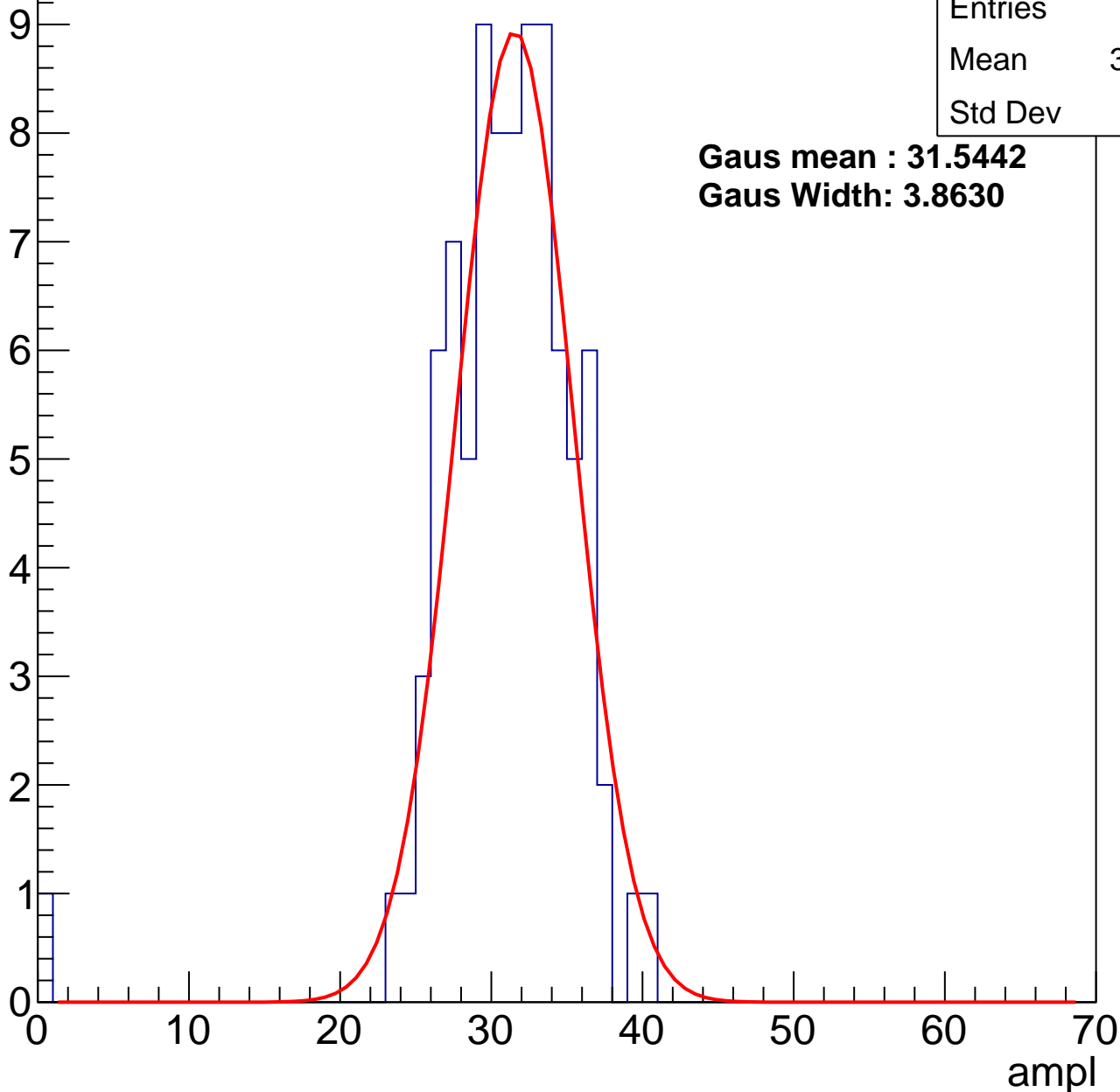
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	30.56
Std Dev	4.84

Gaus mean : 31.5442

Gaus Width: 3.8630



B1L101S, U12-ch17, adc1

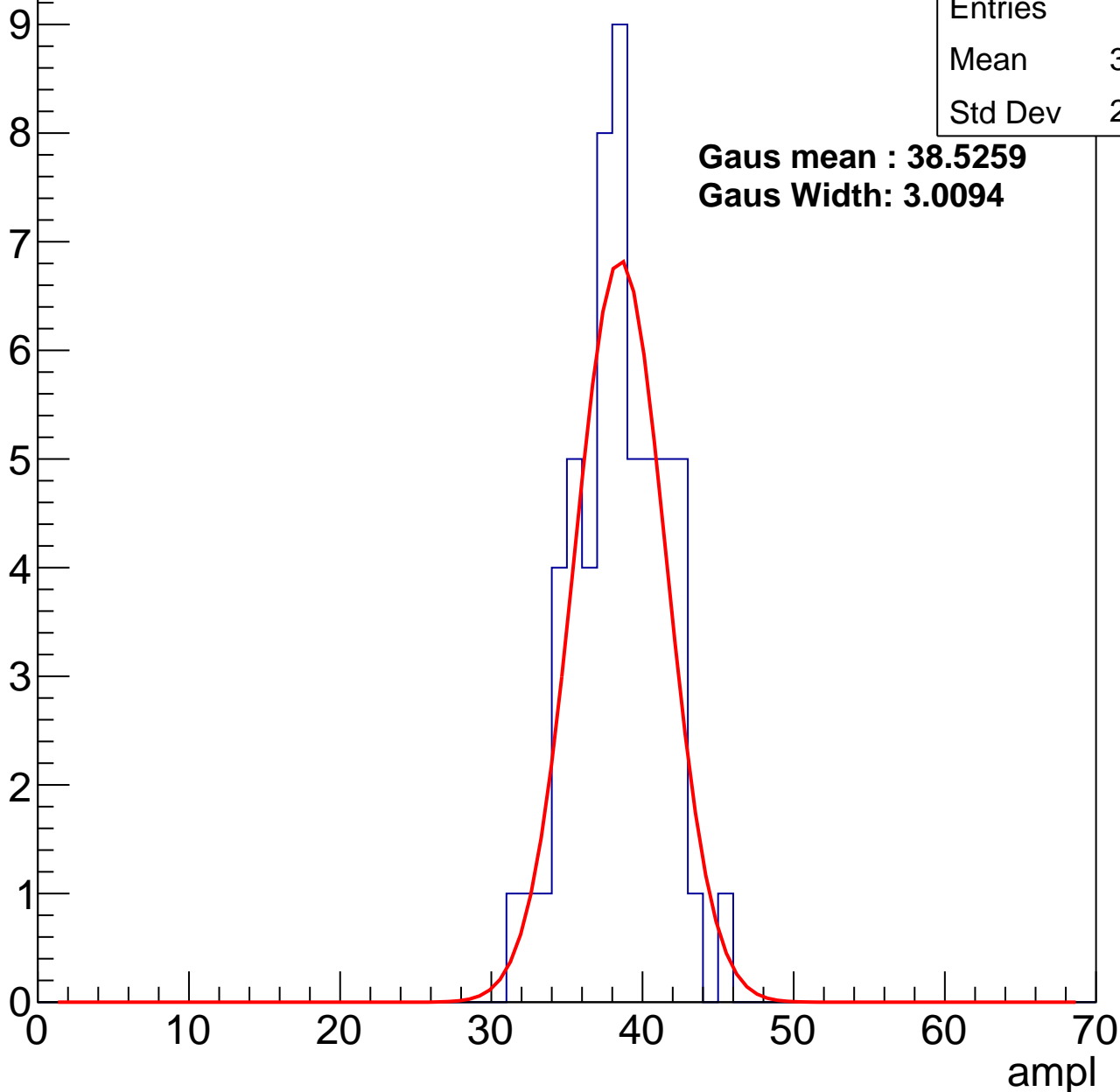
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.95
Std Dev	2.913

Gaus mean : 38.5259

Gaus Width: 3.0094



B1L101S, U12-ch17, adc2

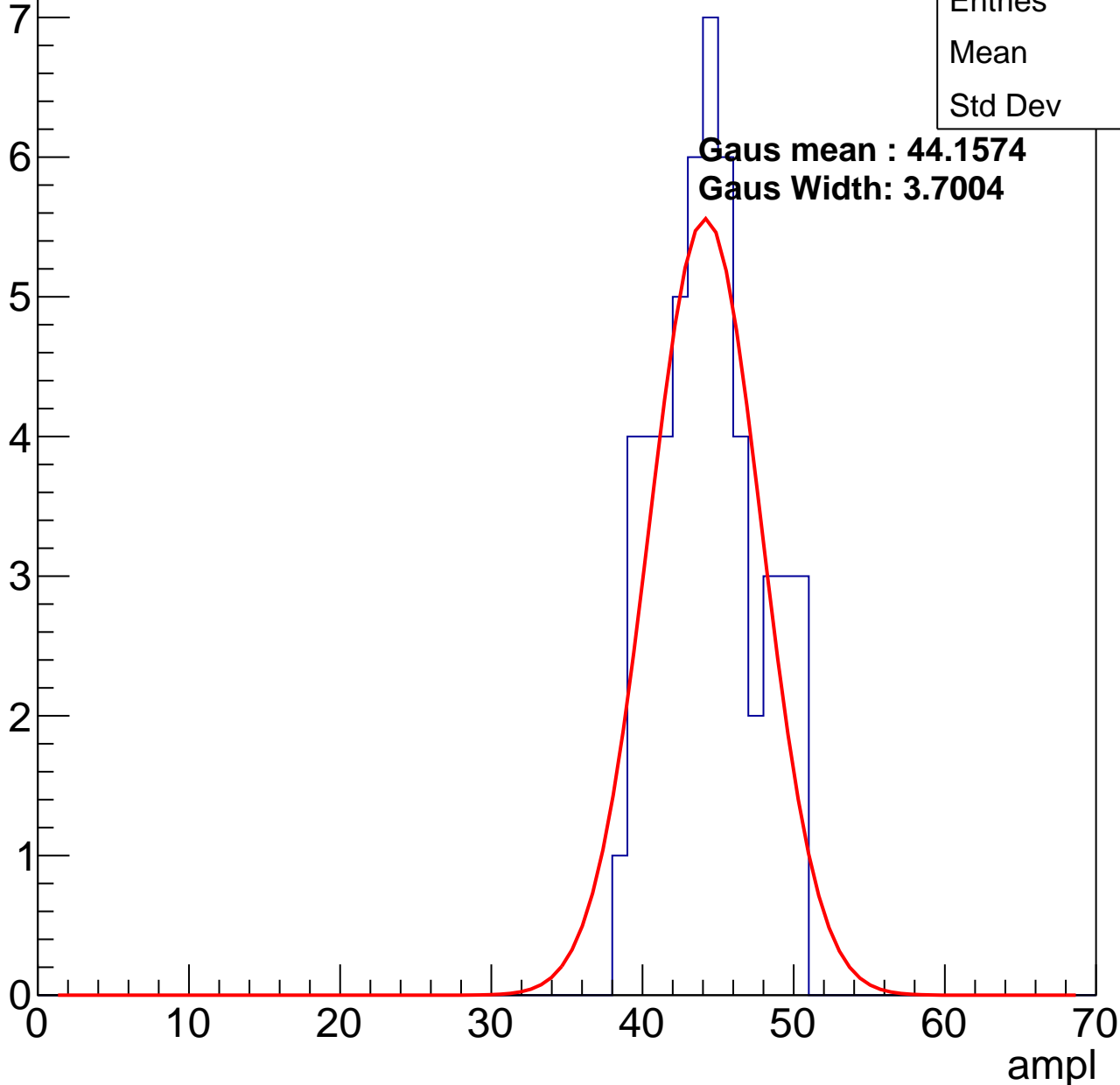
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.9
Std Dev	3.2

Gaus mean : 44.1574

Gaus Width: 3.7004

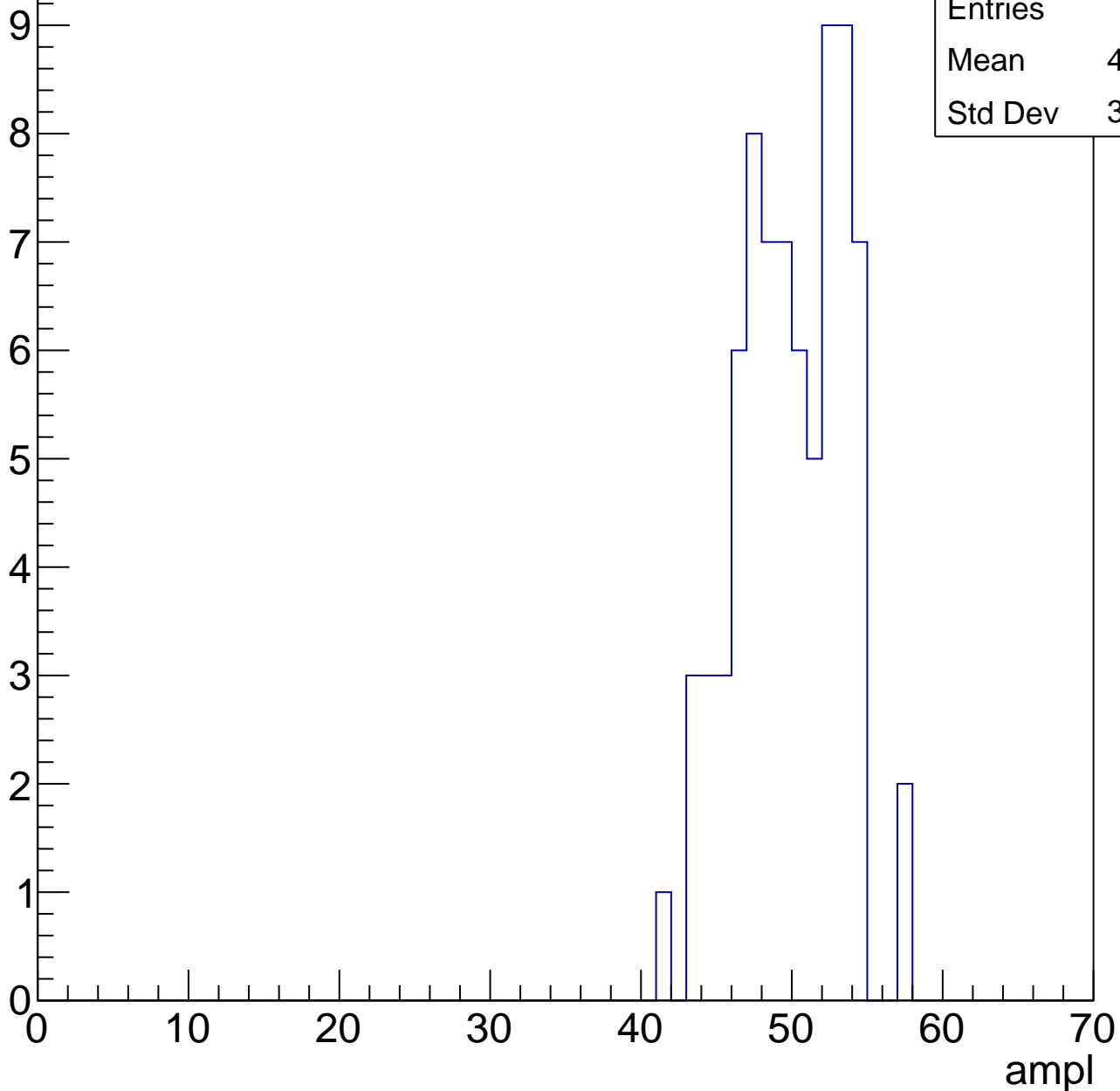


B1L101S, U12-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	49.47
Std Dev	3.489

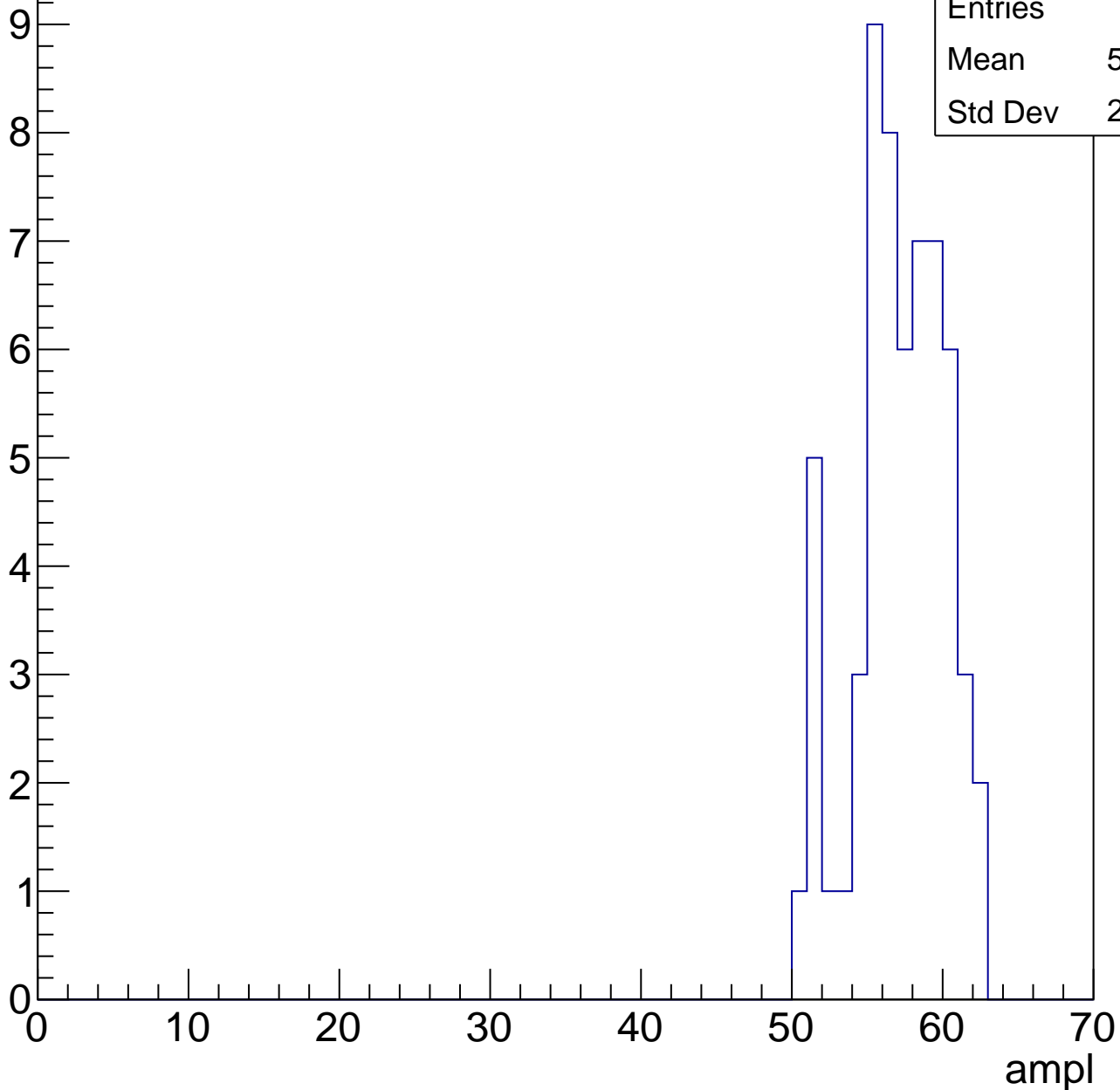


B1L101S, U12-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.66
Std Dev	2.972

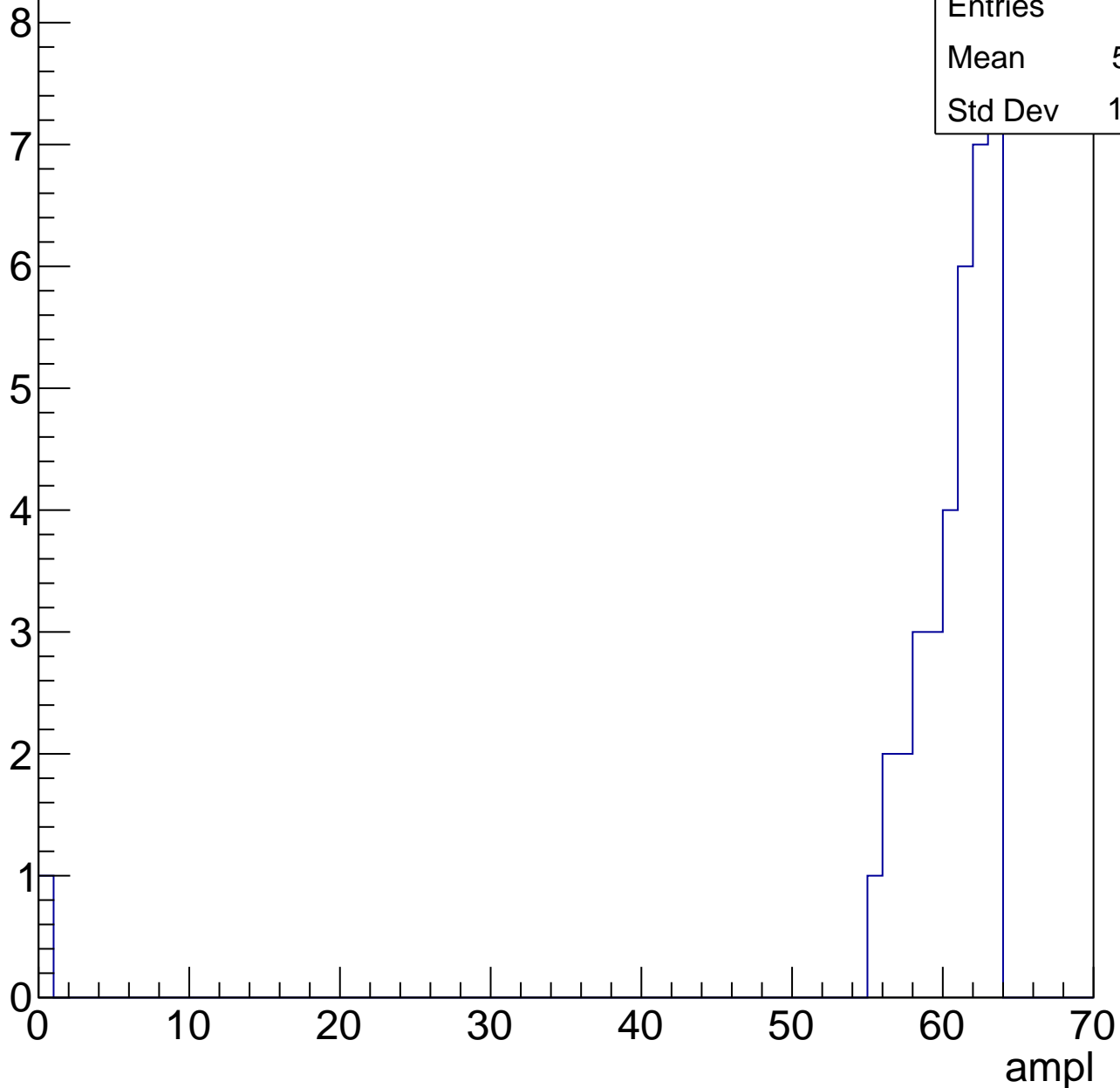


B1L101S, U12-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.81
Std Dev	10.06



B1L101S, U12-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U12-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch18, adc0

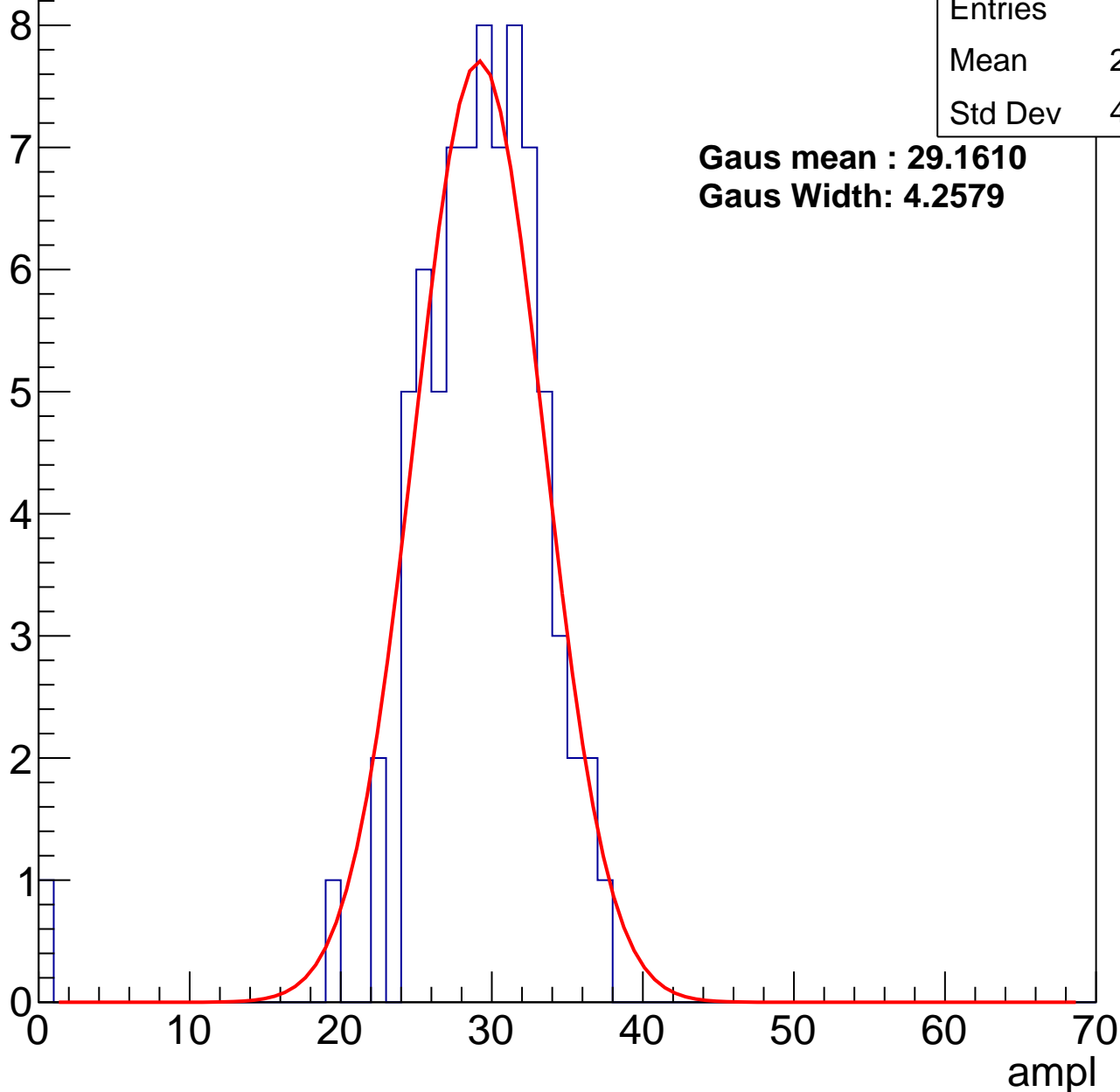
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.68
Std Dev	4.866

Gaus mean : 29.1610

Gaus Width: 4.2579



B1L101S, U12-ch18, adc1

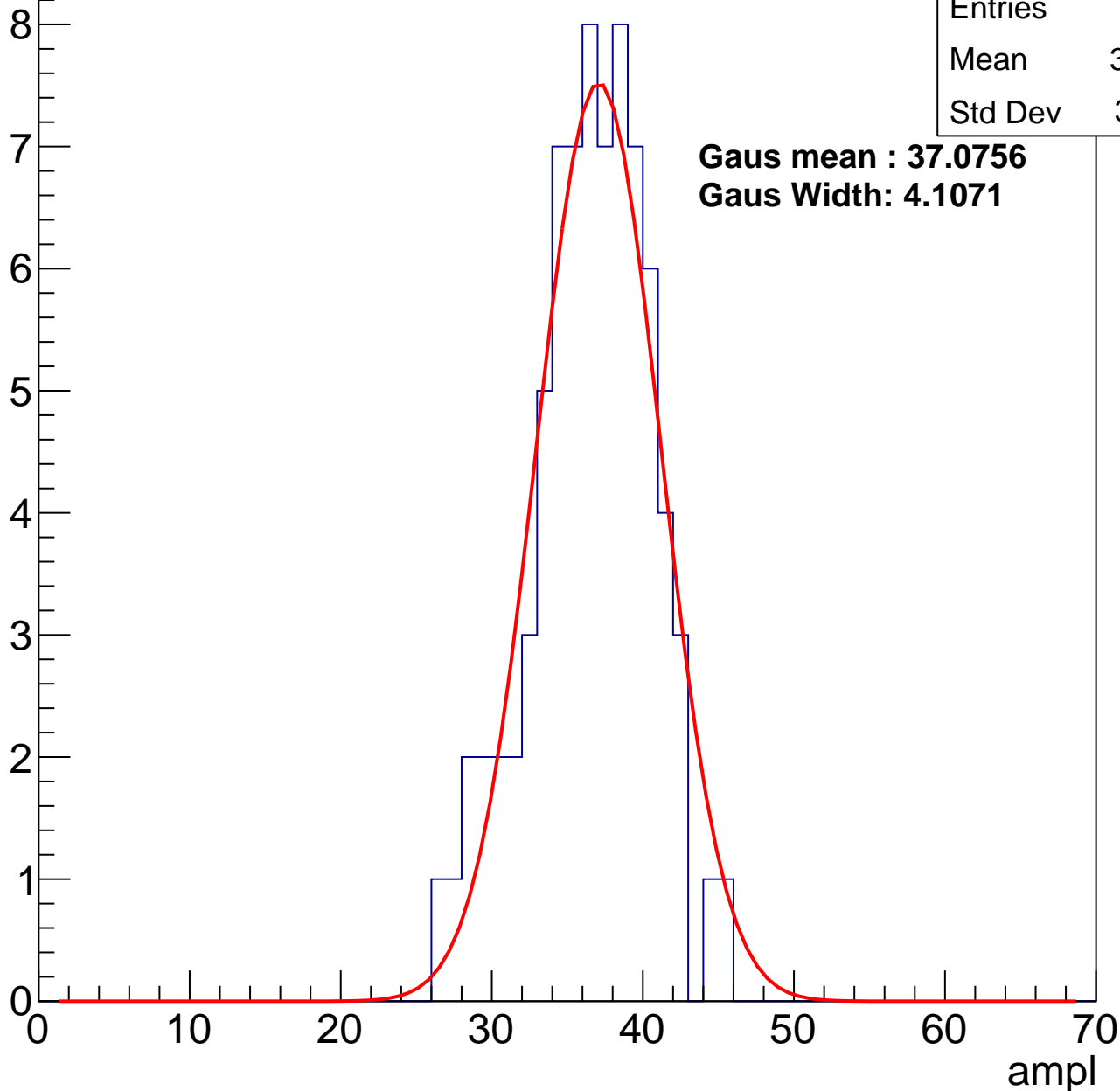
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.05
Std Dev	3.961

Gaus mean : 37.0756

Gaus Width: 4.1071



B1L101S, U12-ch18, adc2

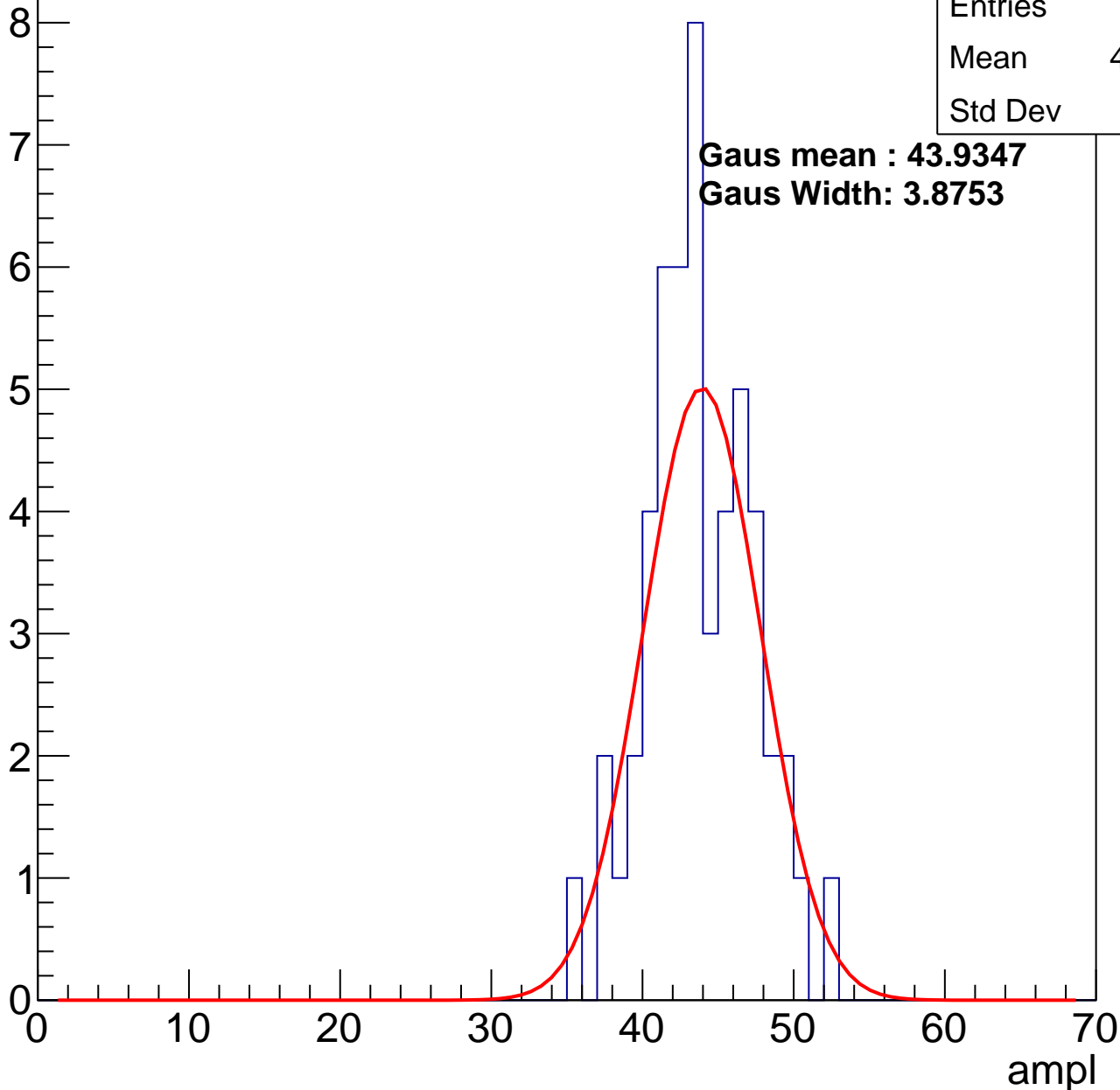
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	43.33
Std Dev	3.49

Gaus mean : 43.9347

Gaus Width: 3.8753

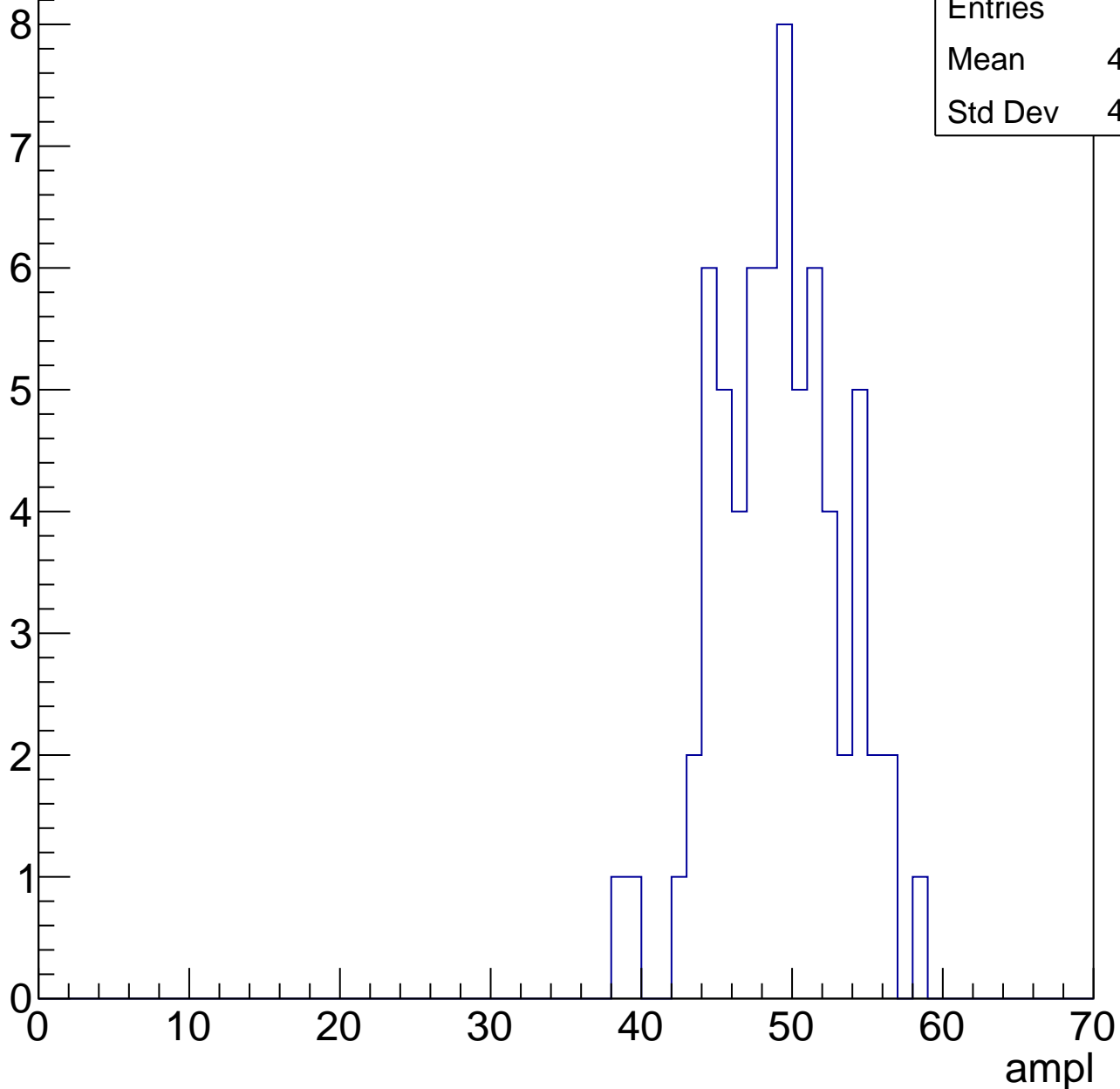


B1L101S, U12-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	48.66
Std Dev	4.058

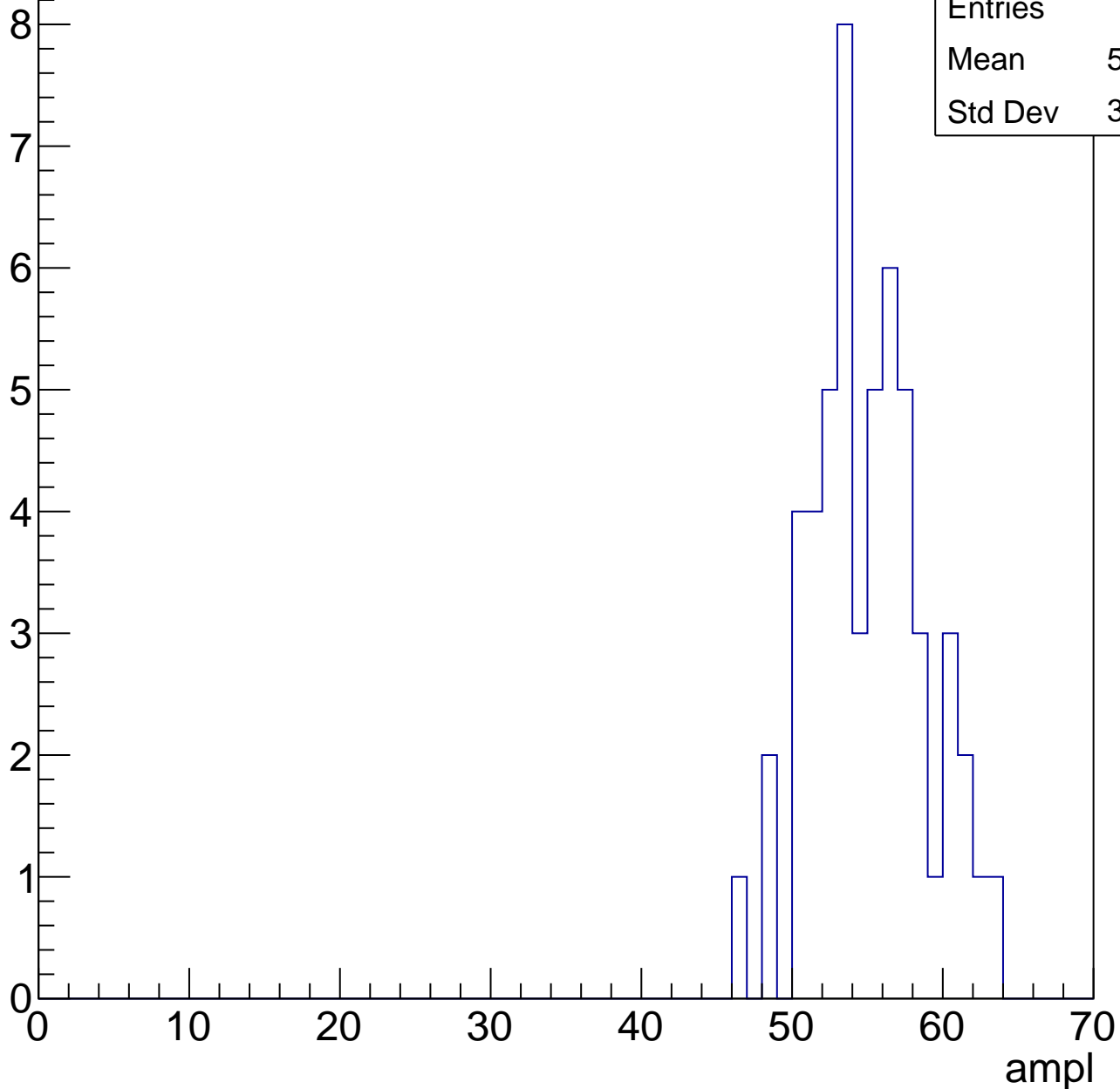


B1L101S, U12-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

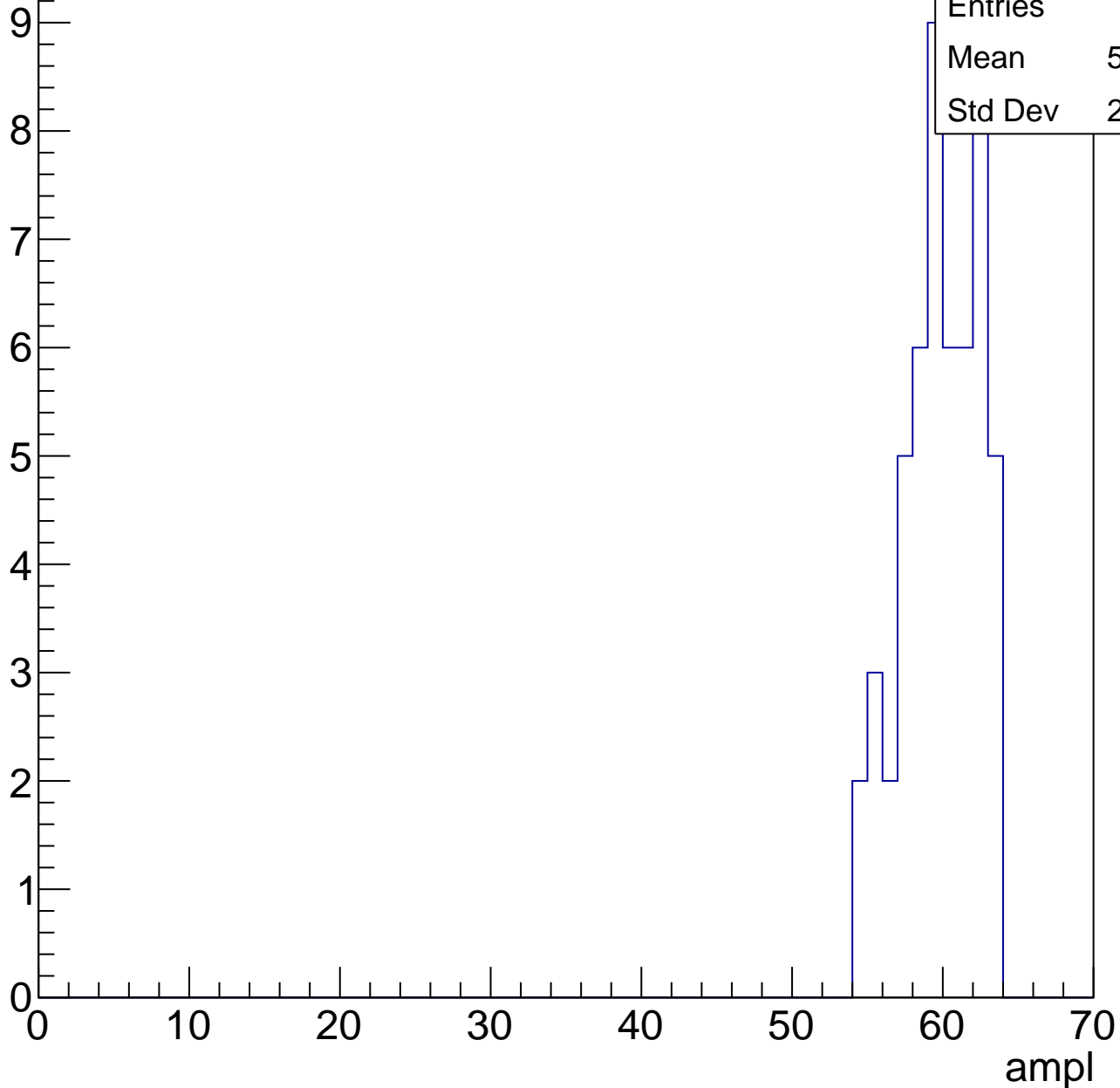
Entries	54
Mean	54.59
Std Dev	3.709



B1L101S, U12-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

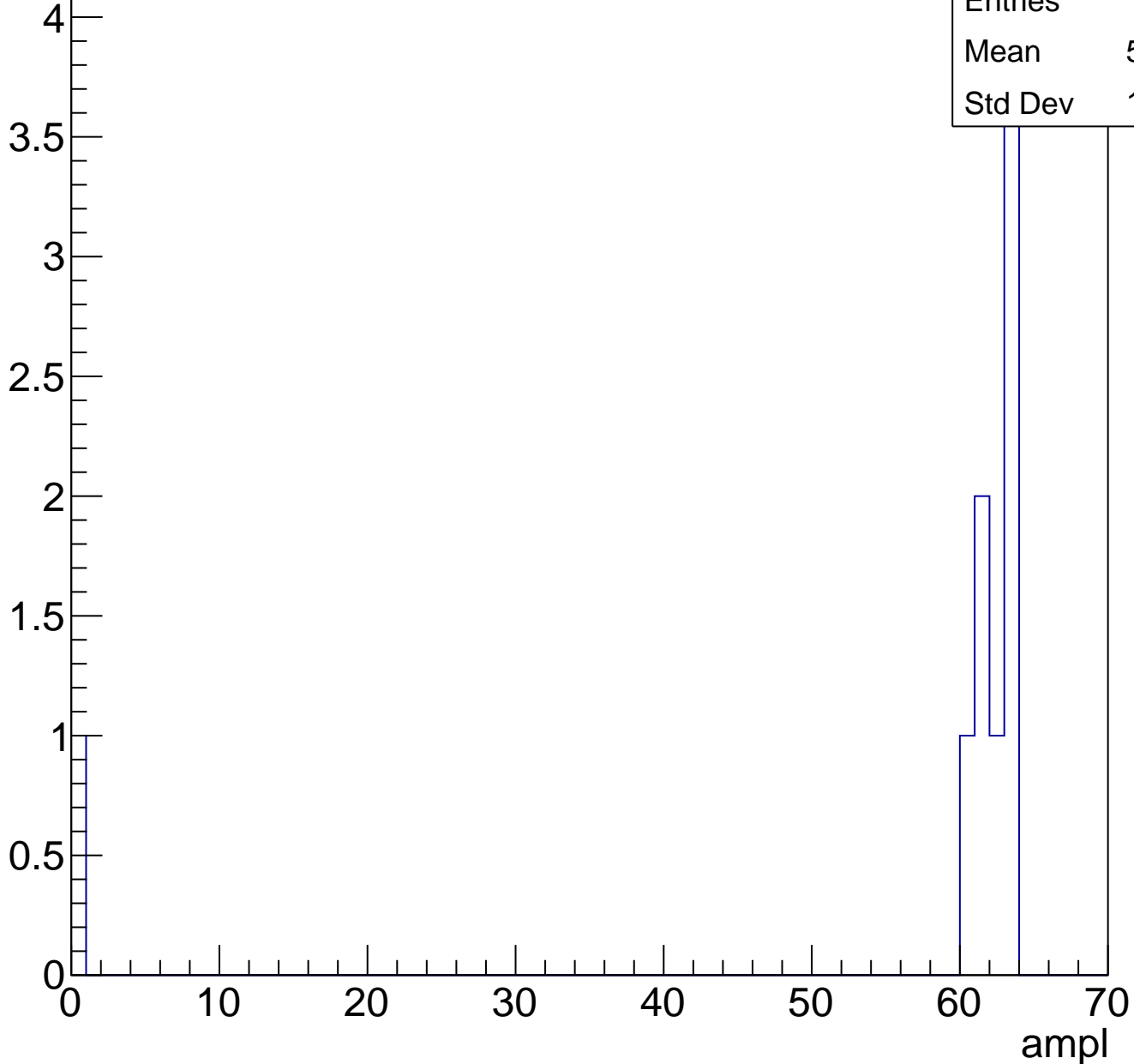
Entry



B1L101S, U12-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch19, adc0

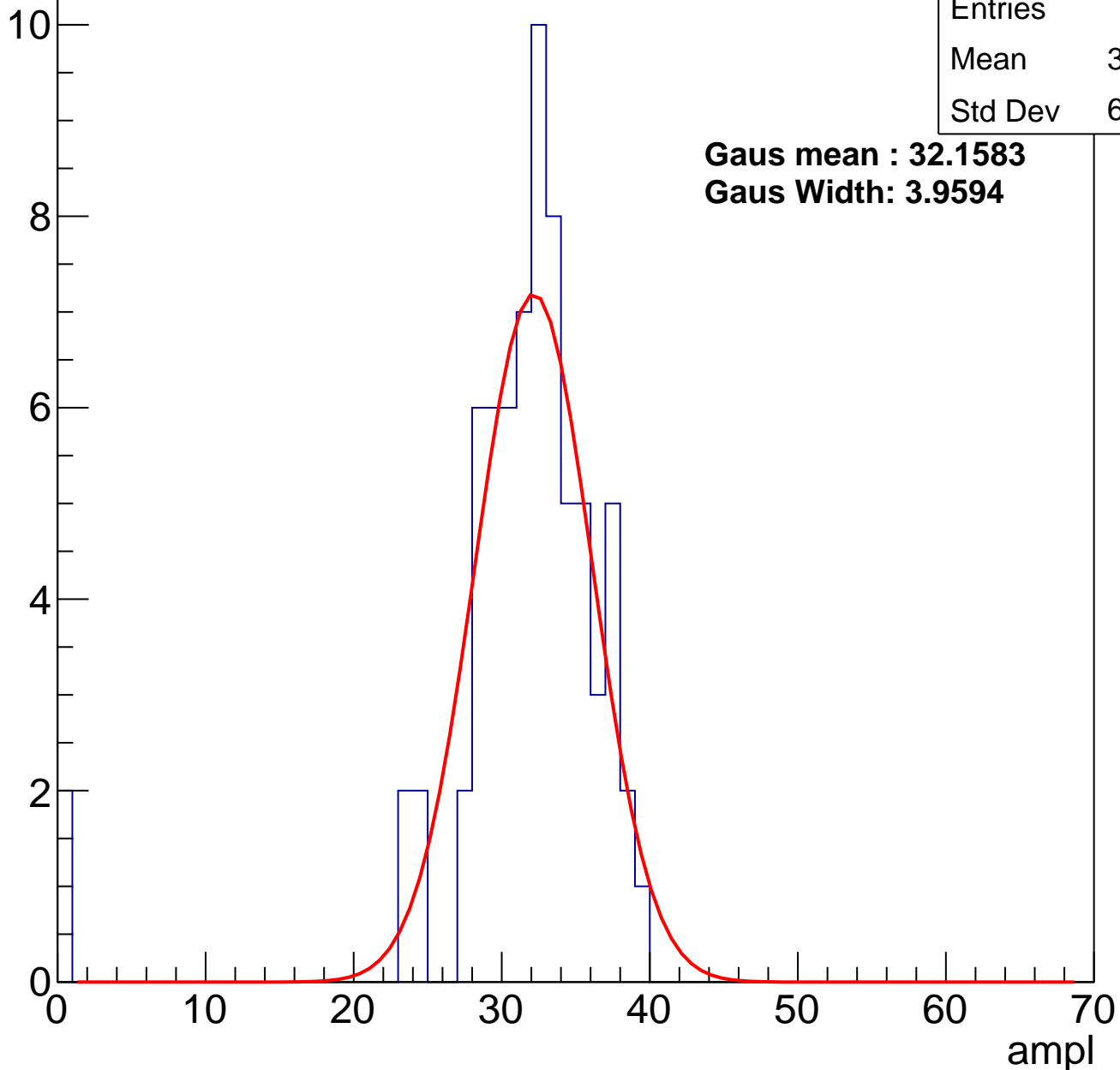
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	30.89
Std Dev	6.288

Gaus mean : 32.1583

Gaus Width: 3.9594

Entry



B1L101S, U12-ch19, adc1

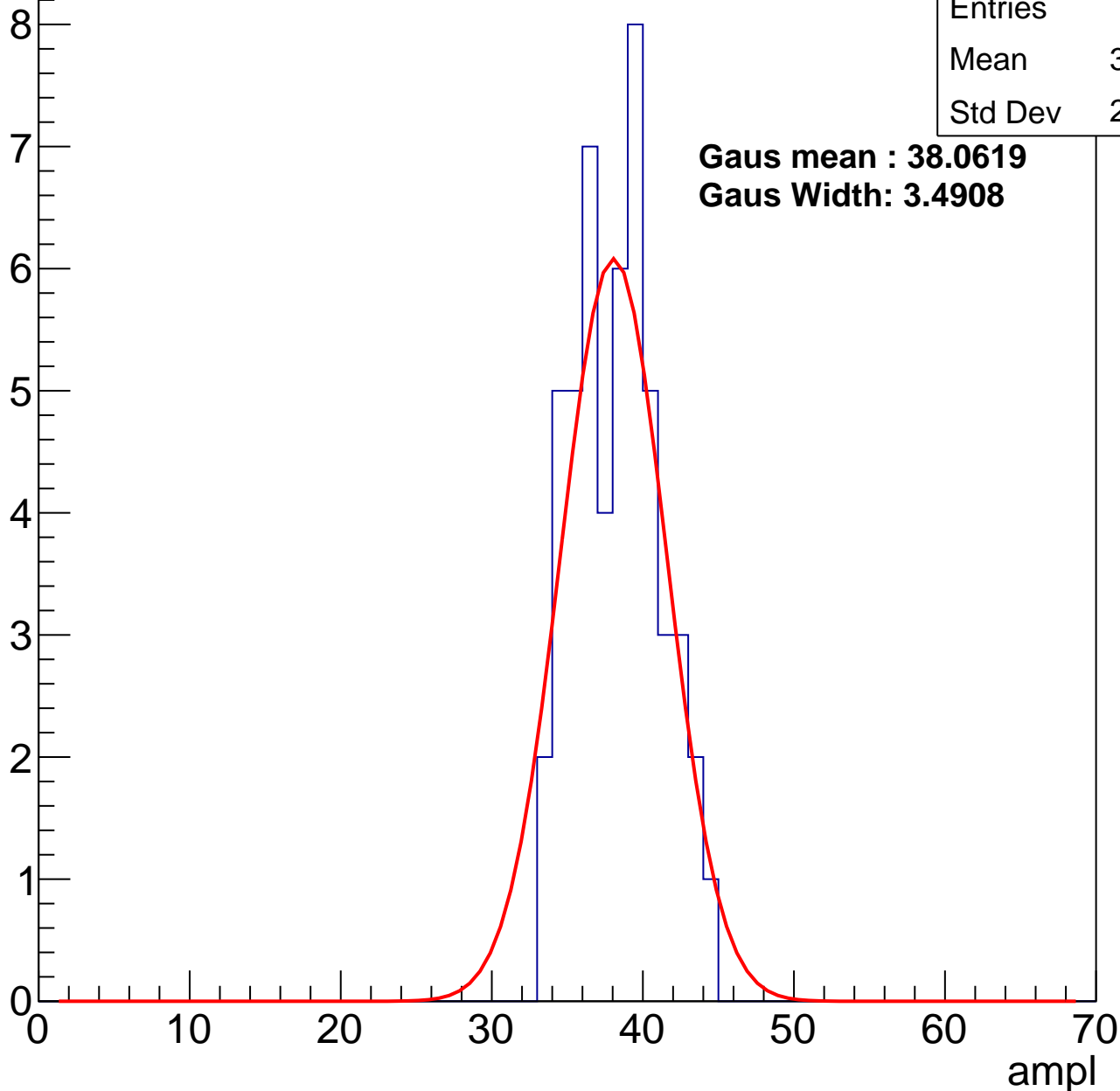
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	37.84
Std Dev	2.782

Gaus mean : 38.0619

Gaus Width: 3.4908



B1L101S, U12-ch19, adc2

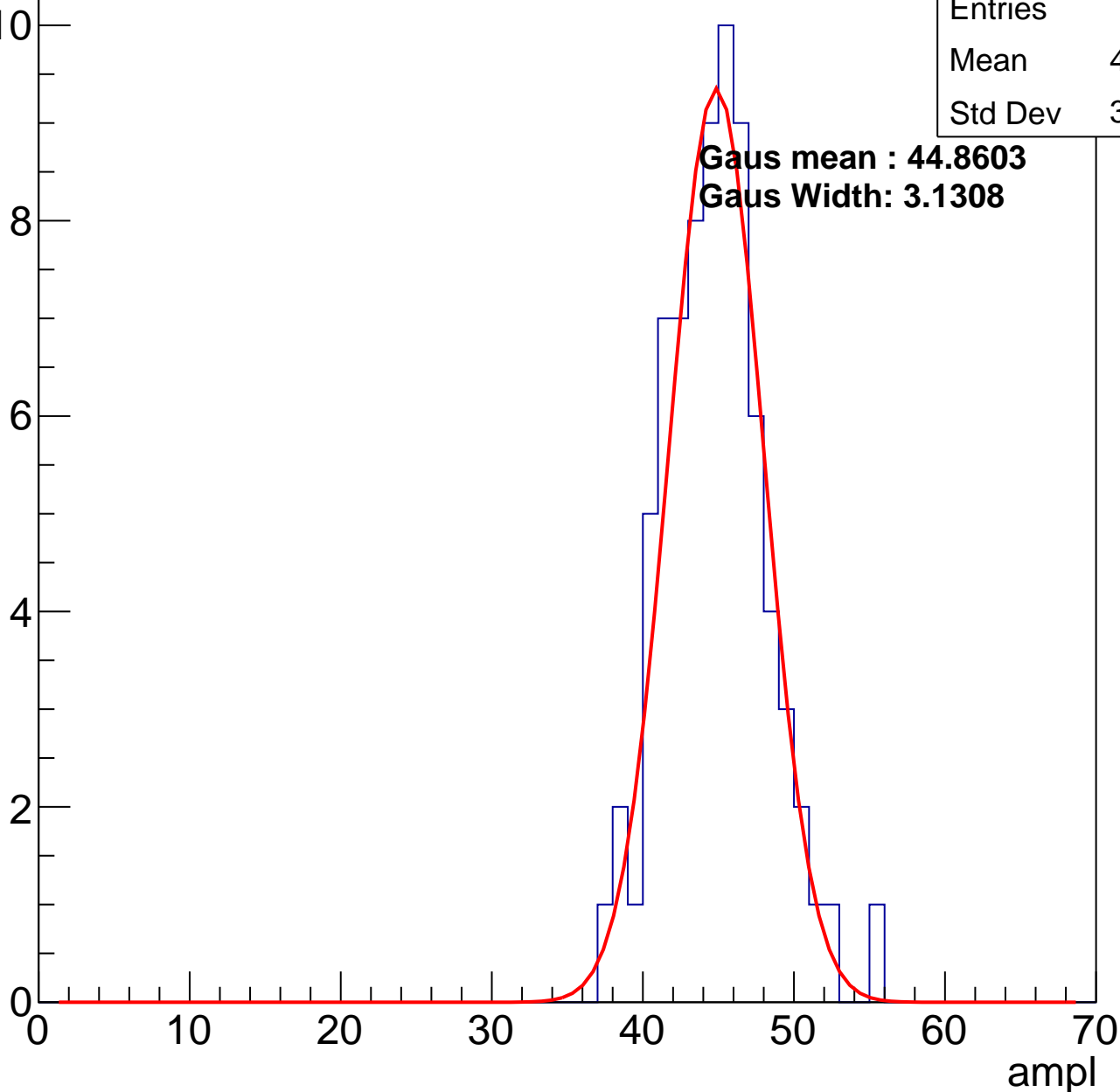
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44.36
Std Dev	3.342

Gaus mean : 44.8603

Gaus Width: 3.1308

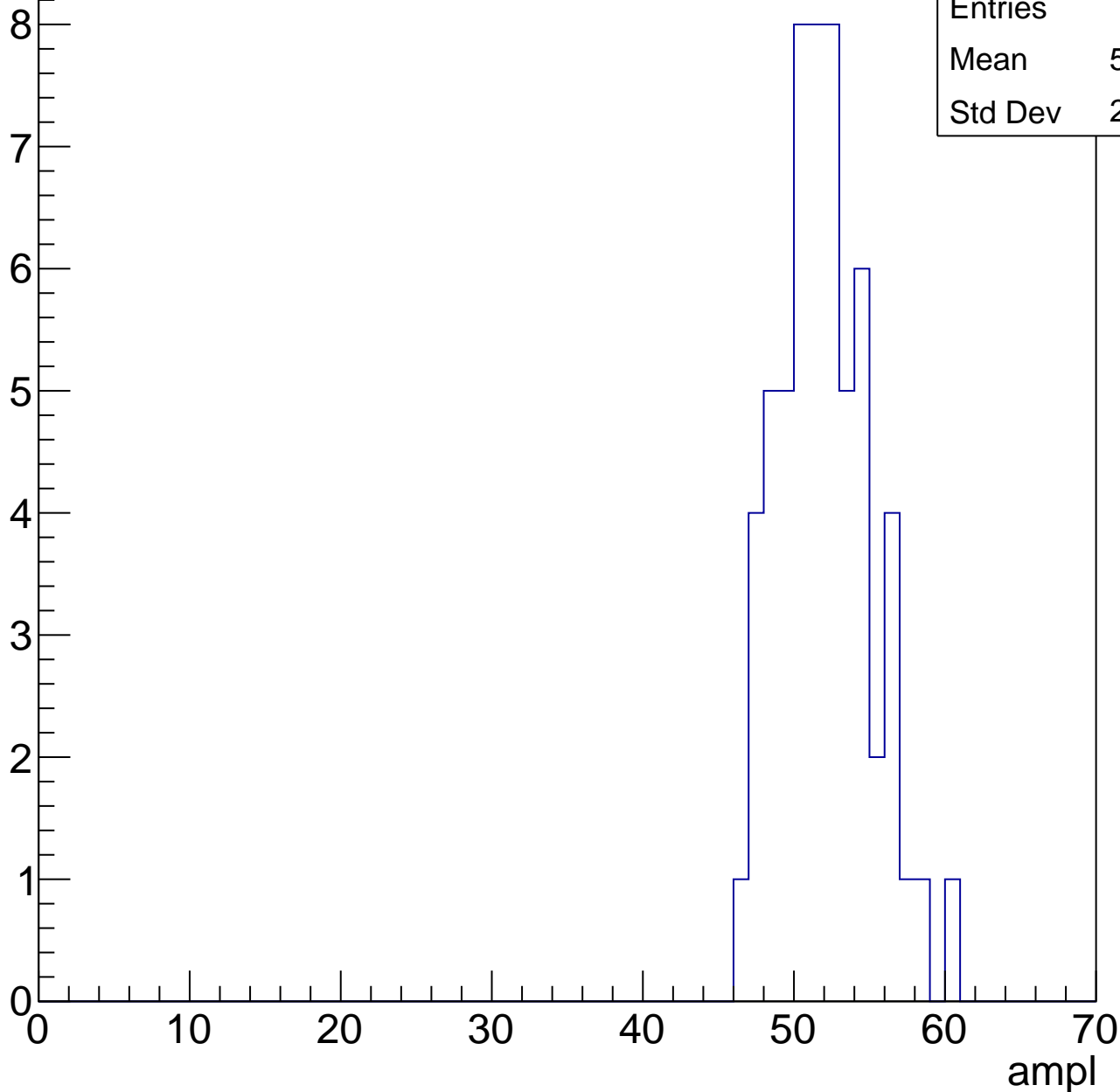


B1L101S, U12-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	51.54
Std Dev	2.982

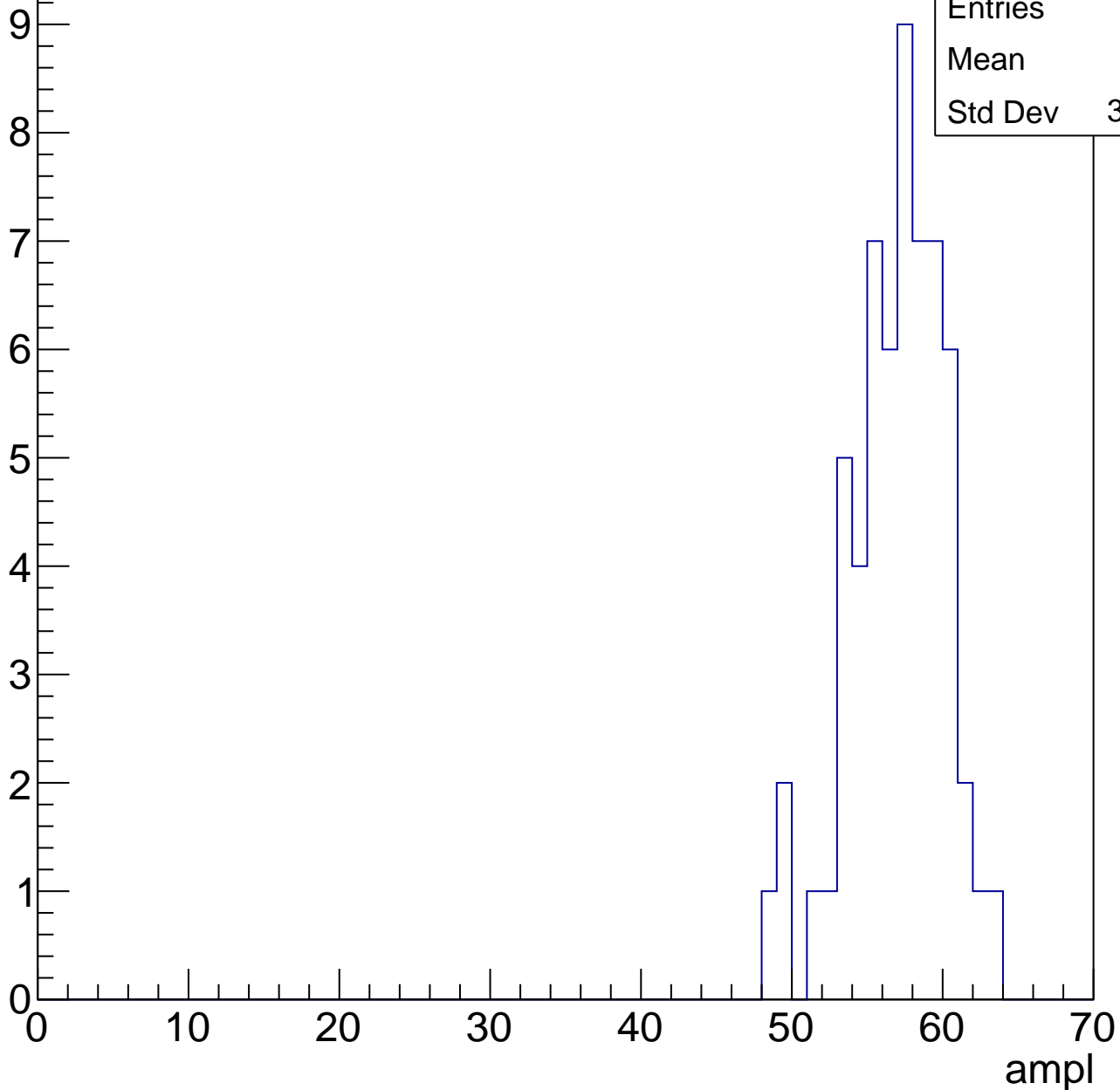


B1L101S, U12-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

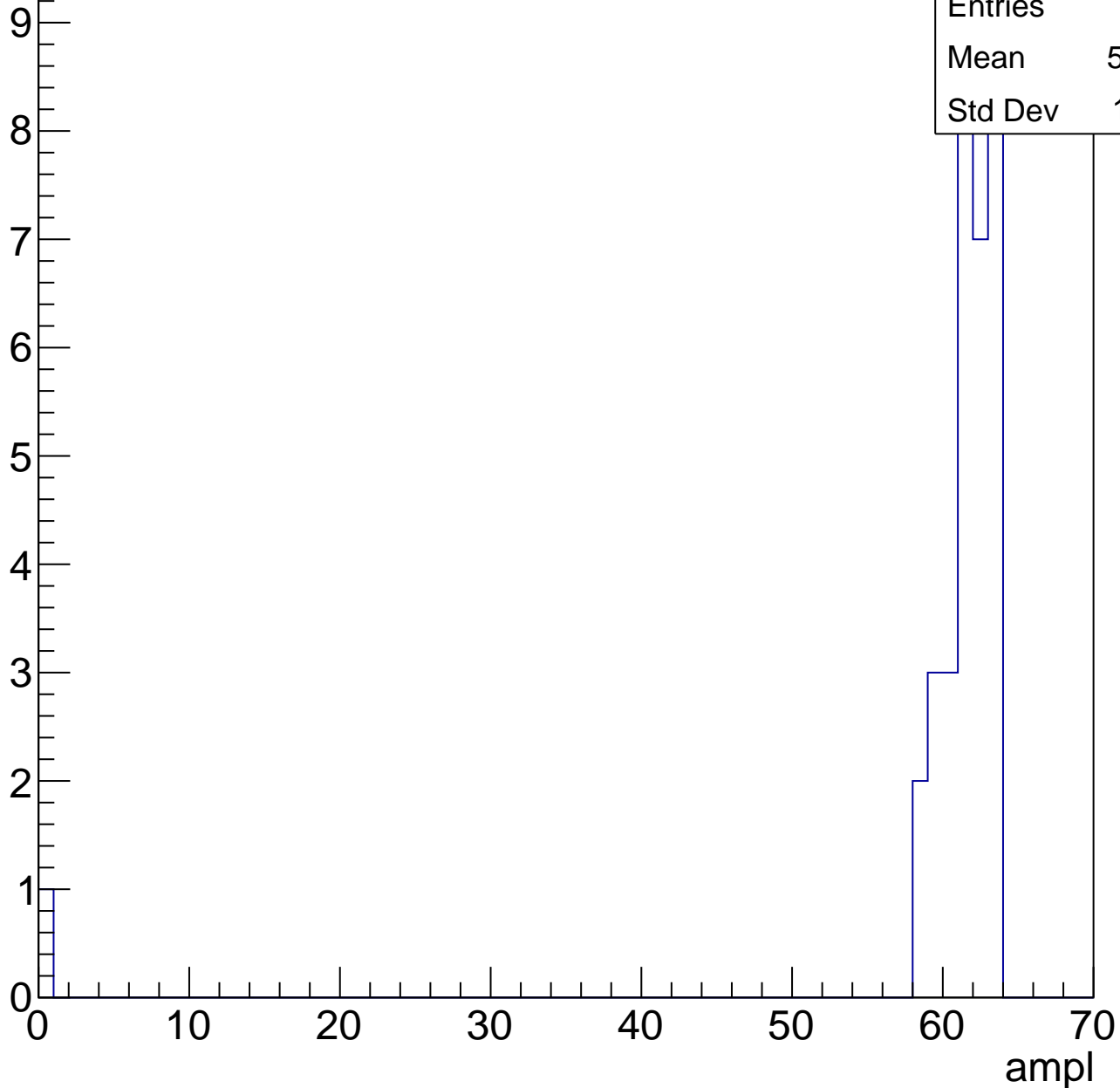
Entries	60
Mean	56.5
Std Dev	3.128



B1L101S, U12-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	33
Mean	59.45
Std Dev	10.61

B1L101S, U12-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L101S, U12-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch20, adc0

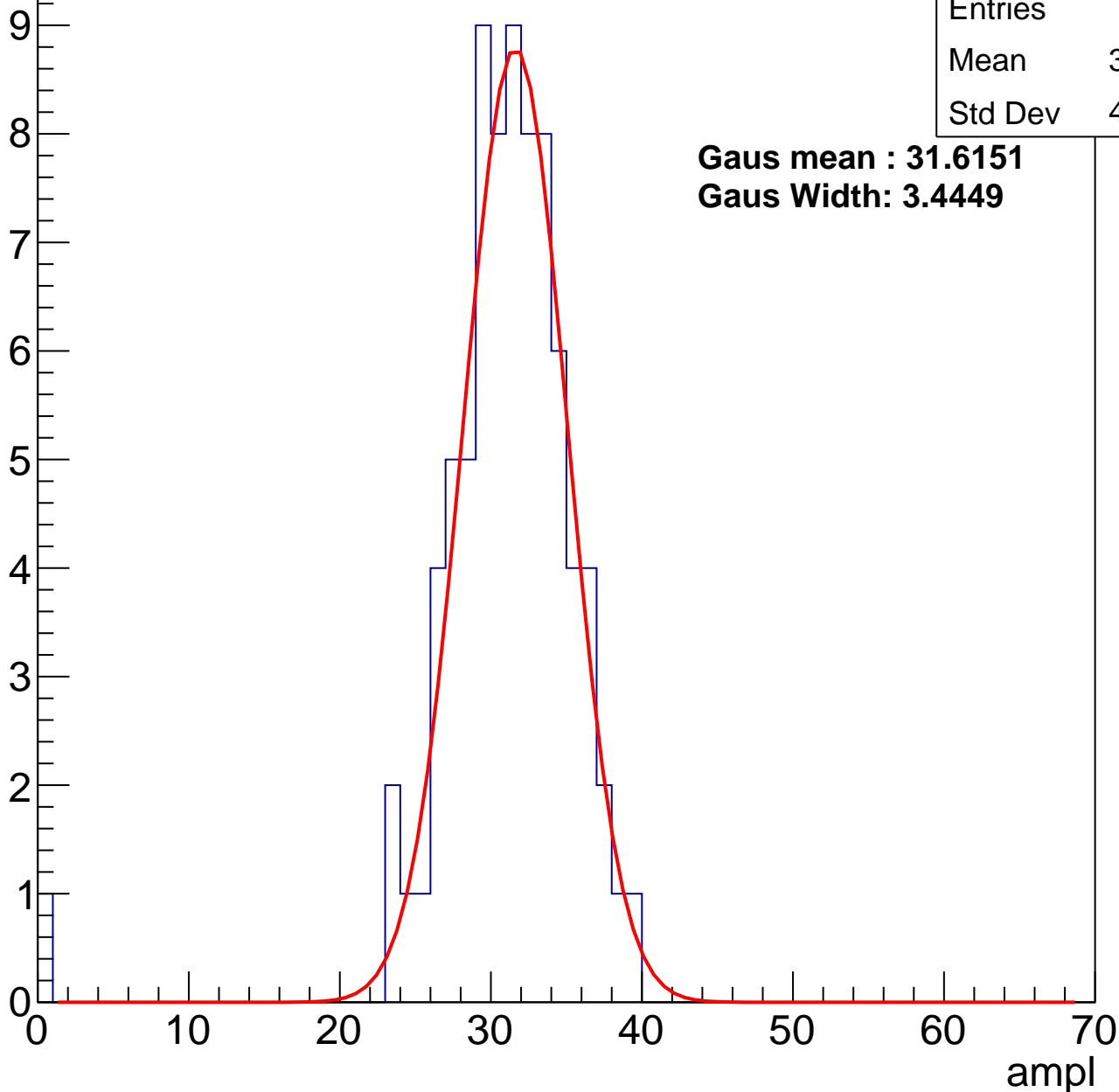
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	30.54
Std Dev	4.865

Gaus mean : 31.6151

Gaus Width: 3.4449



B1L101S, U12-ch20, adc1

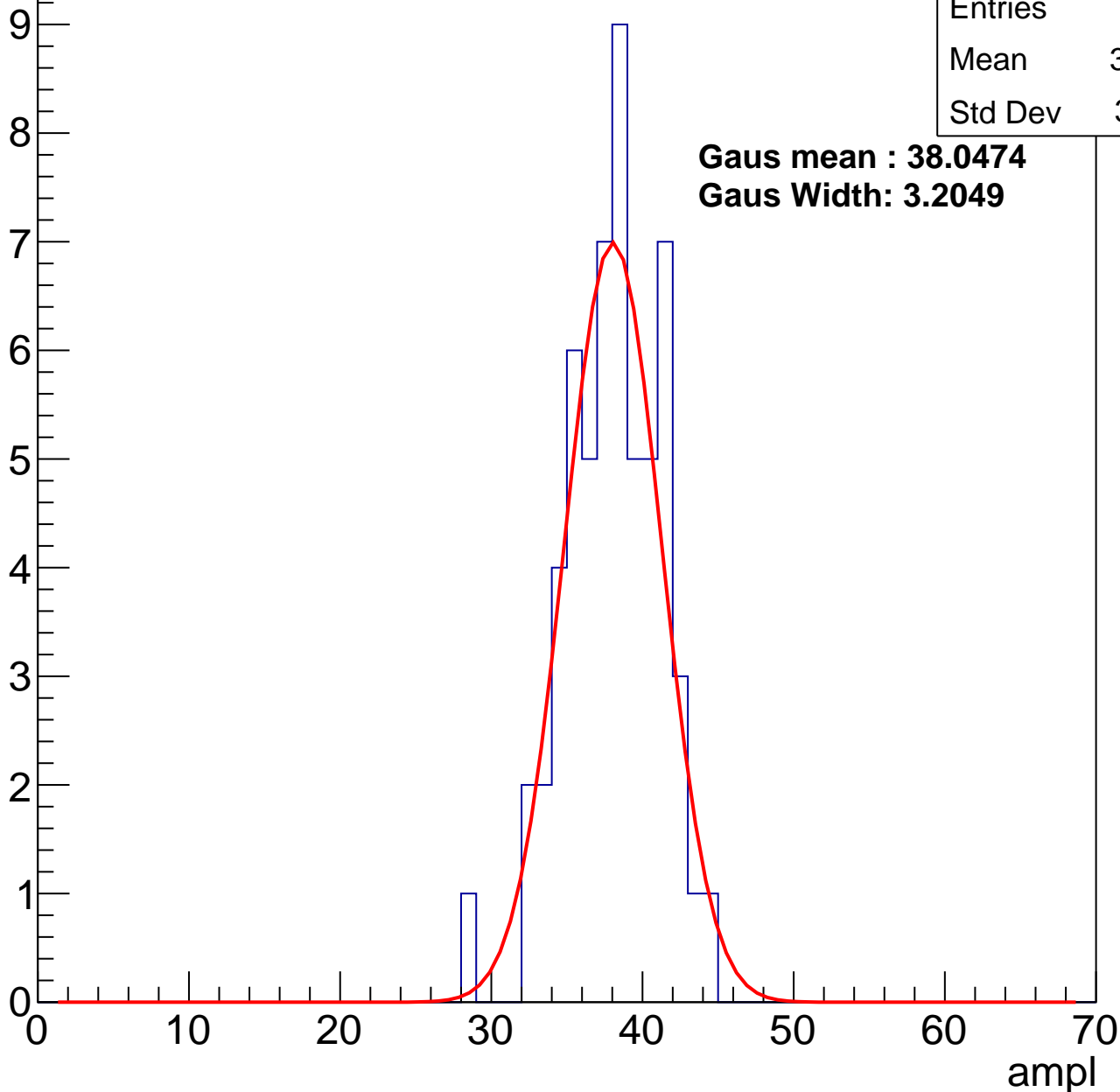
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.59
Std Dev	3.091

Gaus mean : 38.0474

Gaus Width: 3.2049



B1L101S, U12-ch20, adc2

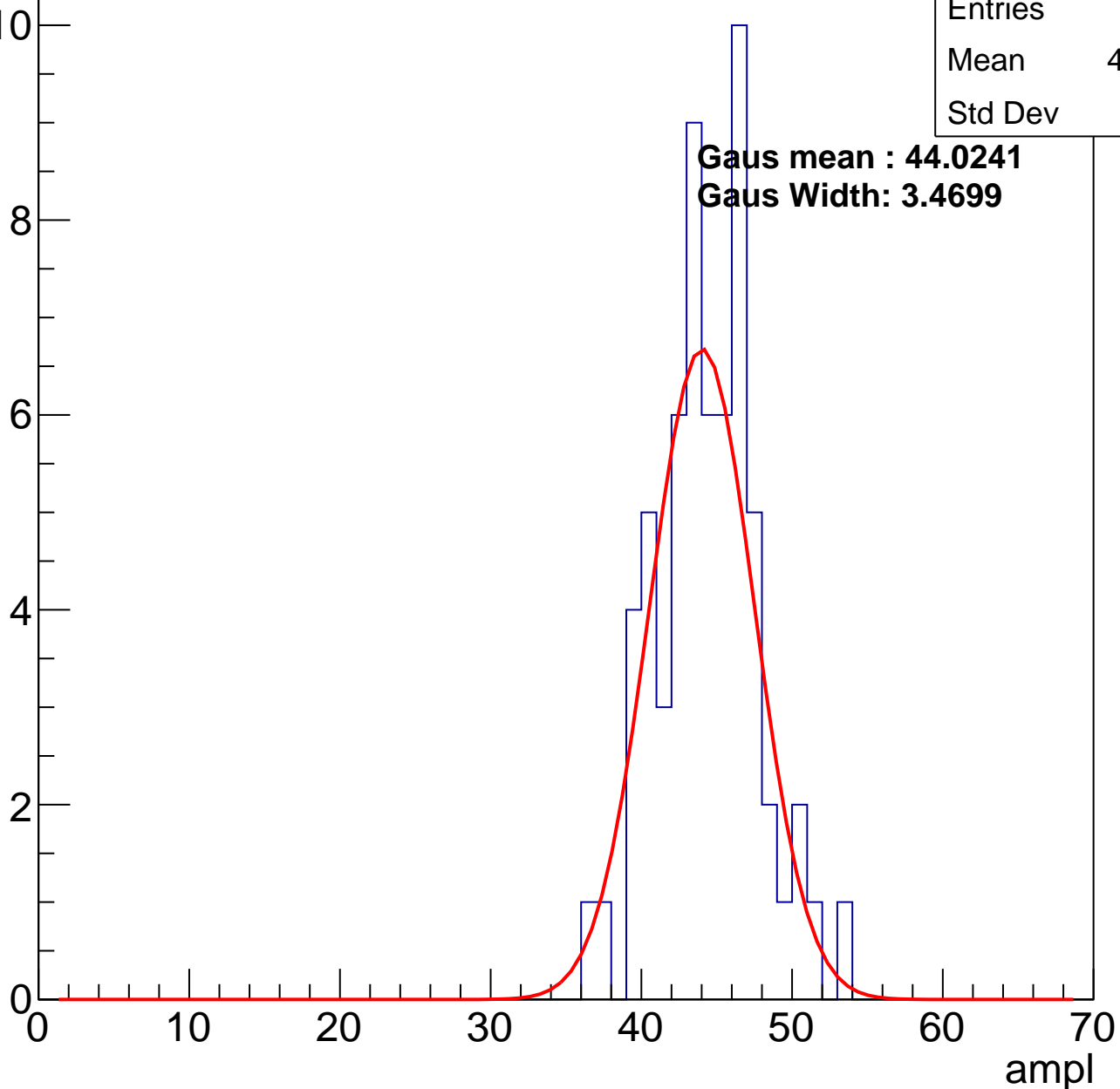
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.95
Std Dev	3.34

Gaus mean : 44.0241

Gaus Width: 3.4699

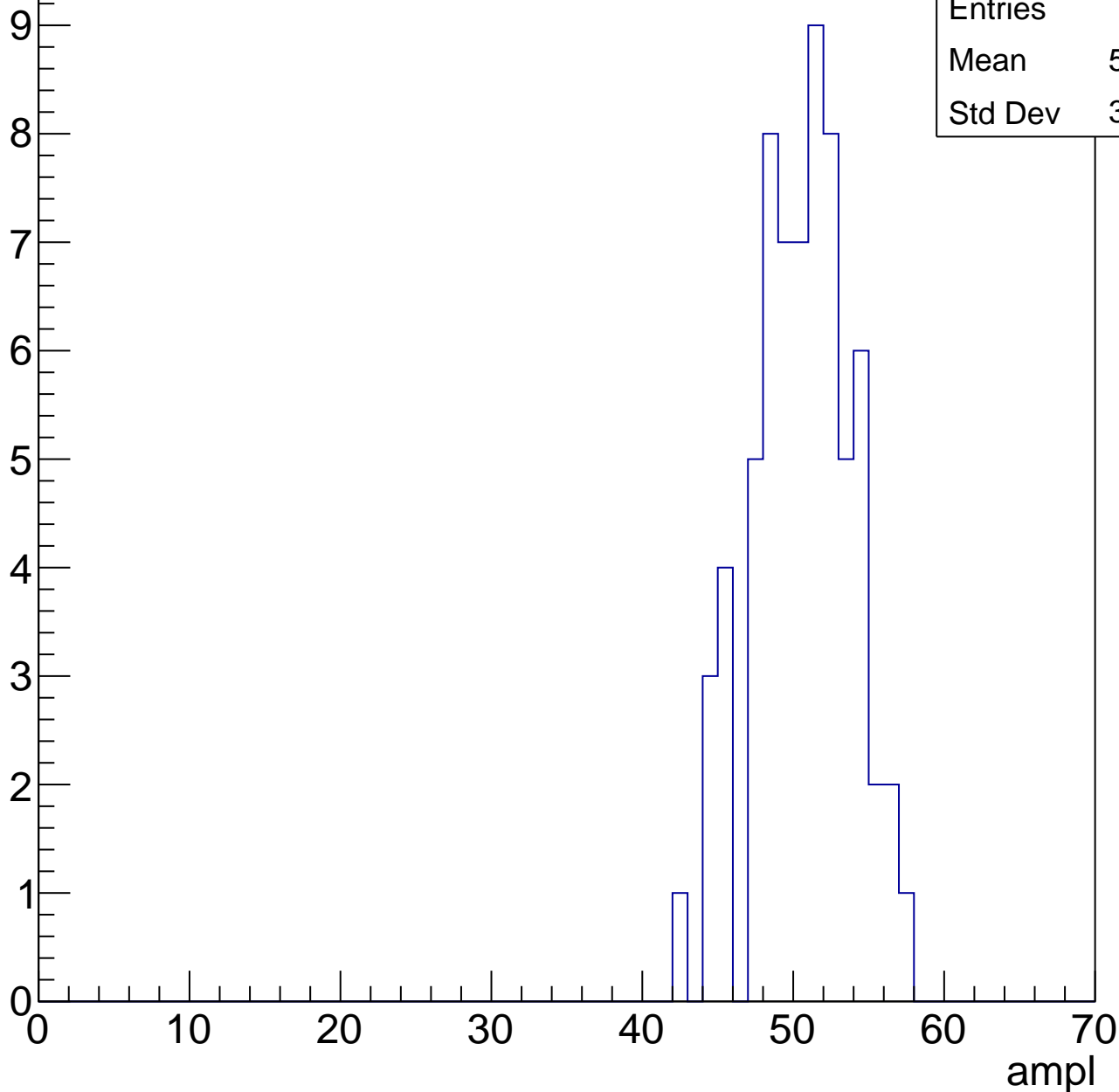


B1L101S, U12-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

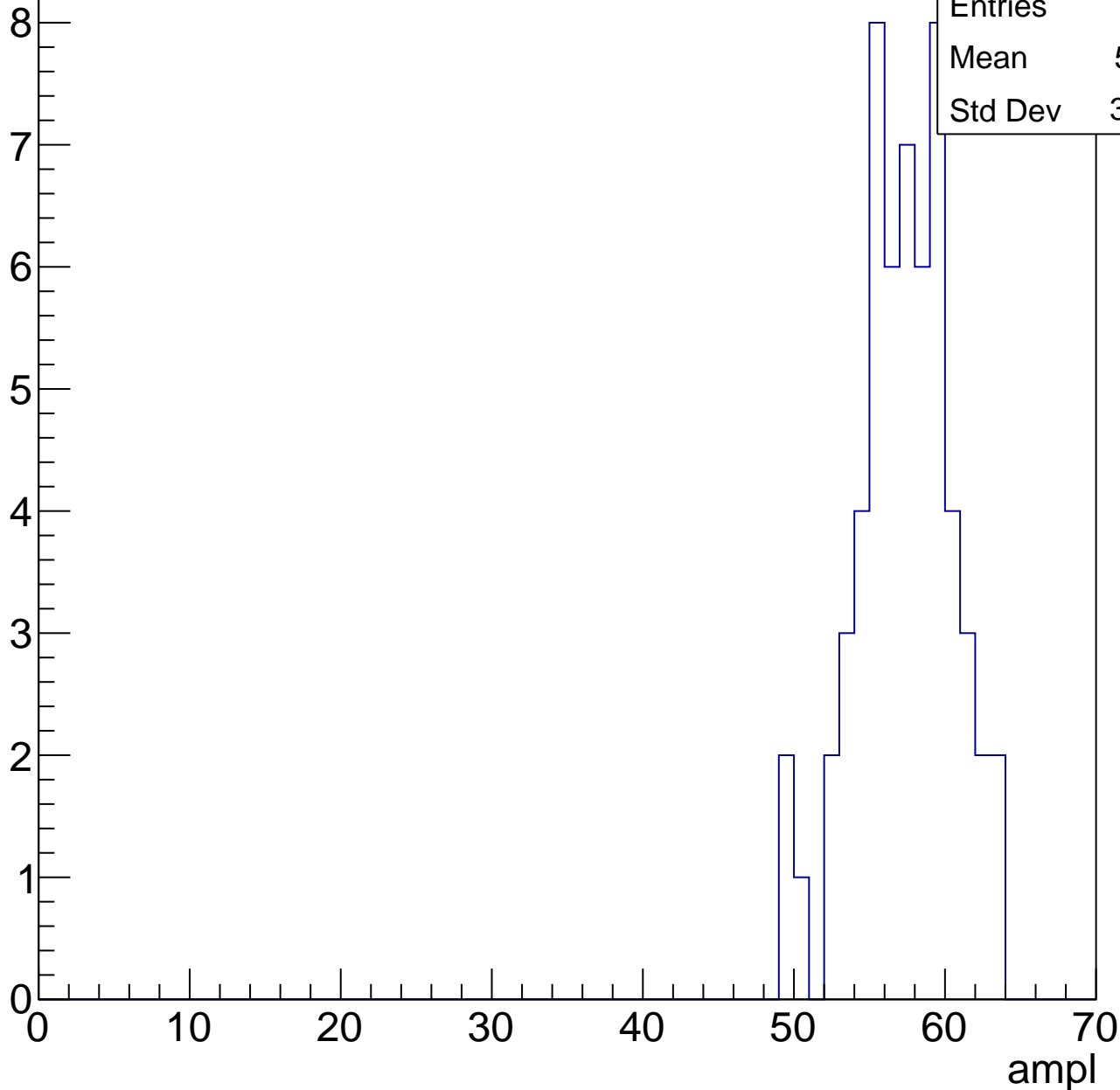
Entries	68
Mean	50.13
Std Dev	3.226



B1L101S, U12-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

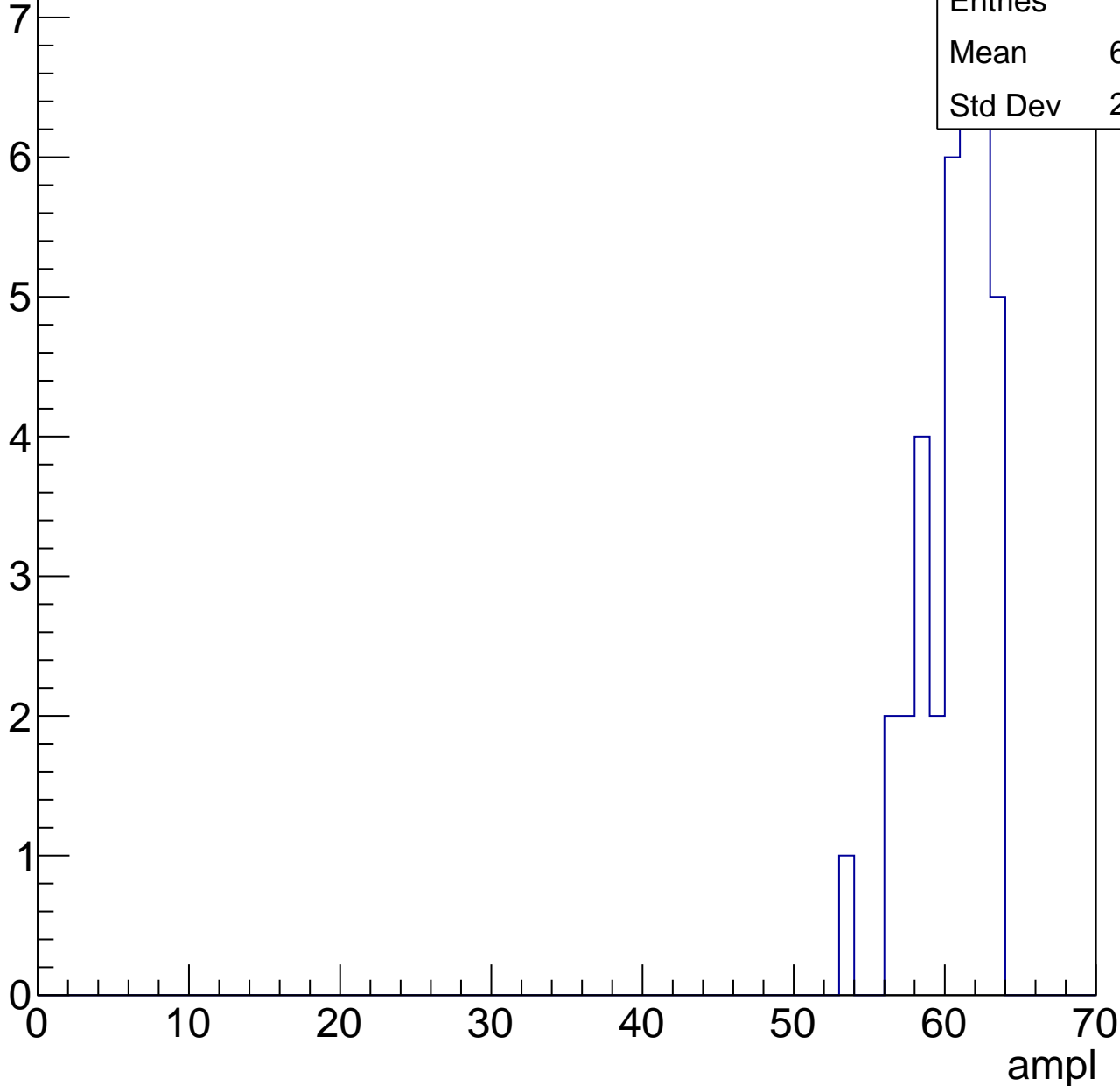


B1L101S, U12-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

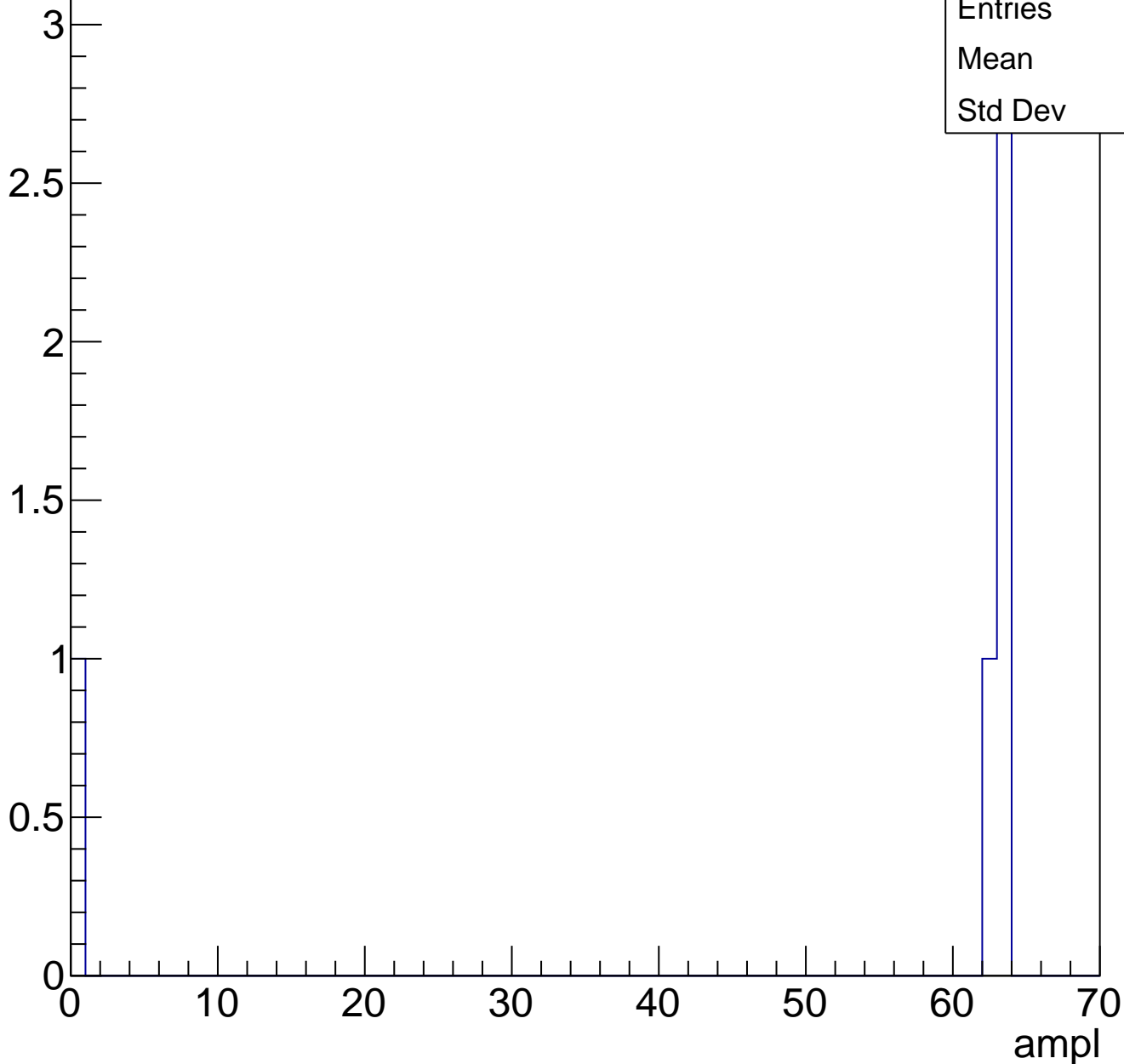
Entries	36
Mean	60.14
Std Dev	2.335



B1L101S, U12-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch21, adc0

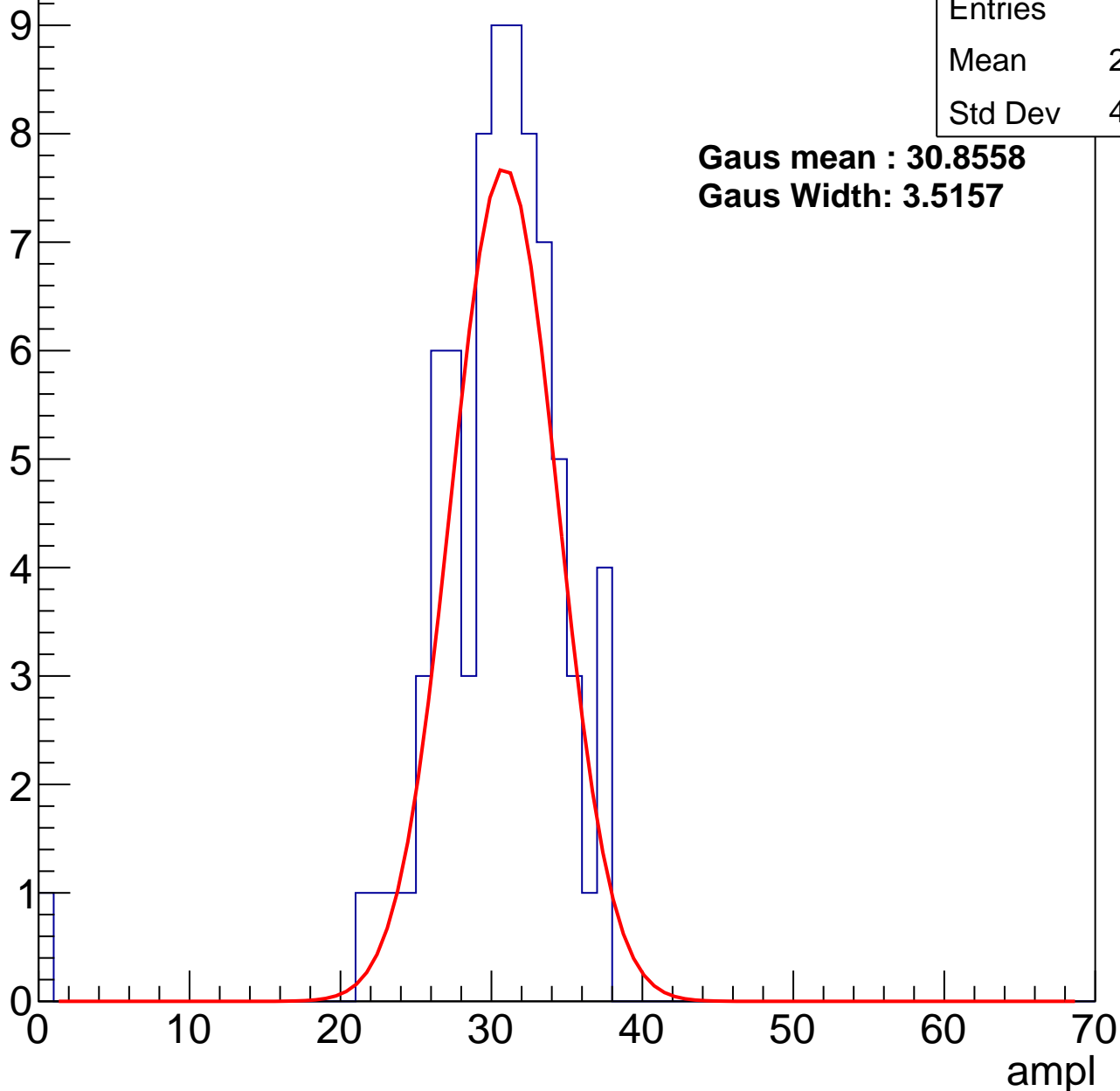
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.79
Std Dev	4.918

Gaus mean : 30.8558

Gaus Width: 3.5157



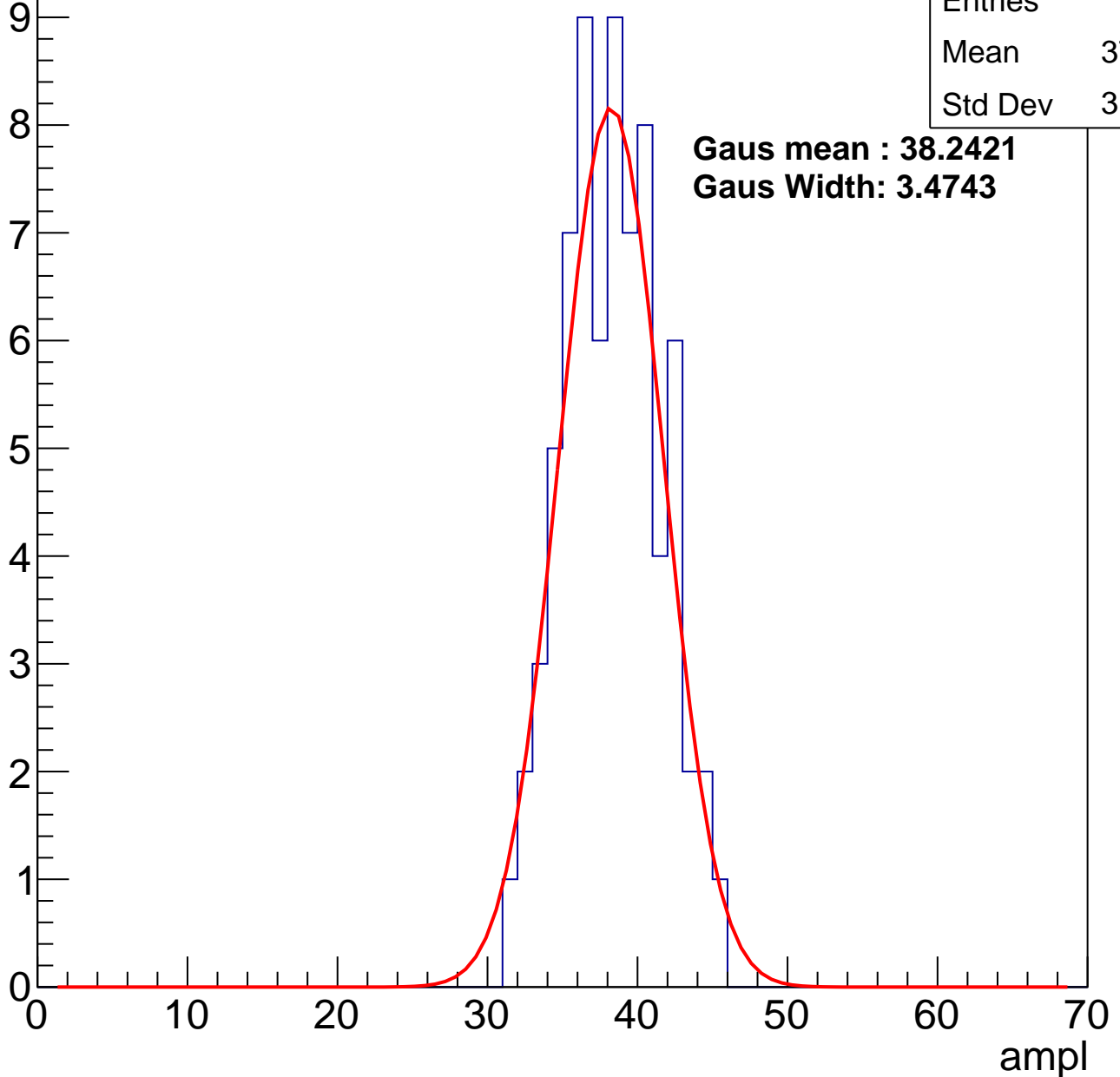
B1L101S, U12-ch21, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.85
Std Dev	3.165

Gaus mean : 38.2421
Gaus Width: 3.4743



B1L101S, U12-ch21, adc2

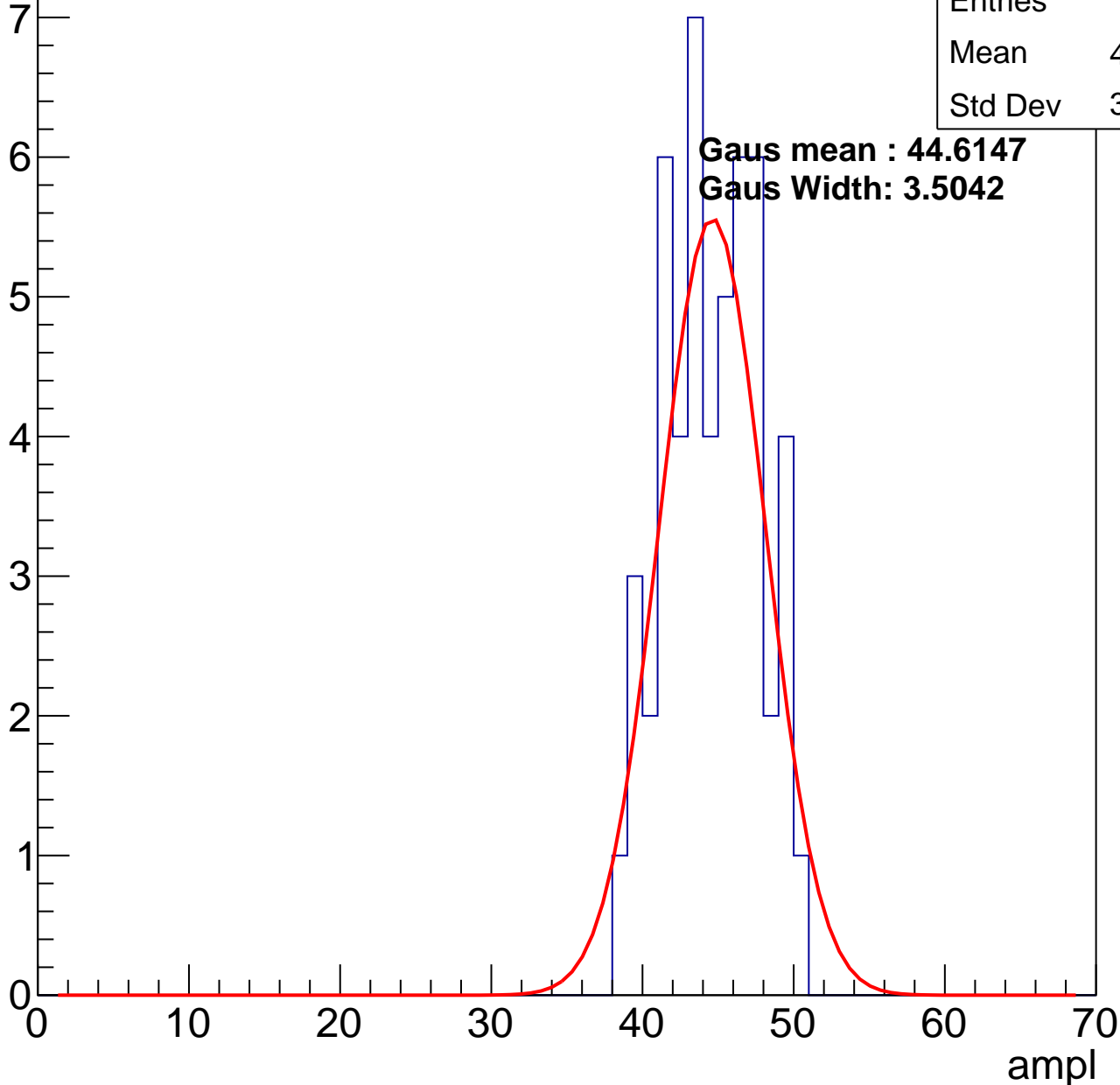
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	44.14
Std Dev	3.036

Gaus mean : 44.6147

Gaus Width: 3.5042

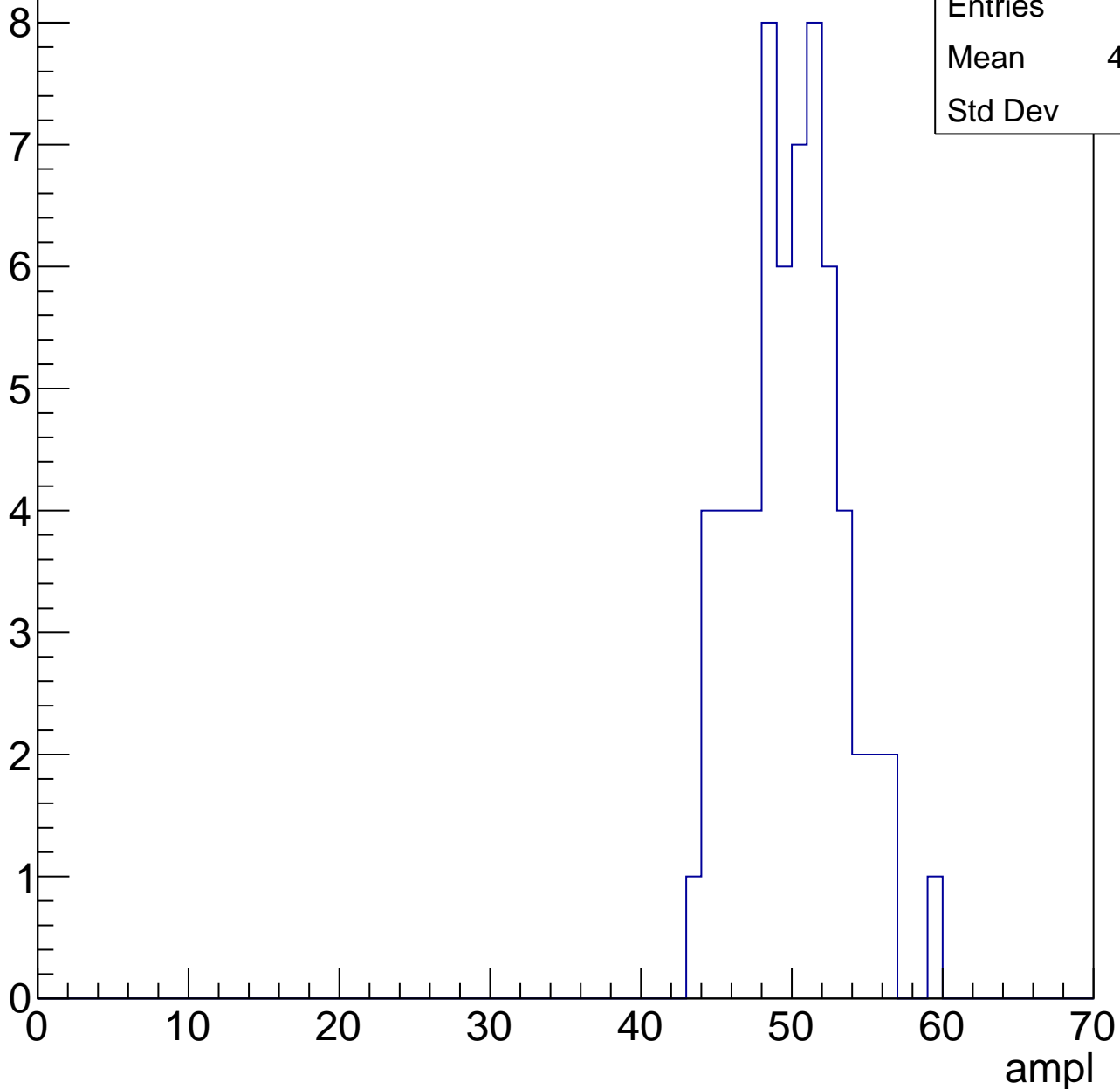


B1L101S, U12-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

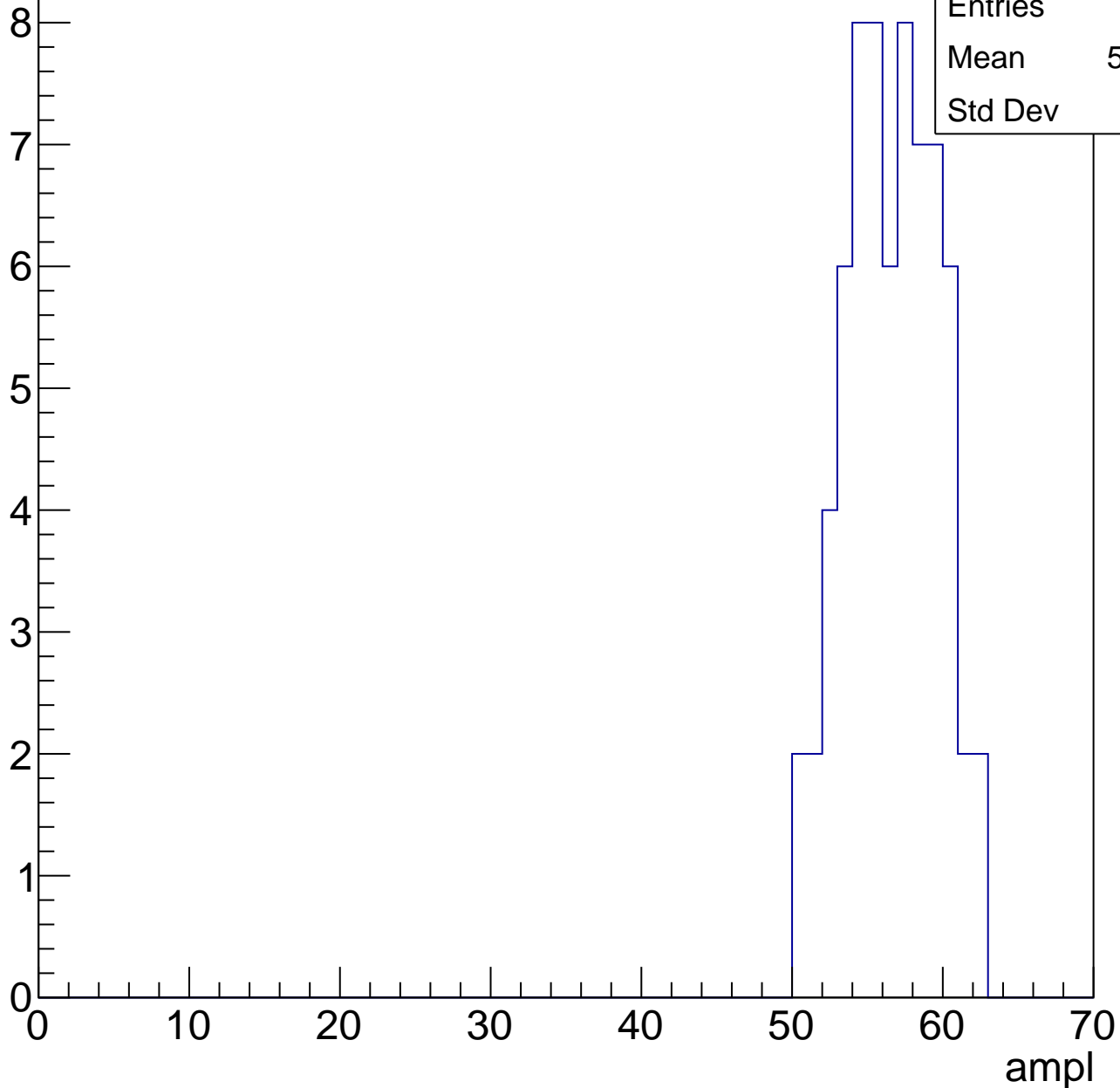
Entries	63
Mean	49.52
Std Dev	3.38



B1L101S, U12-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	68
Mean	56.13
Std Dev	2.96

B1L101S, U12-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 40

Mean 59.3

Std Dev 9.714

8

6

4

2

0

0

10

20

30

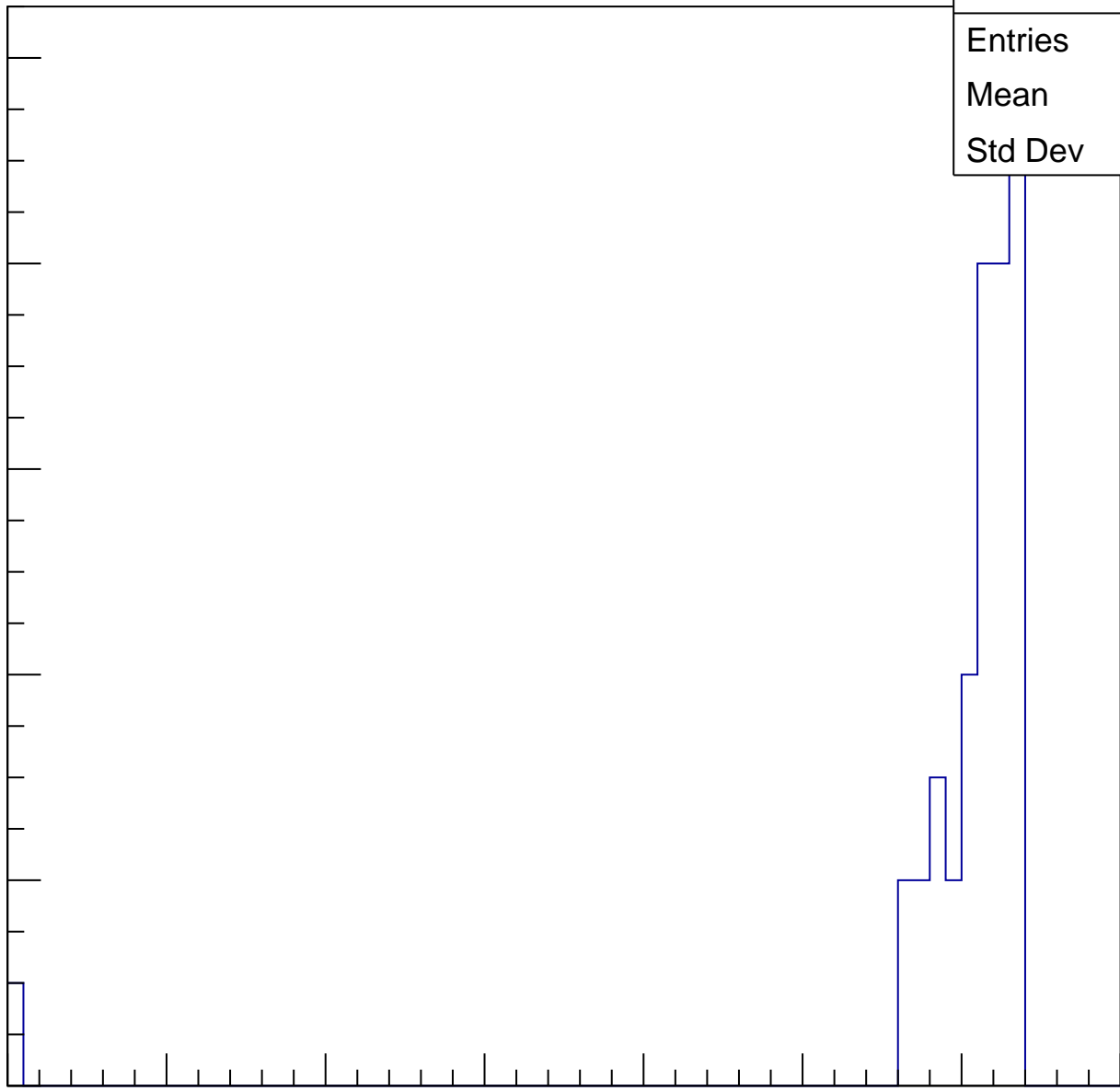
40

50

60

ampl

70



B1L101S, U12-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch22, adc0

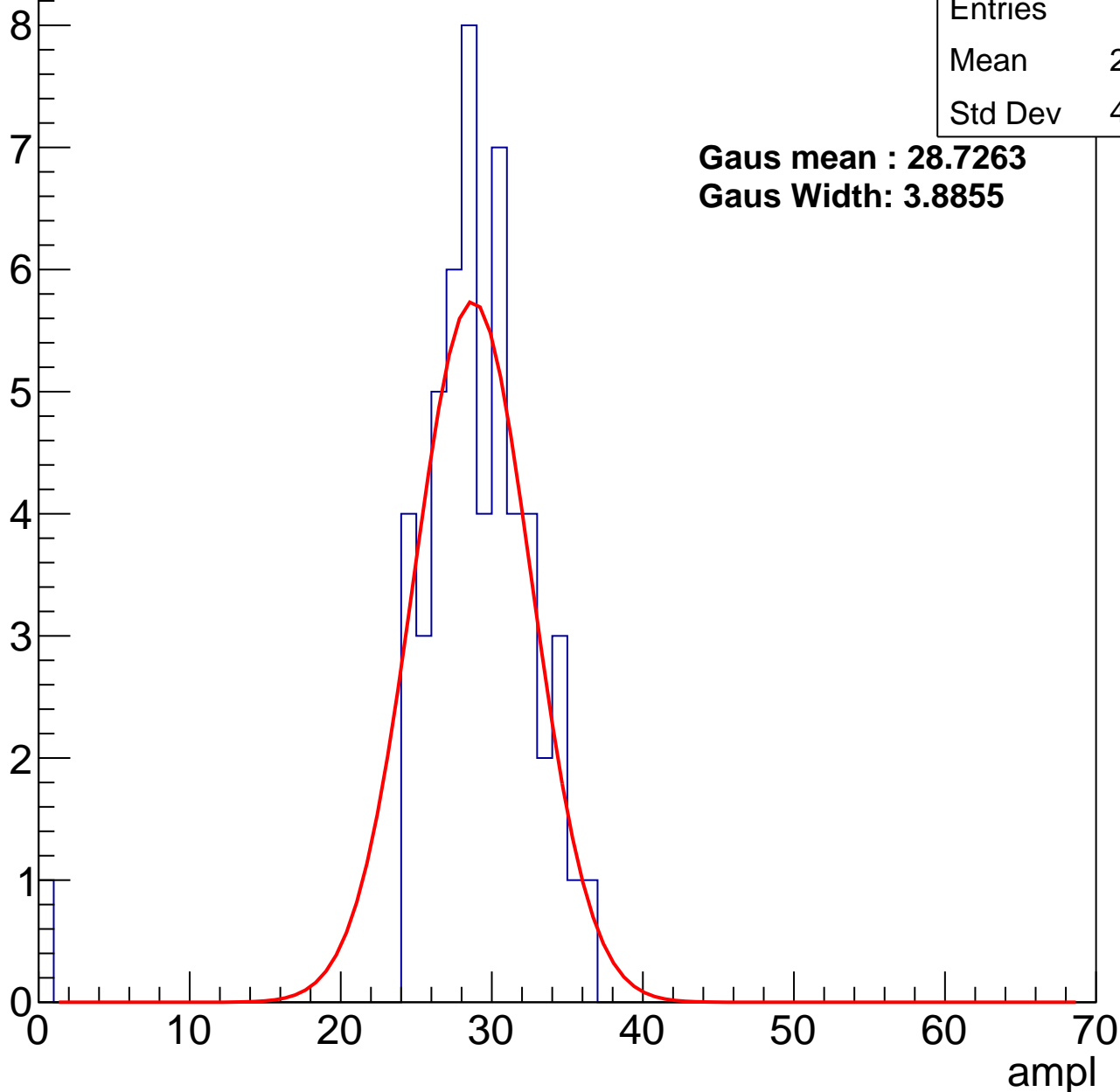
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	28.38
Std Dev	4.946

Gaus mean : 28.7263

Gaus Width: 3.8855



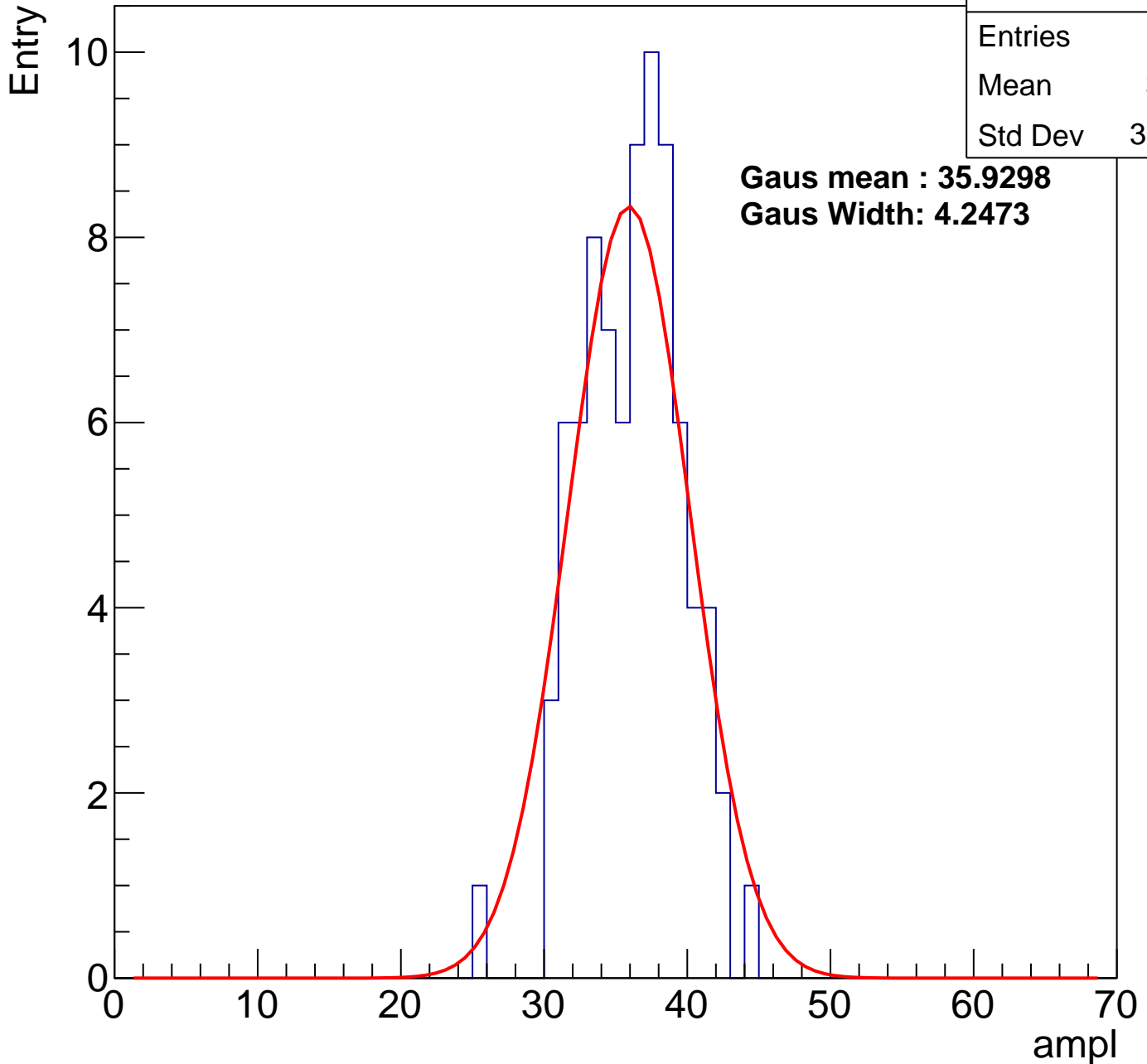
B1L101S, U12-ch22, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	35.7
Std Dev	3.445

Gaus mean : 35.9298

Gaus Width: 4.2473



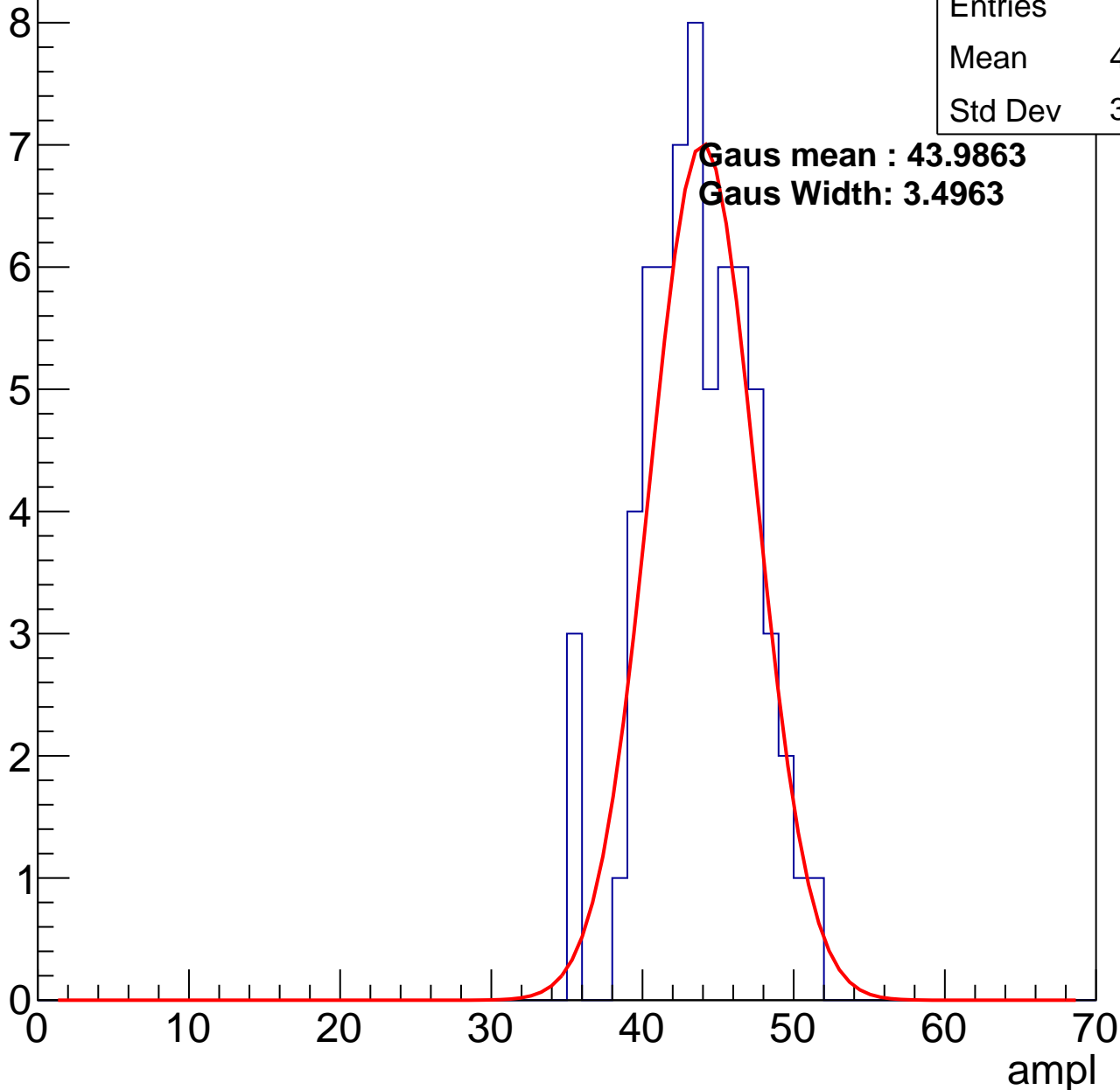
B1L101S, U12-ch22, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.23
Std Dev	3.503

Gaus mean : 43.9863
Gaus Width: 3.4963

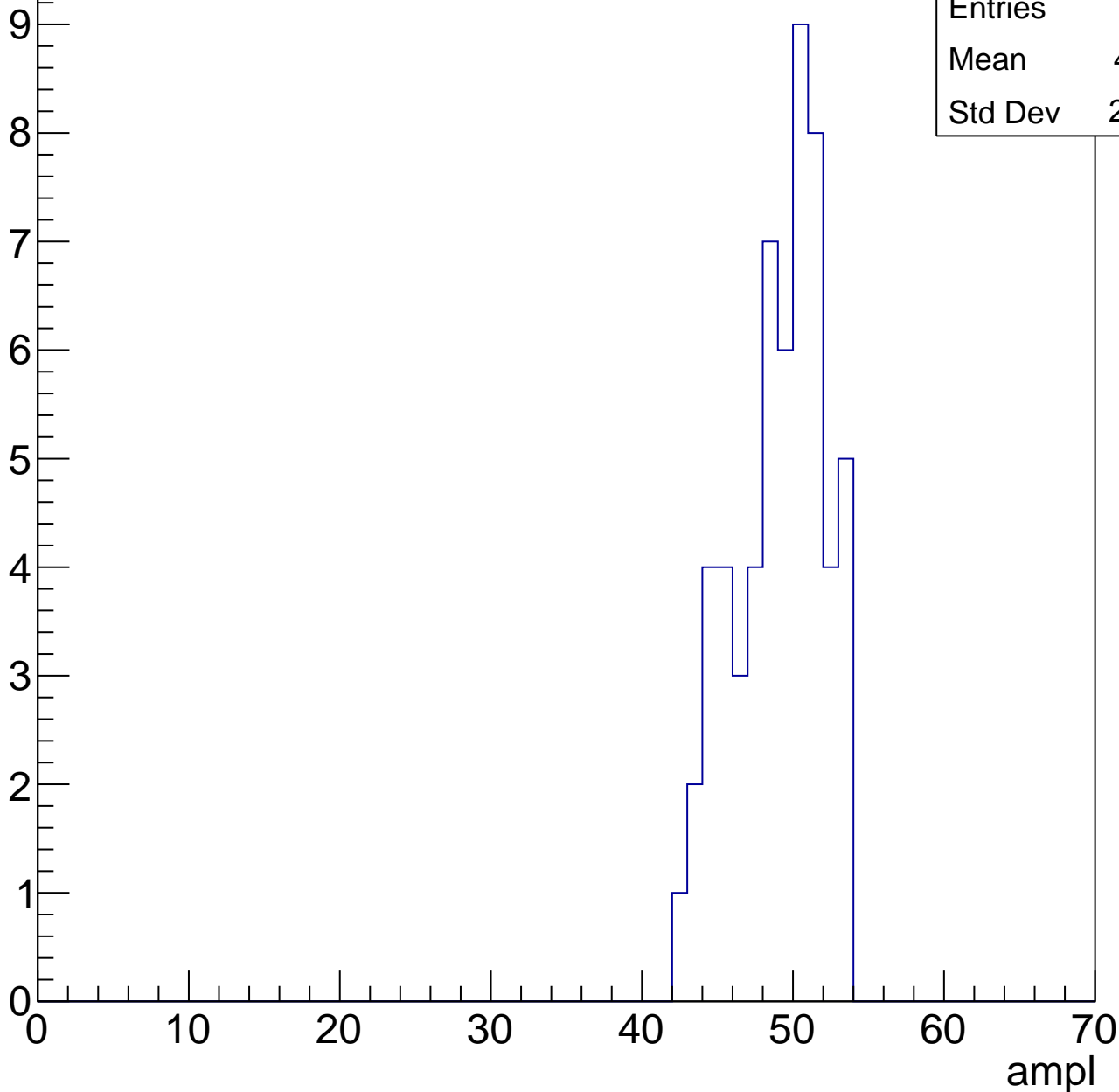


B1L101S, U12-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.61
Std Dev	2.912

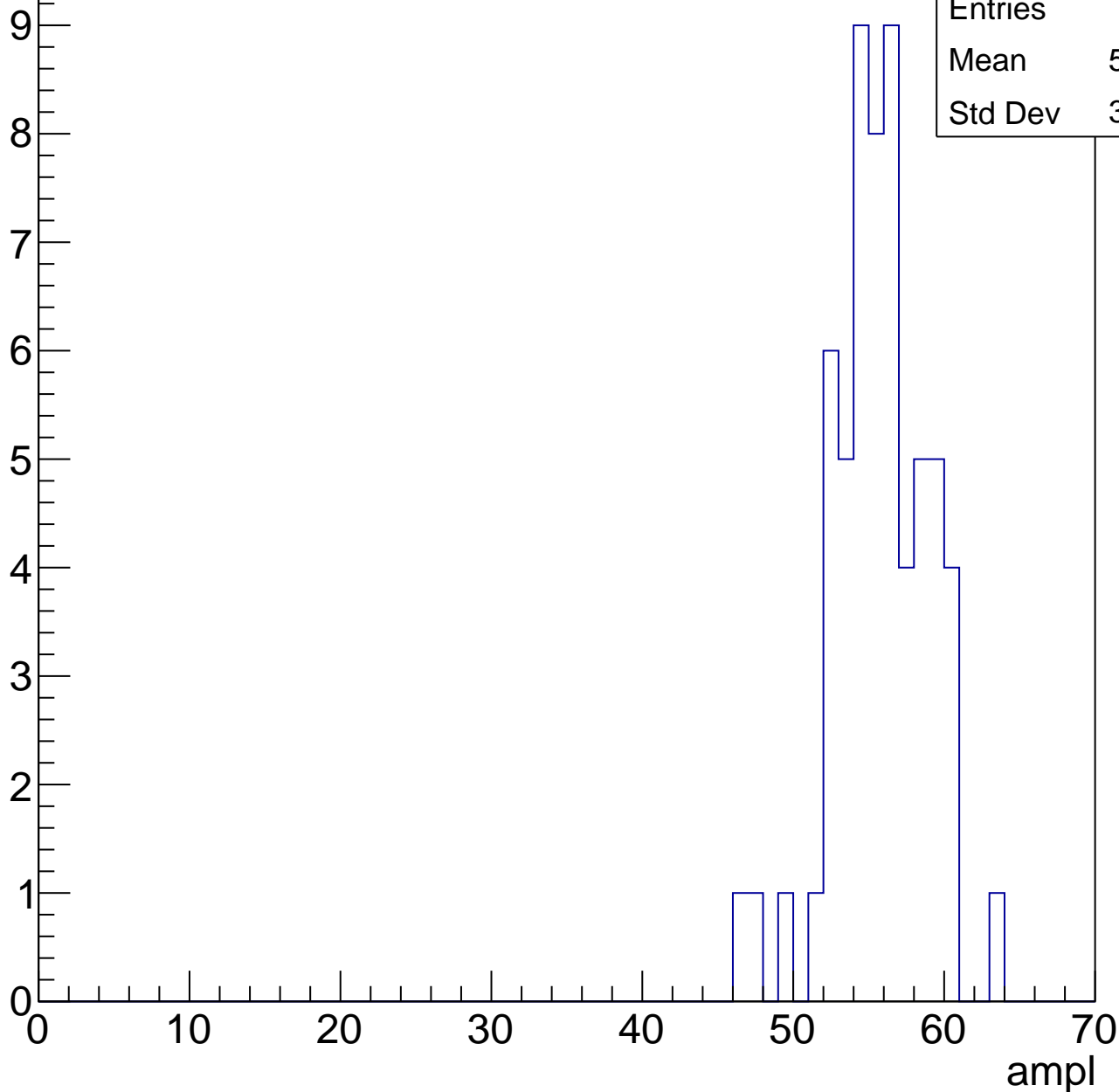


B1L101S, U12-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.27
Std Dev	3.135

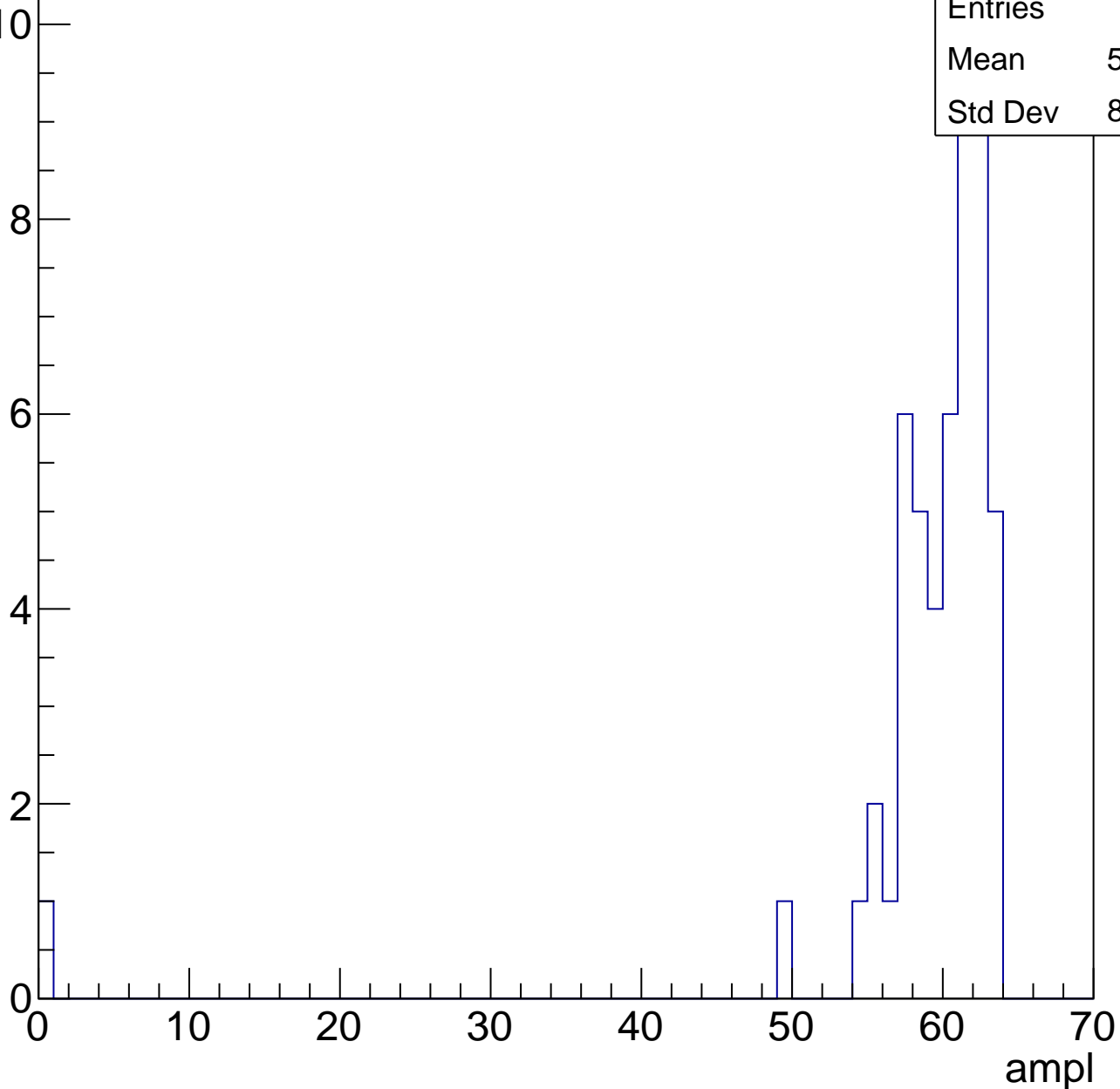


B1L101S, U12-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

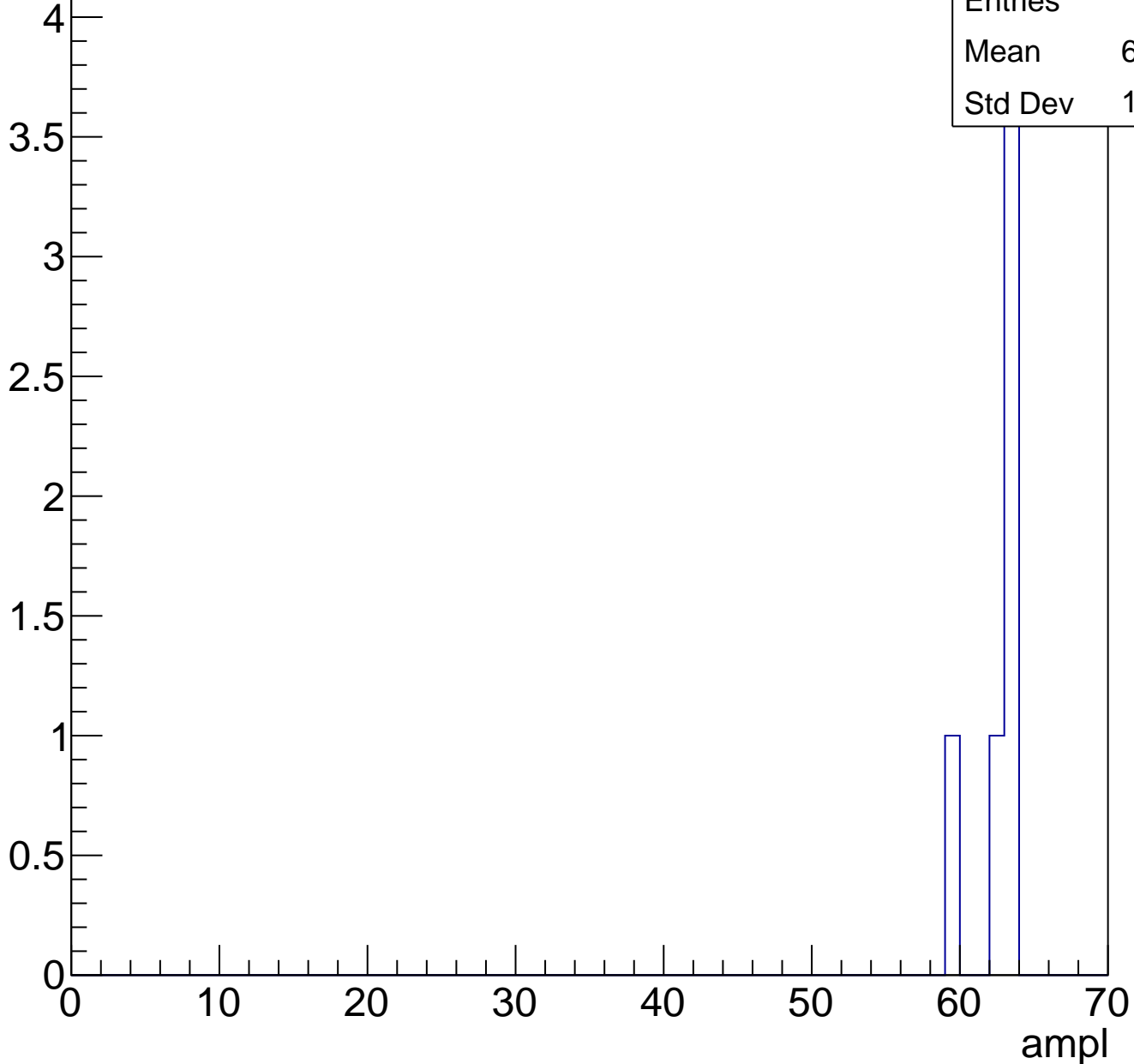
Entries	51
Mean	58.43
Std Dev	8.707



B1L101S, U12-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U12-ch23, adc0

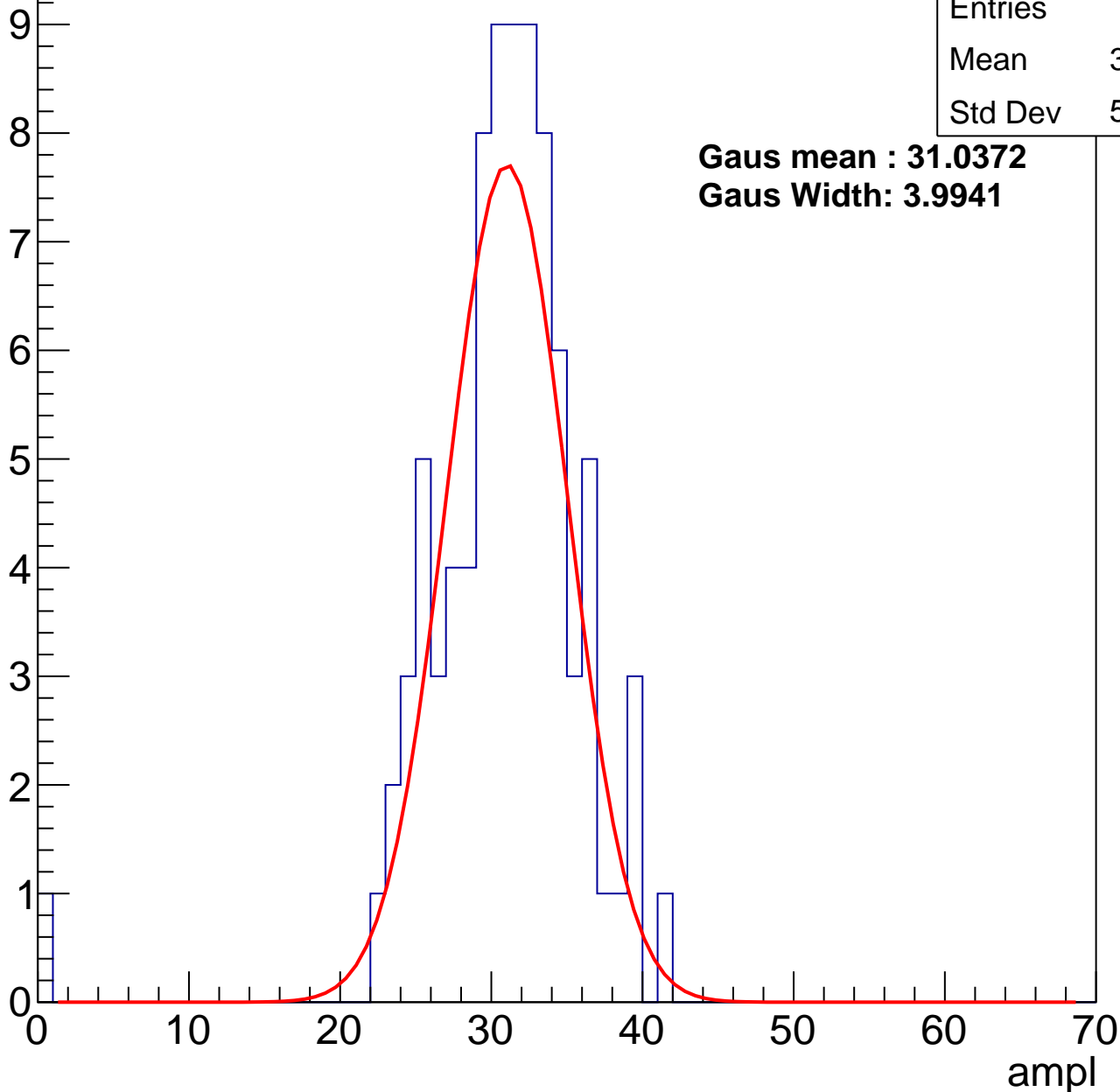
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	30.44
Std Dev	5.206

Gaus mean : 31.0372

Gaus Width: 3.9941



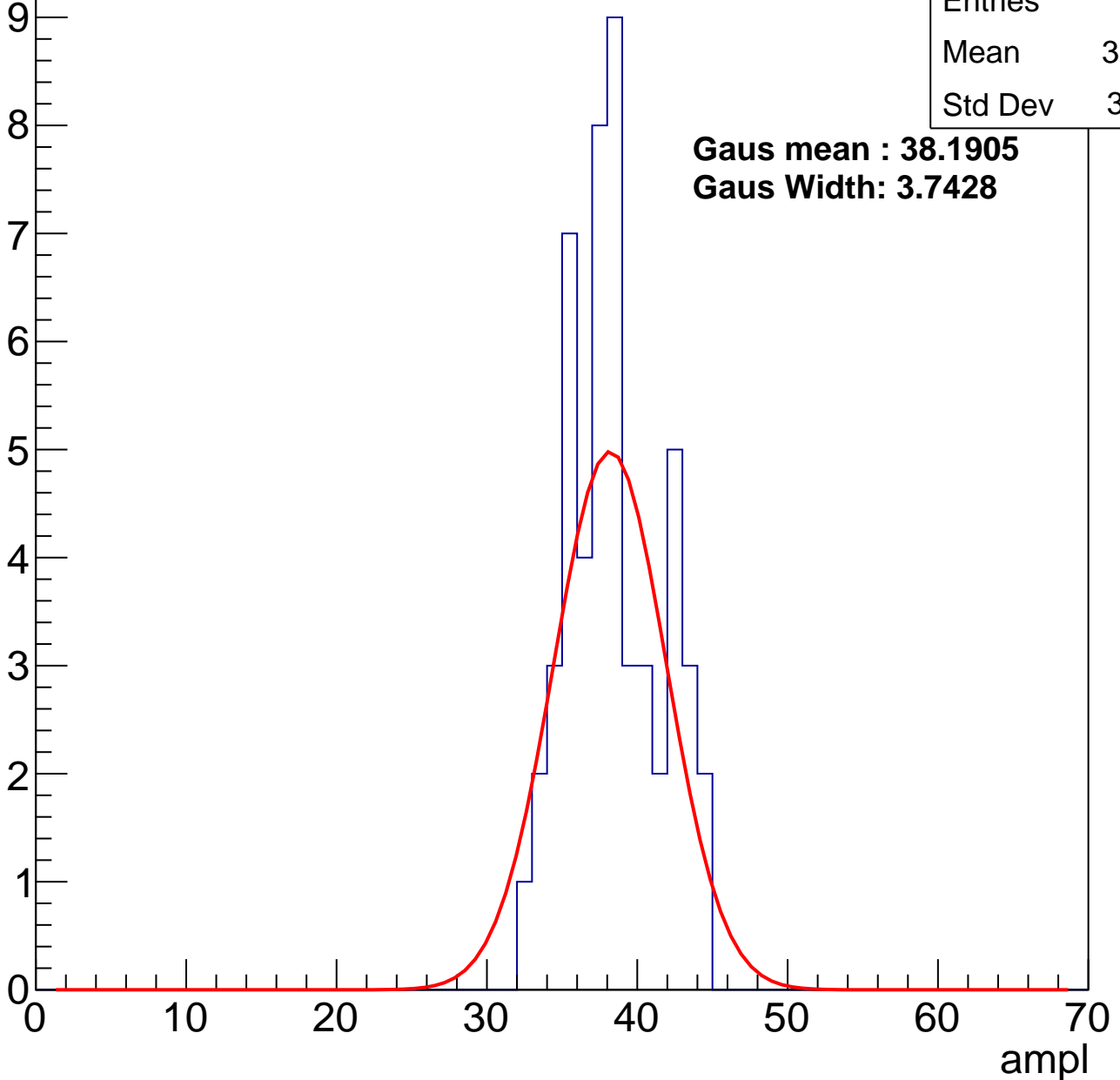
B1L101S, U12-ch23, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	37.94
Std Dev	3.041

Gaus mean : 38.1905
Gaus Width: 3.7428



B1L101S, U12-ch23, adc2

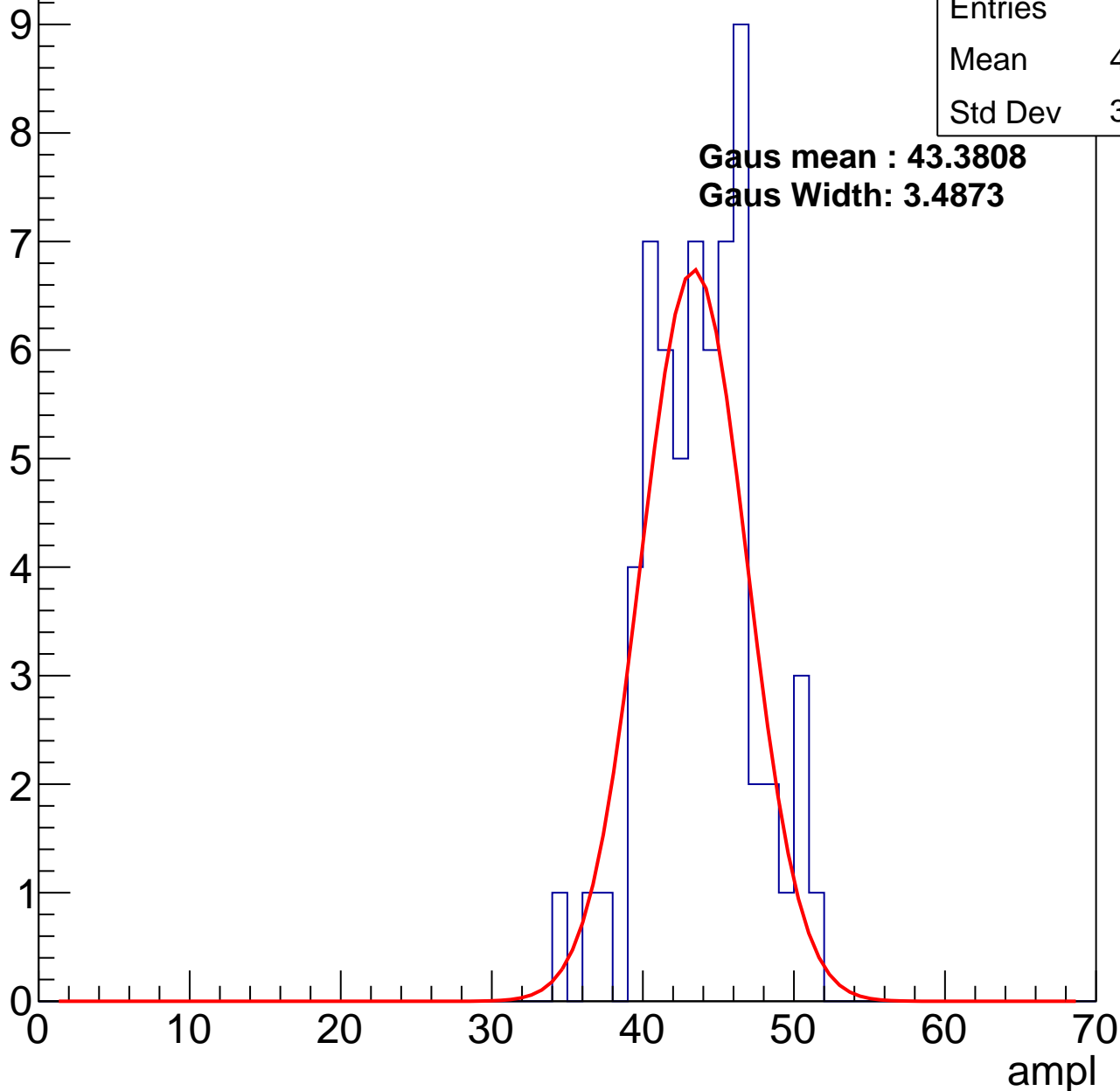
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.38
Std Dev	3.466

Gaus mean : 43.3808

Gaus Width: 3.4873

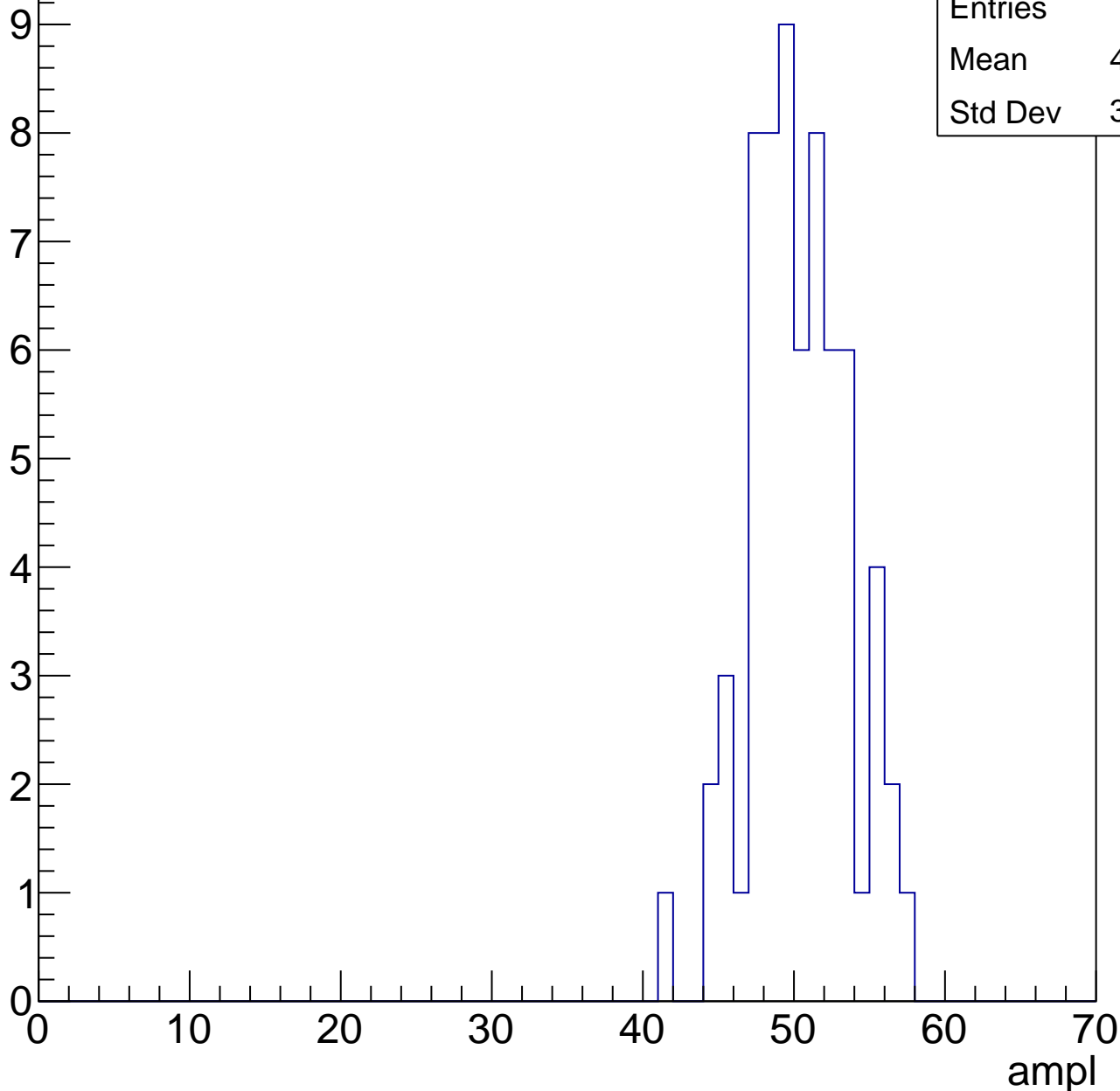


B1L101S, U12-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

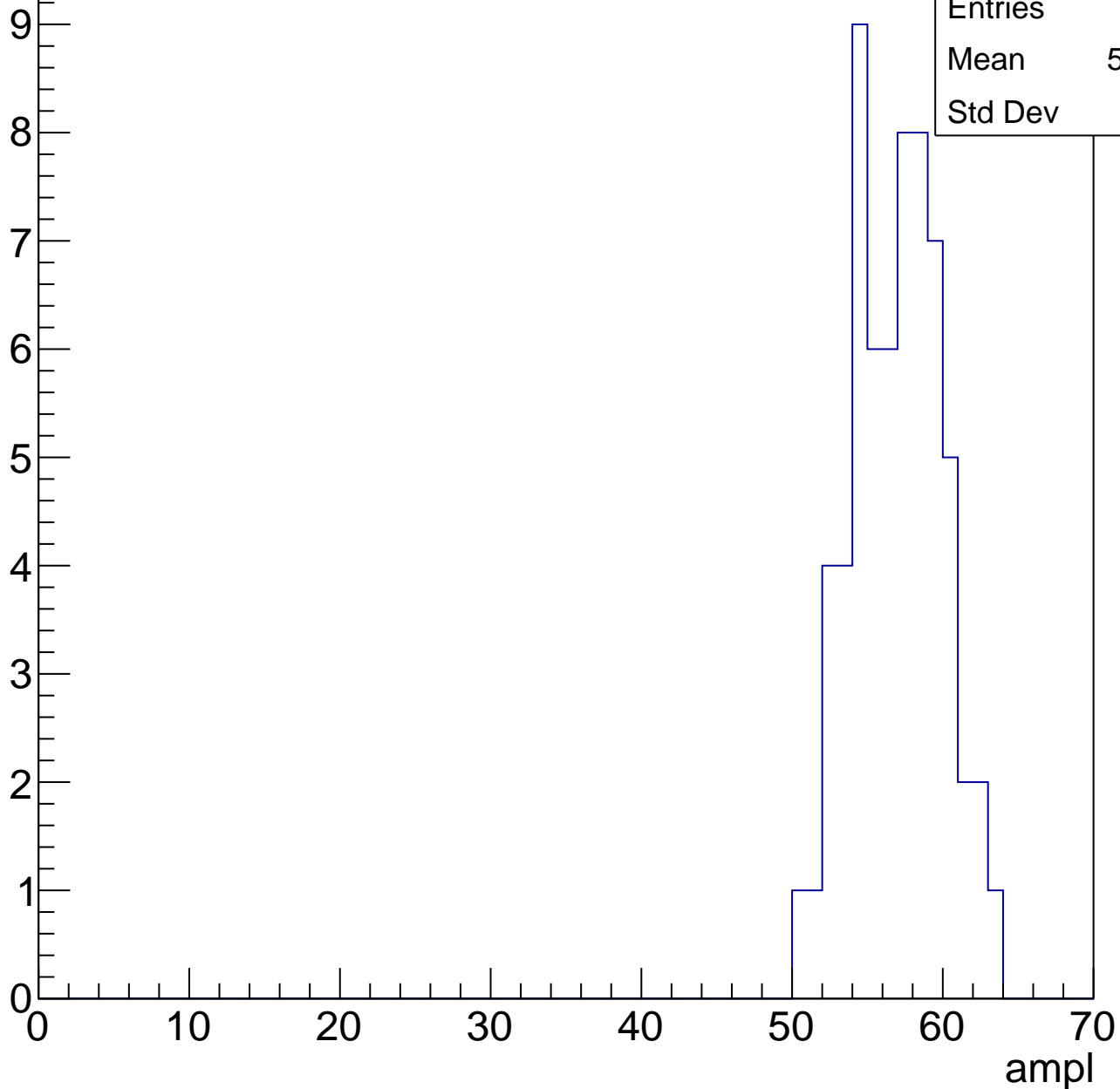
Entries	66
Mean	49.88
Std Dev	3.208



B1L101S, U12-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

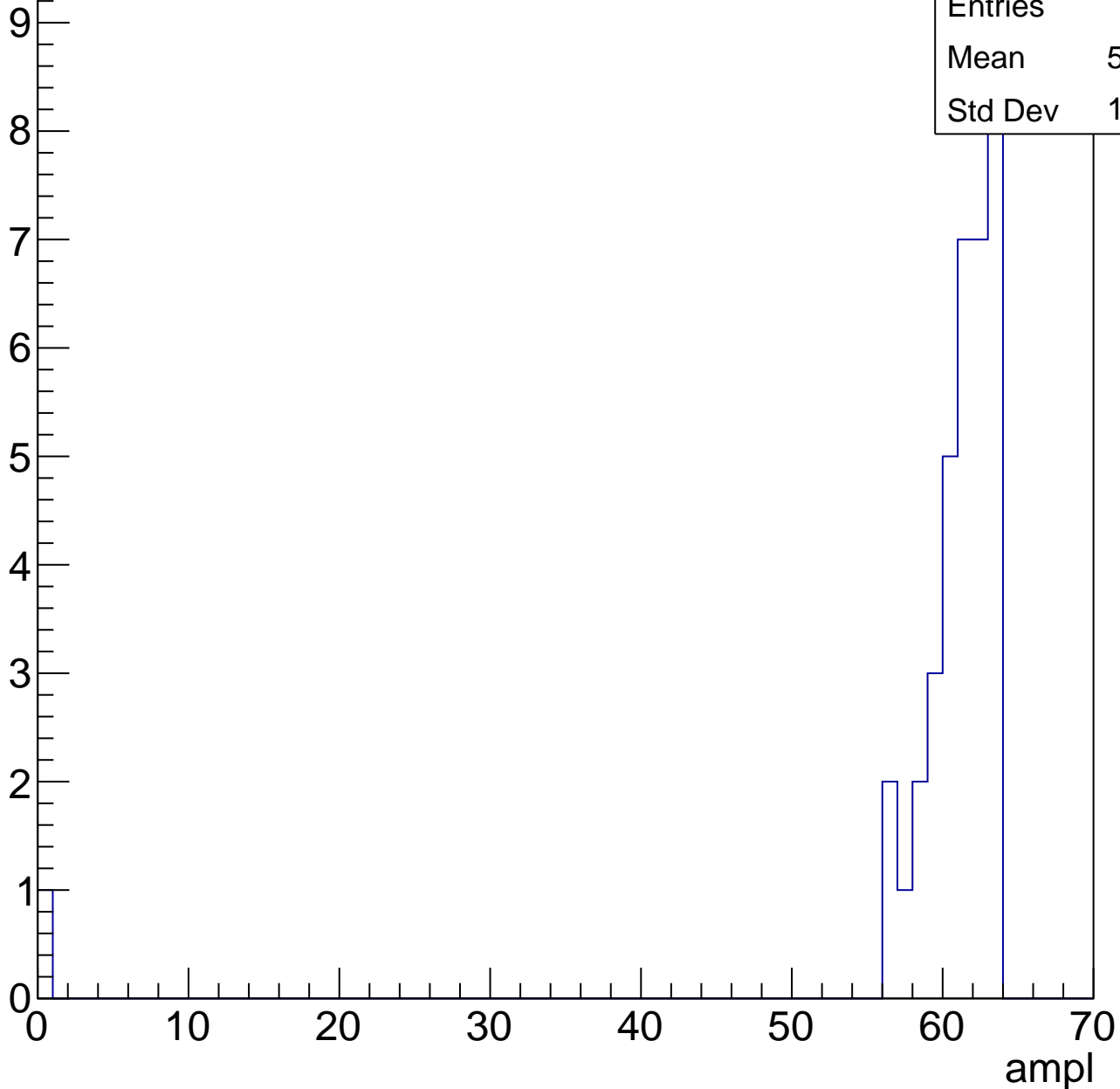


B1L101S, U12-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	59.19
Std Dev	10.06



B1L101S, U12-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch24, adc0

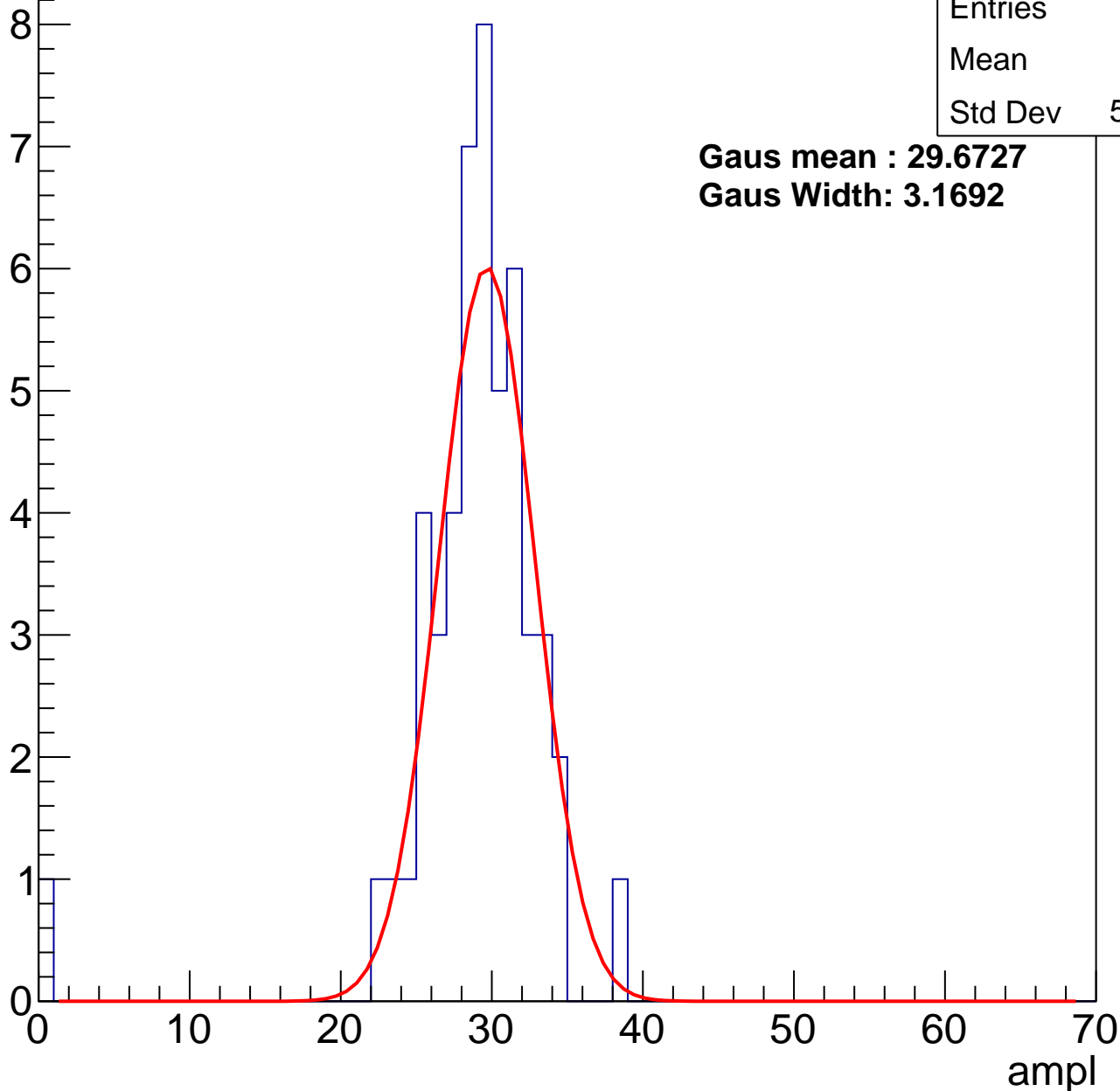
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	28.4
Std Dev	5.064

Gaus mean : 29.6727

Gaus Width: 3.1692



B1L101S, U12-ch24, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	34.95
Std Dev	3.385

Gaus mean : 35.6070

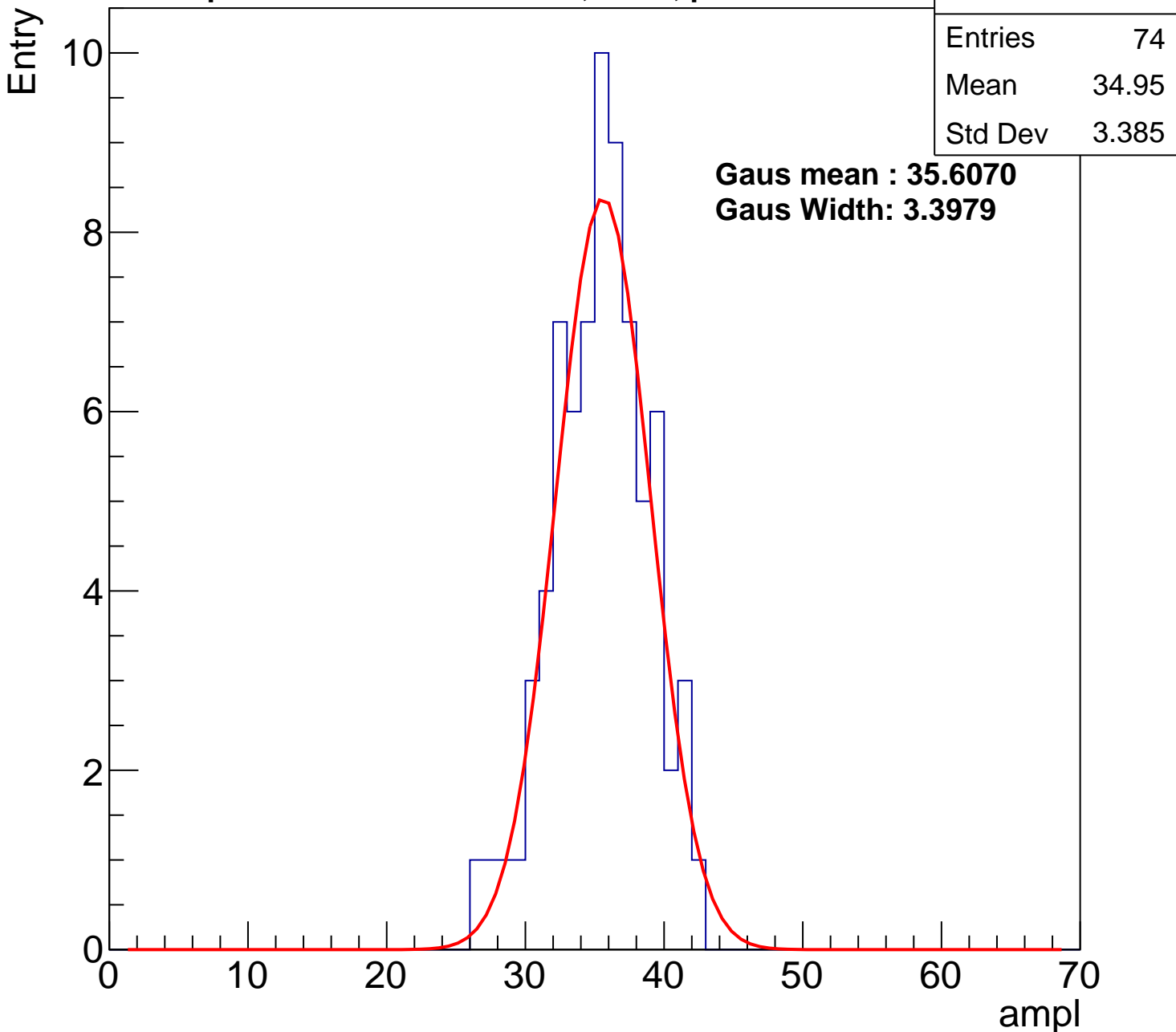
Gaus Width: 3.3979

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch24, adc2

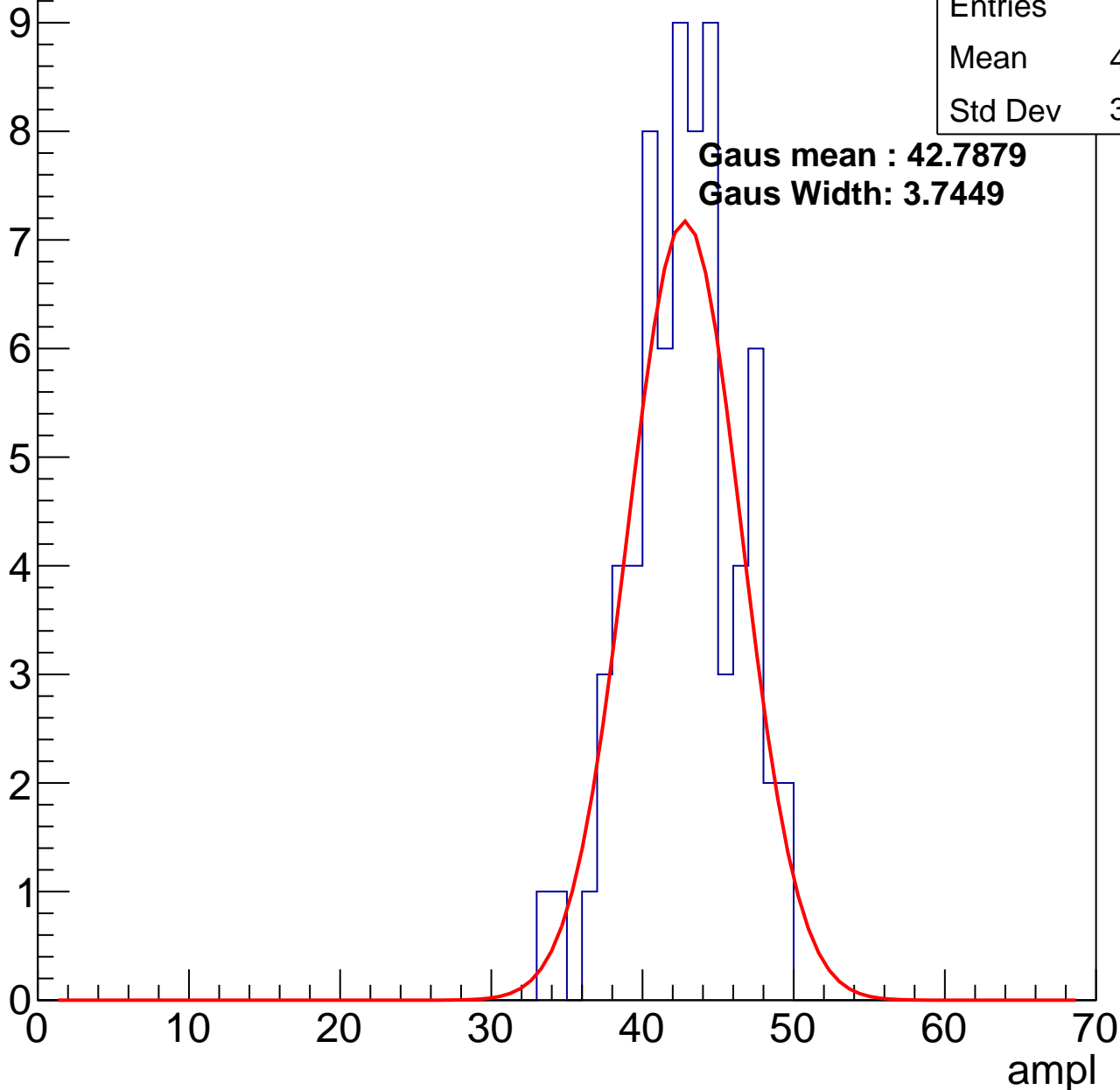
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.27
Std Dev	3.448

Gaus mean : 42.7879

Gaus Width: 3.7449

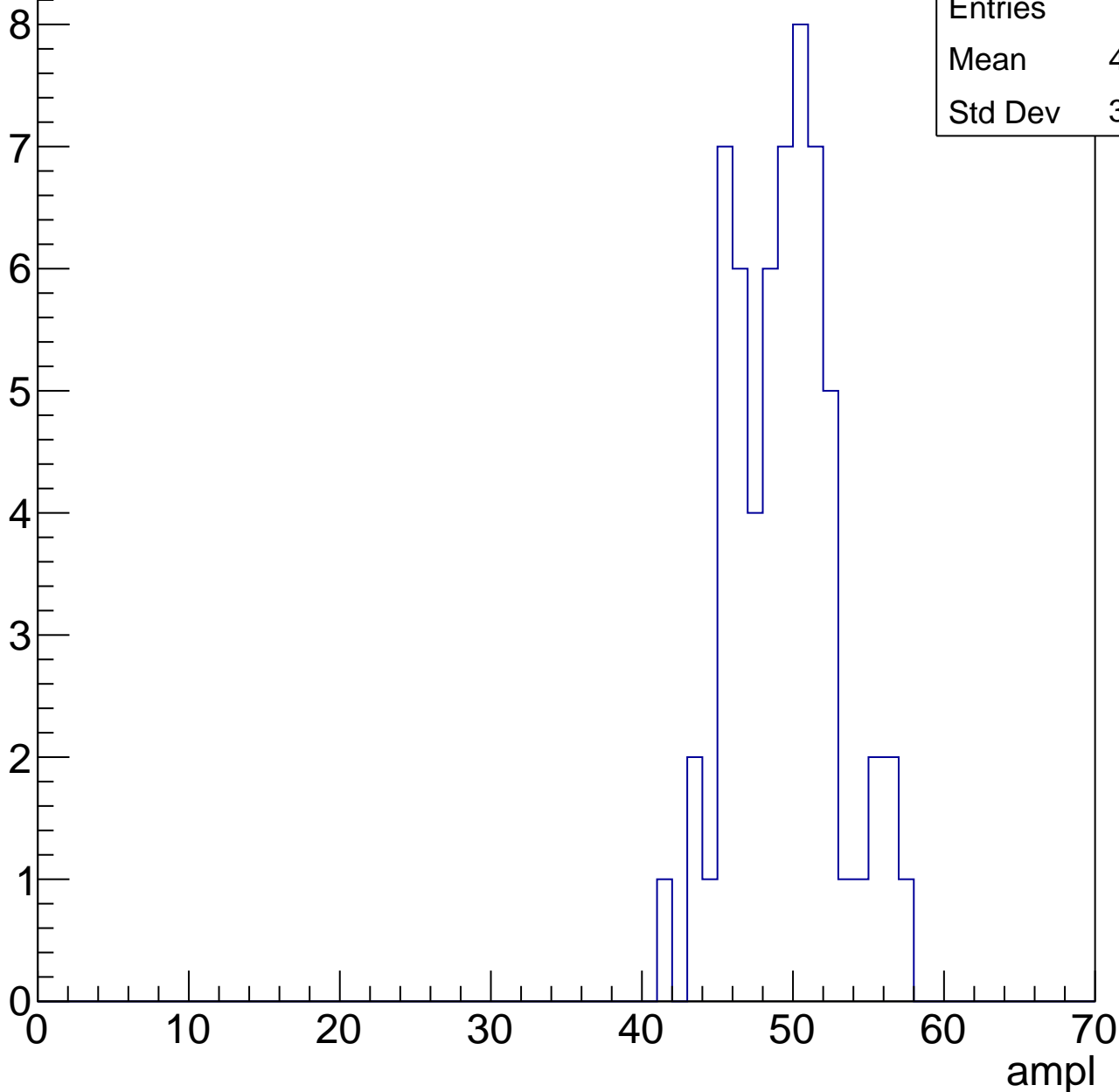


B1L101S, U12-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.92
Std Dev	3.399

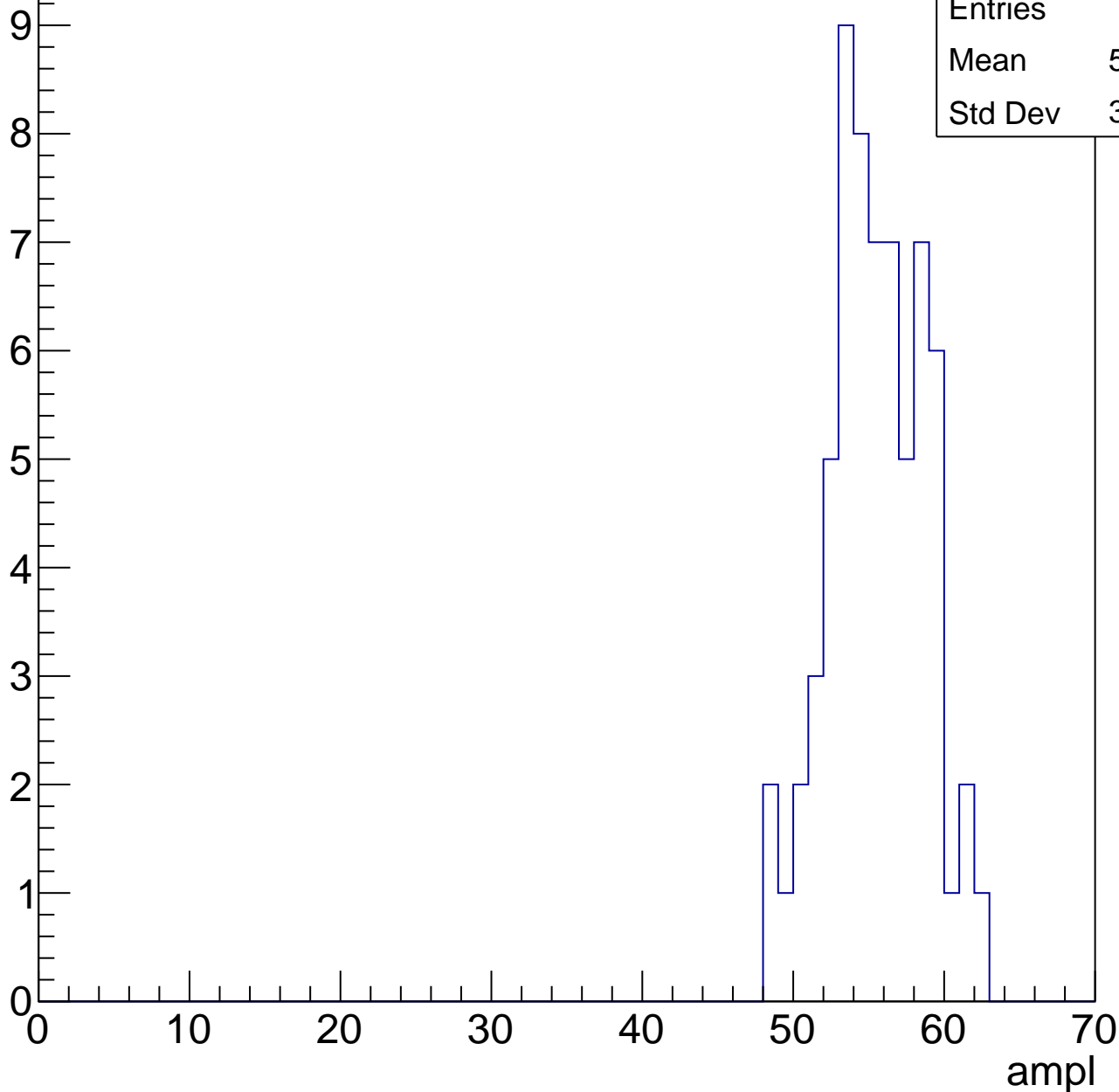


B1L101S, U12-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

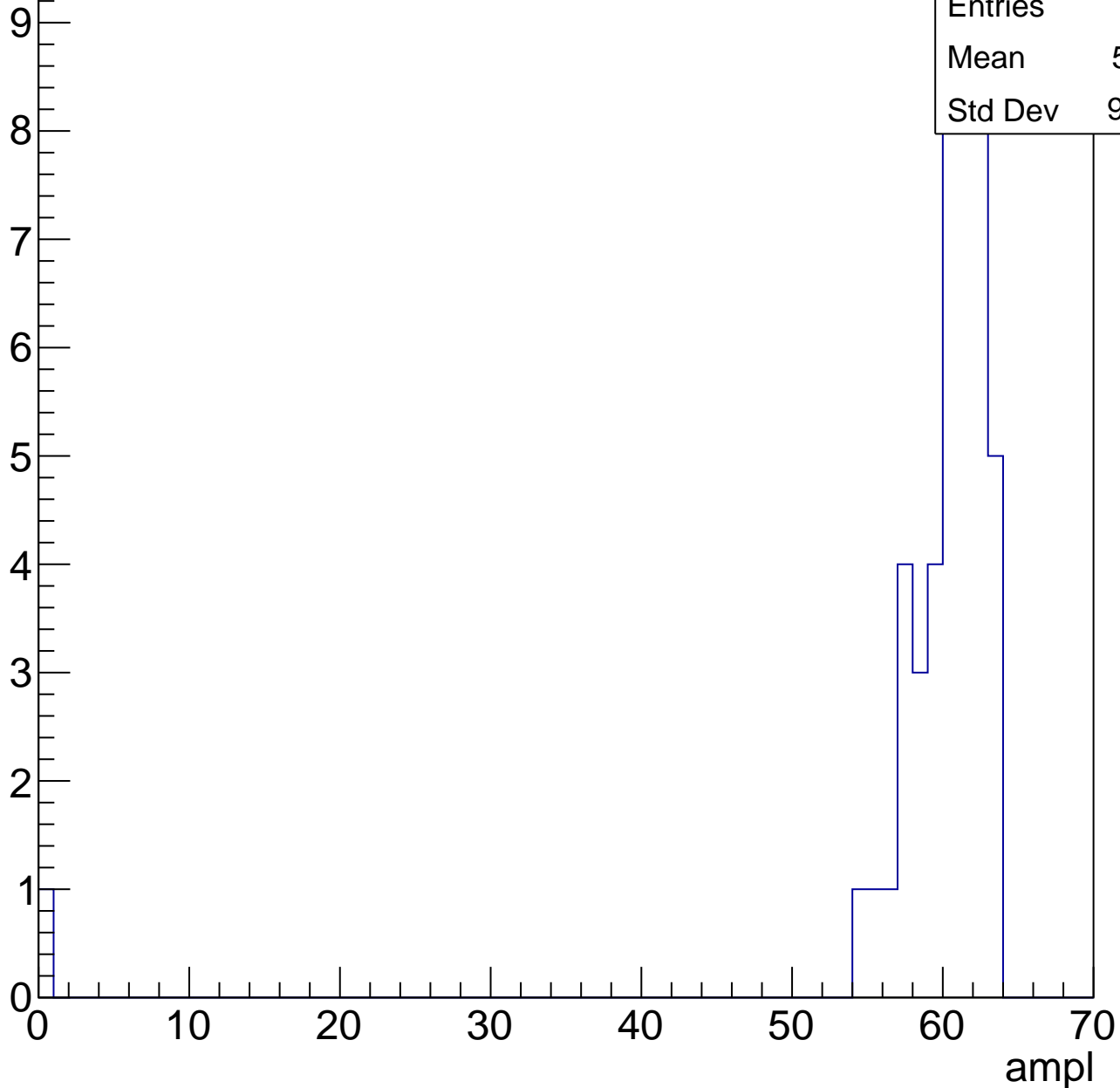
Entries	66
Mean	55.05
Std Dev	3.145



B1L101S, U12-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



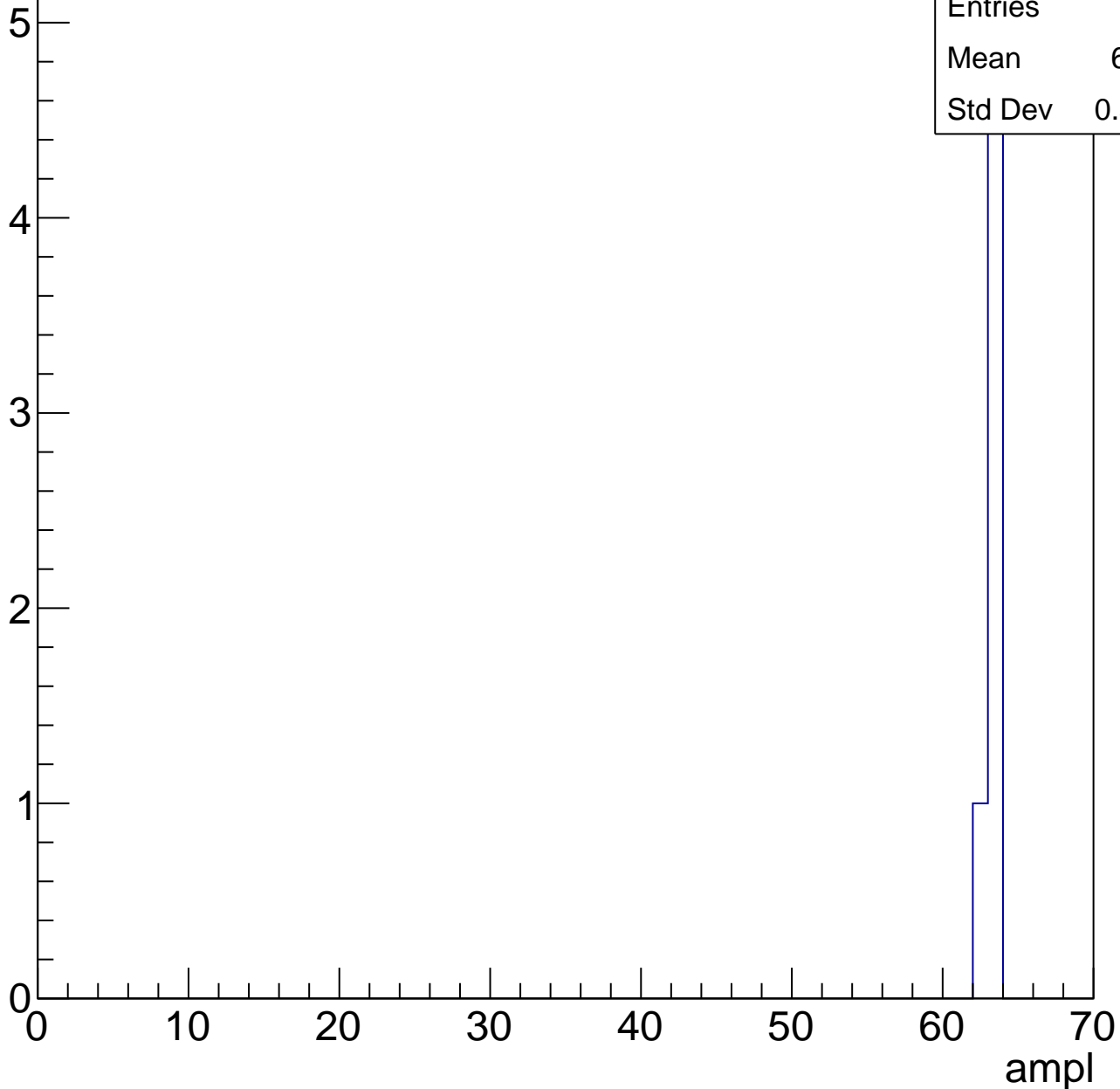
Entries	45
Mean	58.71
Std Dev	9.116

B1L101S, U12-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L101S, U12-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch25, adc0

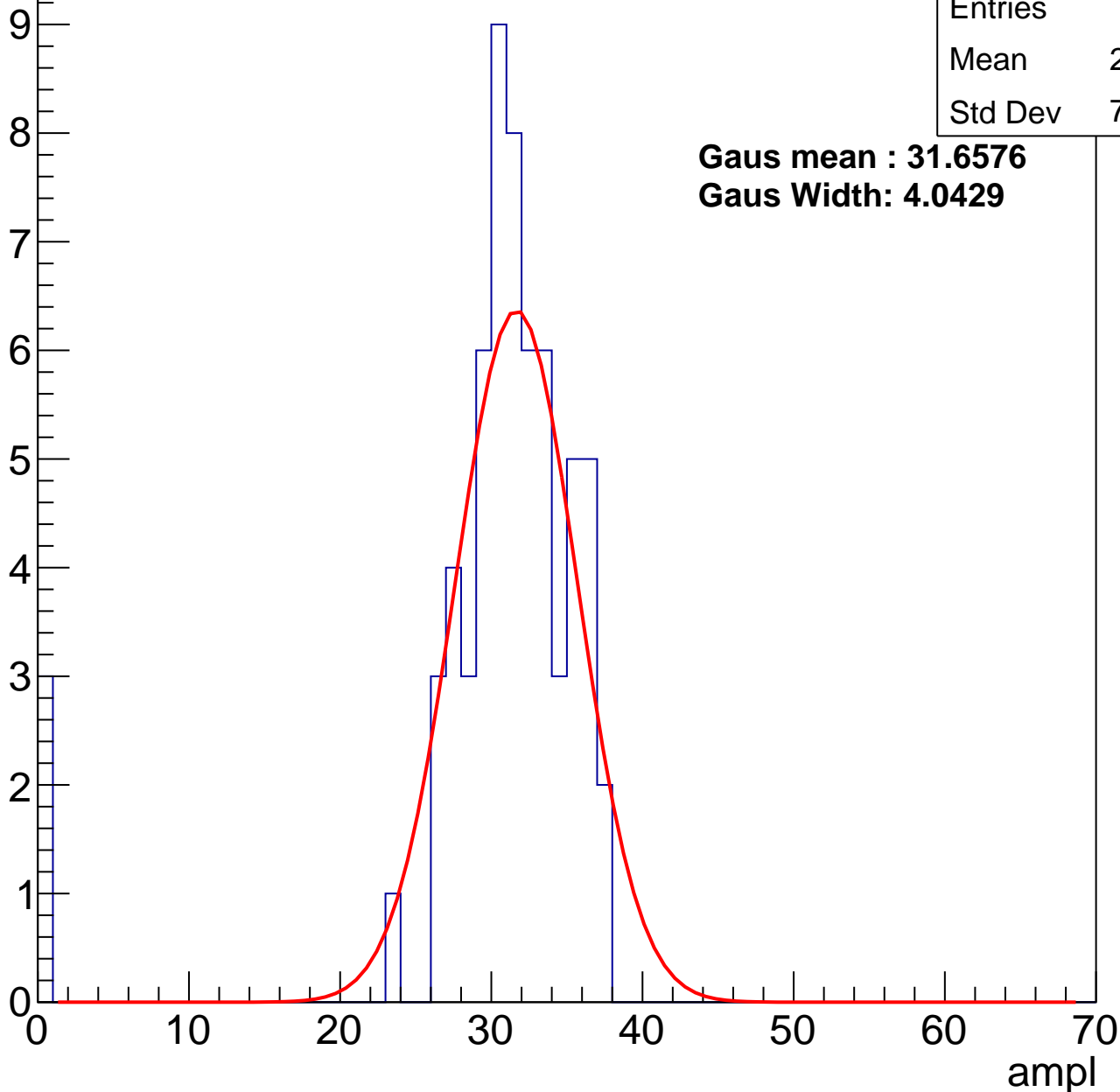
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.78
Std Dev	7.273

Gaus mean : 31.6576

Gaus Width: 4.0429



B1L101S, U12-ch25, adc1

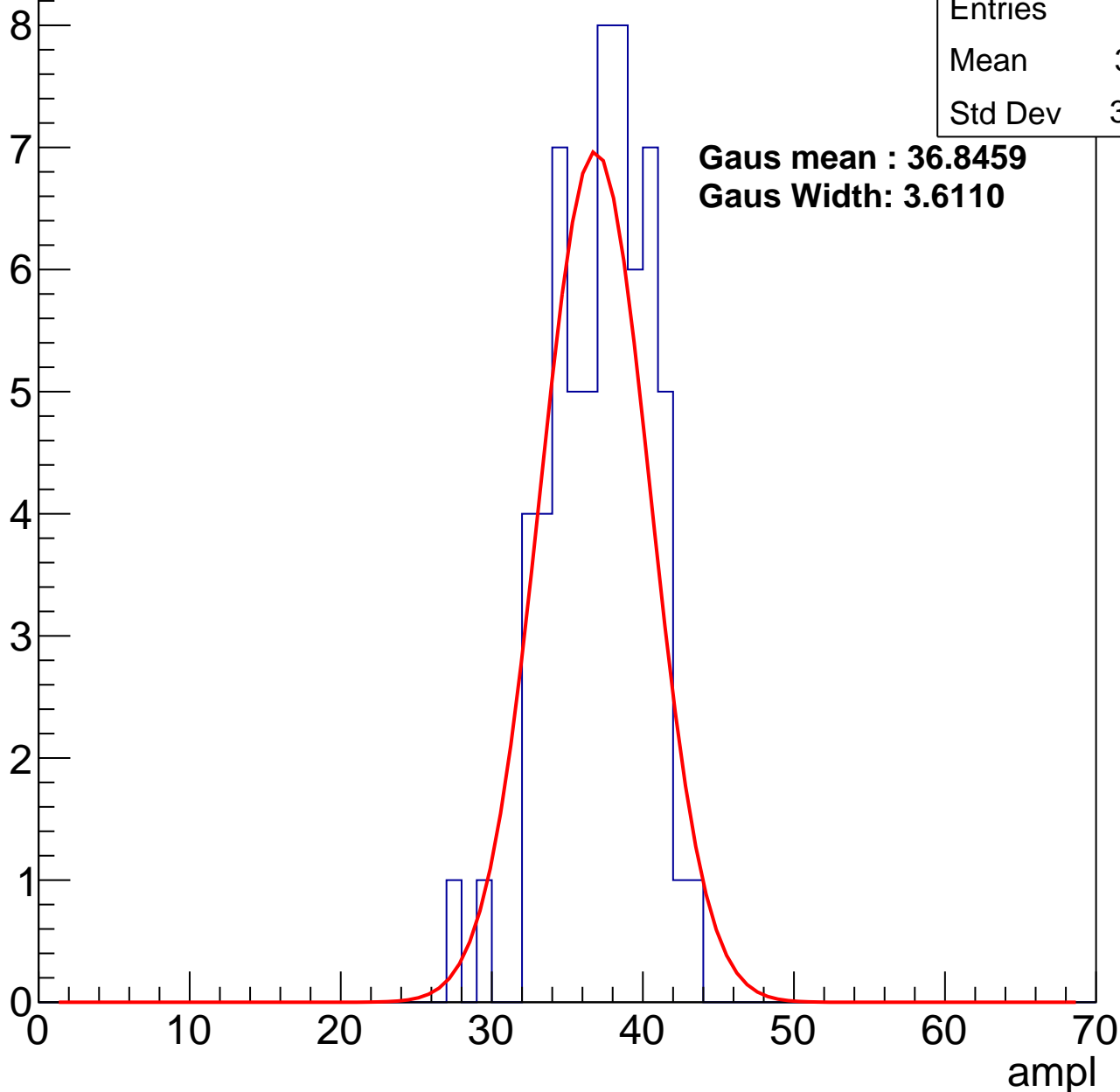
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.71
Std Dev	3.204

Gaus mean : 36.8459

Gaus Width: 3.6110



B1L101S, U12-ch25, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

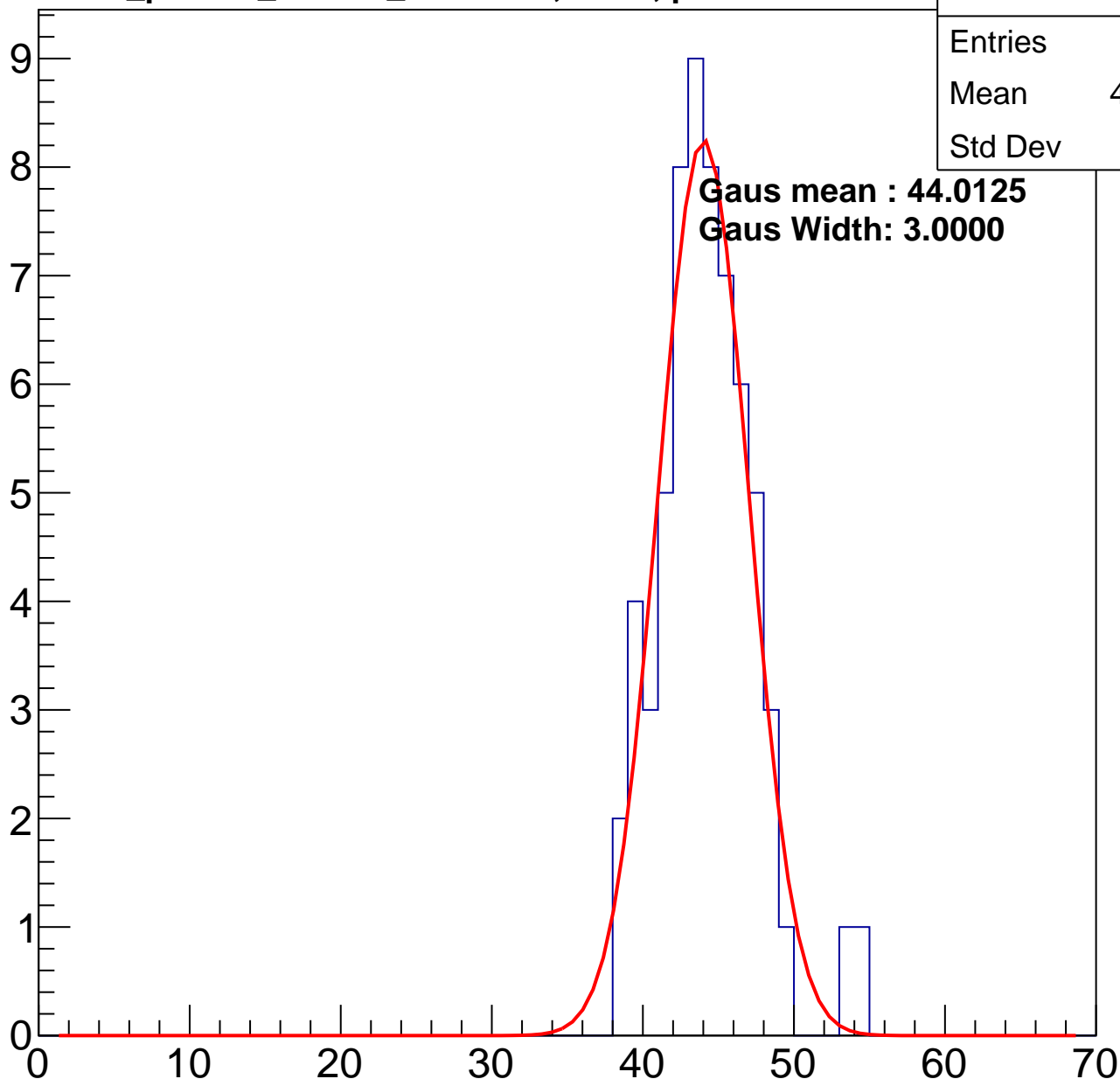
9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	43.78
Std Dev	3.17

Gaus mean : 44.0125

Gaus Width: 3.0000

ampl



B1L101S, U12-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	50.08
Std Dev	3.174

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

1

2

3

4

5

6

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B1L101S, U12-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

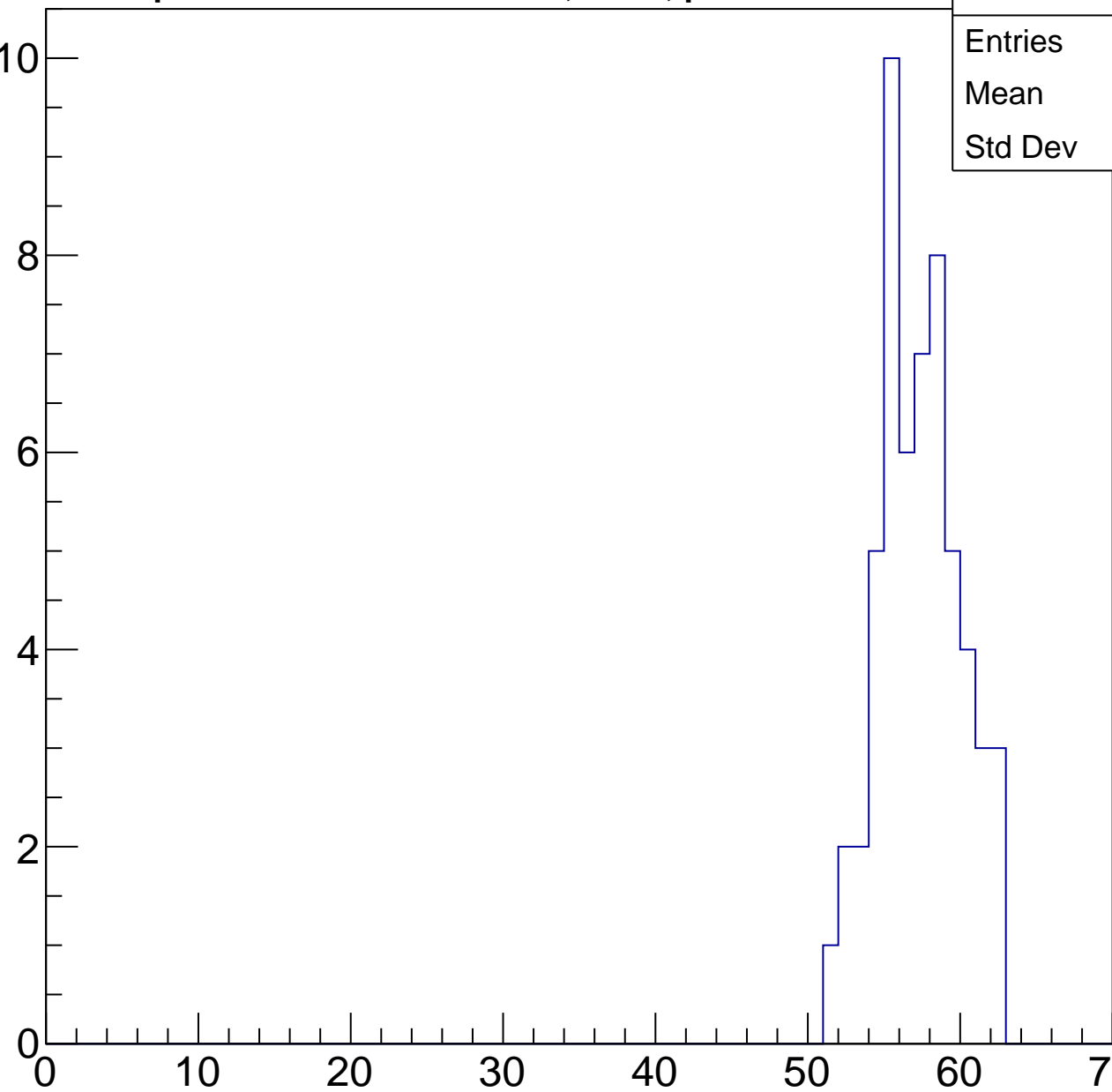
Entries	56
Mean	56.86
Std Dev	2.655

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

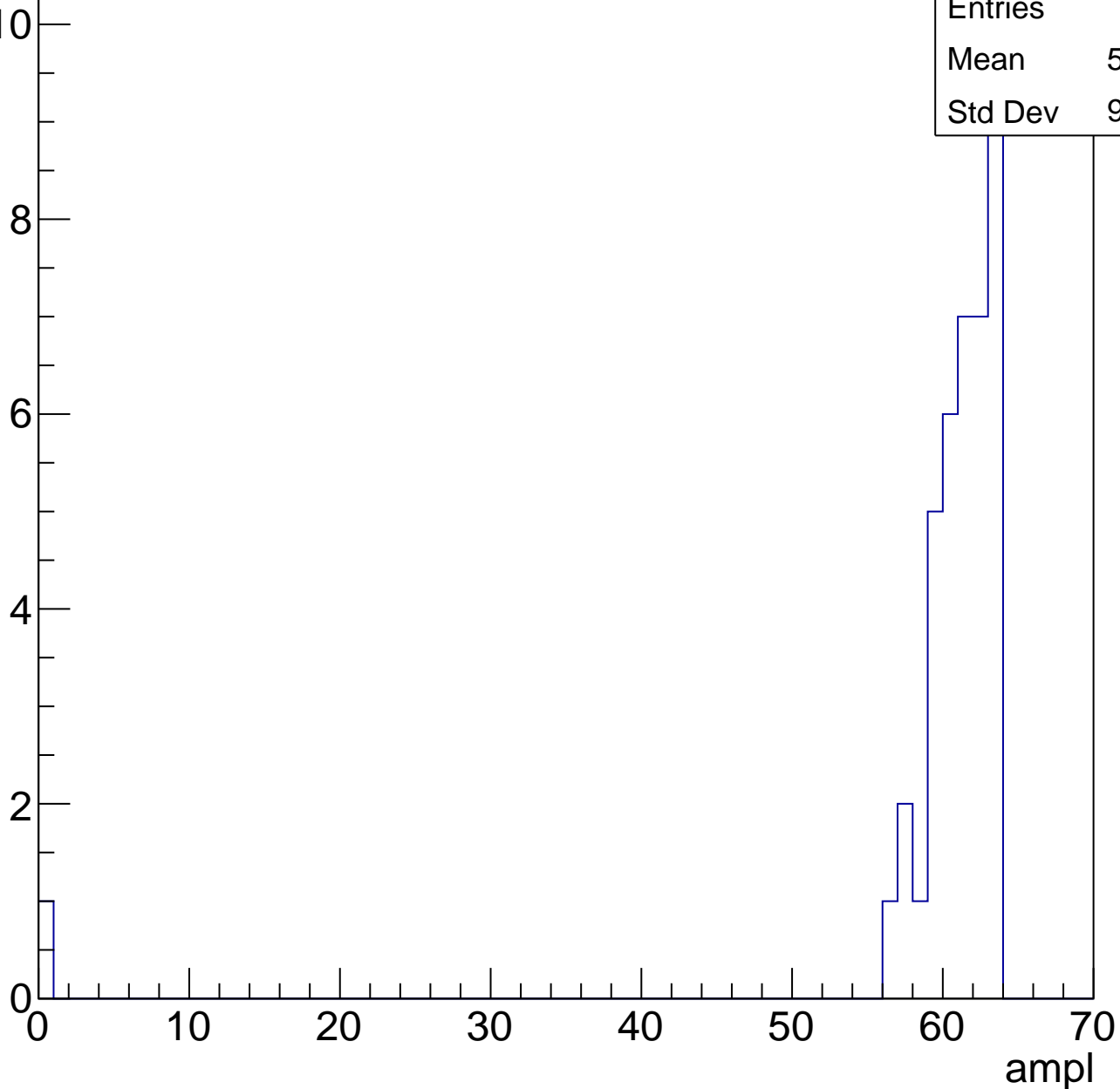


B1L101S, U12-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.35
Std Dev	9.684



B1L101S, U12-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch26, adc0

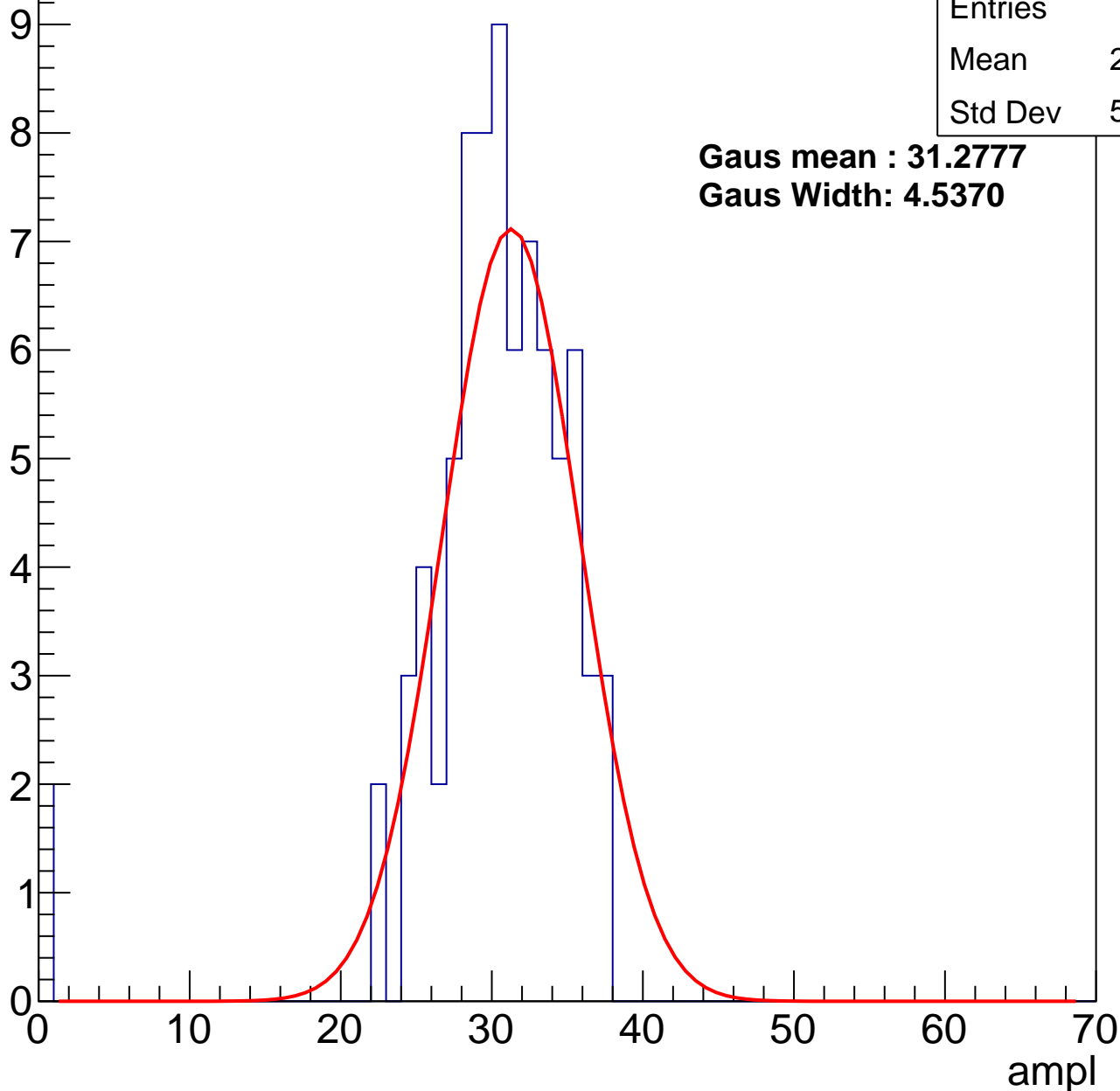
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.57
Std Dev	5.965

Gaus mean : 31.2777

Gaus Width: 4.5370



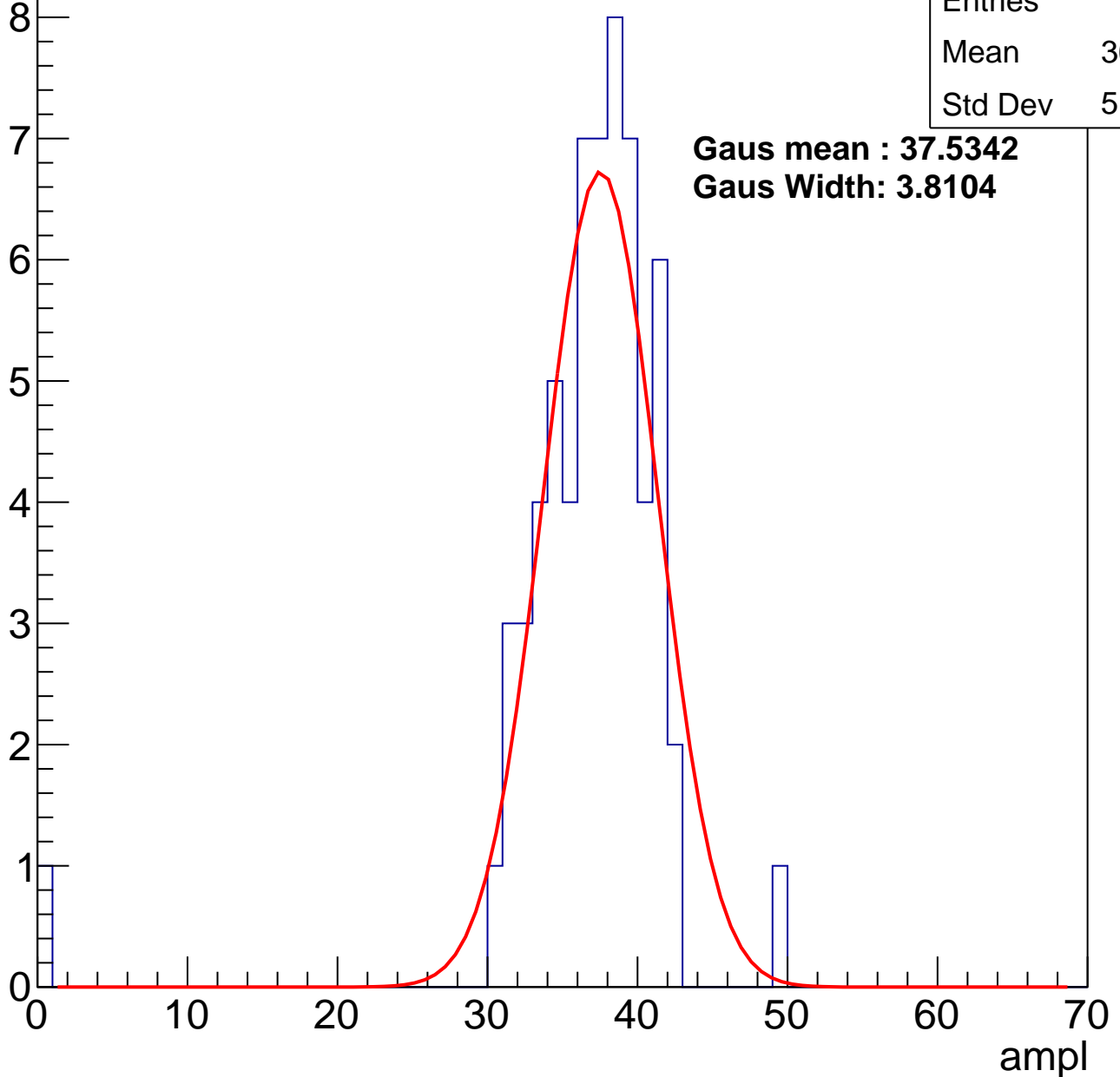
B1L101S, U12-ch26, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.32
Std Dev	5.726

Gaus mean : 37.5342
Gaus Width: 3.8104



B1L101S, U12-ch26, adc2

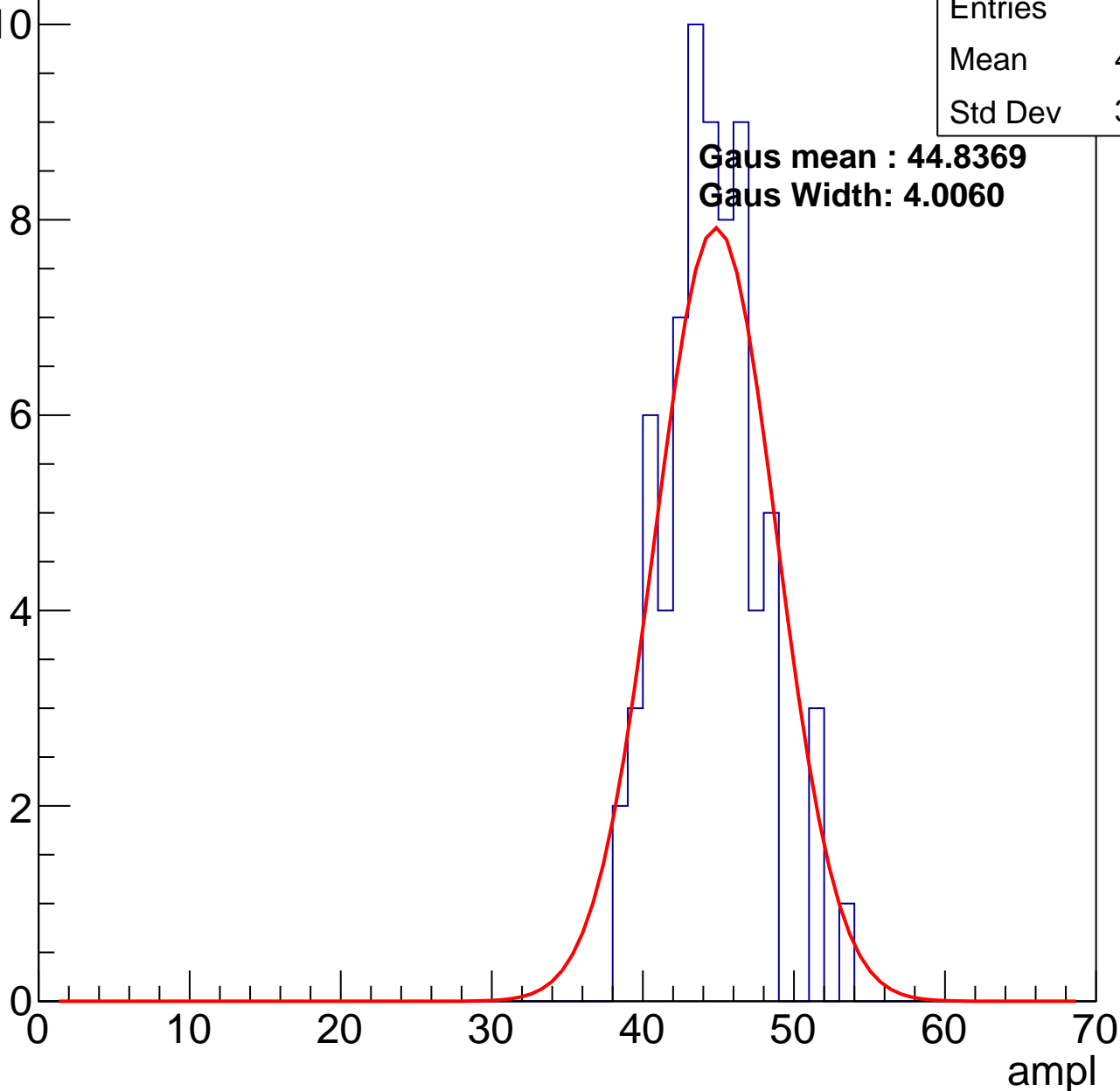
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.01
Std Dev	3.151

Gaus mean : 44.8369

Gaus Width: 4.0060

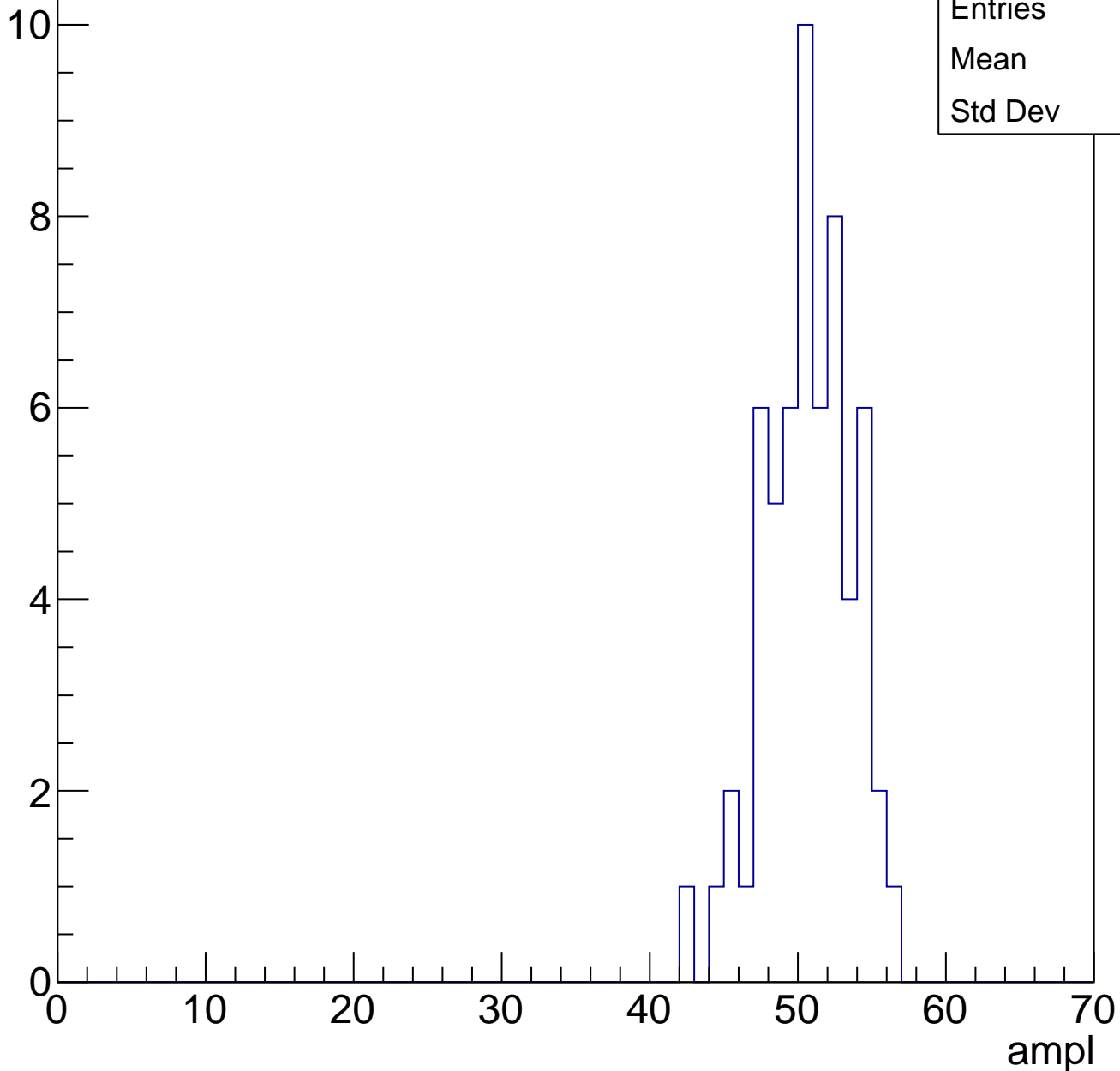


B1L101S, U12-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.2
Std Dev	2.91

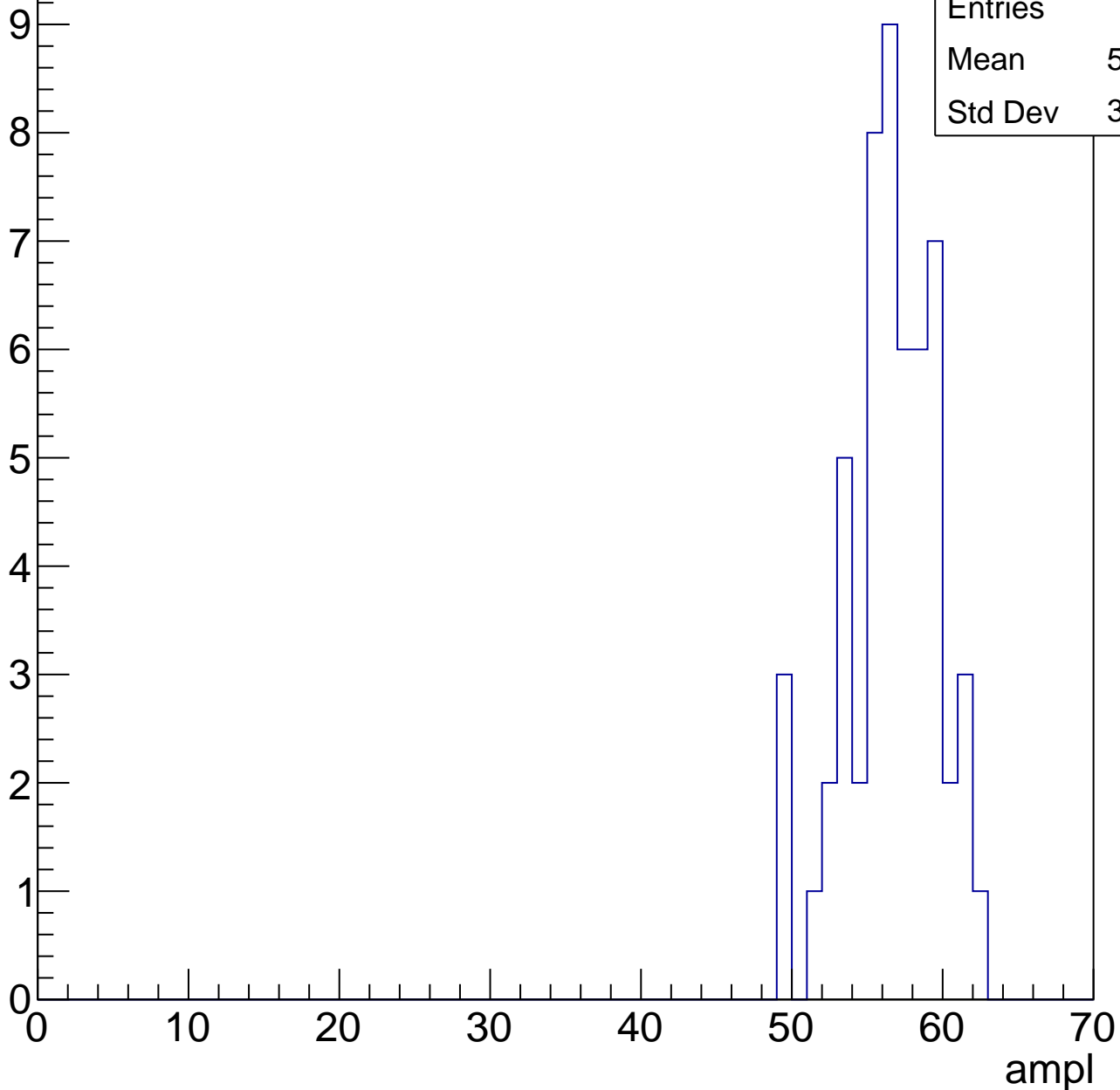


B1L101S, U12-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.13
Std Dev	3.015

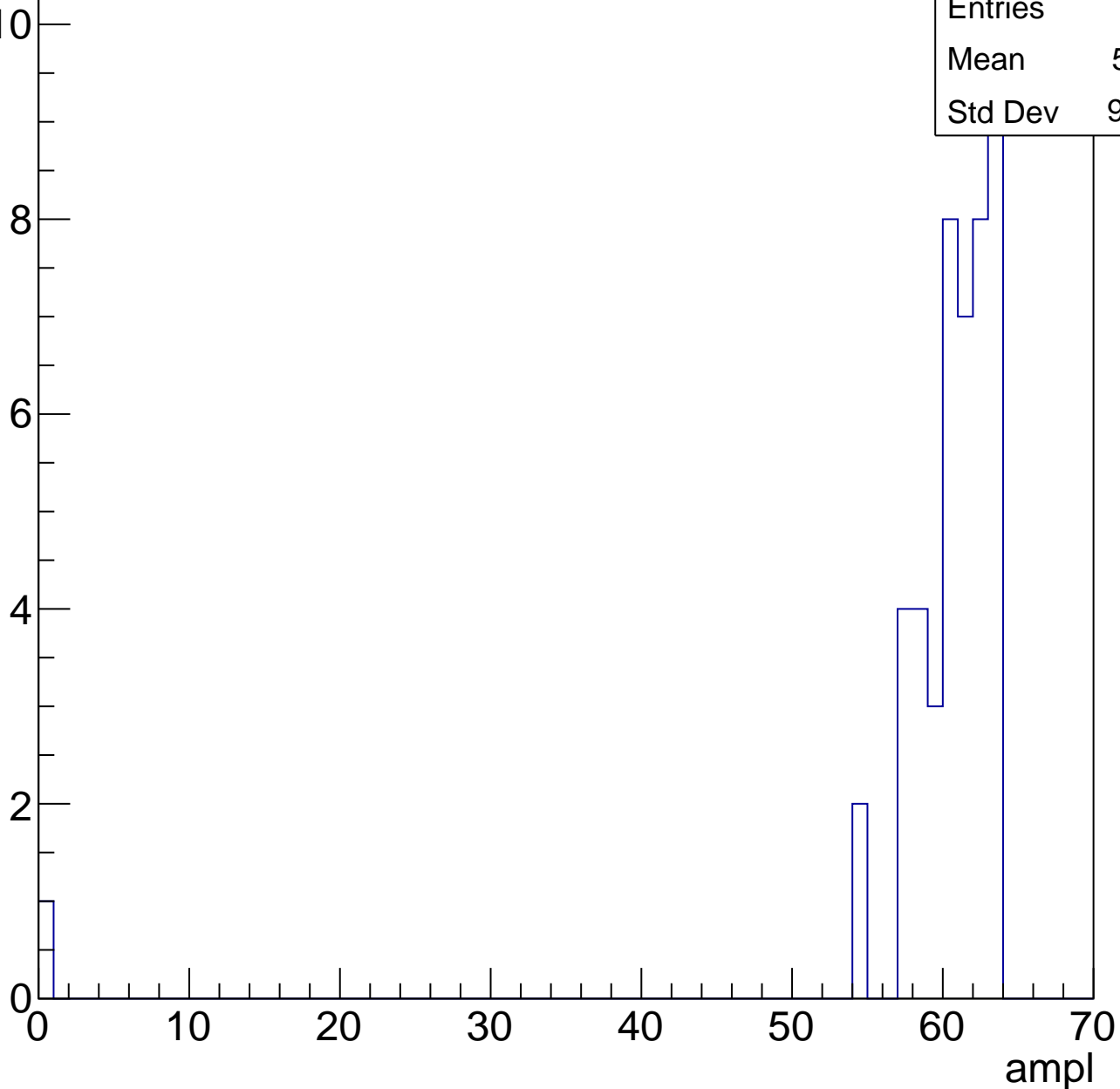


B1L101S, U12-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59.11
Std Dev	9.014



B1L101S, U12-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch27, adc0

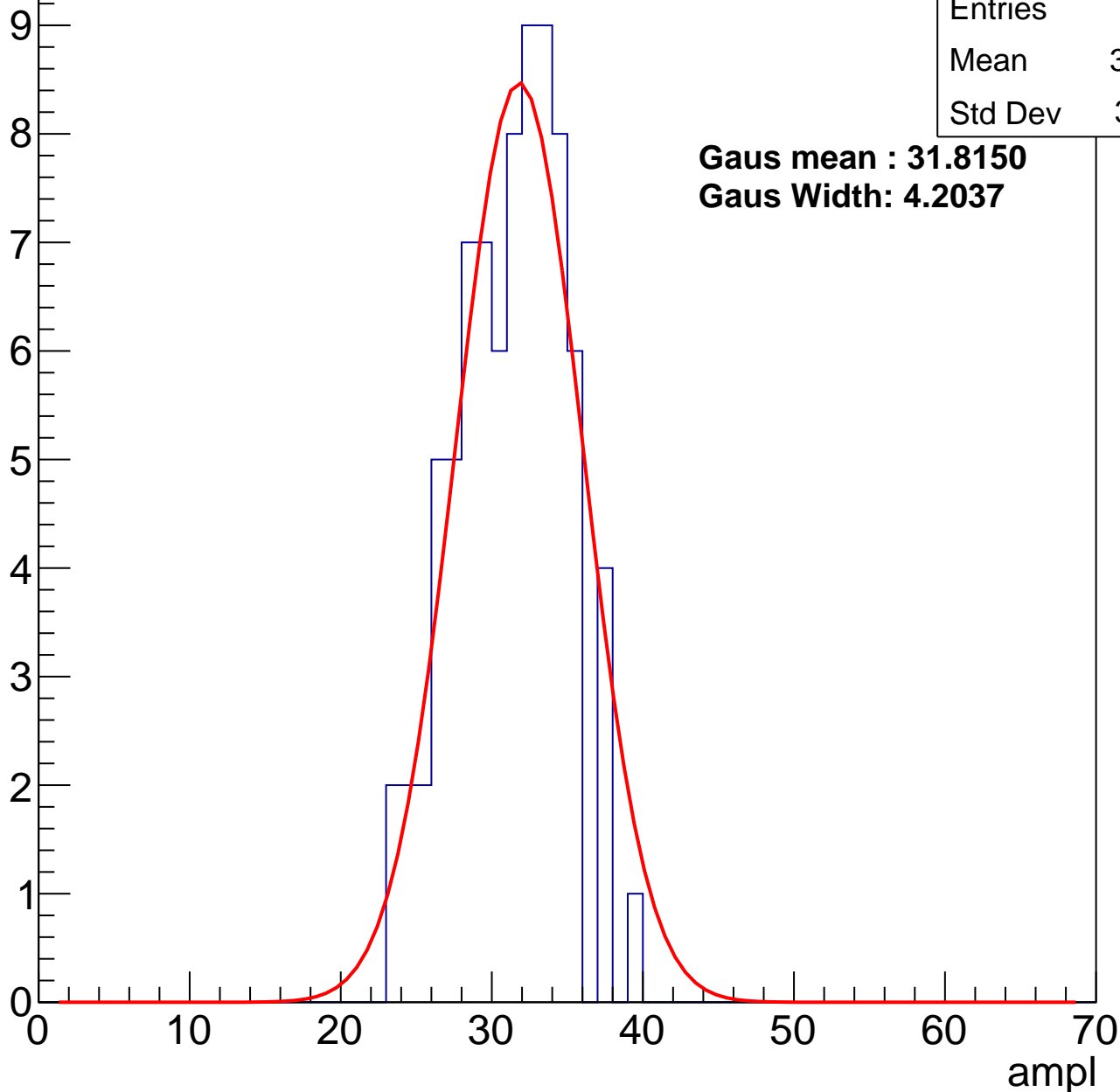
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.74
Std Dev	3.541

Gaus mean : 31.8150

Gaus Width: 4.2037



B1L101S, U12-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	38.15
Std Dev	3.497

Gaus mean : 39.2902

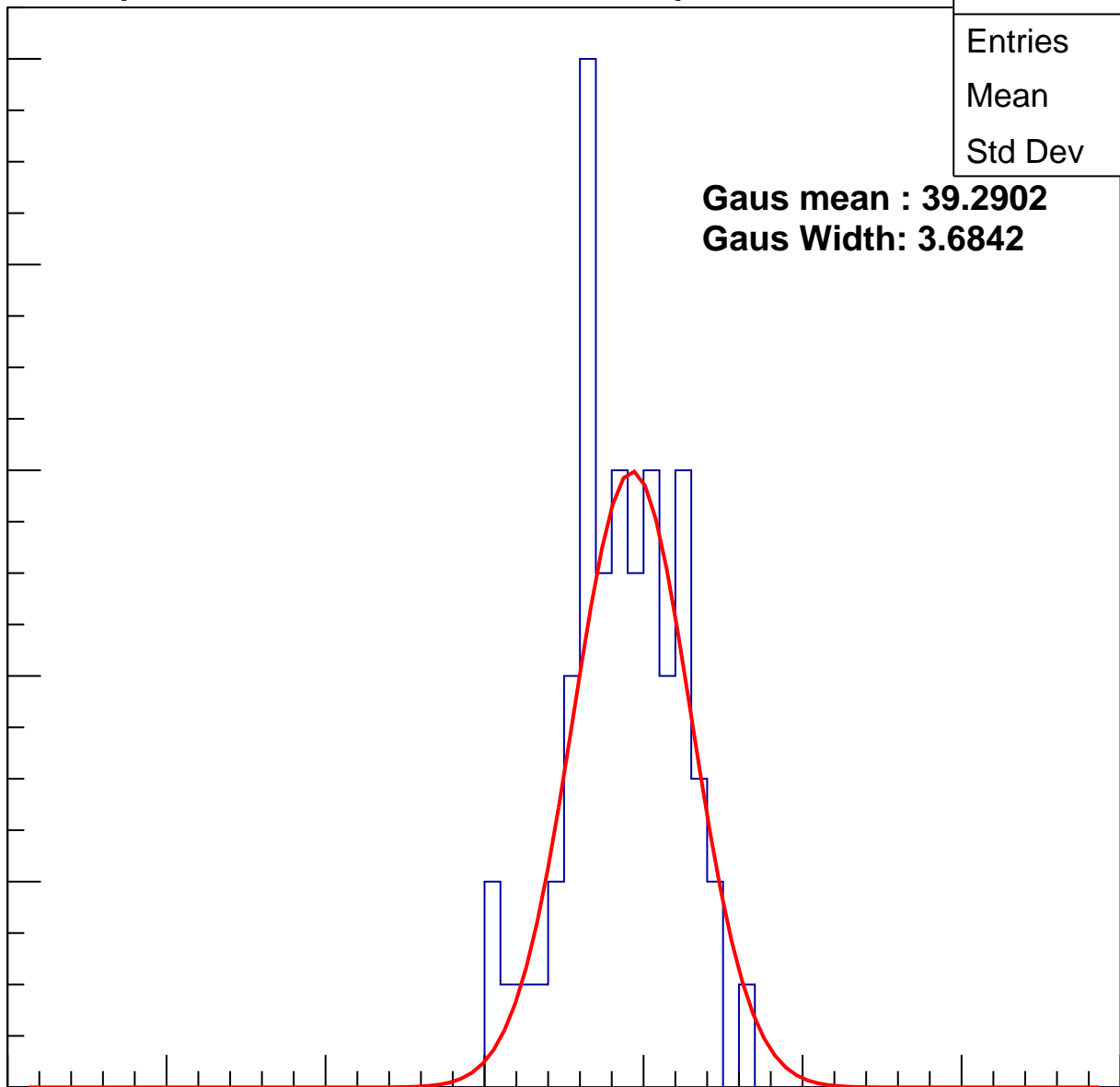
Gaus Width: 3.6842

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch27, adc2

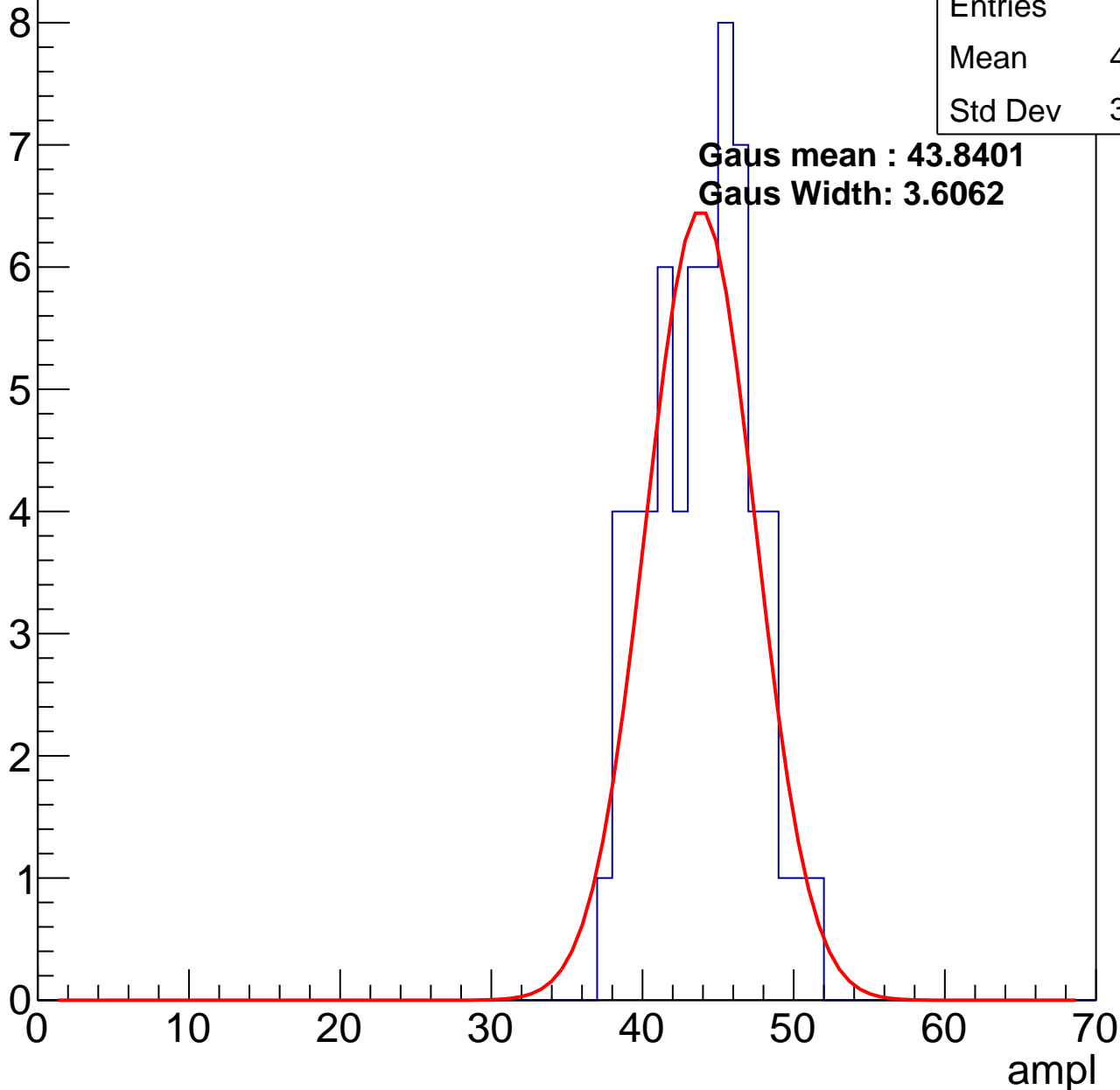
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.49
Std Dev	3.297

Gaus mean : 43.8401

Gaus Width: 3.6062

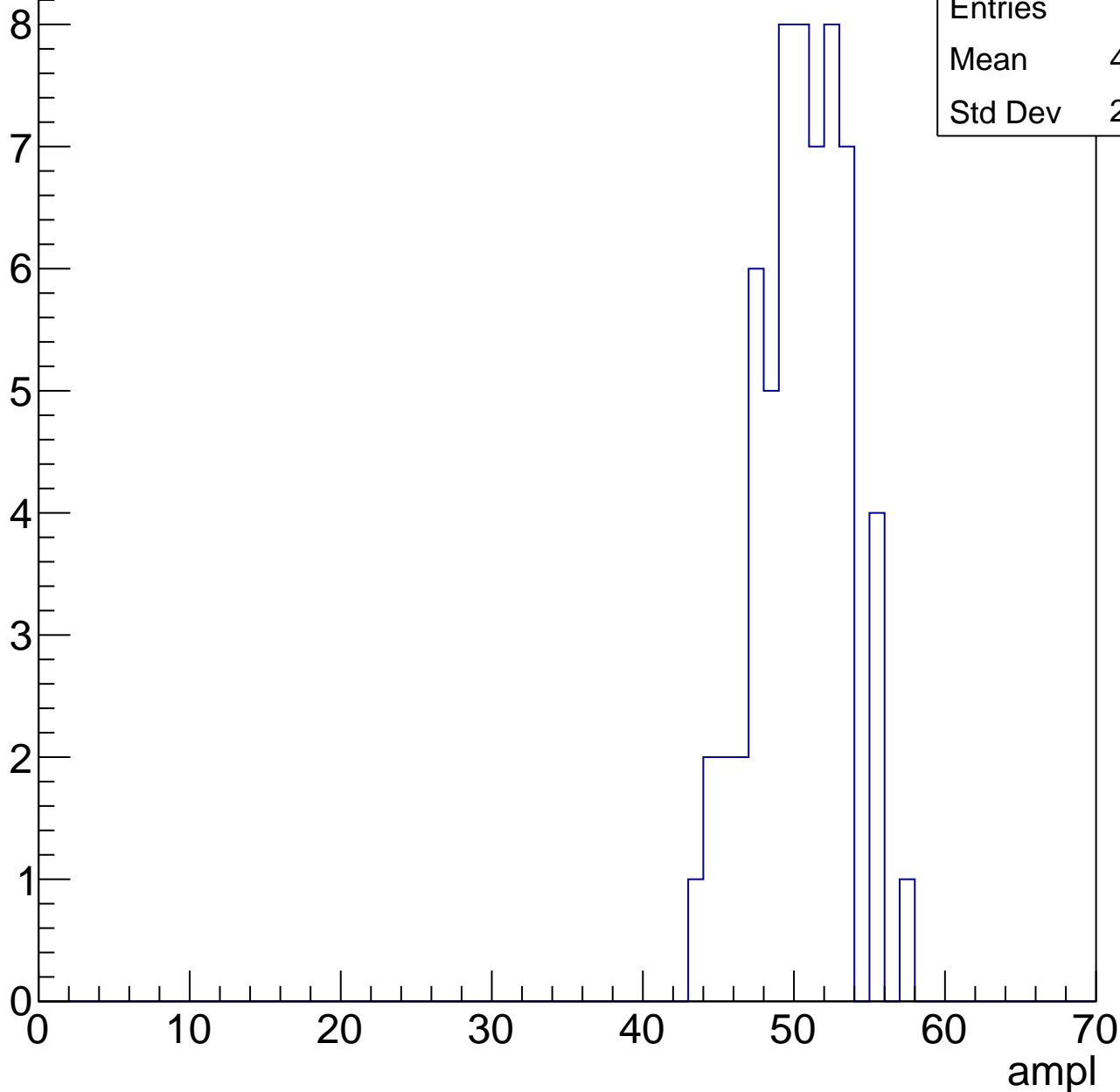


B1L101S, U12-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

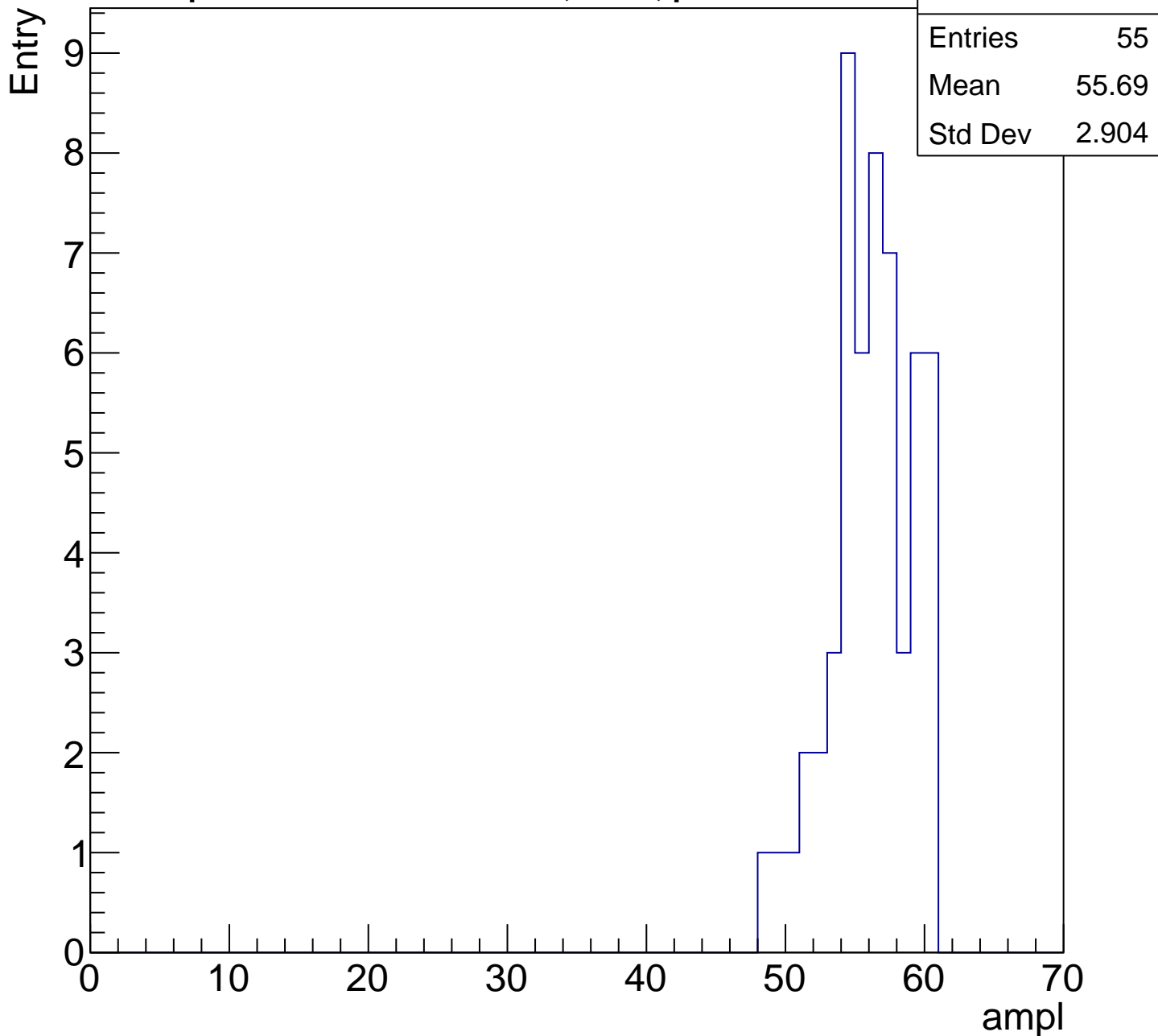
Entry

Entries	61
Mean	49.97
Std Dev	2.964



B1L101S, U12-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

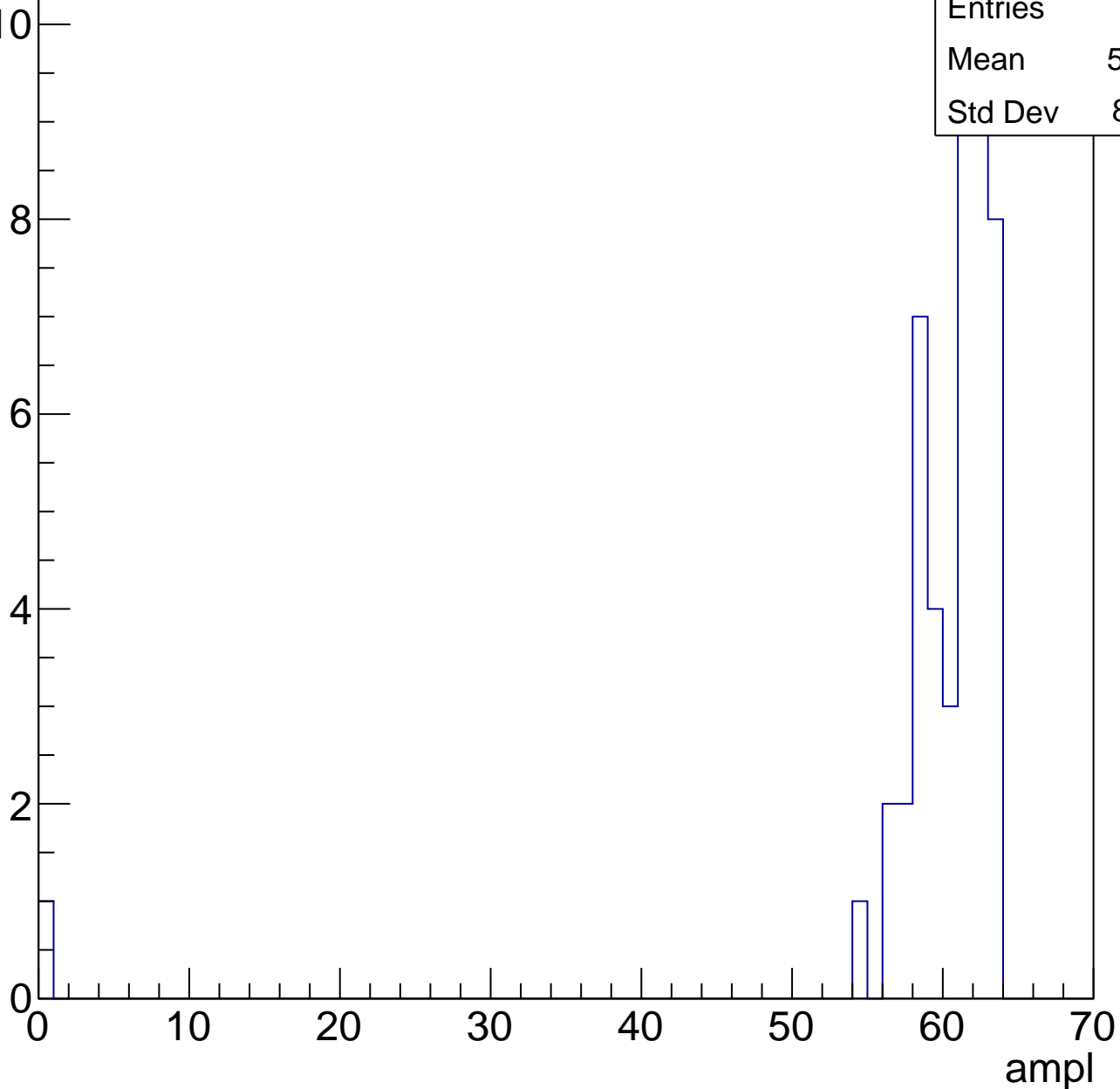


B1L101S, U12-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59.02
Std Dev	8.981



B1L101S, U12-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch28, adc0

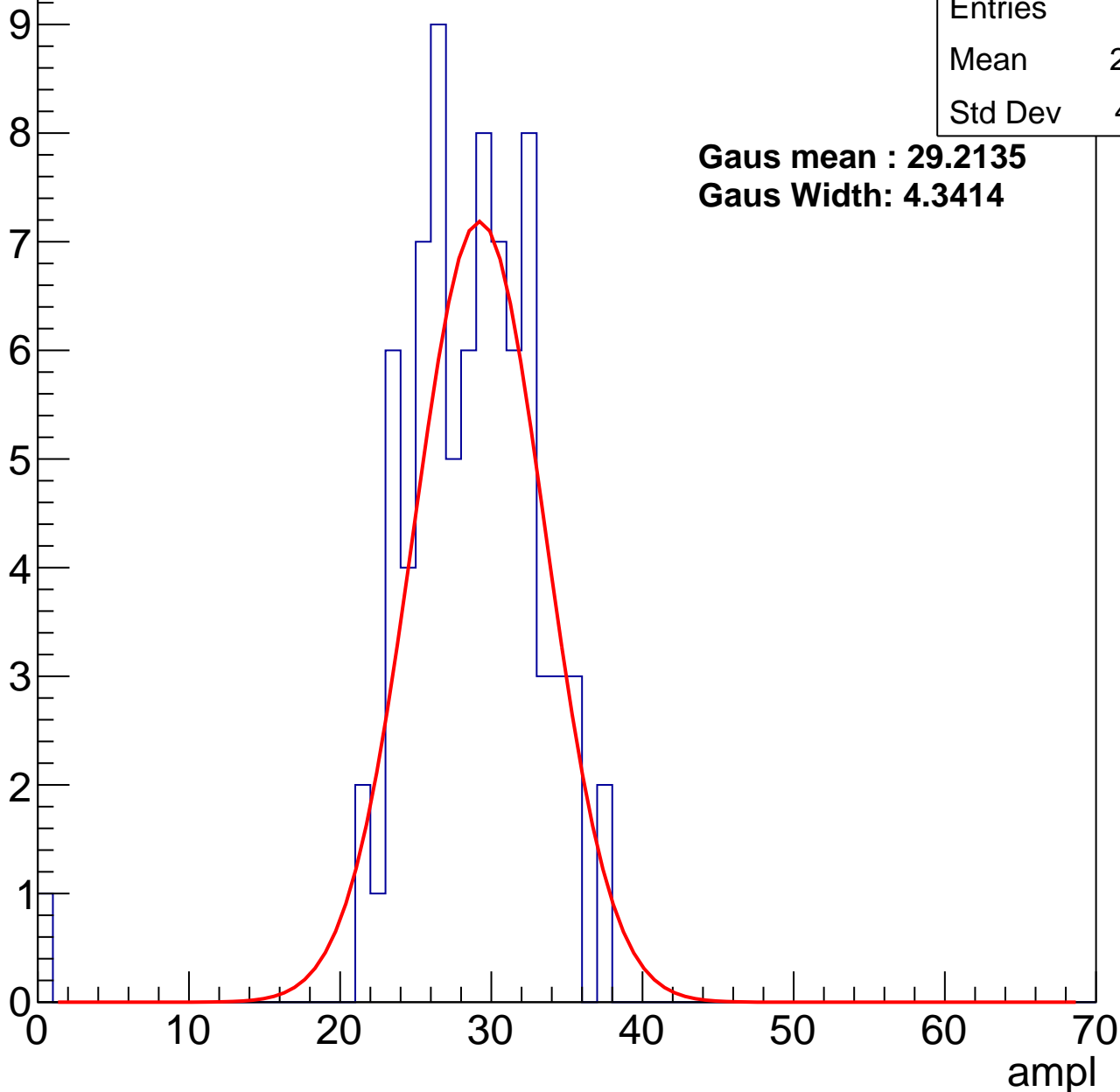
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.07
Std Dev	4.901

Gaus mean : 29.2135

Gaus Width: 4.3414



B1L101S, U12-ch28, adc1

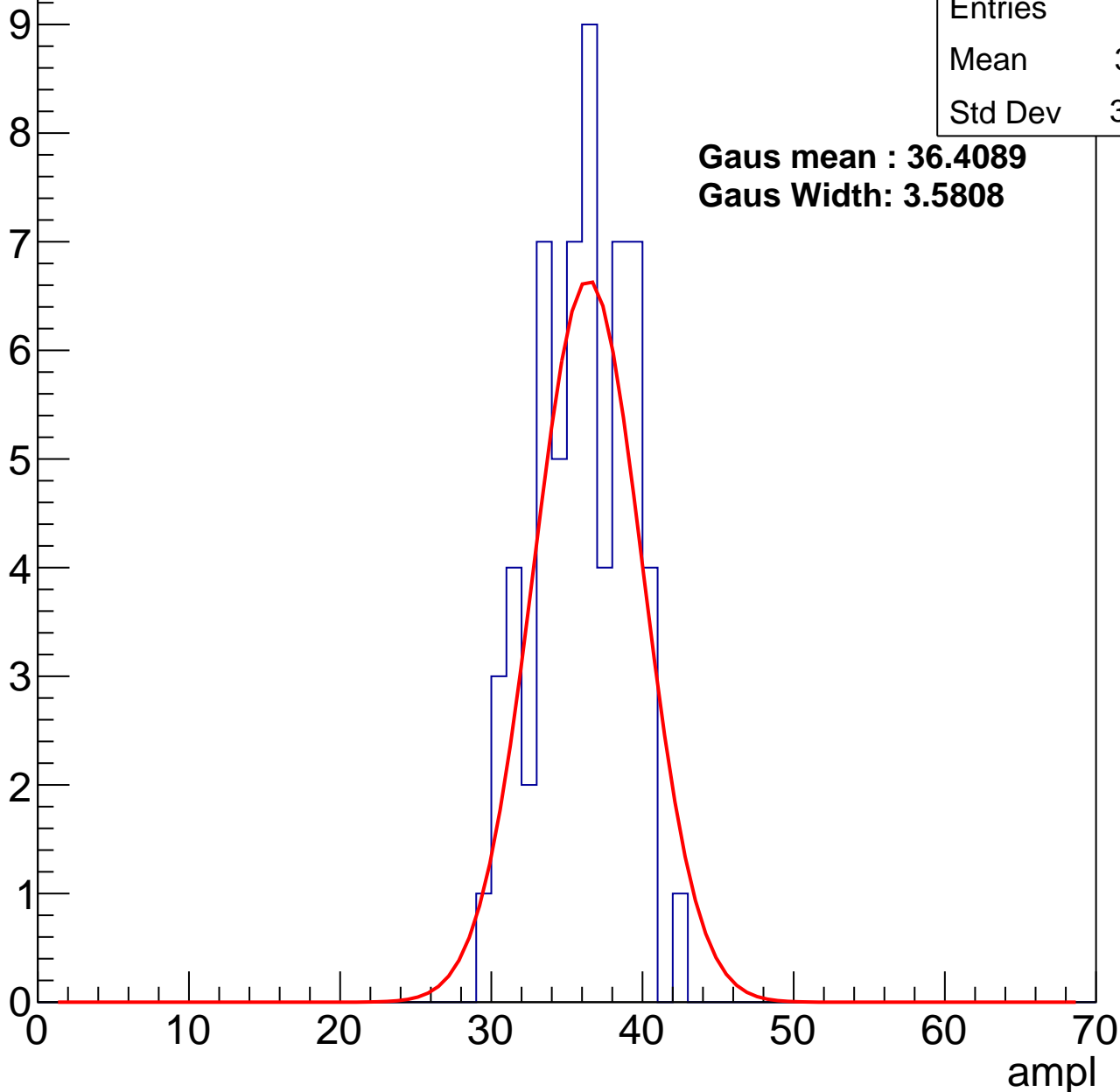
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.51
Std Dev	3.028

Gaus mean : 36.4089

Gaus Width: 3.5808



B1L101S, U12-ch28, adc2

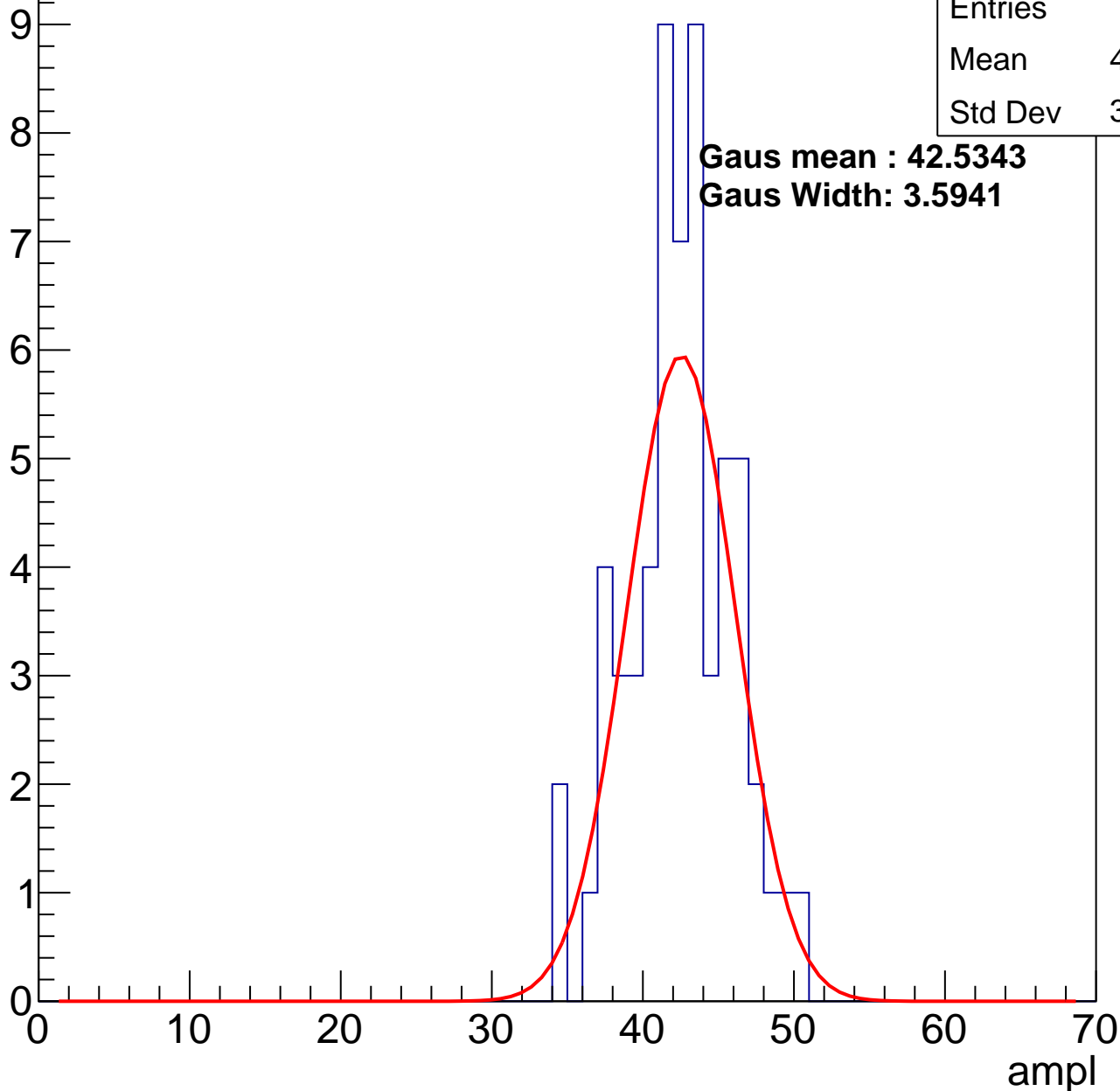
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.02
Std Dev	3.437

Gaus mean : 42.5343

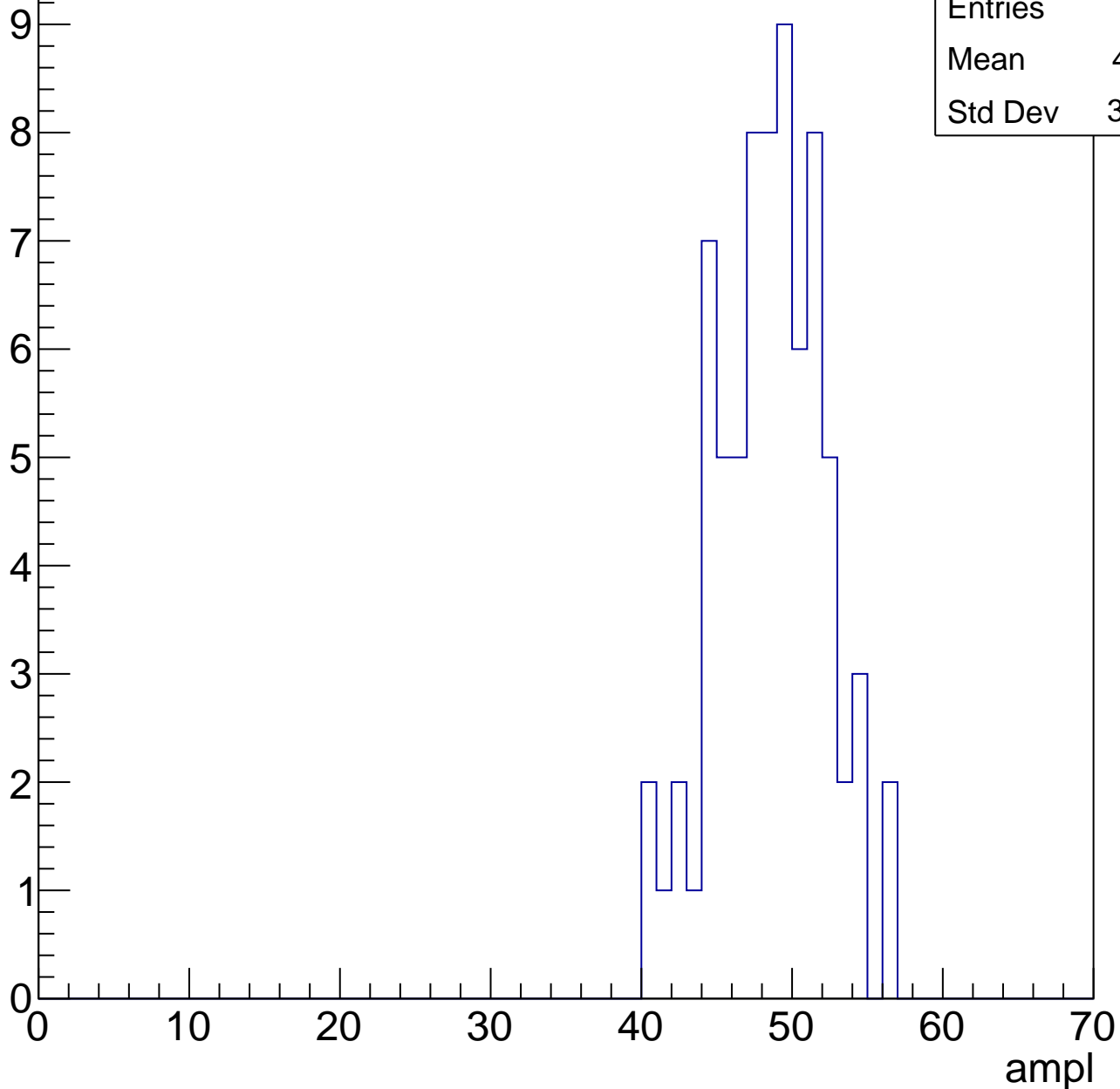
Gaus Width: 3.5941



B1L101S, U12-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

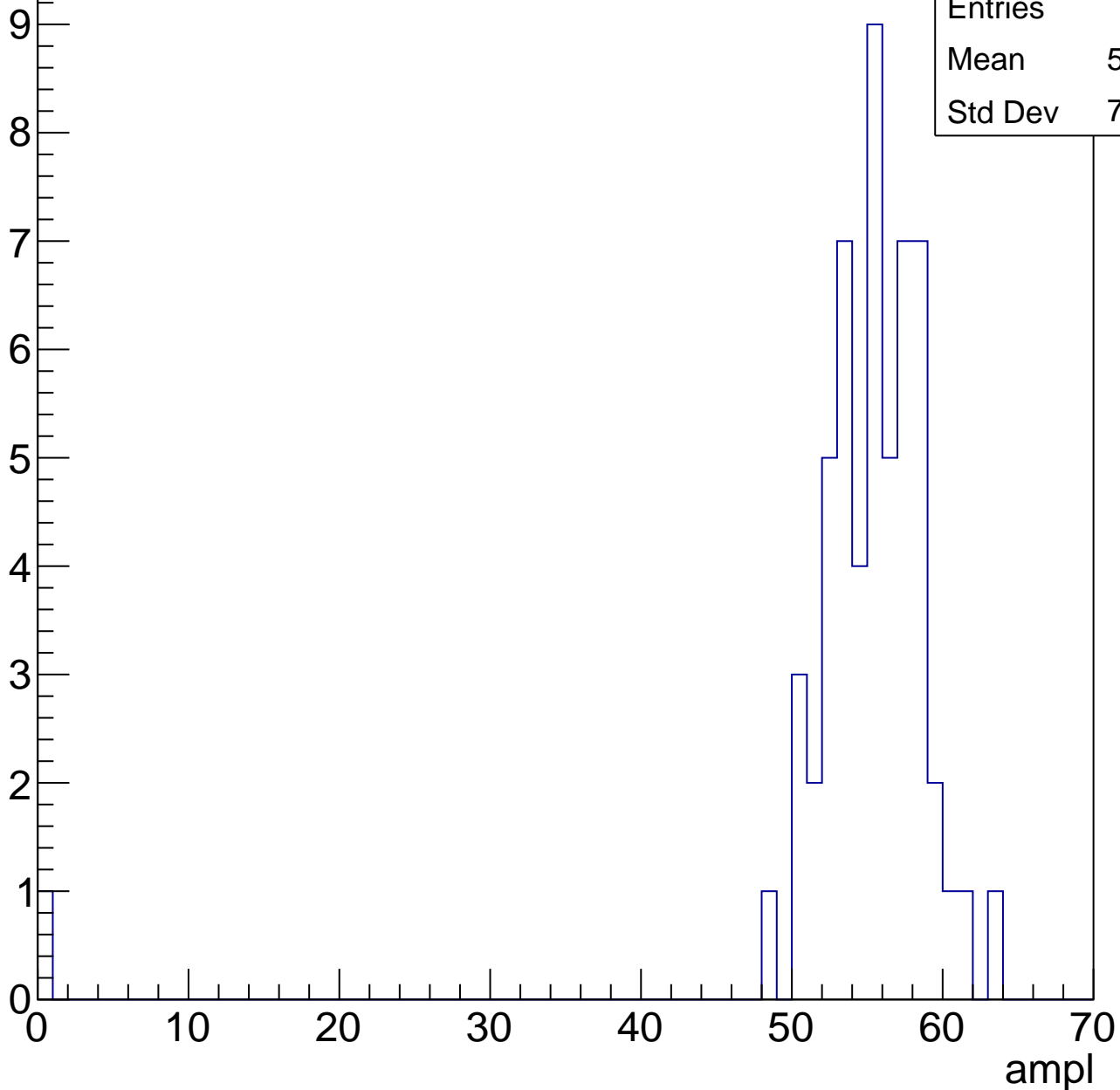


B1L101S, U12-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

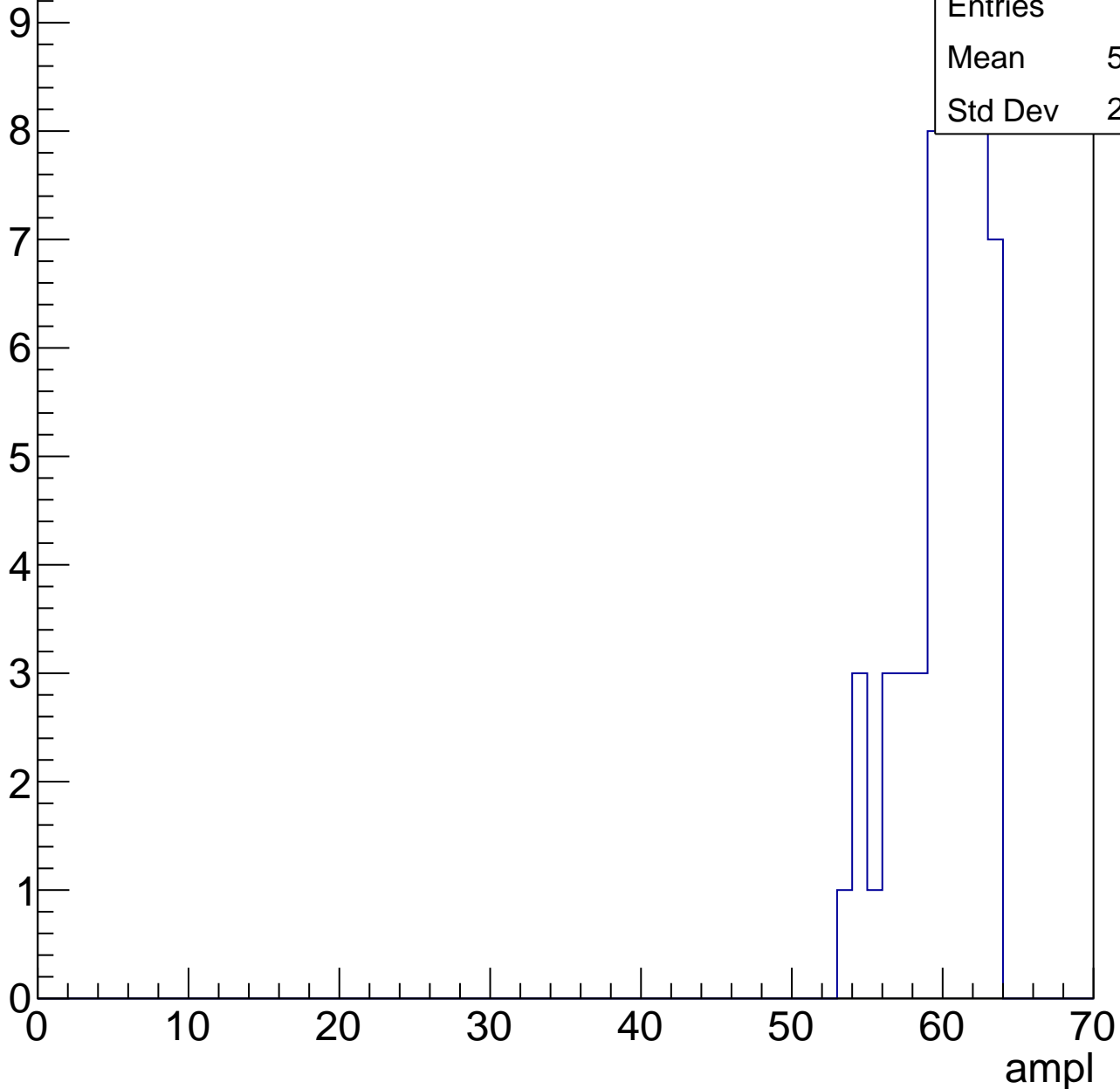
Entries	56
Mean	54.09
Std Dev	7.866



B1L101S, U12-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	55
Mean	59.67
Std Dev	2.622

B1L101S, U12-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L101S, U12-ch29, adc0

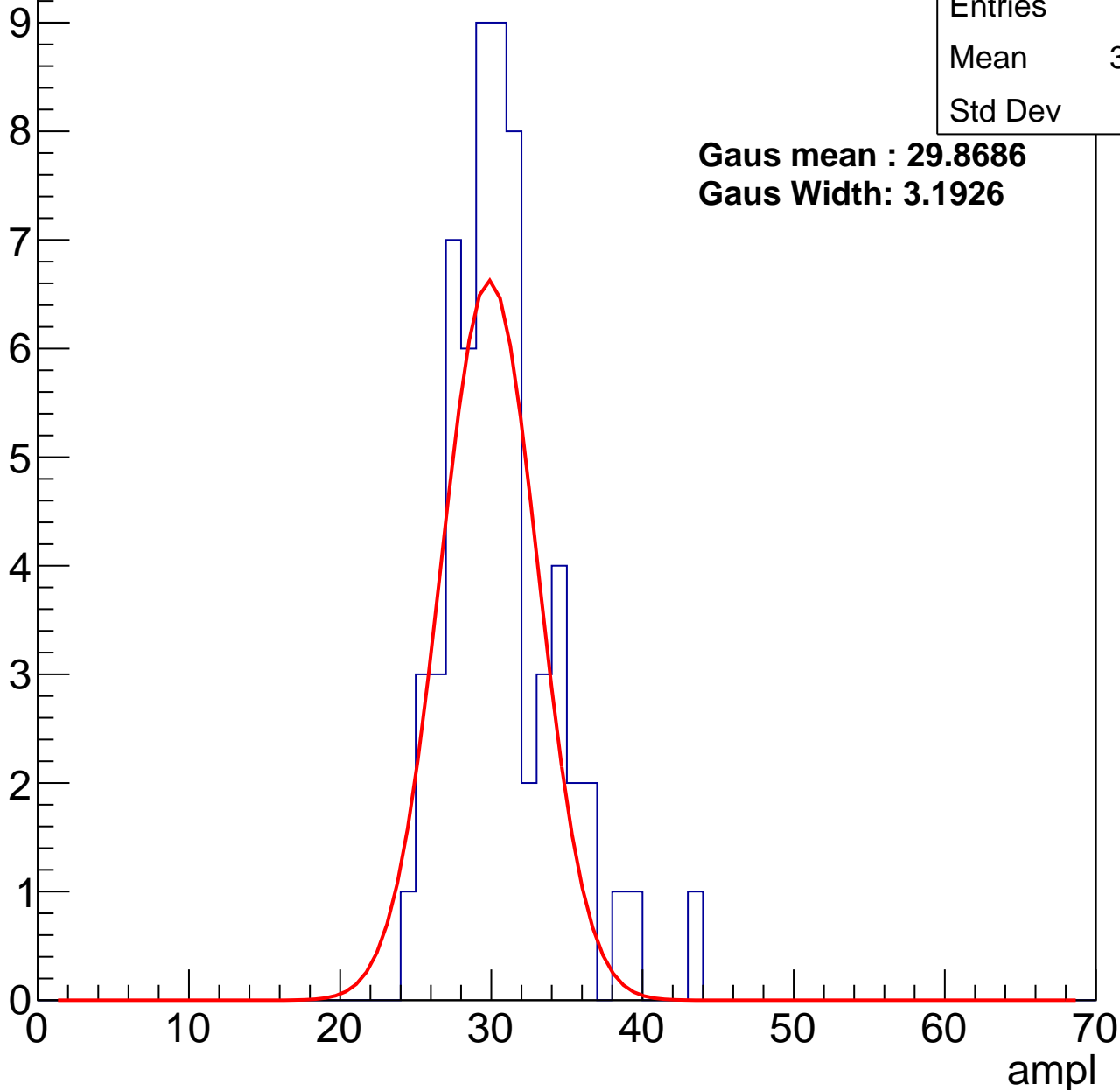
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.23
Std Dev	3.58

Gaus mean : 29.8686

Gaus Width: 3.1926



B1L101S, U12-ch29, adc1

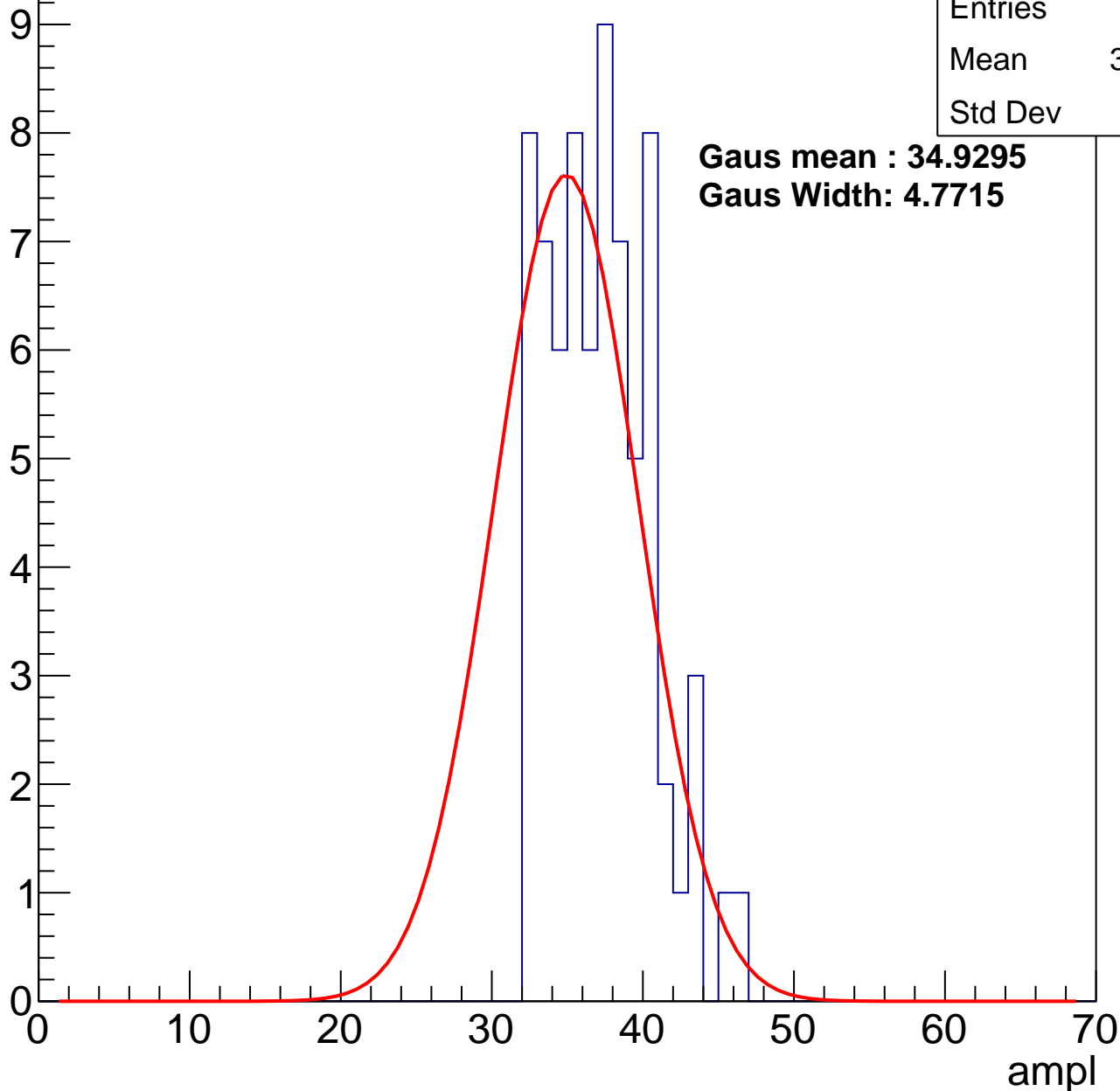
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.74
Std Dev	3.35

Gaus mean : 34.9295

Gaus Width: 4.7715



B1L101S, U12-ch29, adc2

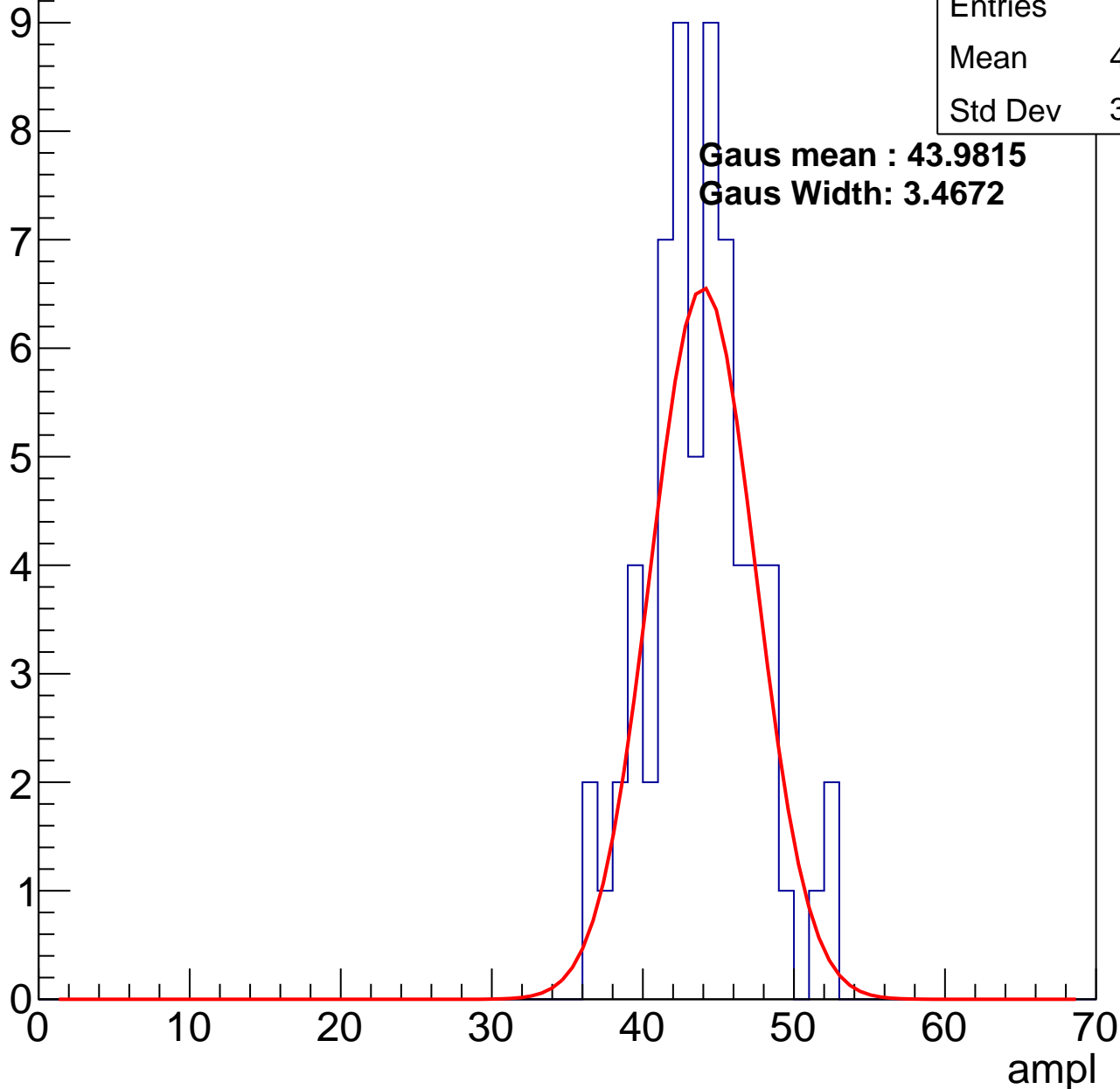
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.44
Std Dev	3.513

Gaus mean : 43.9815

Gaus Width: 3.4672

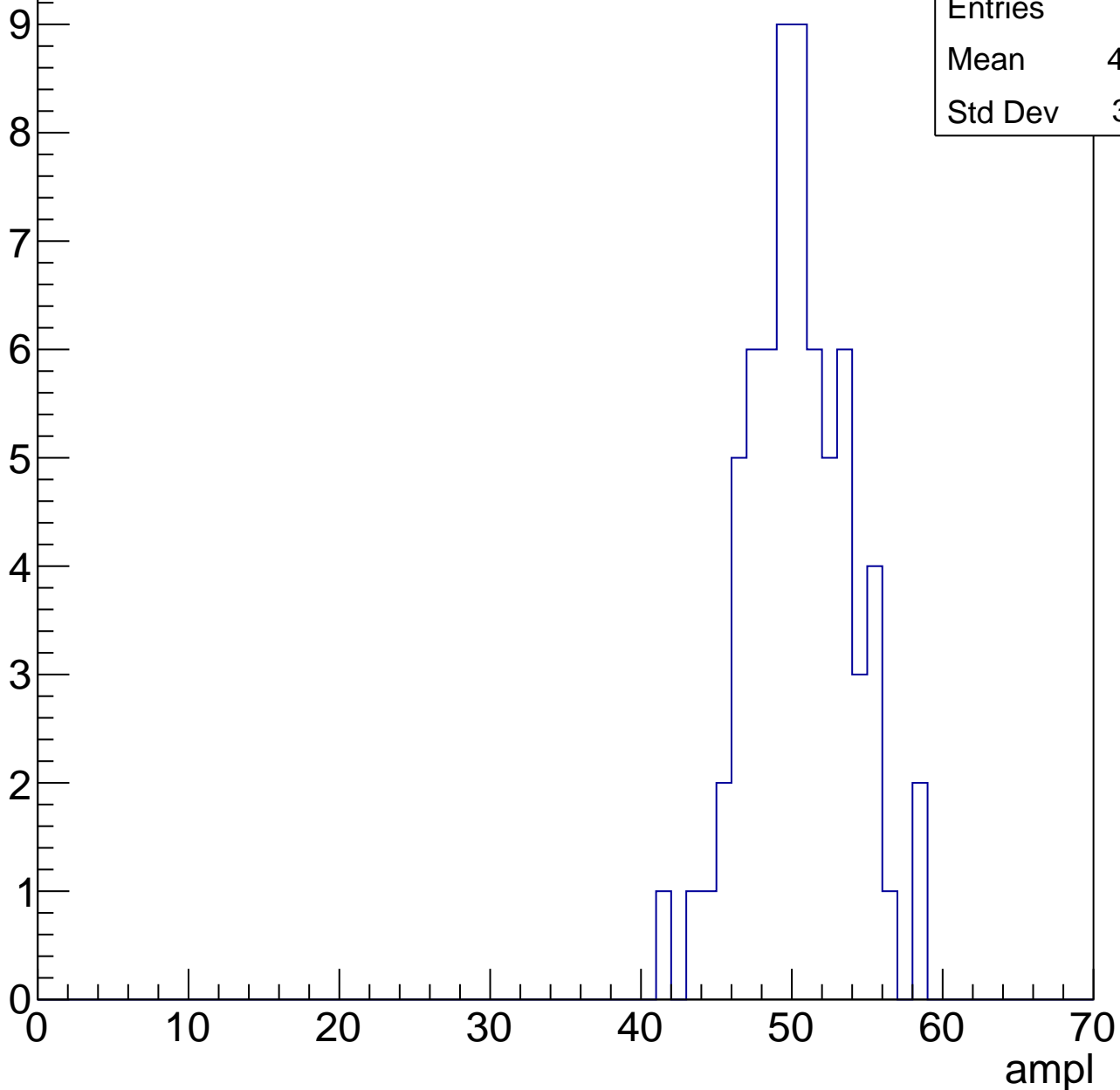


B1L101S, U12-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.96
Std Dev	3.401

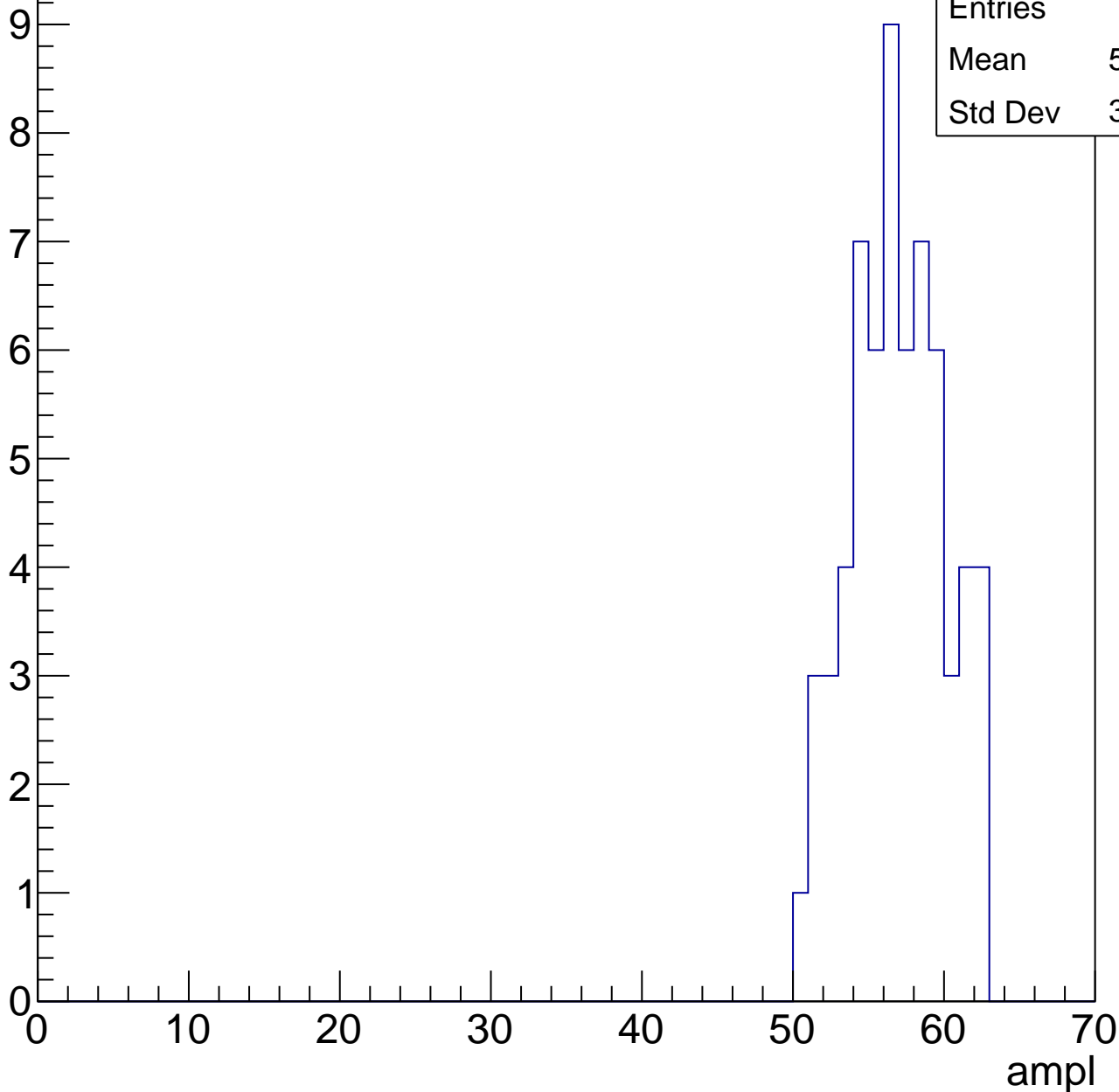


B1L101S, U12-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

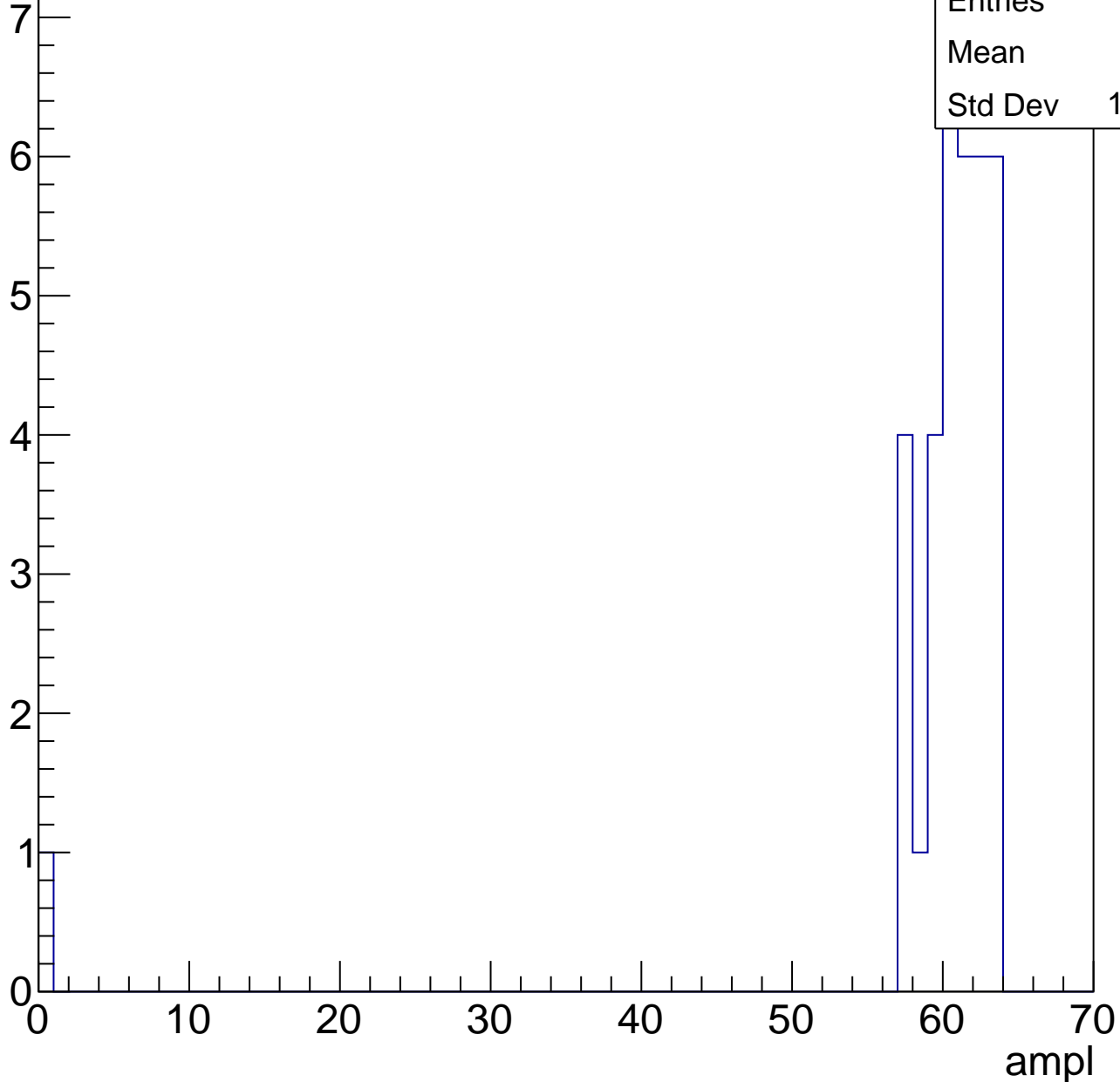
Entries	63
Mean	56.46
Std Dev	3.075



B1L101S, U12-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U12-ch30, adc0

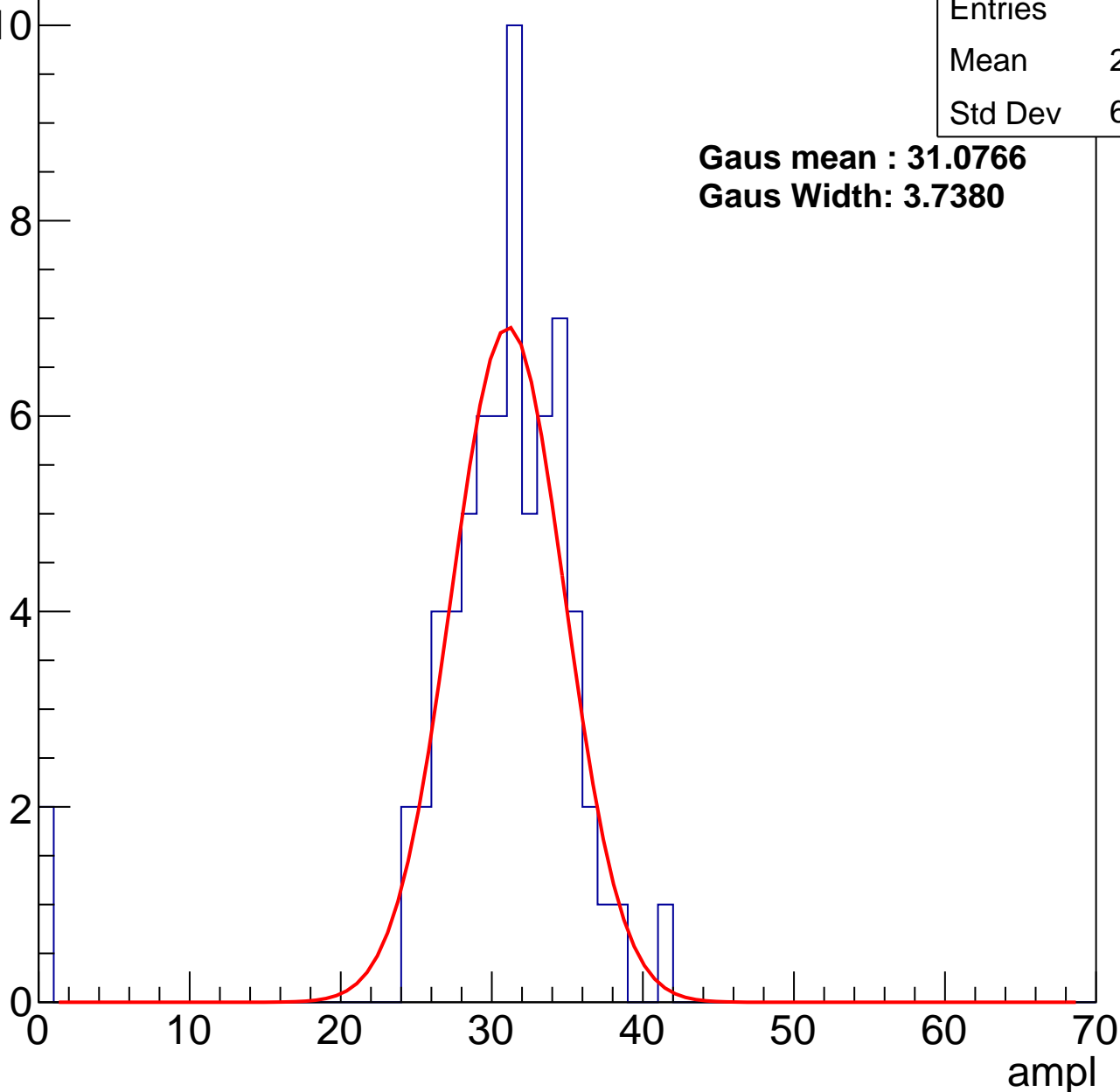
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.97
Std Dev	6.243

Gaus mean : 31.0766

Gaus Width: 3.7380



B1L101S, U12-ch30, adc1

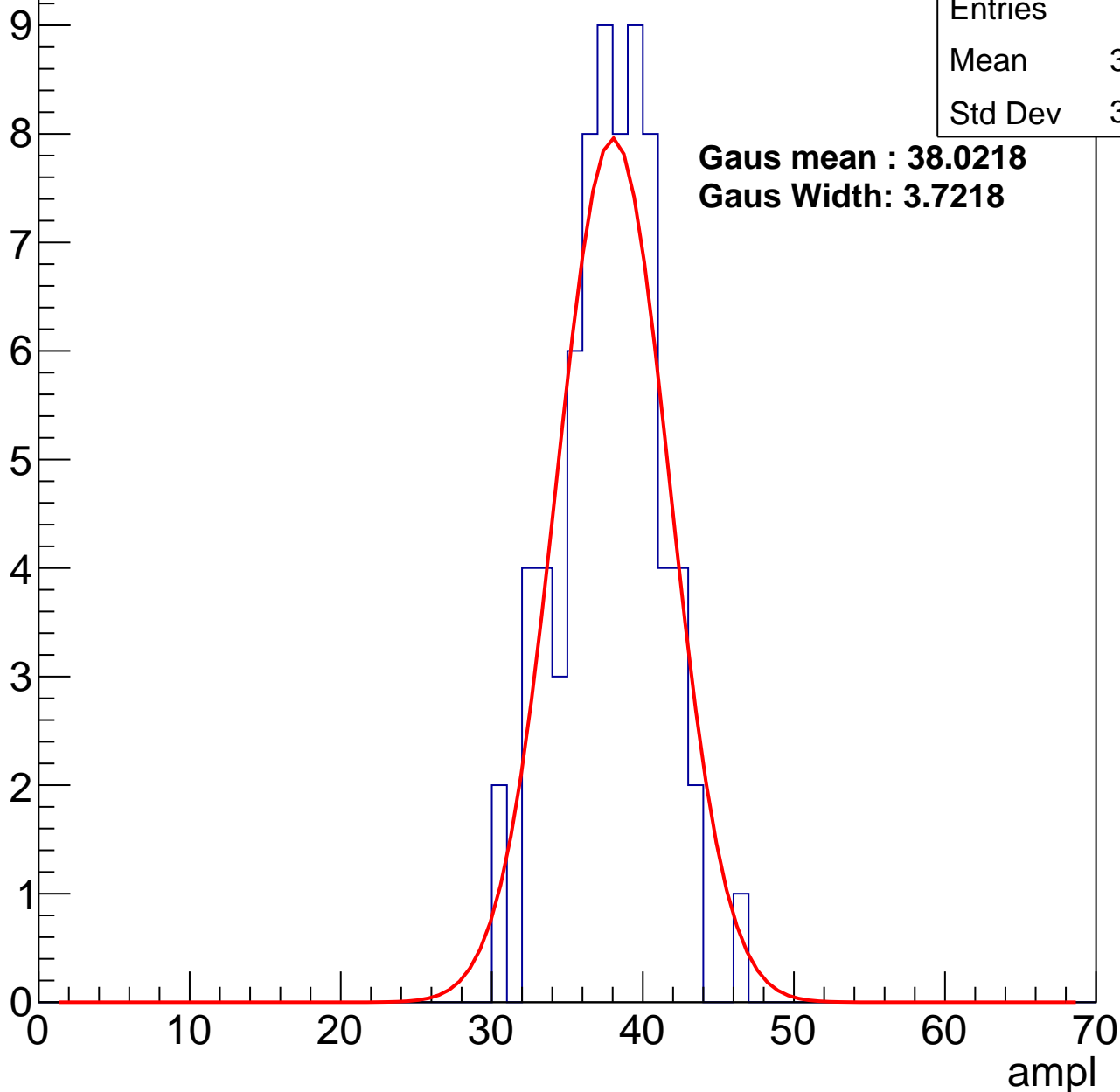
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.39
Std Dev	3.213

Gaus mean : 38.0218

Gaus Width: 3.7218



B1L101S, U12-ch30, adc2

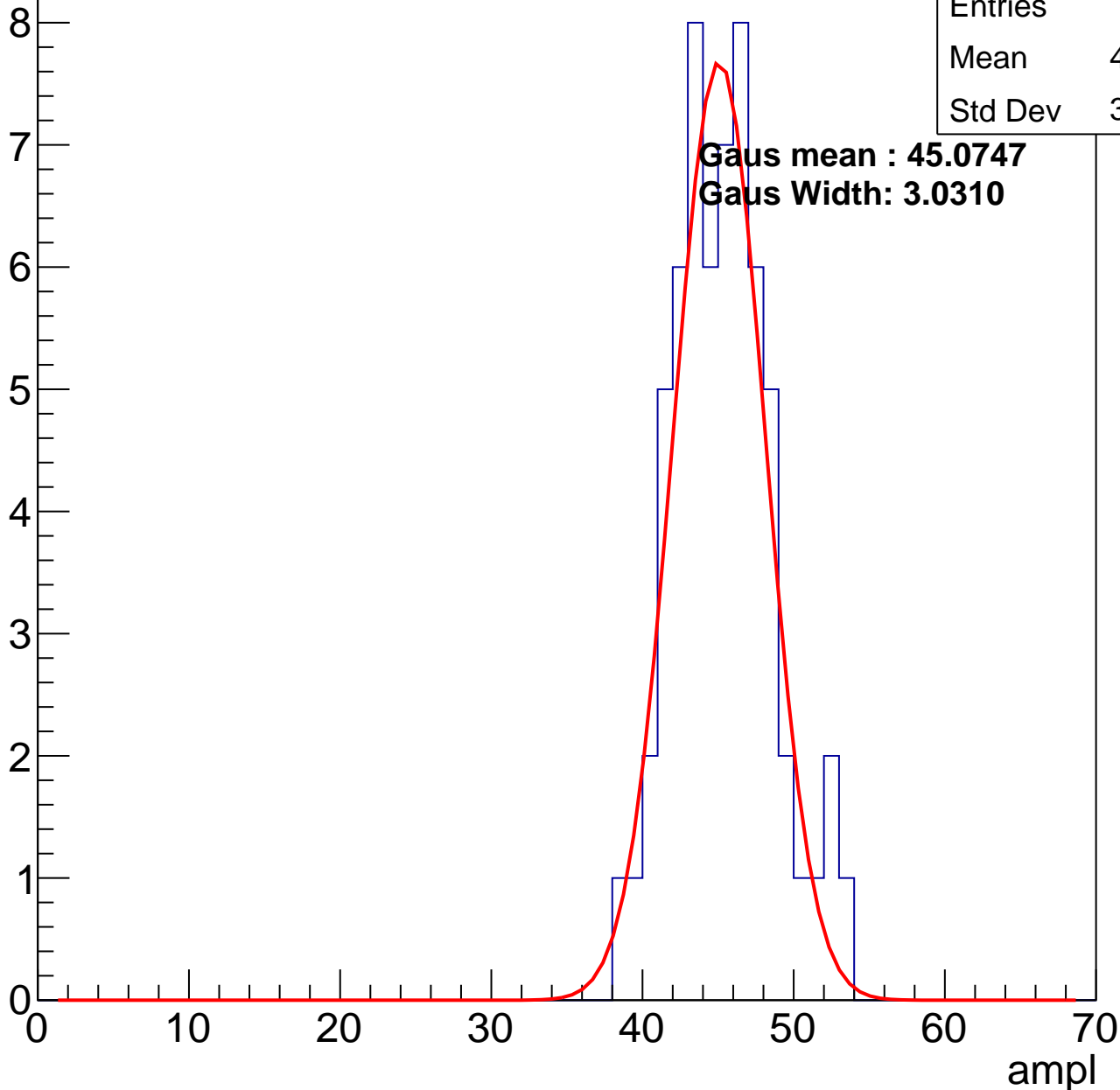
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.89
Std Dev	3.208

Gaus mean : 45.0747

Gaus Width: 3.0310

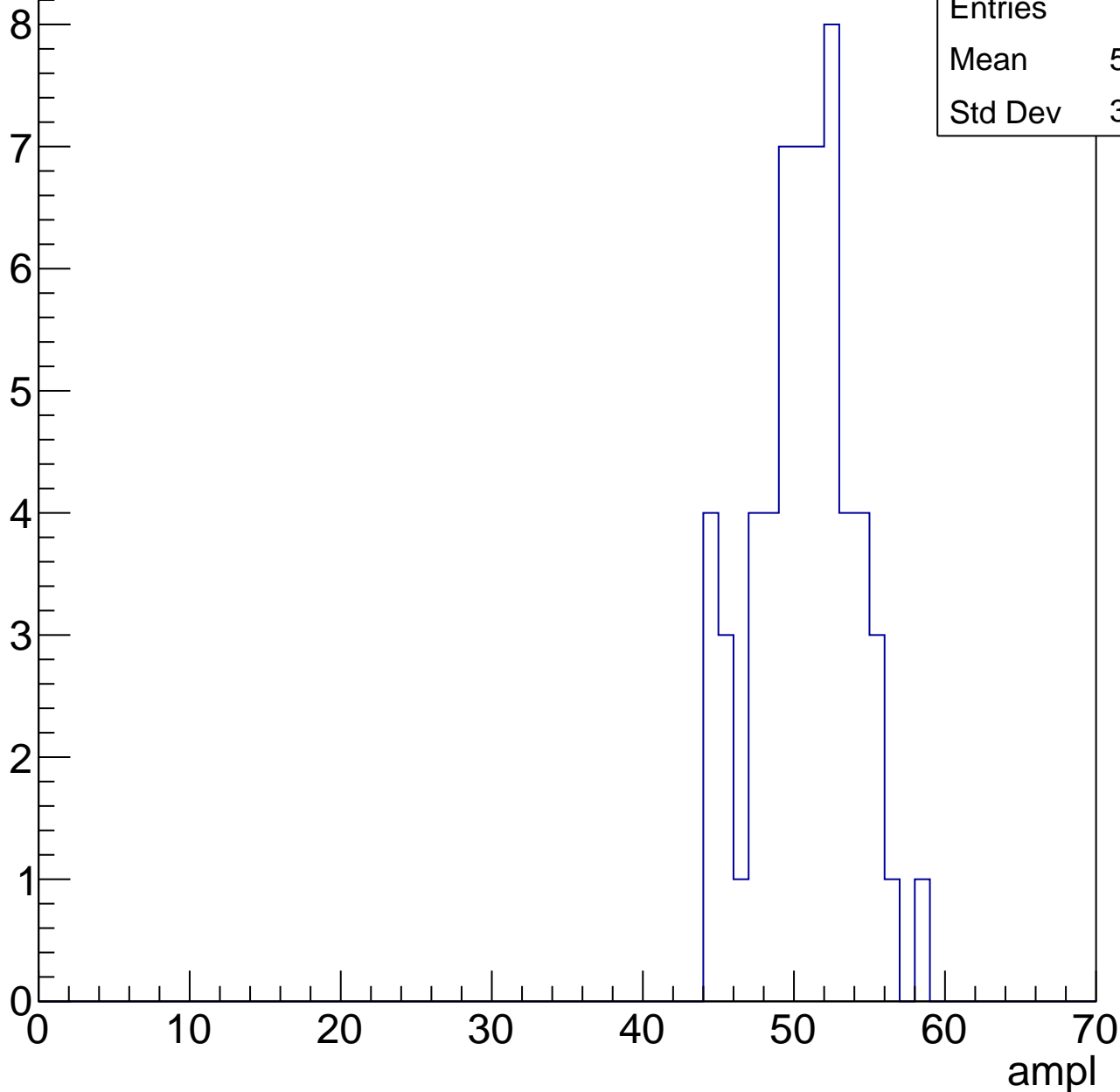


B1L101S, U12-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.17
Std Dev	3.233



B1L101S, U12-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

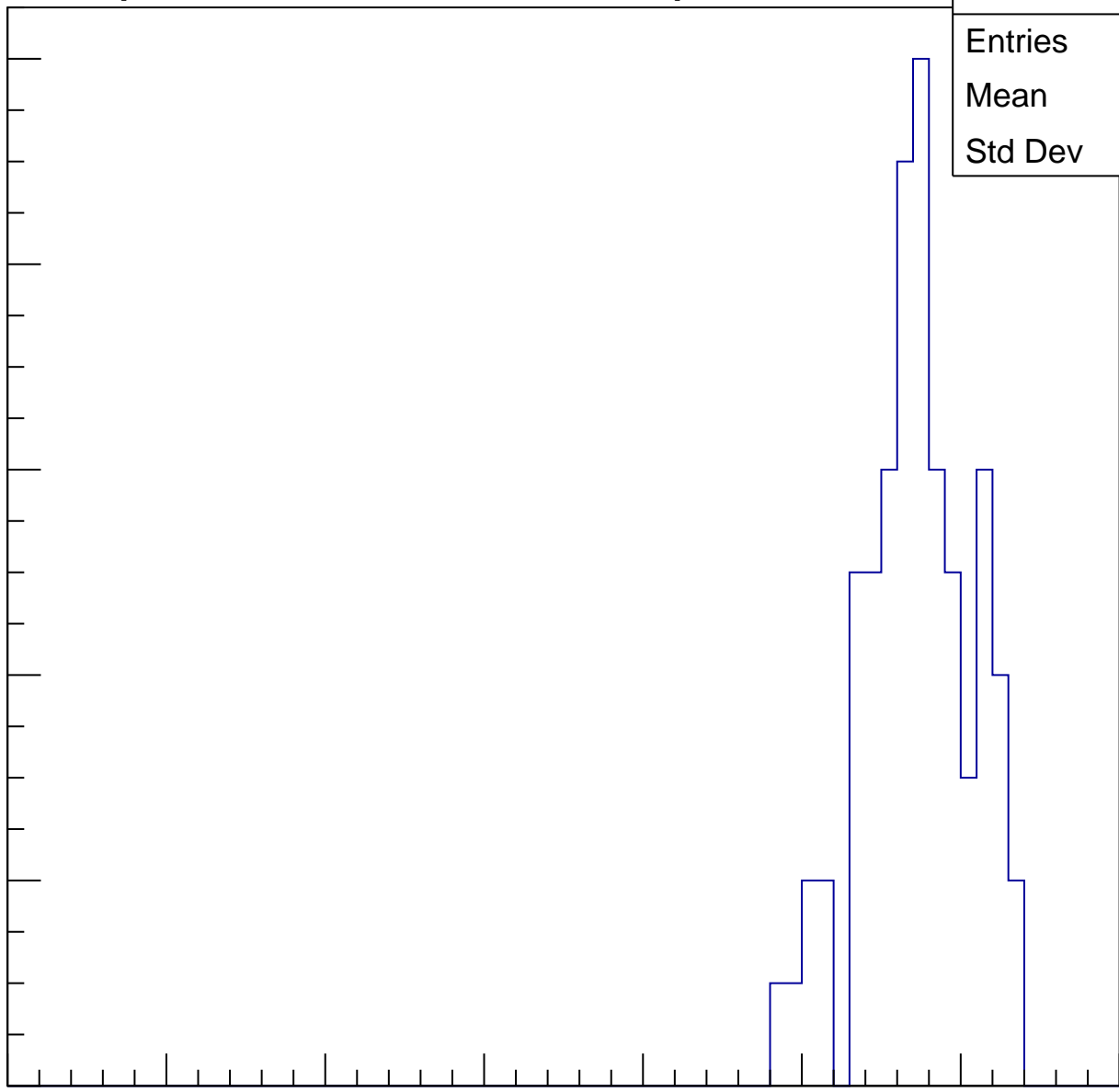
Entries	67
Mean	56.73
Std Dev	3.432

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

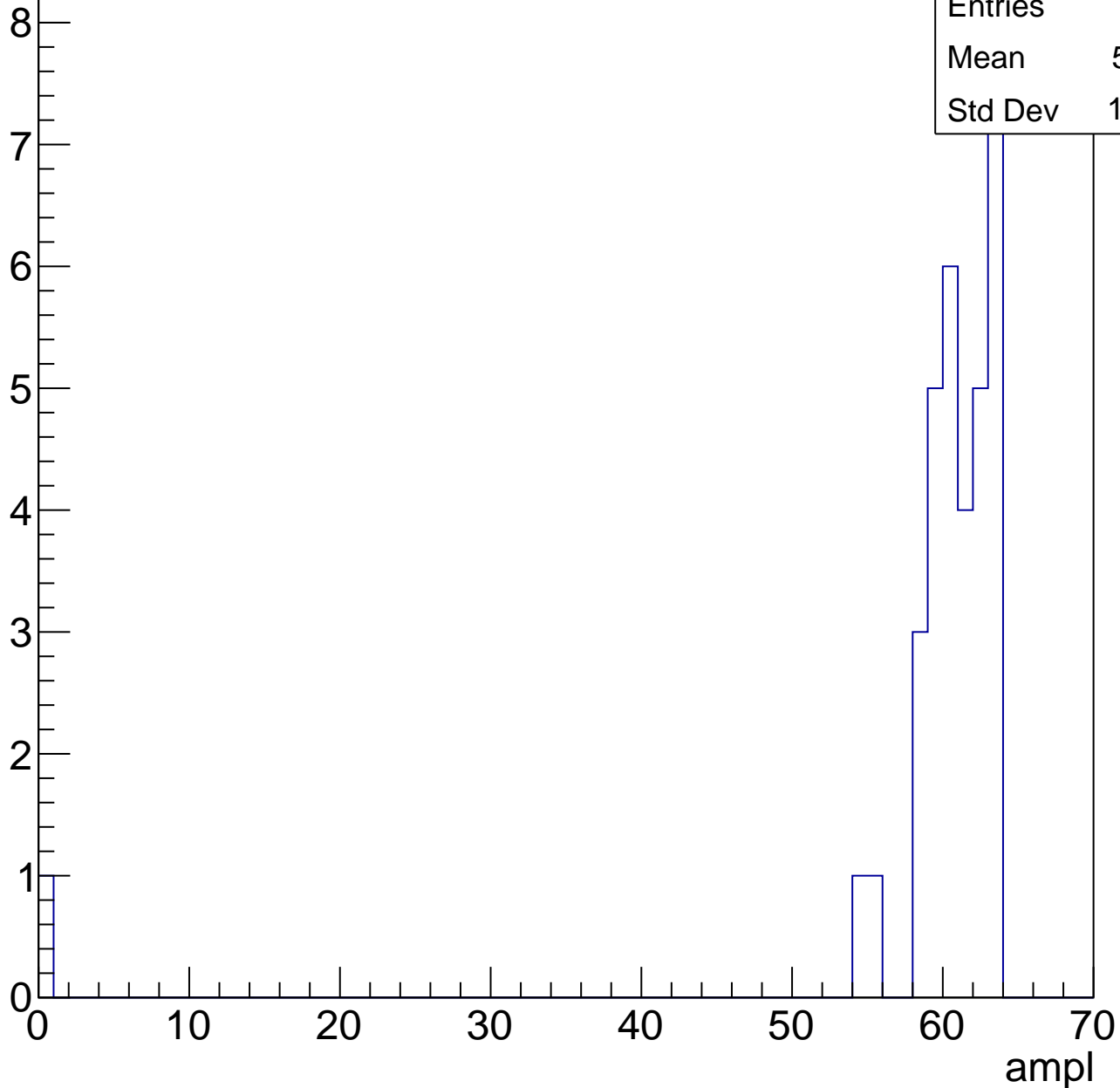


B1L101S, U12-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.71
Std Dev	10.46



B1L101S, U12-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch31, adc0

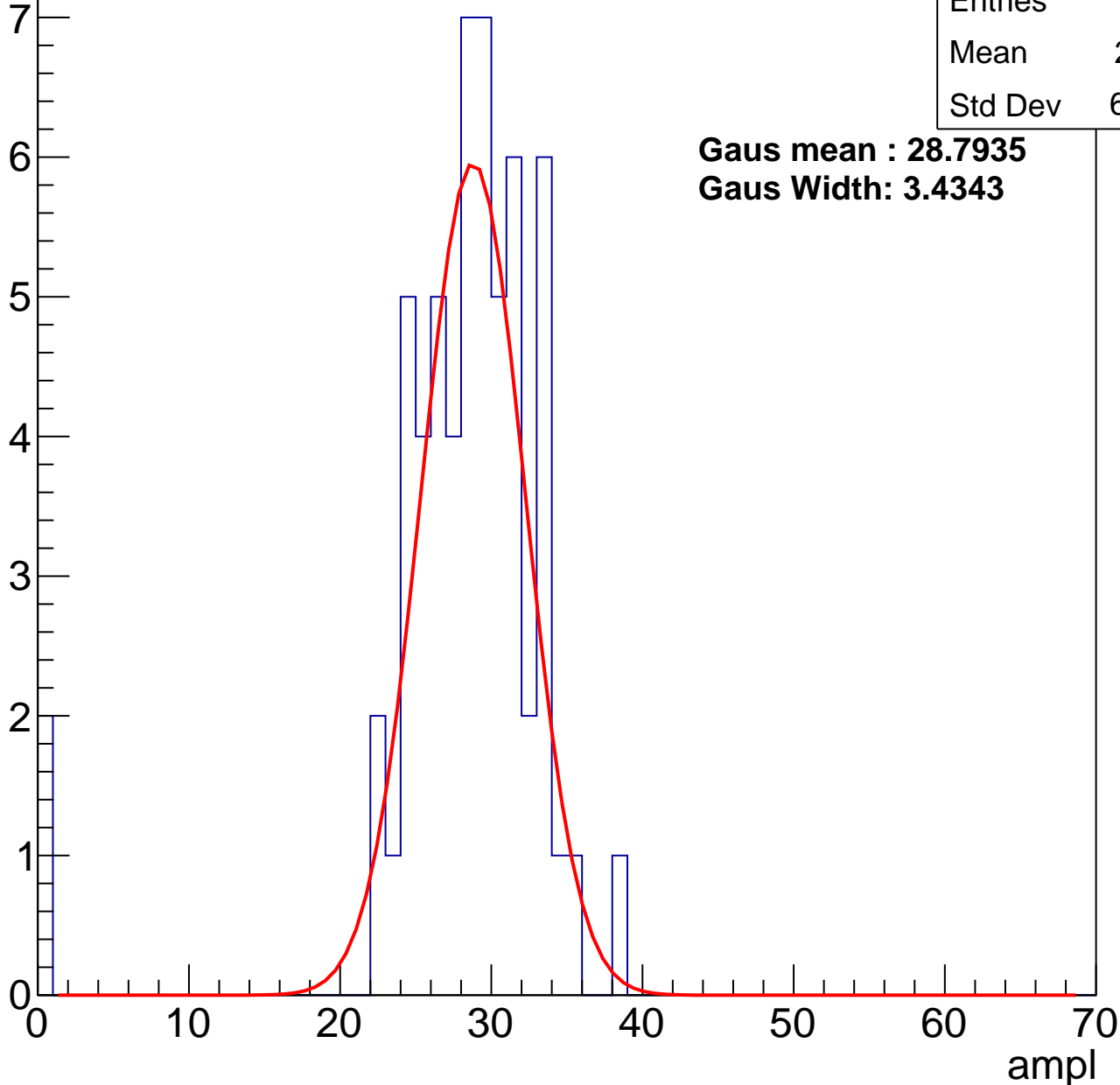
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.61
Std Dev	6.167

Gaus mean : 28.7935

Gaus Width: 3.4343



B1L101S, U12-ch31, adc1

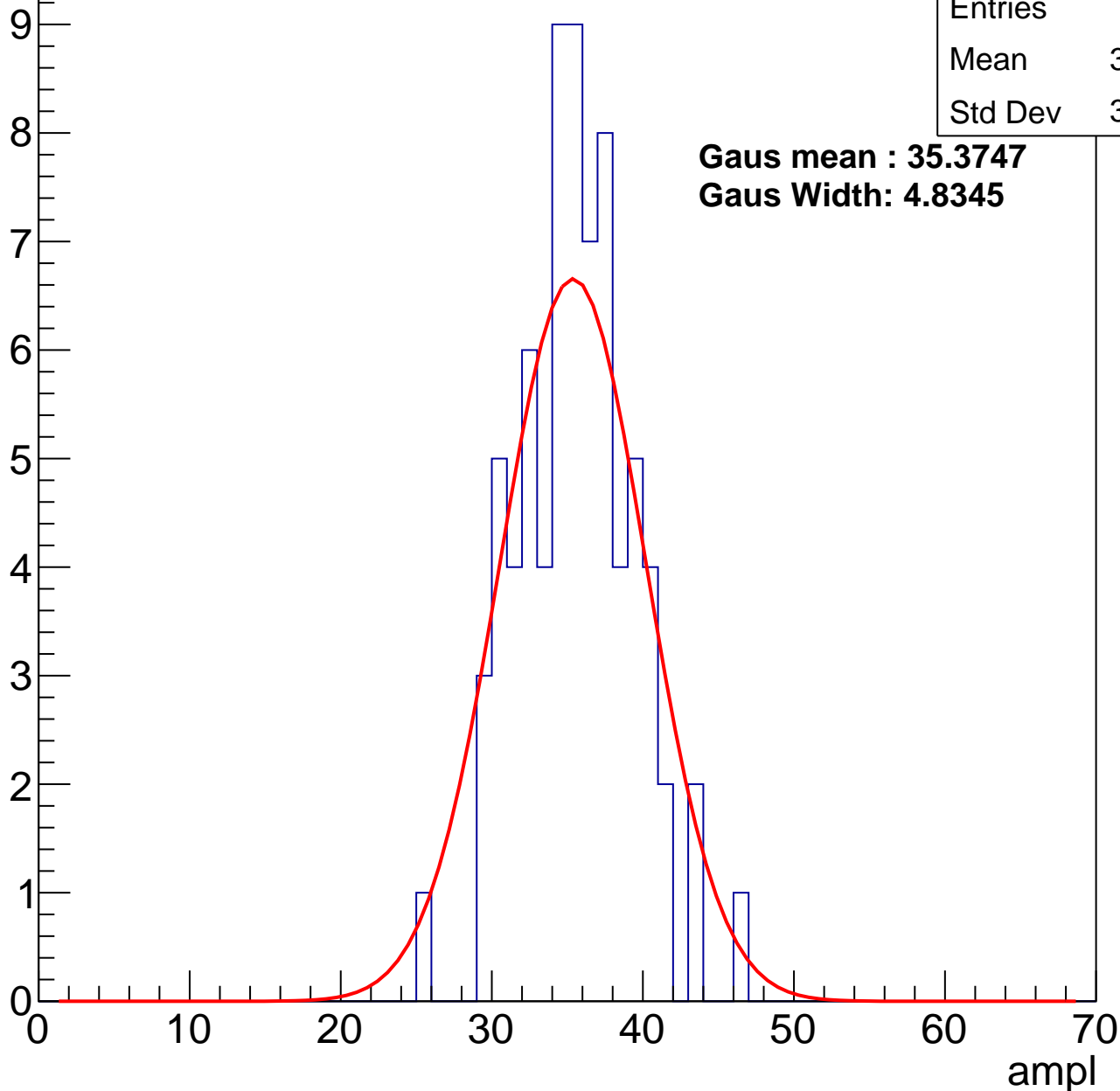
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.14
Std Dev	3.768

Gaus mean : 35.3747

Gaus Width: 4.8345



B1L101S, U12-ch31, adc2

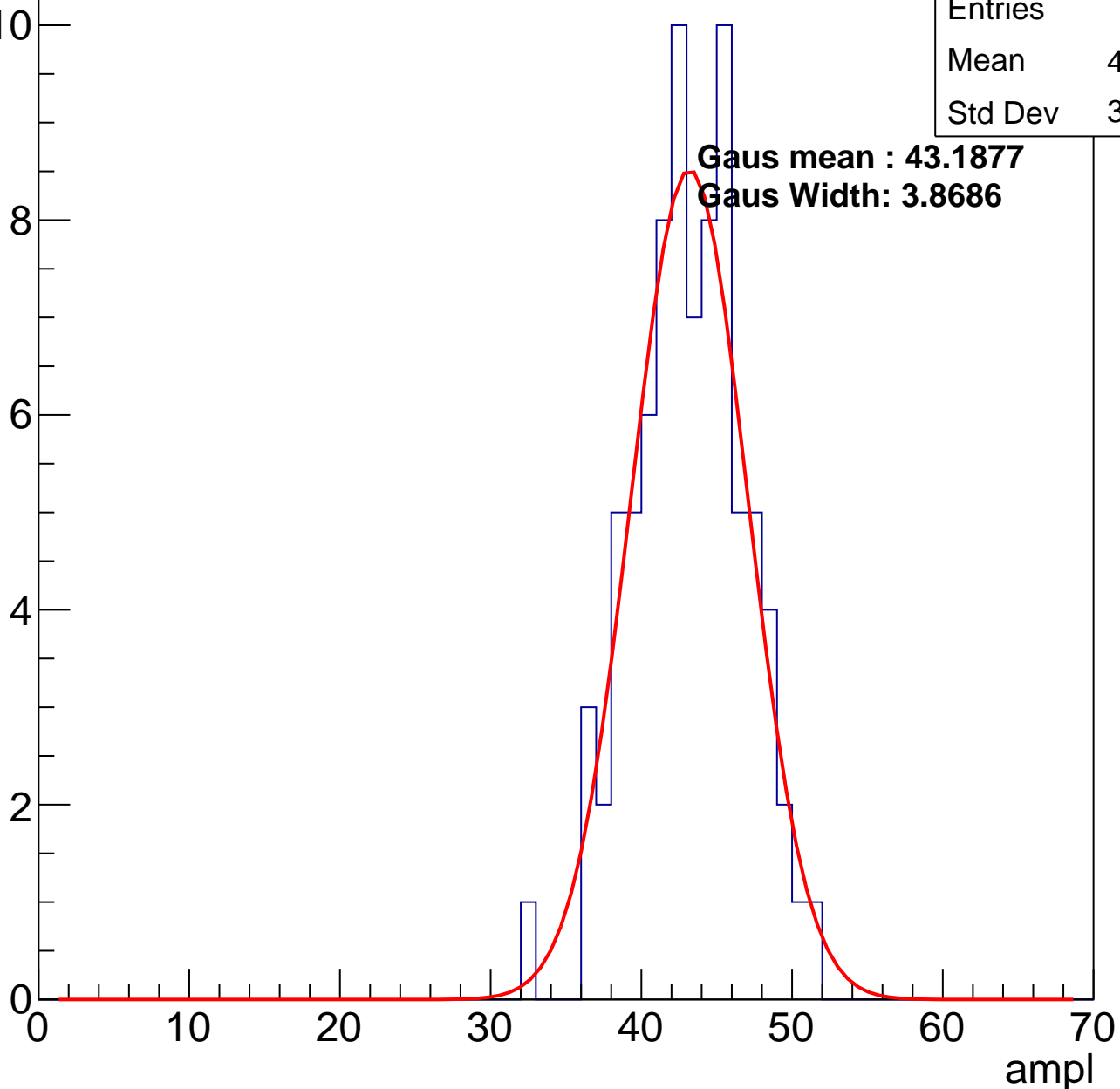
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	42.72
Std Dev	3.628

Gaus mean : 43.1877

Gaus Width: 3.8686

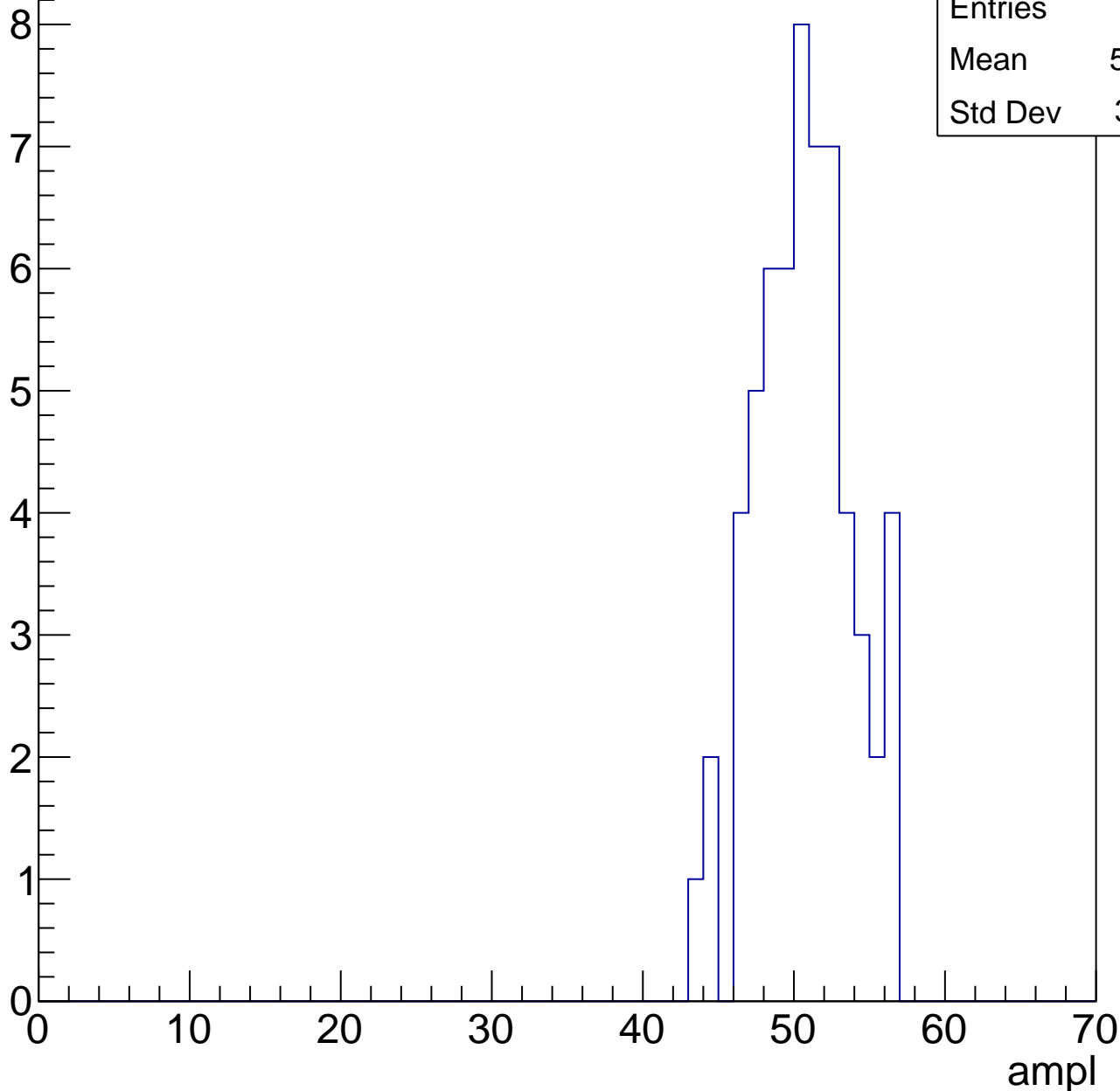


B1L101S, U12-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.19
Std Dev	3.111

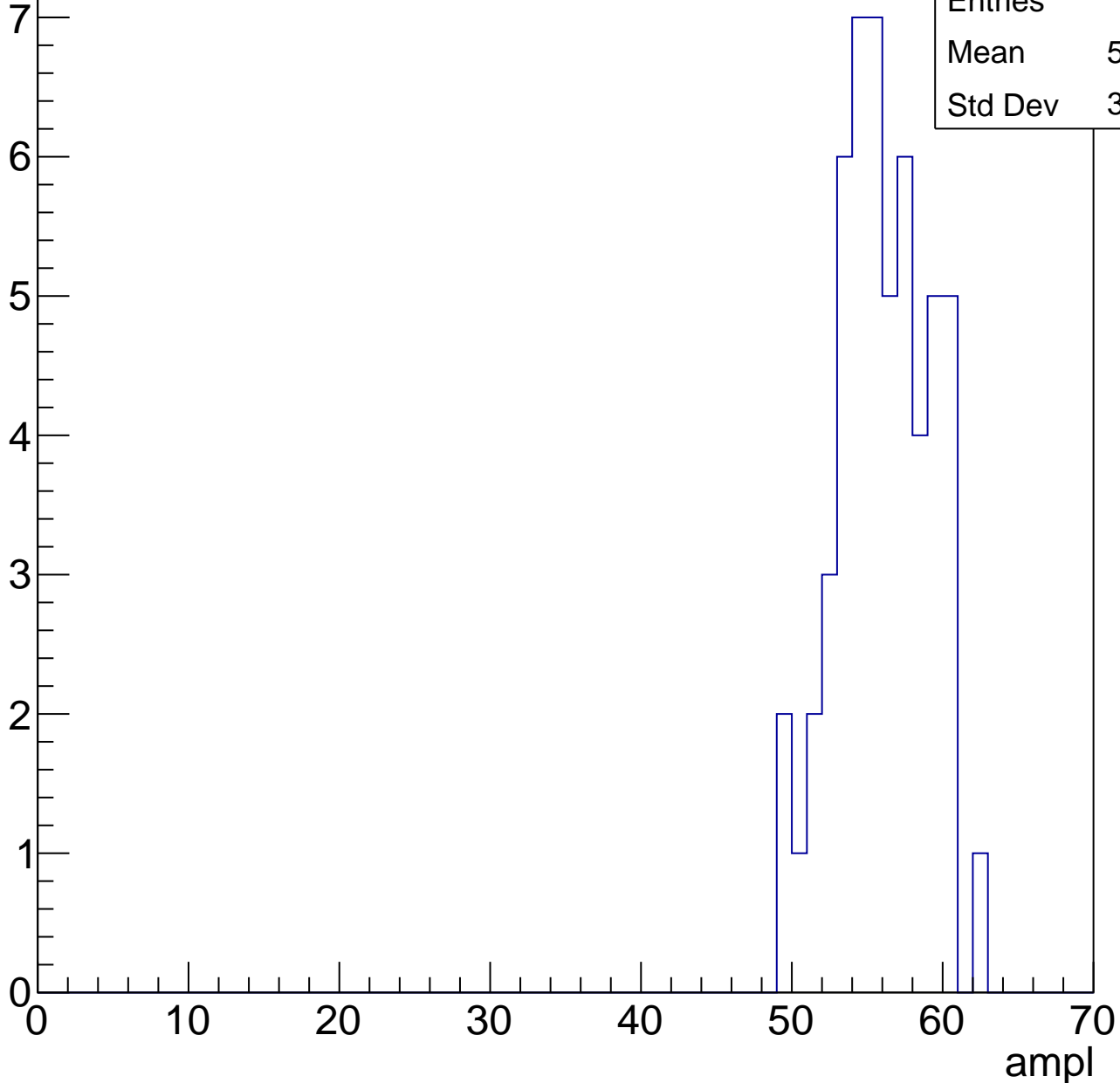


B1L101S, U12-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

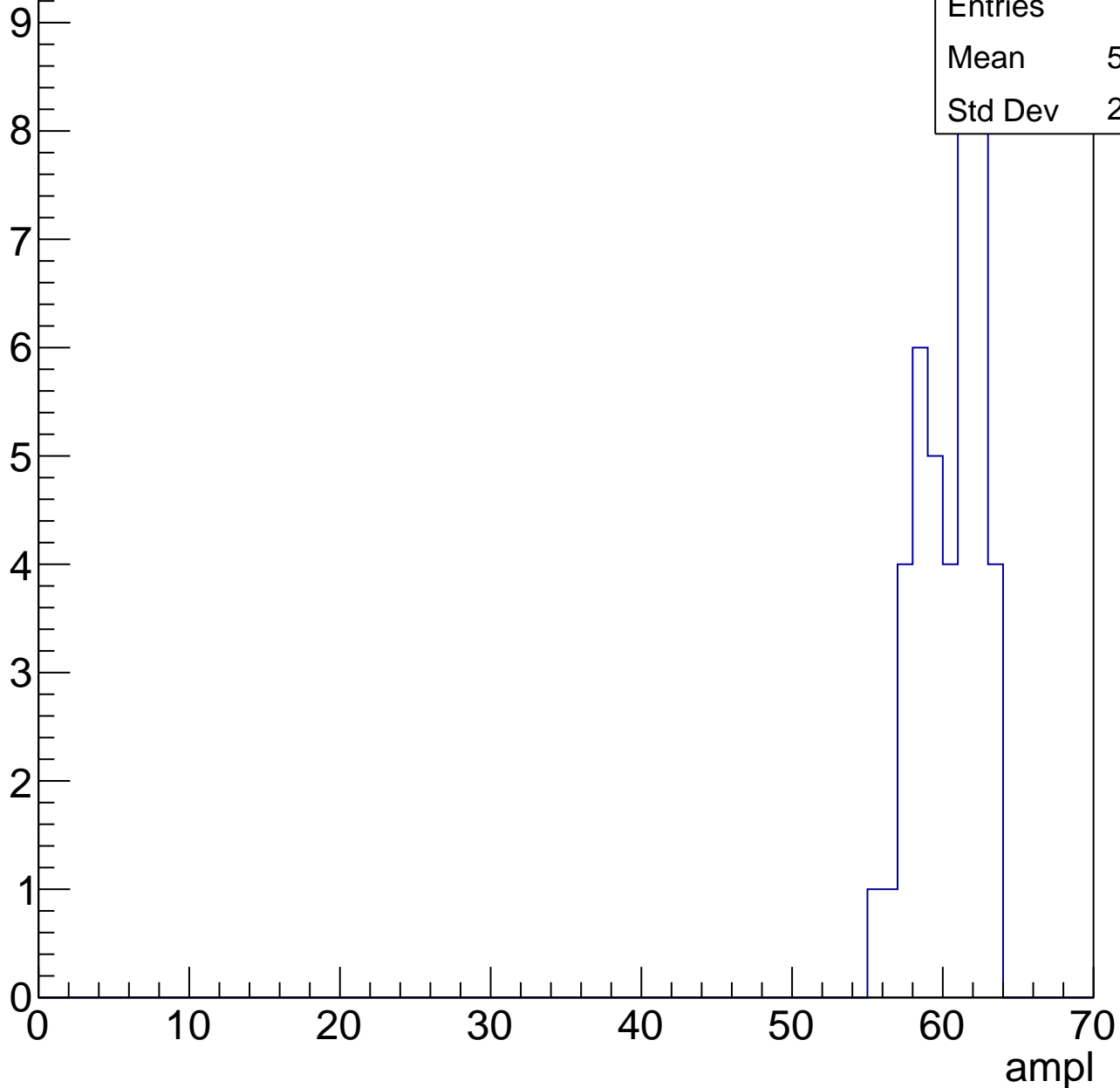
Entries	54
Mean	55.52
Std Dev	3.017



B1L101S, U12-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



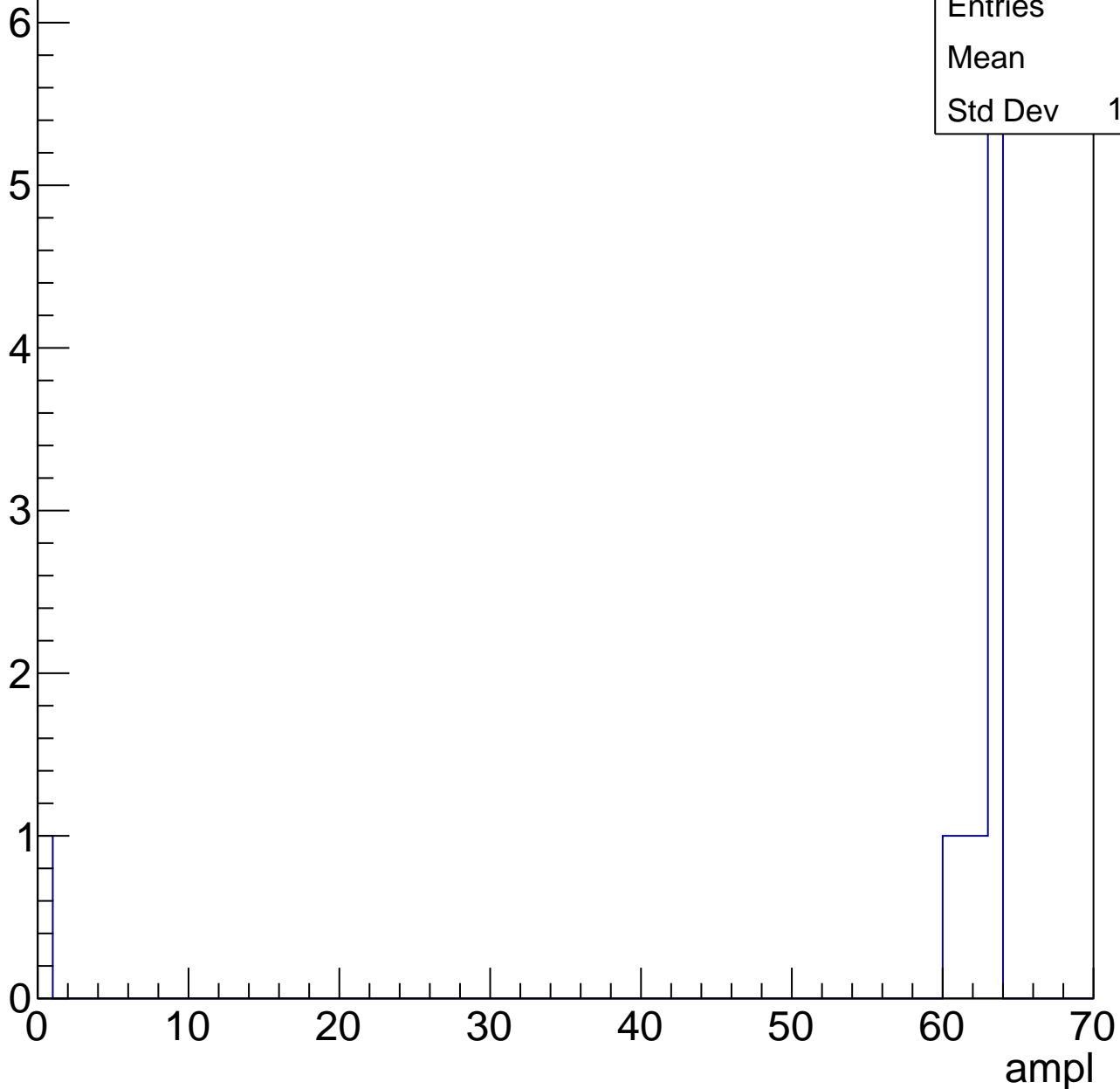
Entries	42
Mean	59.98
Std Dev	2.087

B1L101S, U12-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	56.1
Std Dev	18.73



B1L101S, U12-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch32, adc0

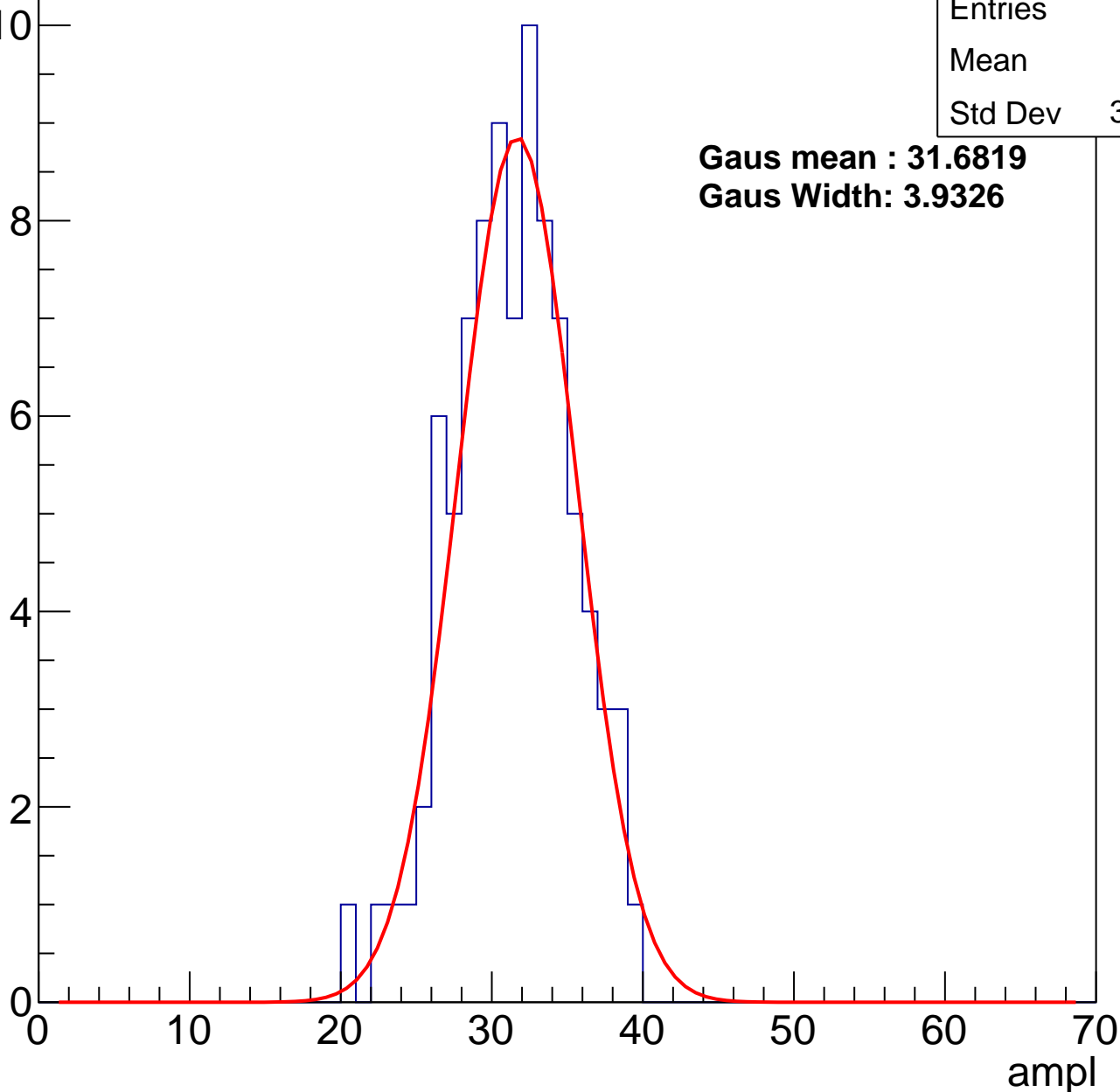
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	30.9
Std Dev	3.857

Gaus mean : 31.6819

Gaus Width: 3.9326



B1L101S, U12-ch32, adc1

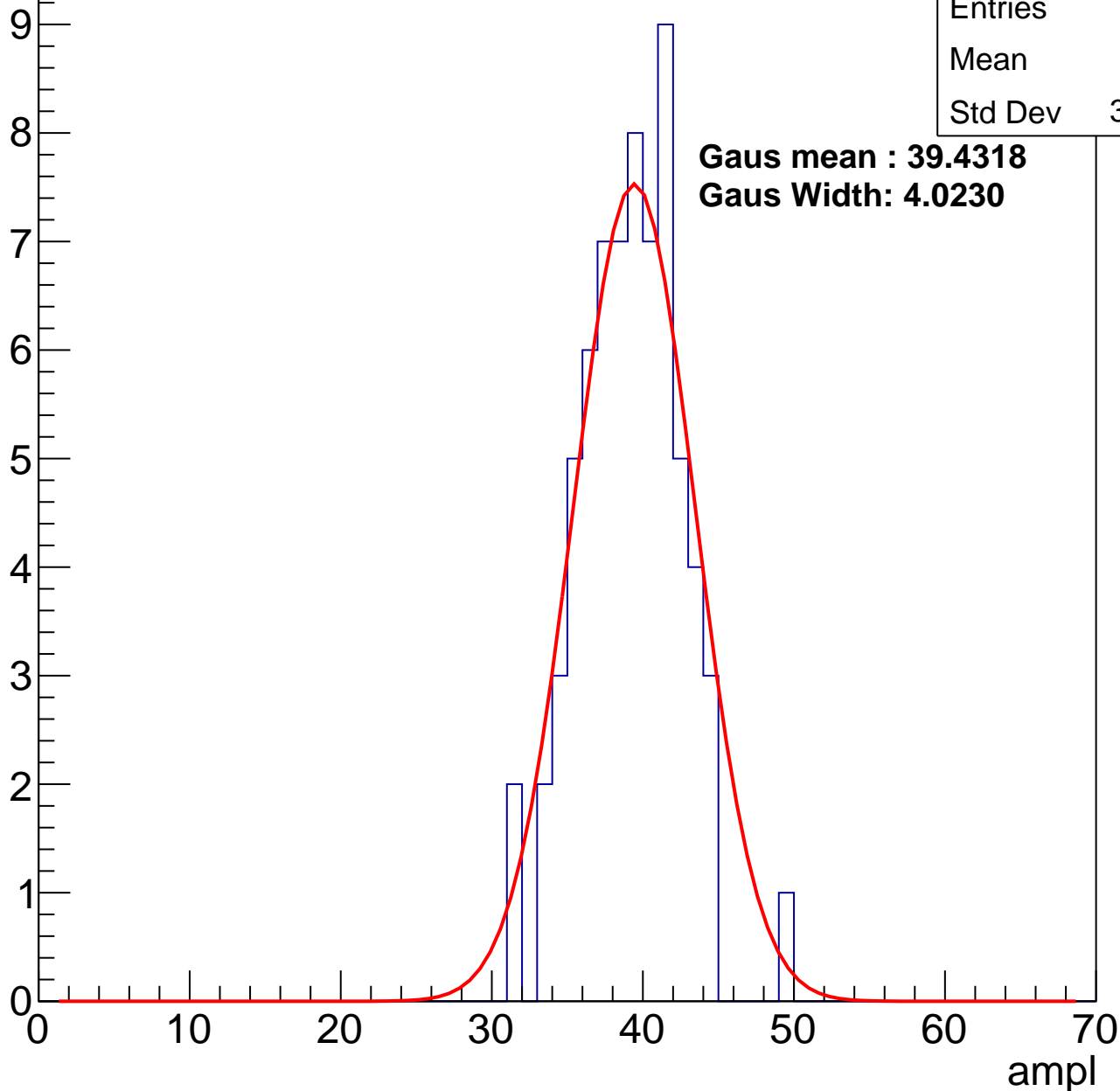
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.7
Std Dev	3.329

Gaus mean : 39.4318

Gaus Width: 4.0230



B1L101S, U12-ch32, adc2

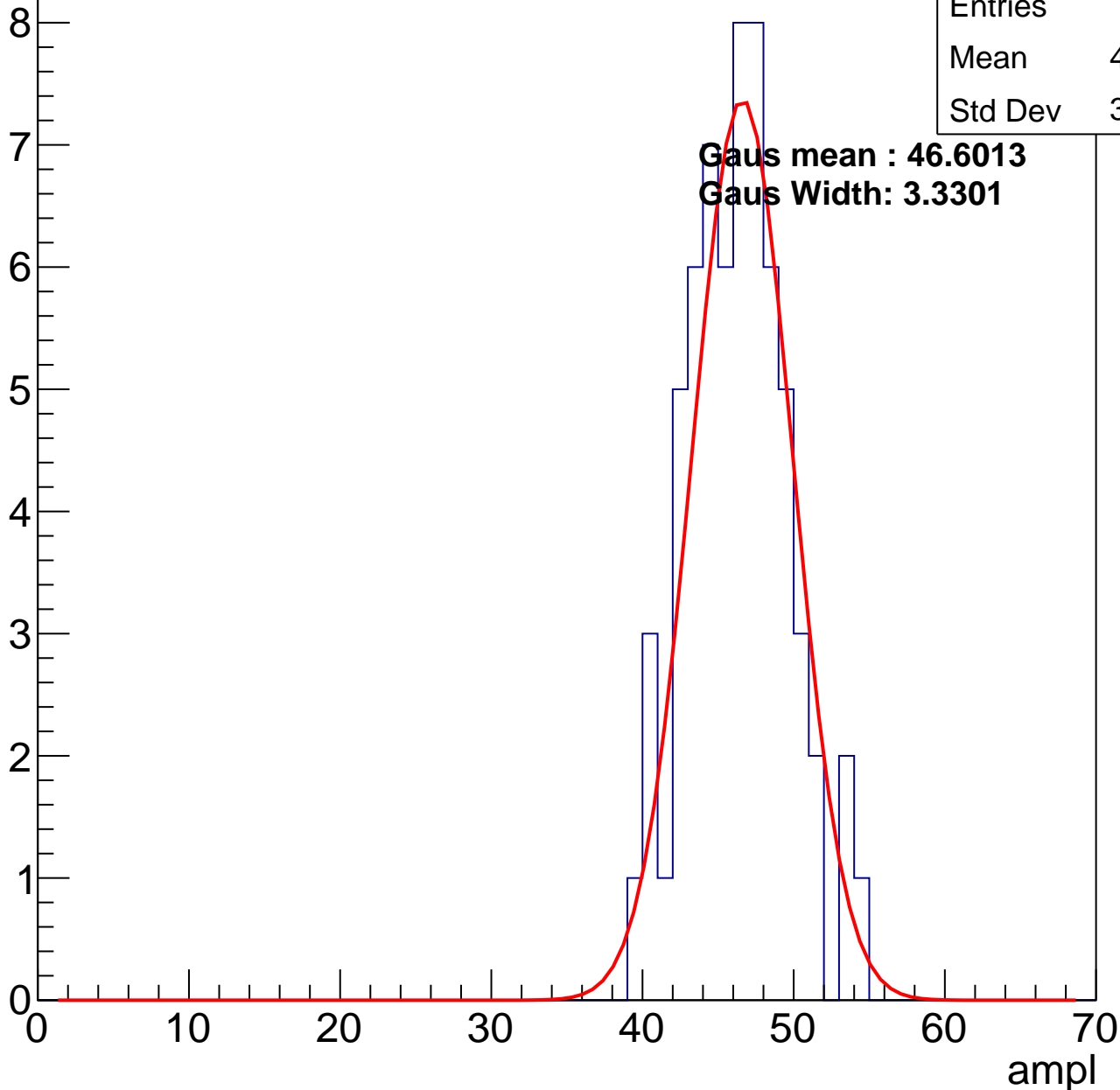
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.86
Std Dev	3.273

Gaus mean : 46.6013

Gaus Width: 3.3301

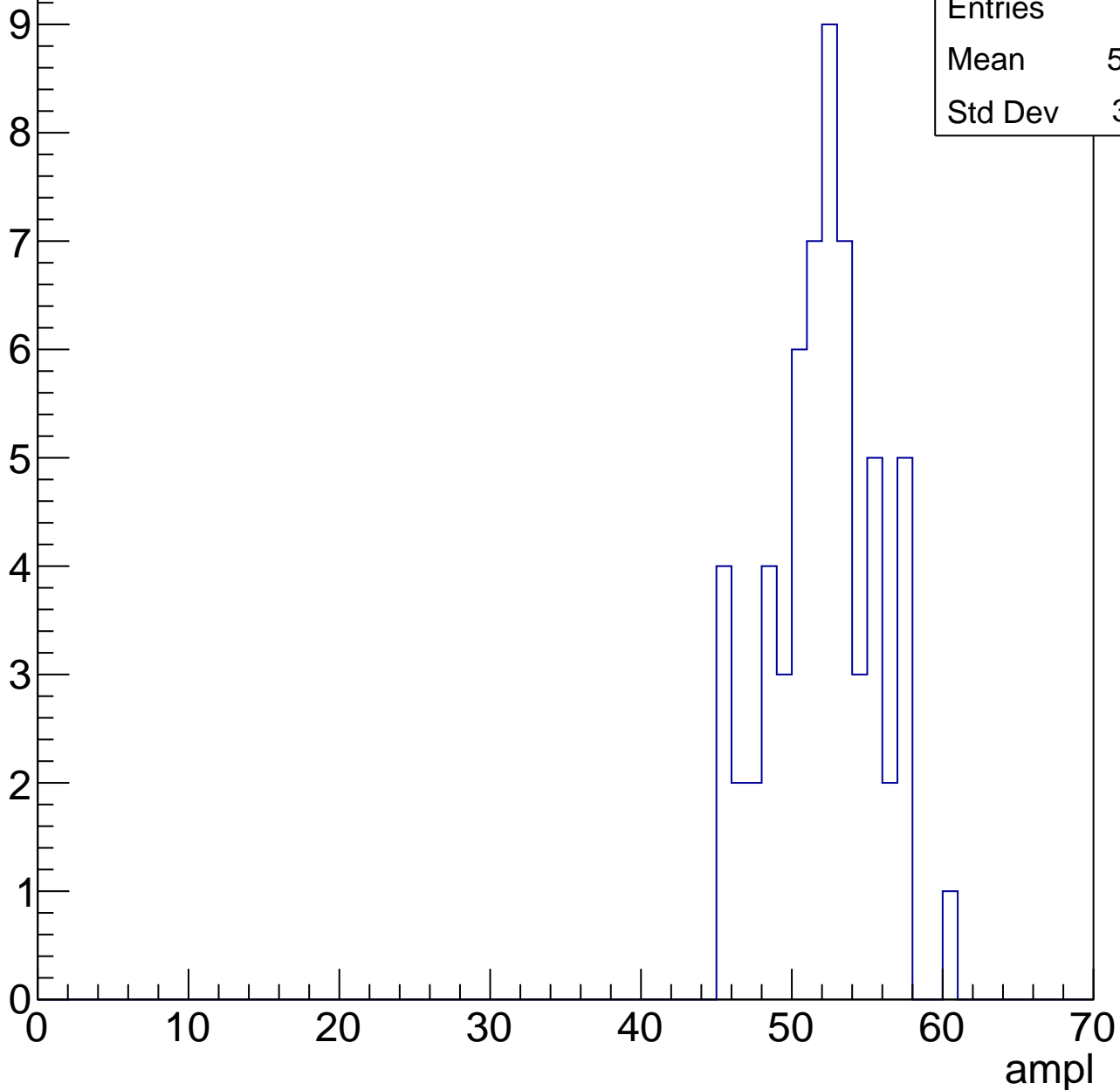


B1L101S, U12-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	51.58
Std Dev	3.451



B1L101S, U12-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	58
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Mean	57.74
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Std Dev	3.472
---------	-------

ampl

0

10

20

30

40

50

60

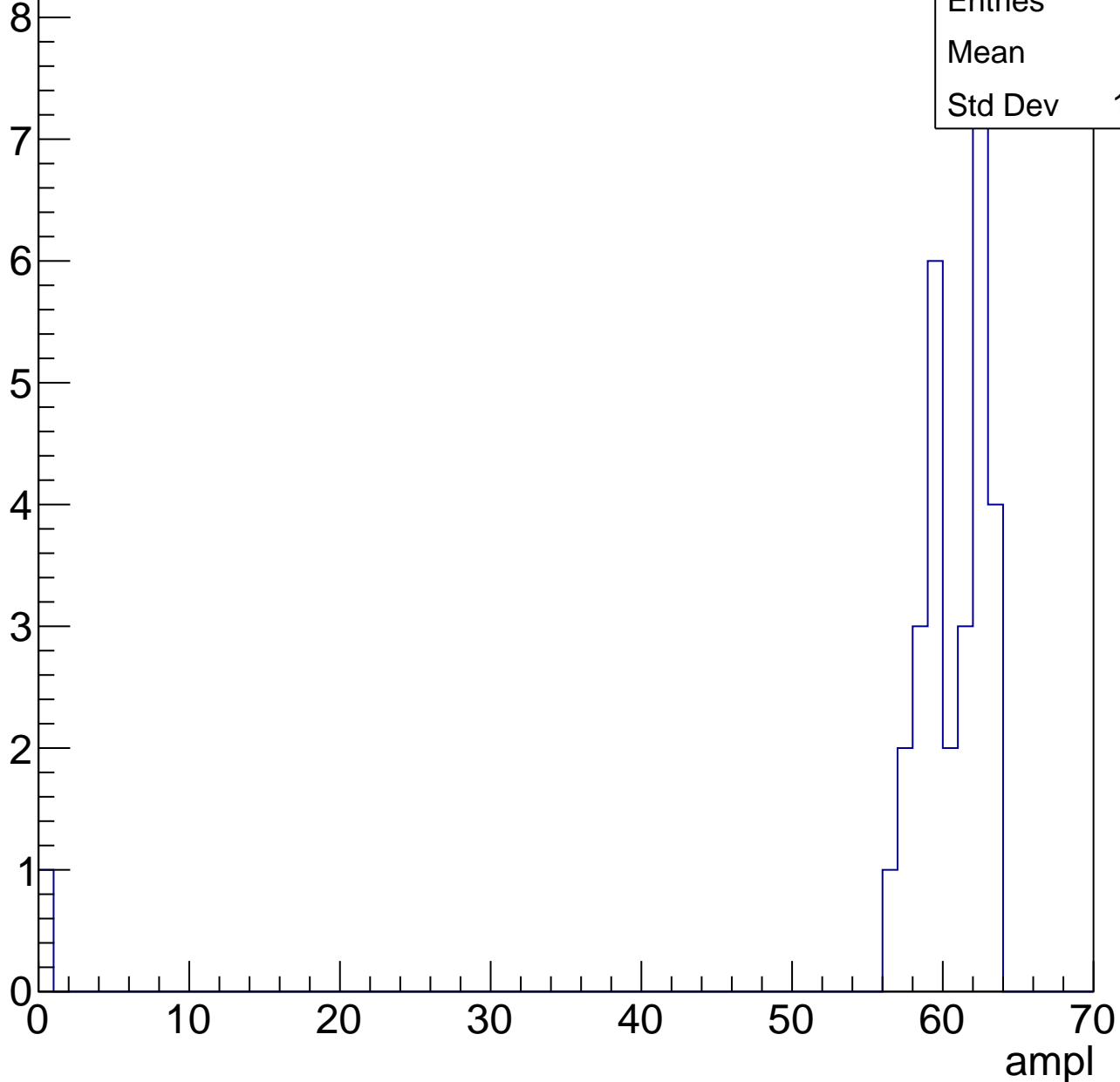
70

B1L101S, U12-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

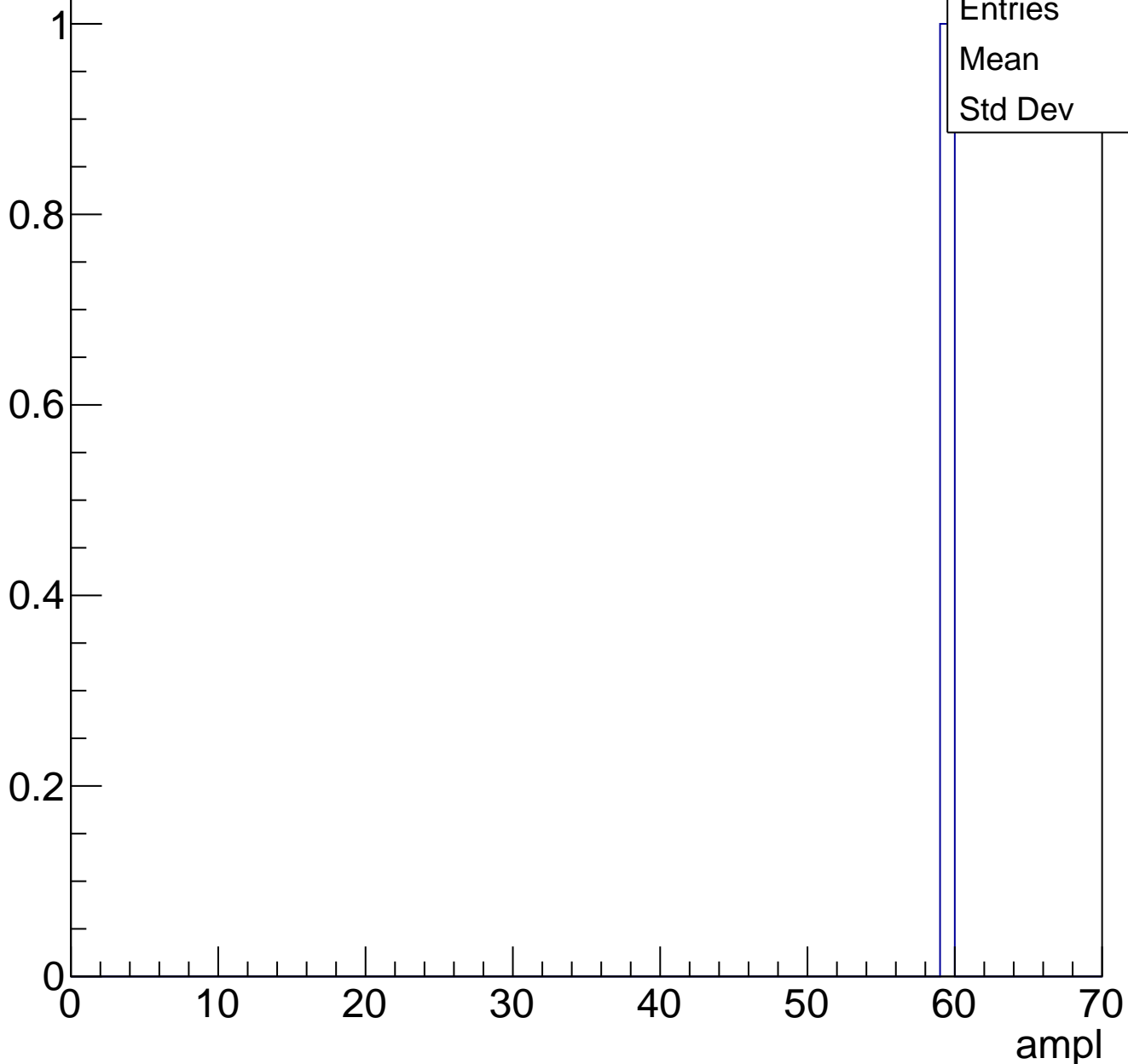
Entries	30
Mean	58.3
Std Dev	11.01



B1L101S, U12-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

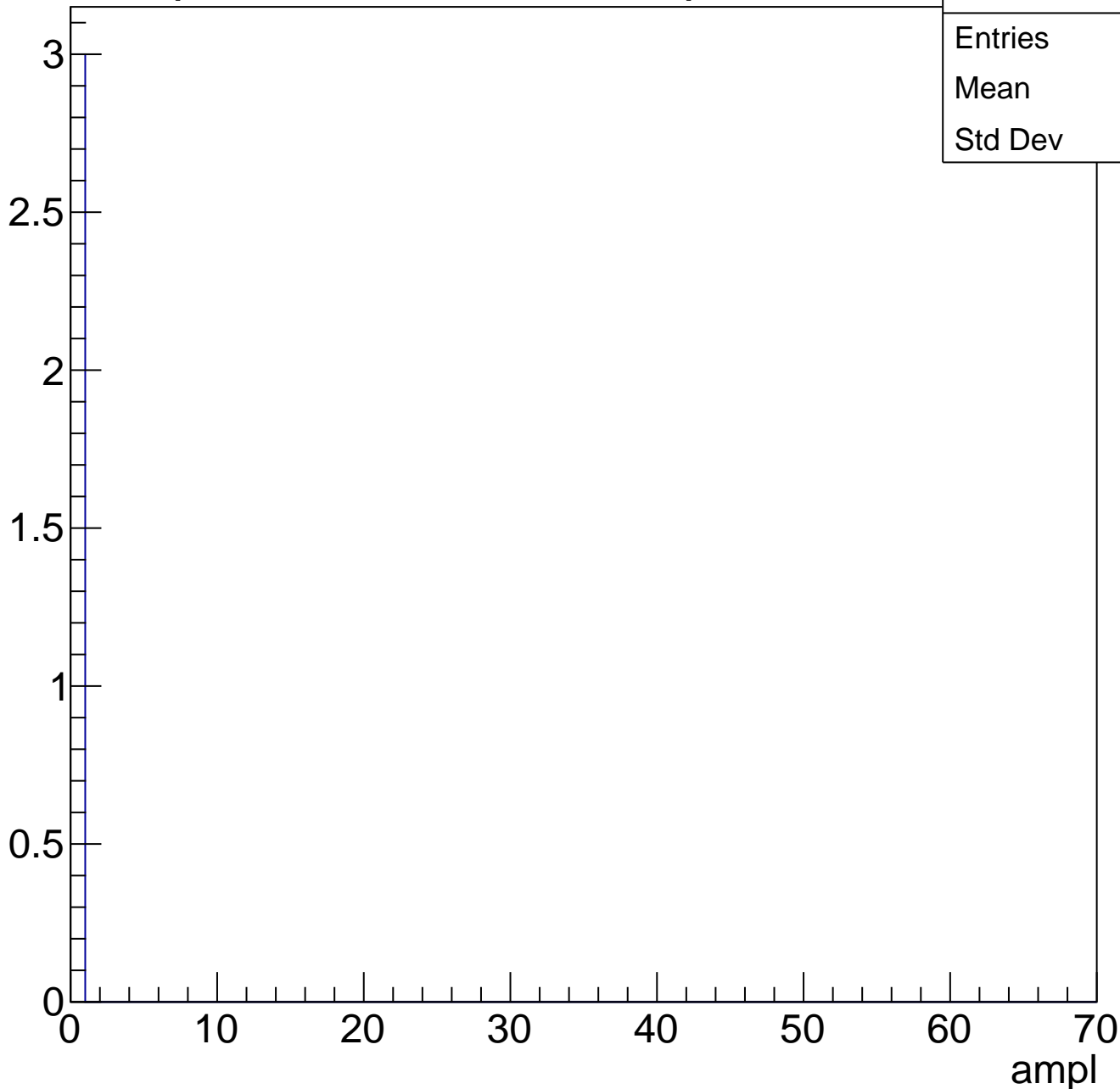


Entries	1
Mean	59
Std Dev	0

B1L101S, U12-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U12-ch33, adc0

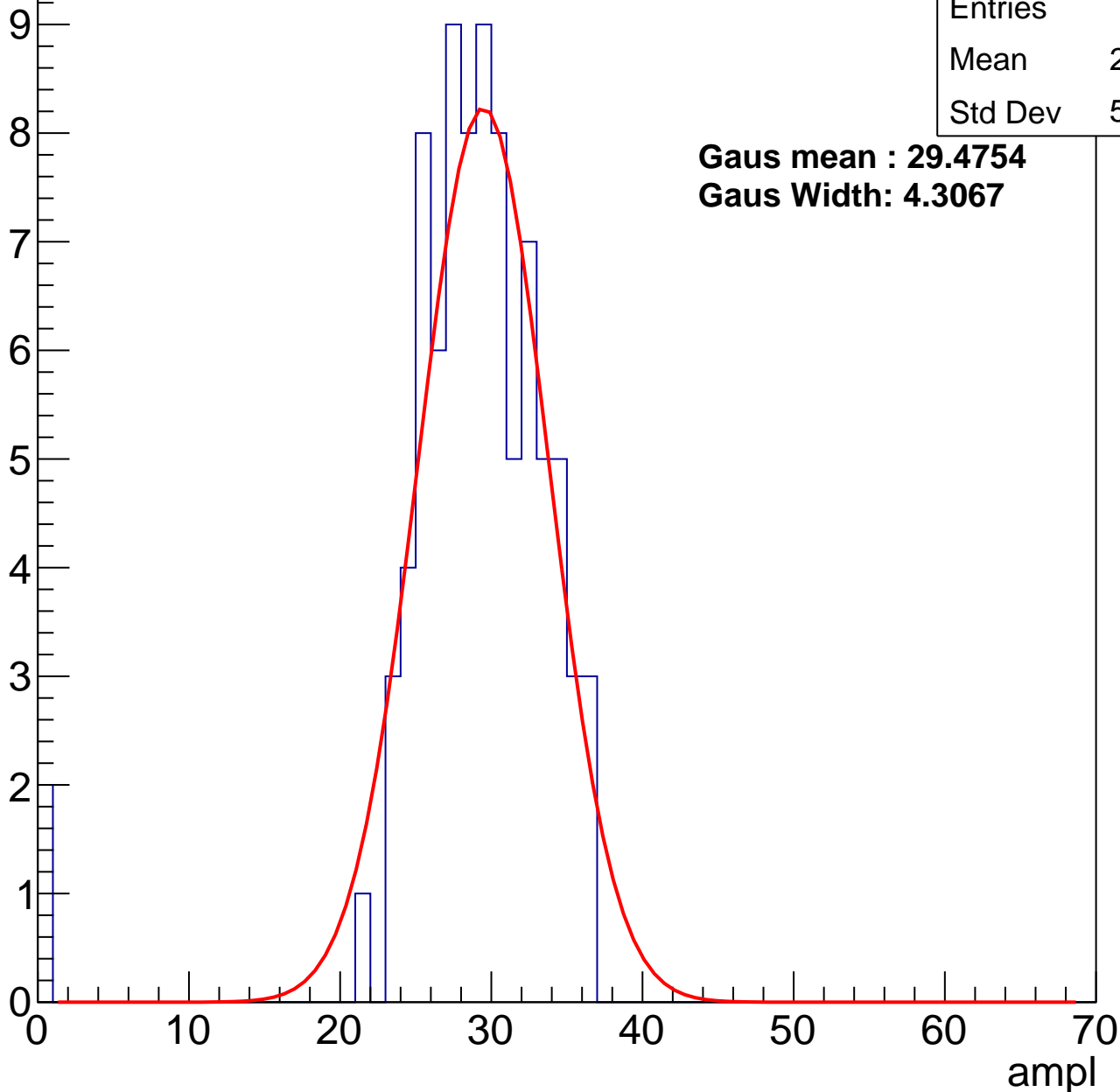
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	28.34
Std Dev	5.596

Gaus mean : 29.4754

Gaus Width: 4.3067



B1L101S, U12-ch33, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	36.46
Std Dev	3.287

Gaus mean : 36.6309

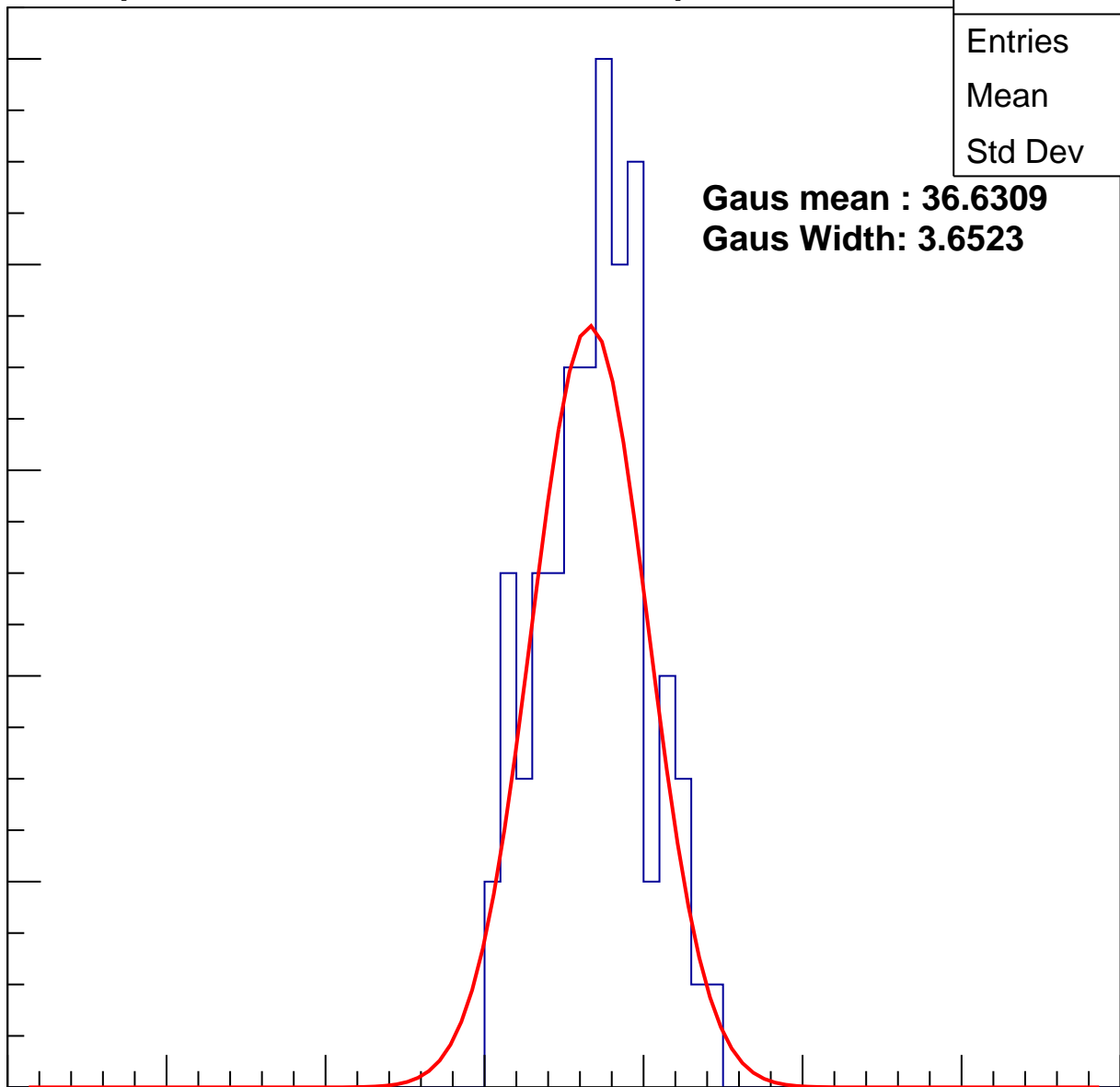
Gaus Width: 3.6523

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch33, adc2

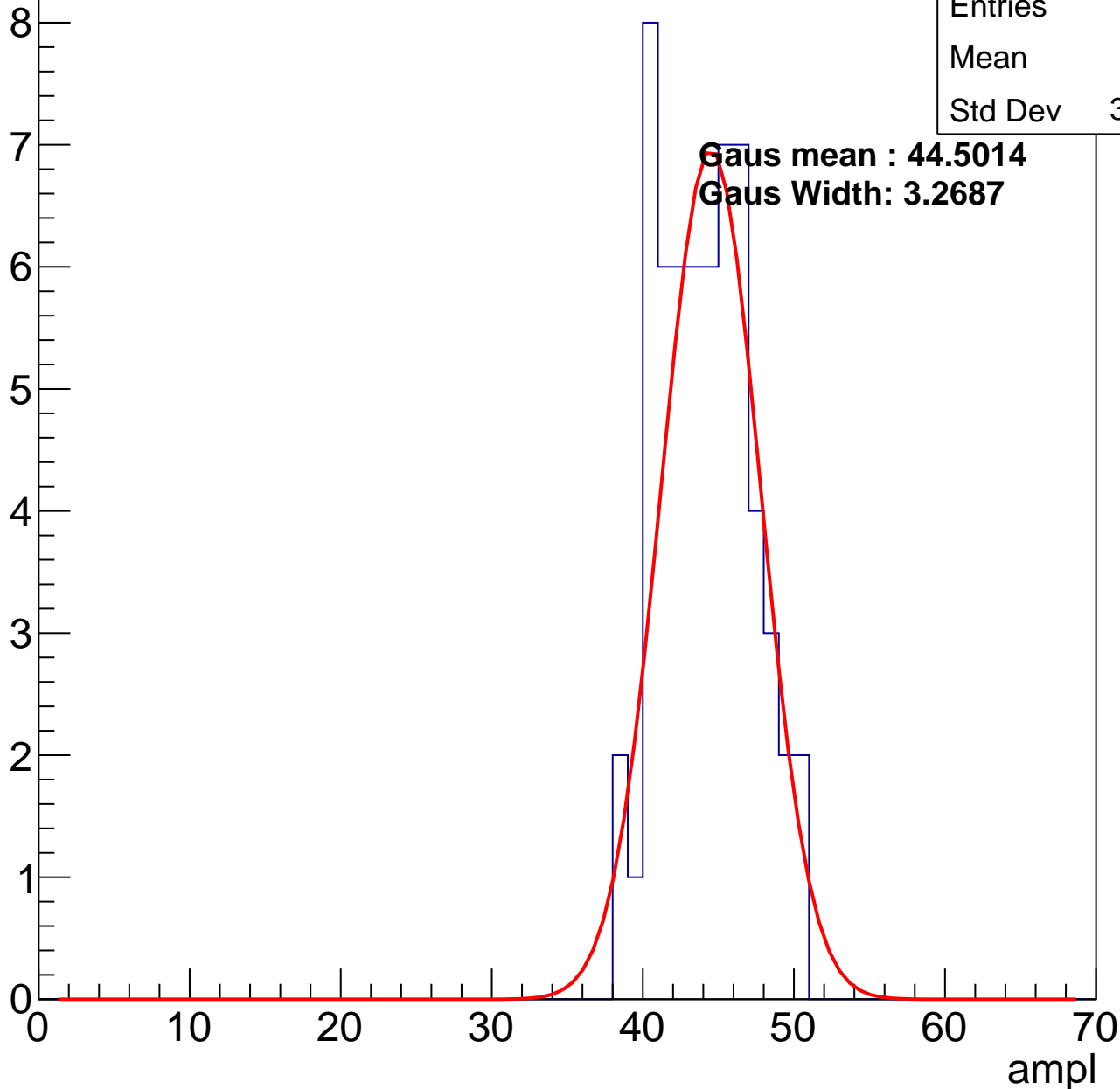
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.7
Std Dev	3.013

Gaus mean : 44.5014

Gaus Width: 3.2687

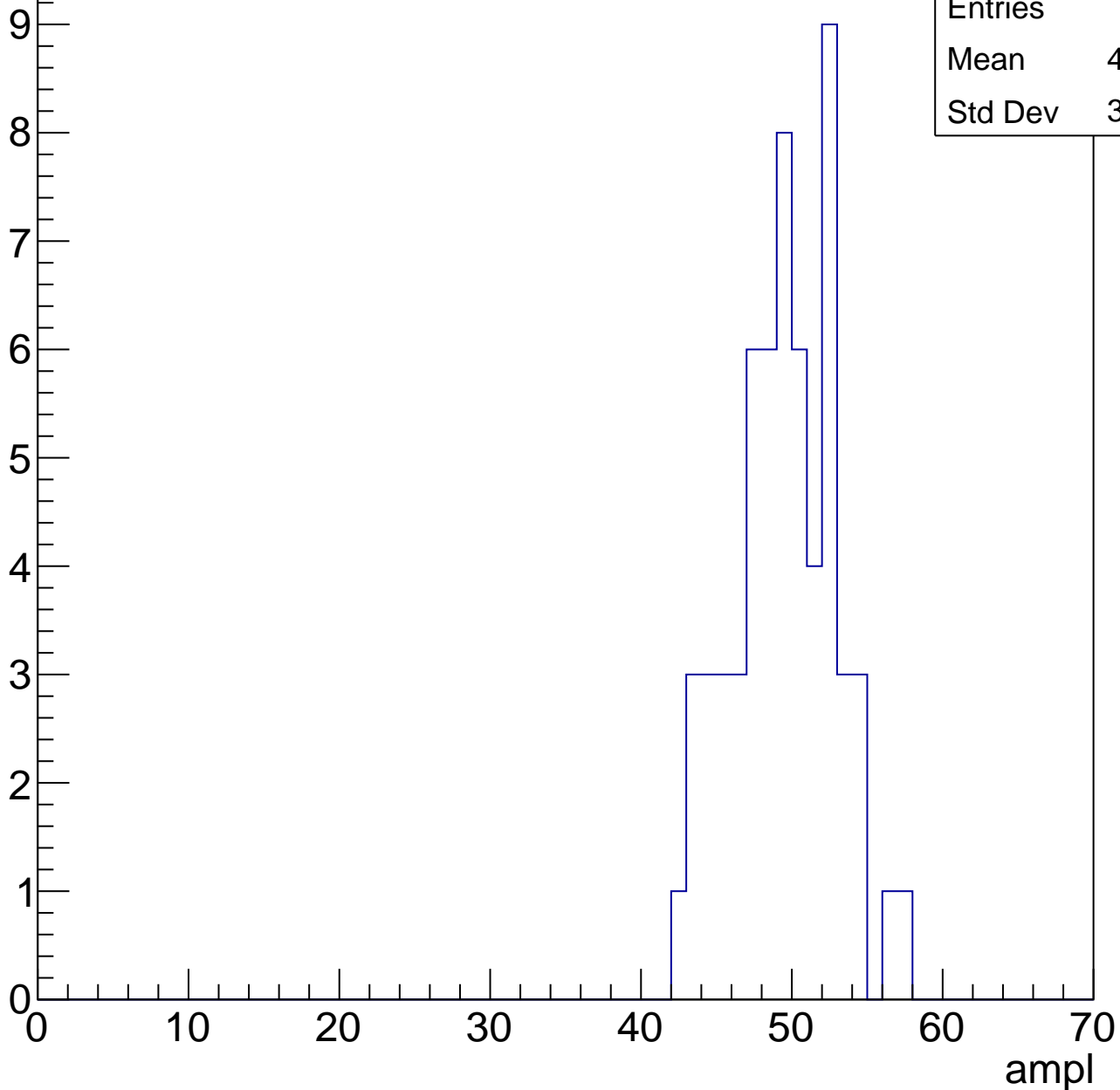


B1L101S, U12-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.07
Std Dev	3.356

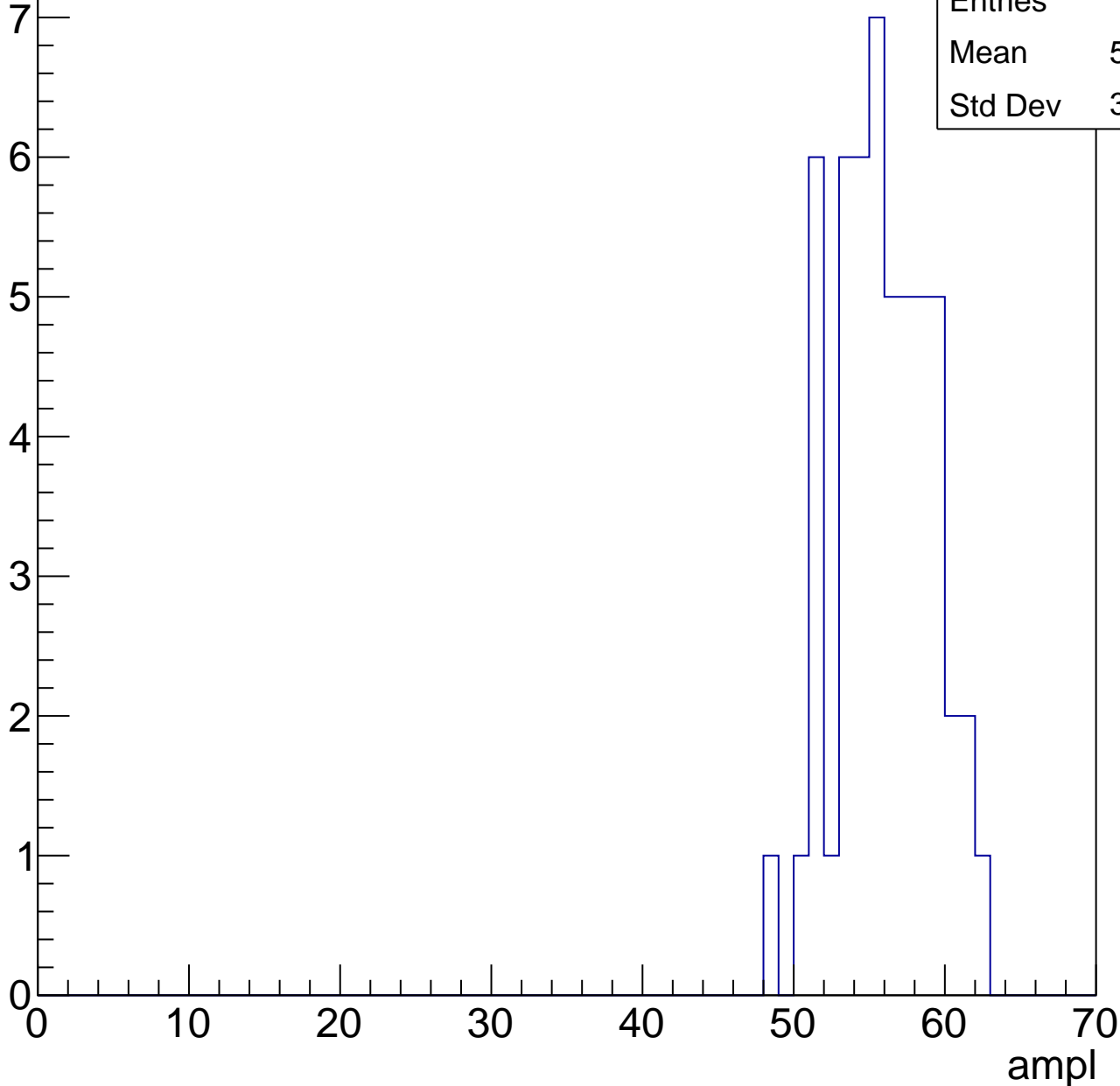


B1L101S, U12-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

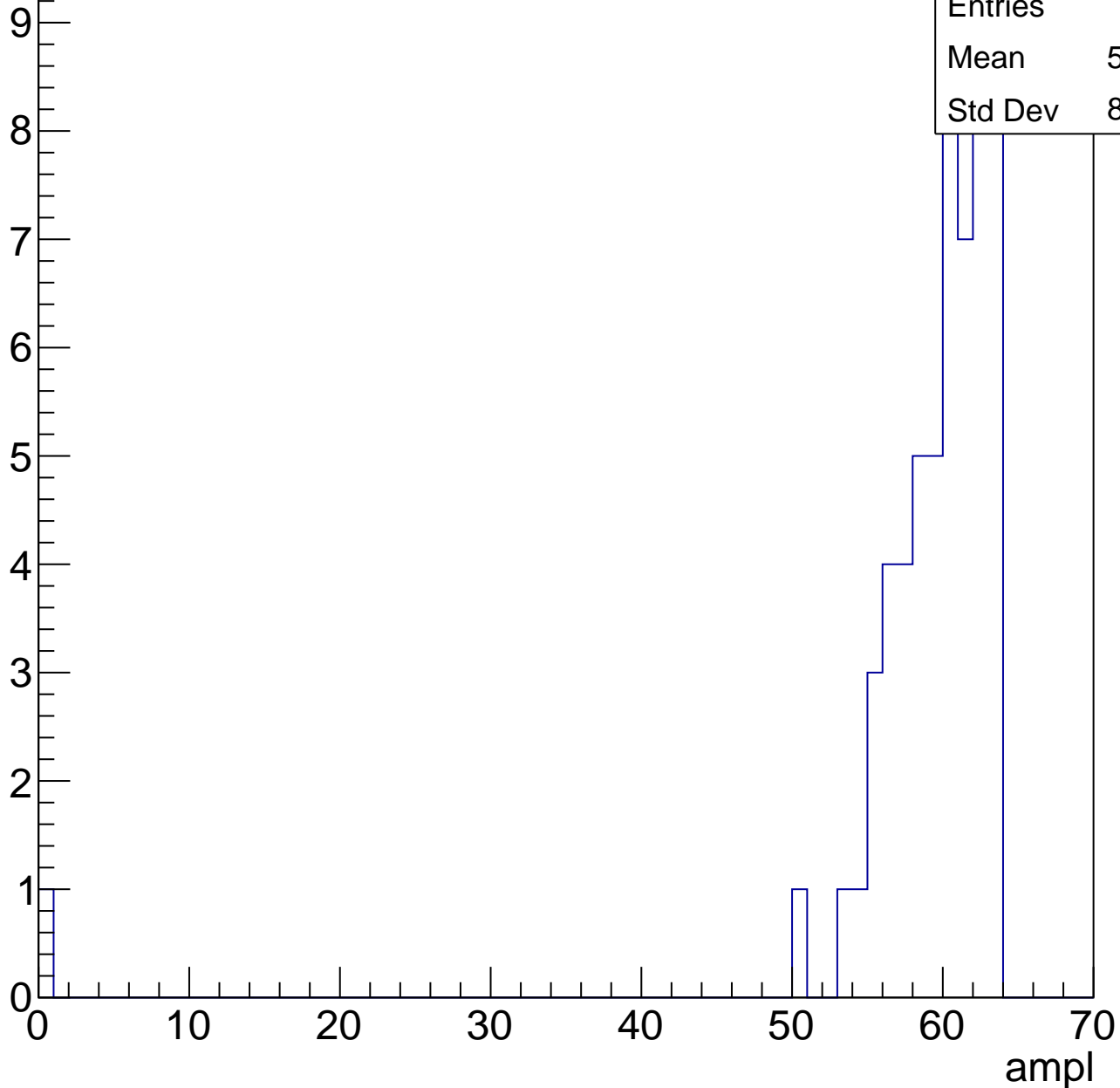
Entries	53
Mean	55.42
Std Dev	3.135



B1L101S, U12-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch34, adc0

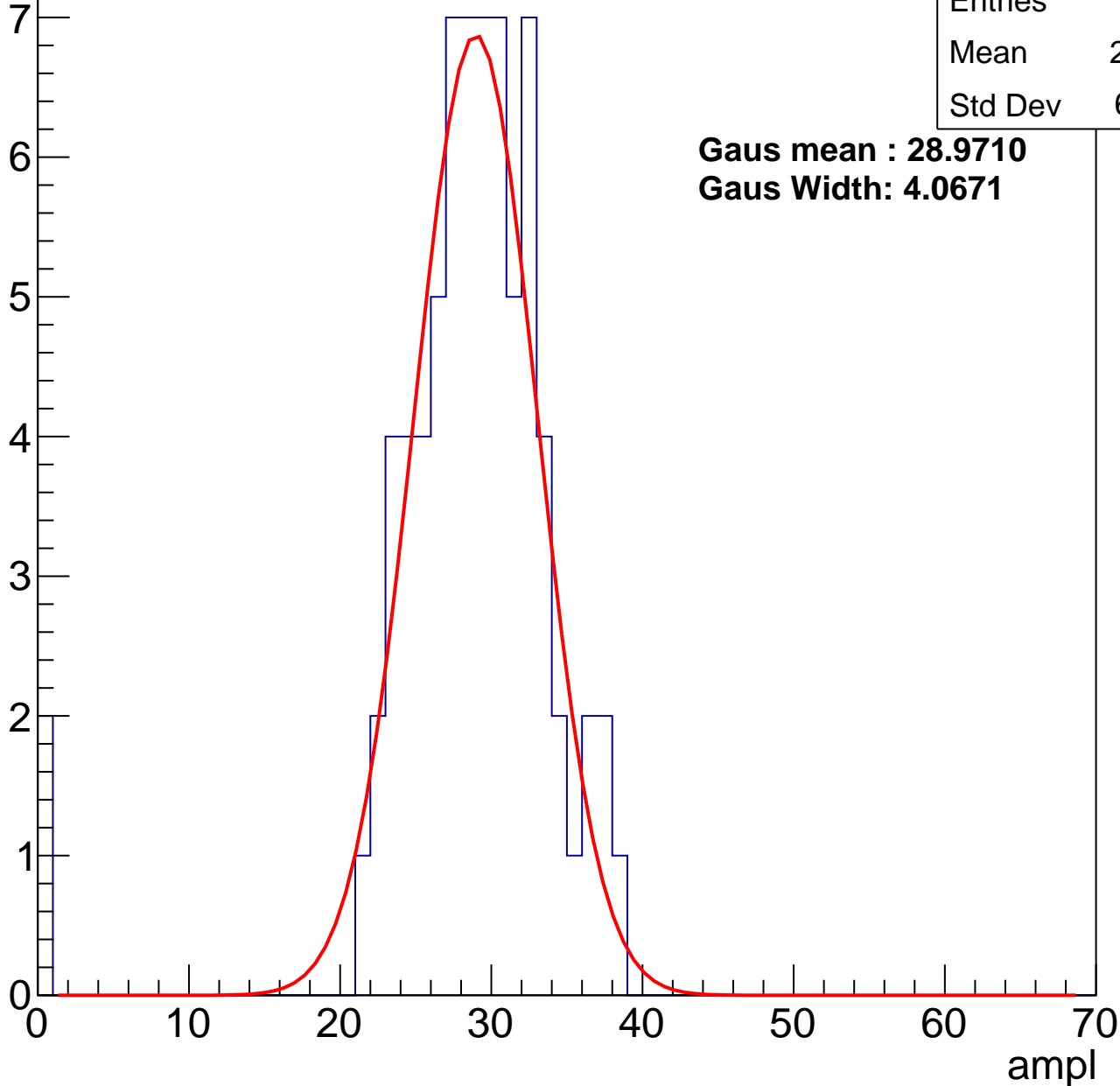
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.09
Std Dev	6.061

Gaus mean : 28.9710

Gaus Width: 4.0671



B1L101S, U12-ch34, adc1

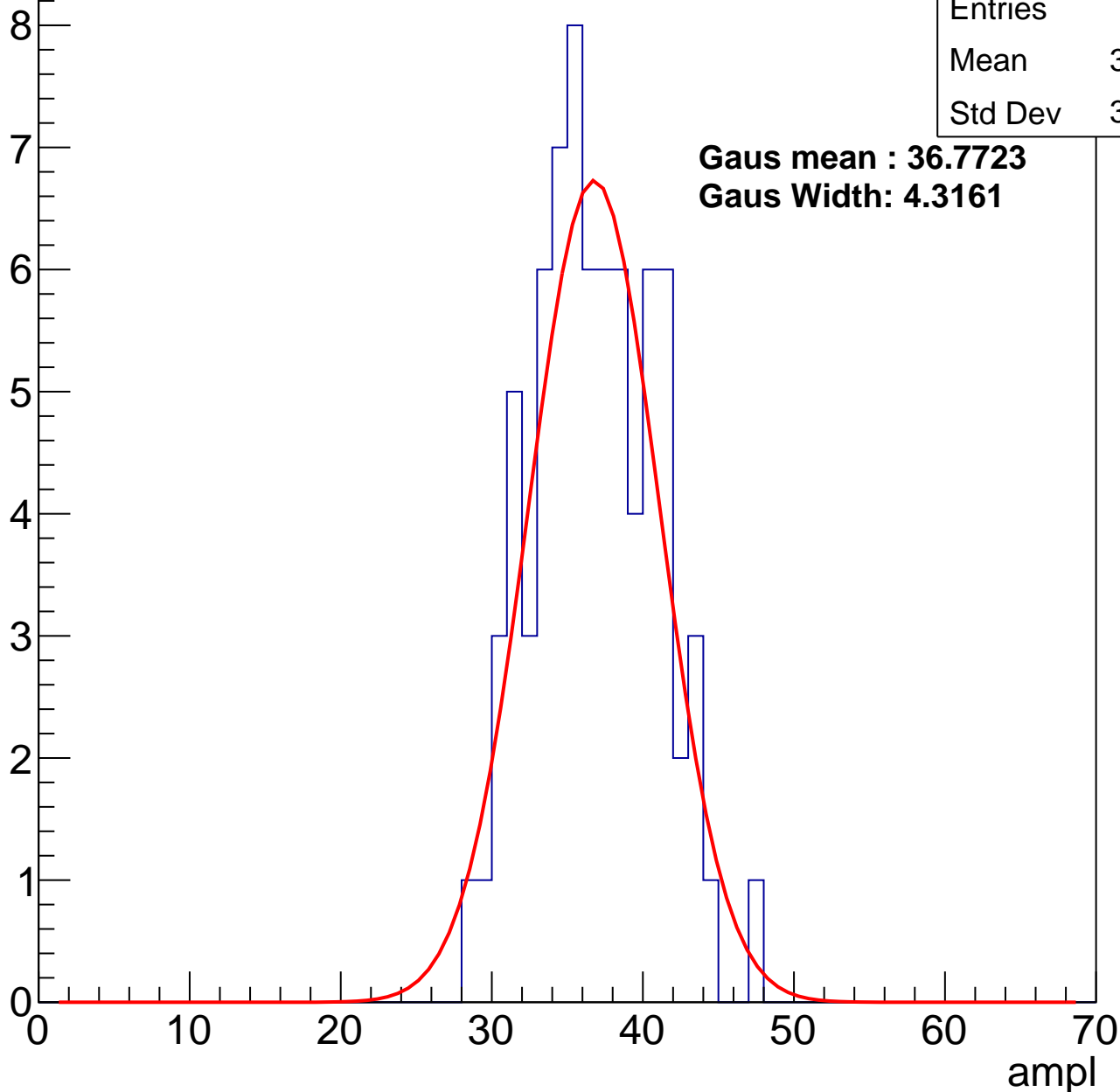
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.35
Std Dev	3.985

Gaus mean : 36.7723

Gaus Width: 4.3161



B1L101S, U12-ch34, adc2

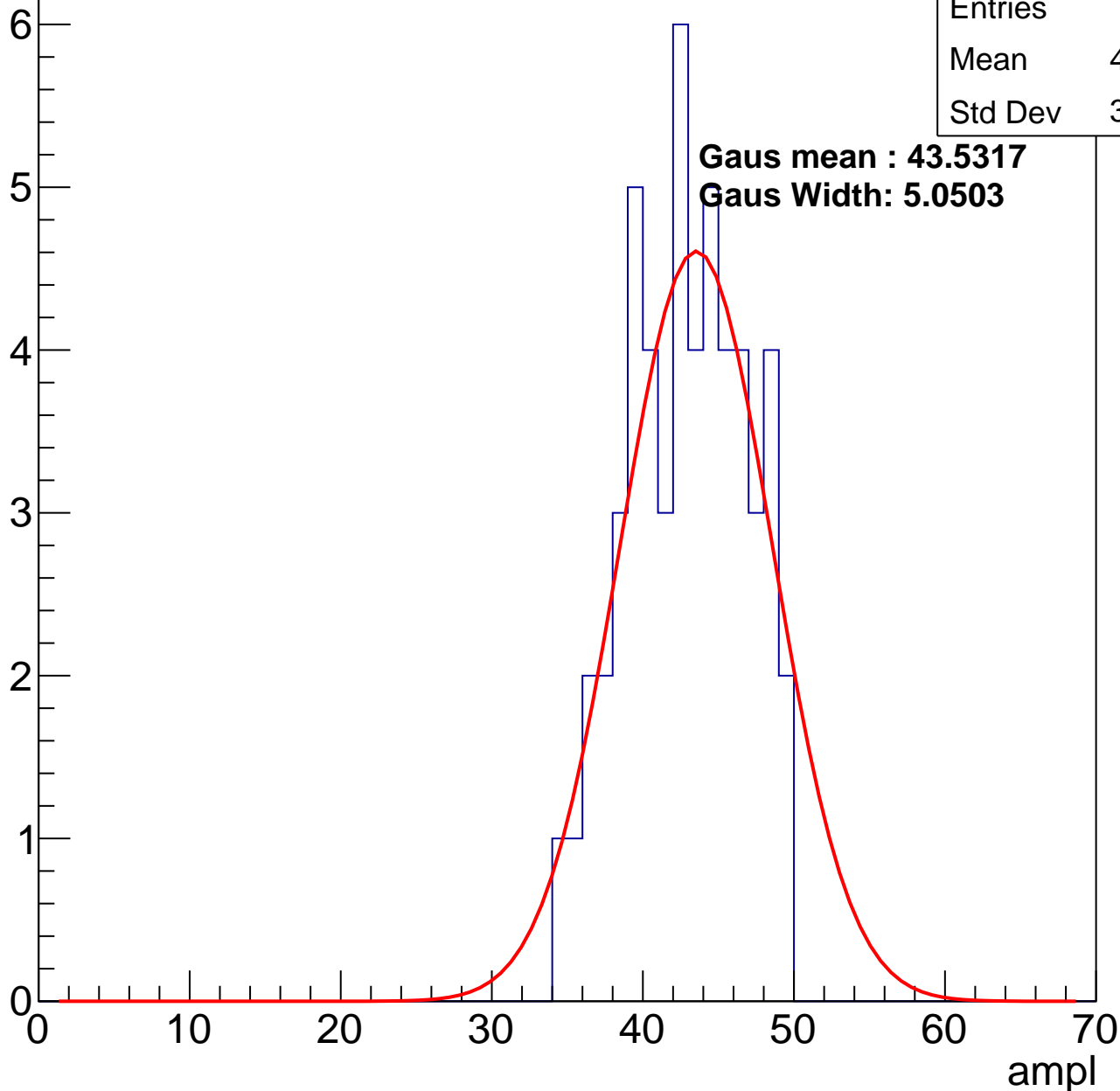
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.38
Std Dev	3.837

Gaus mean : 43.5317

Gaus Width: 5.0503

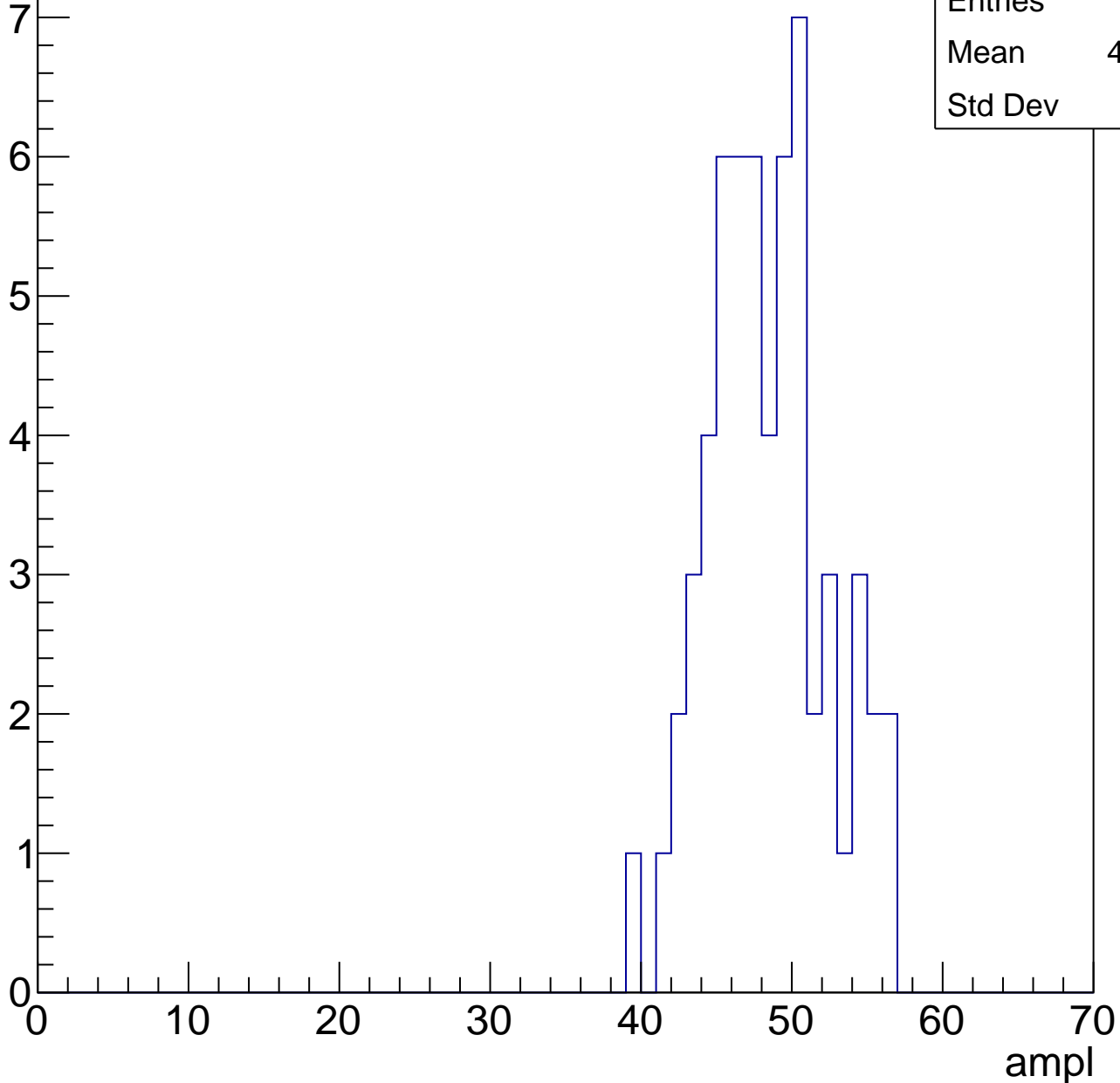


B1L101S, U12-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

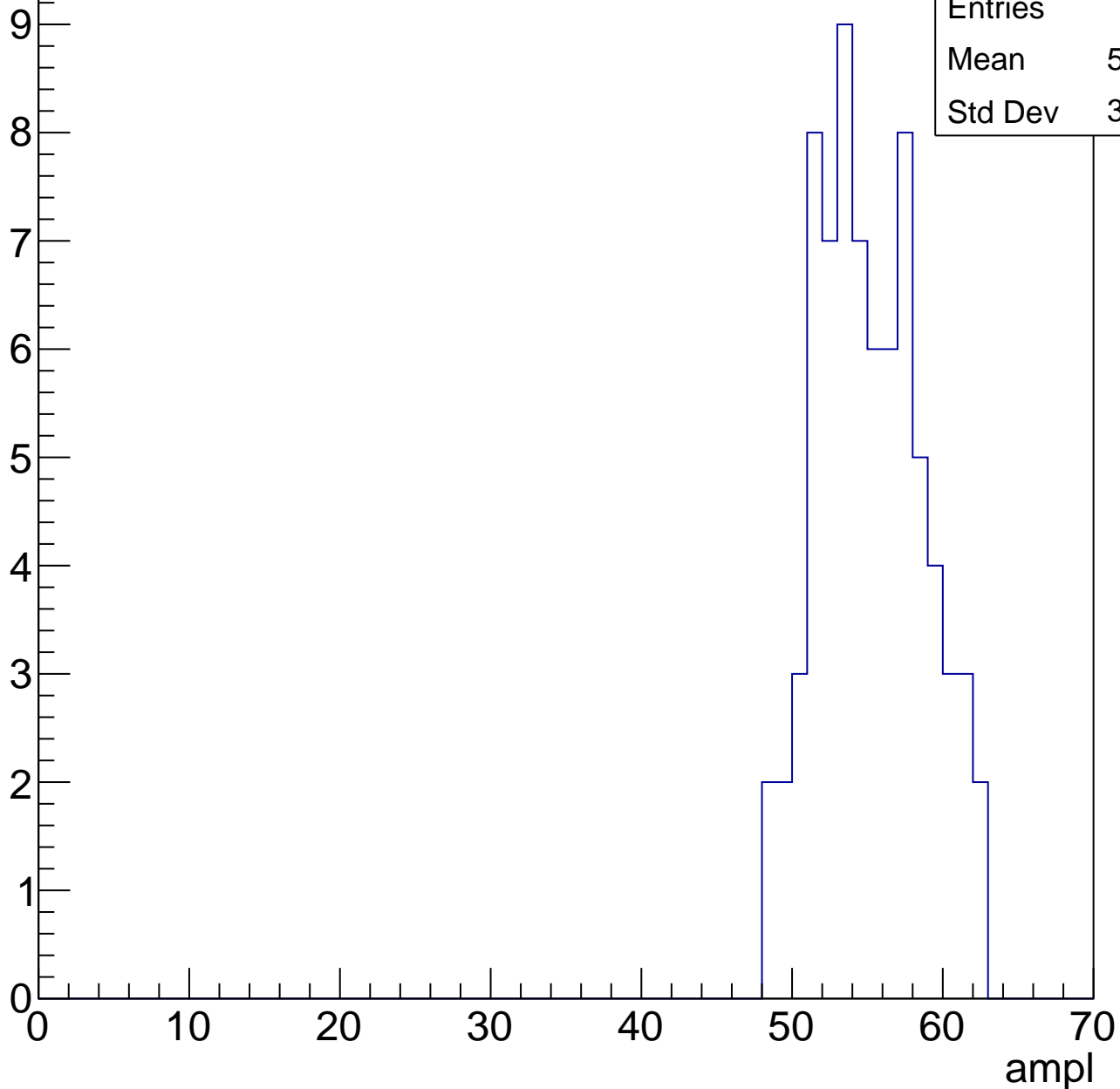
Entries	59
Mean	47.93
Std Dev	3.87



B1L101S, U12-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

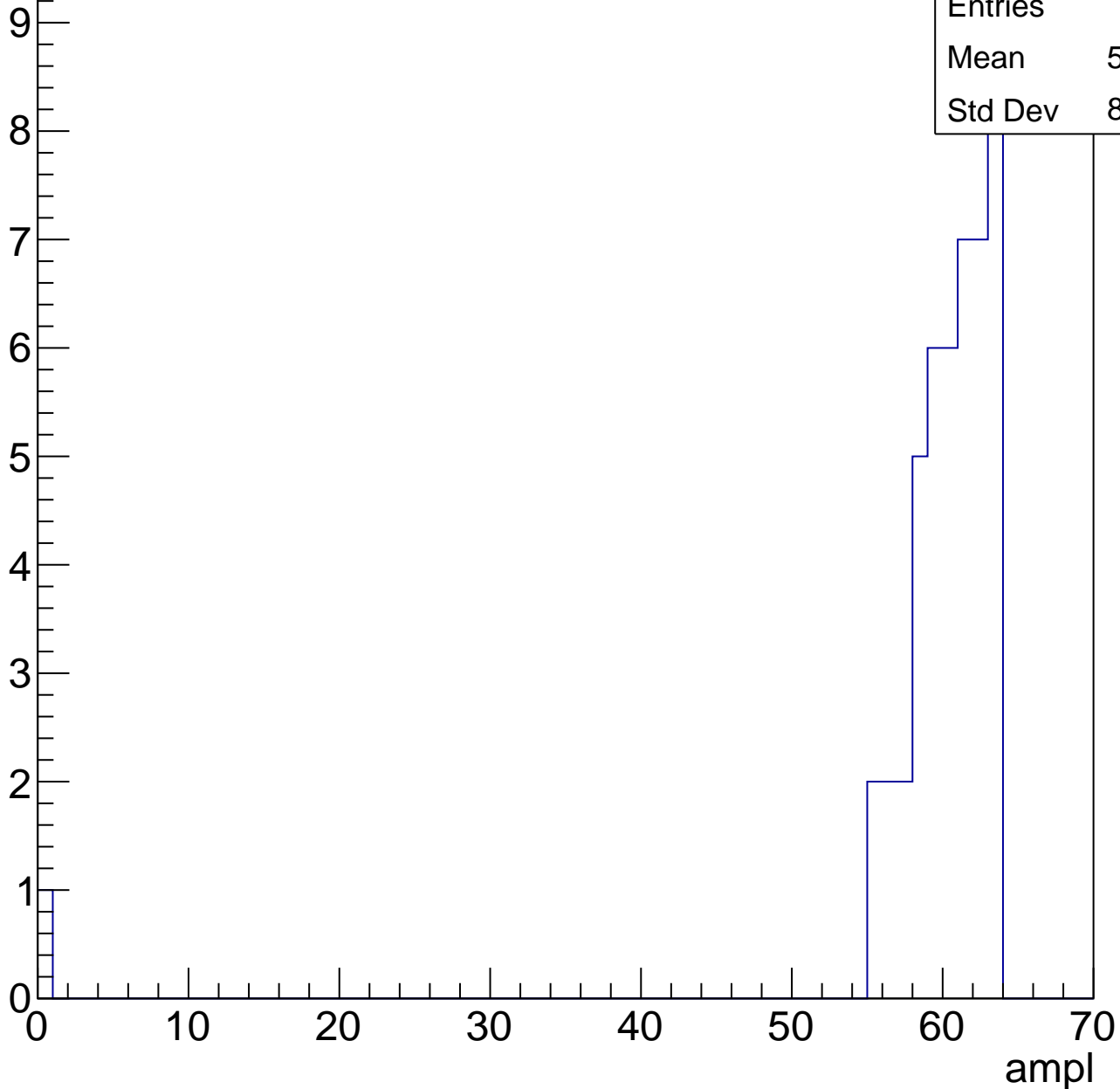


B1L101S, U12-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

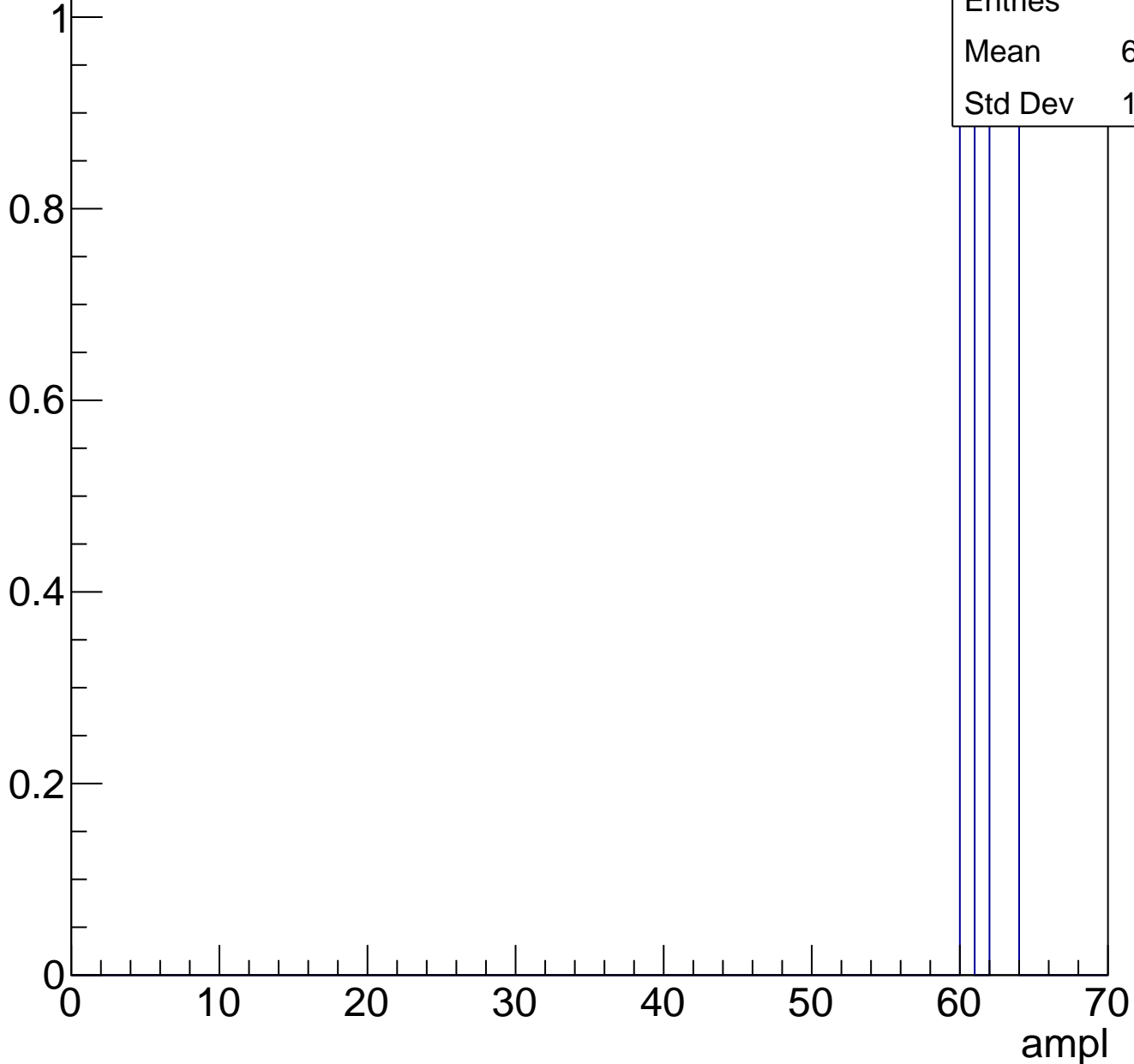
Entries	47
Mean	58.89
Std Dev	8.973



B1L101S, U12-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

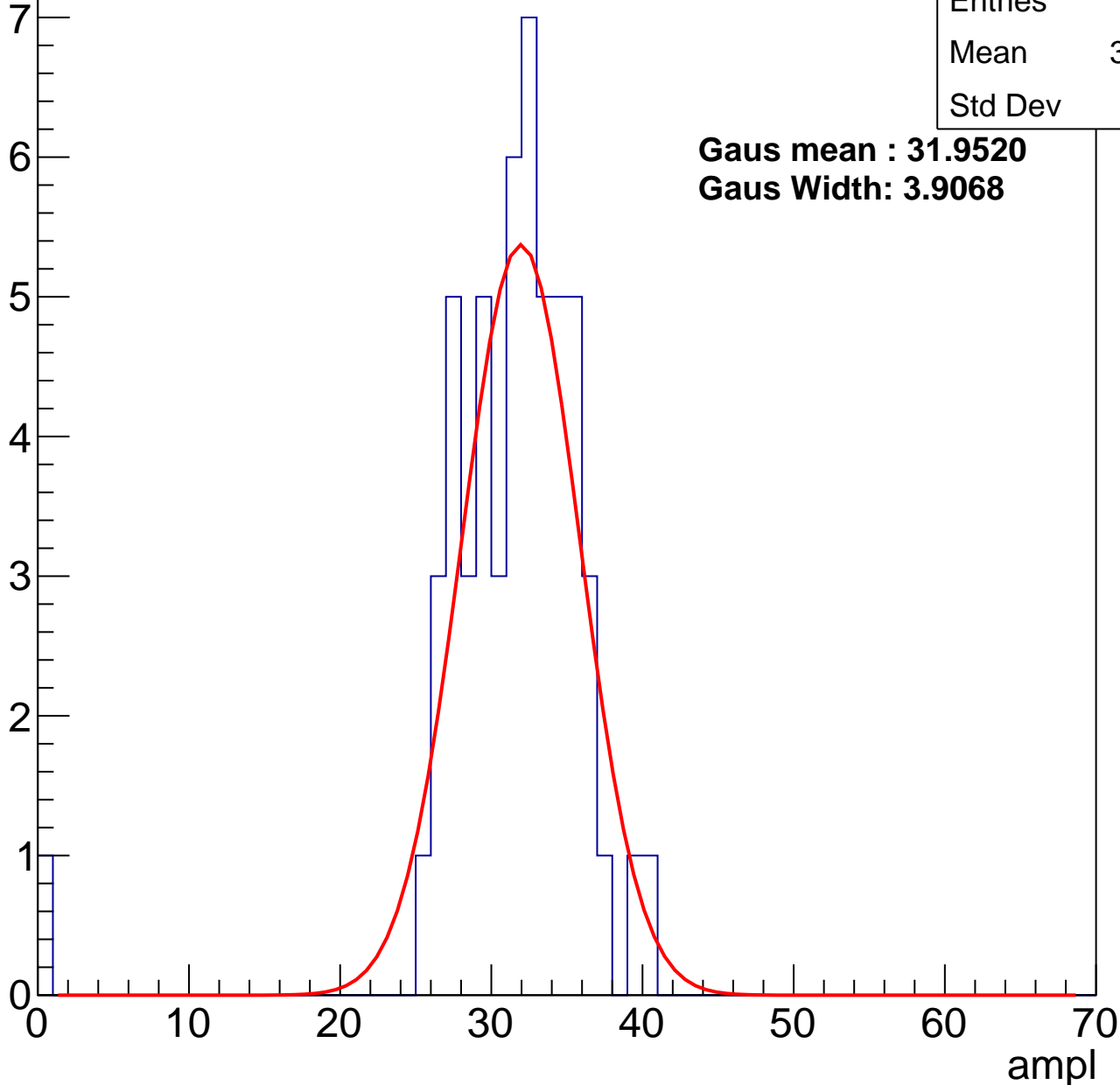
B1L101S, U12-ch35, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	30.93
Std Dev	5.41

Gaus mean : 31.9520
Gaus Width: 3.9068



B1L101S, U12-ch35, adc1

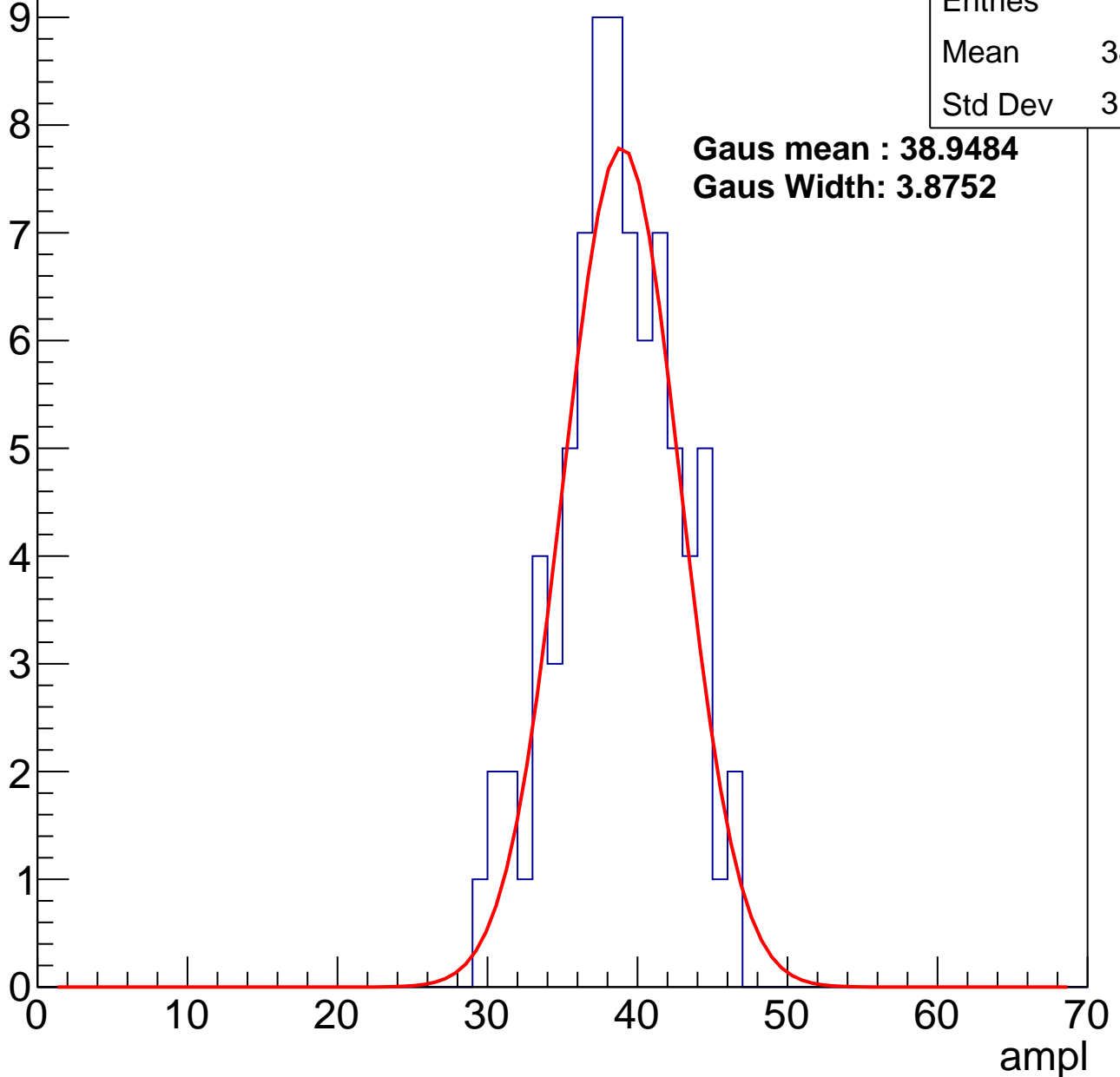
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	38.23
Std Dev	3.873

Gaus mean : 38.9484

Gaus Width: 3.8752



B1L101S, U12-ch35, adc2

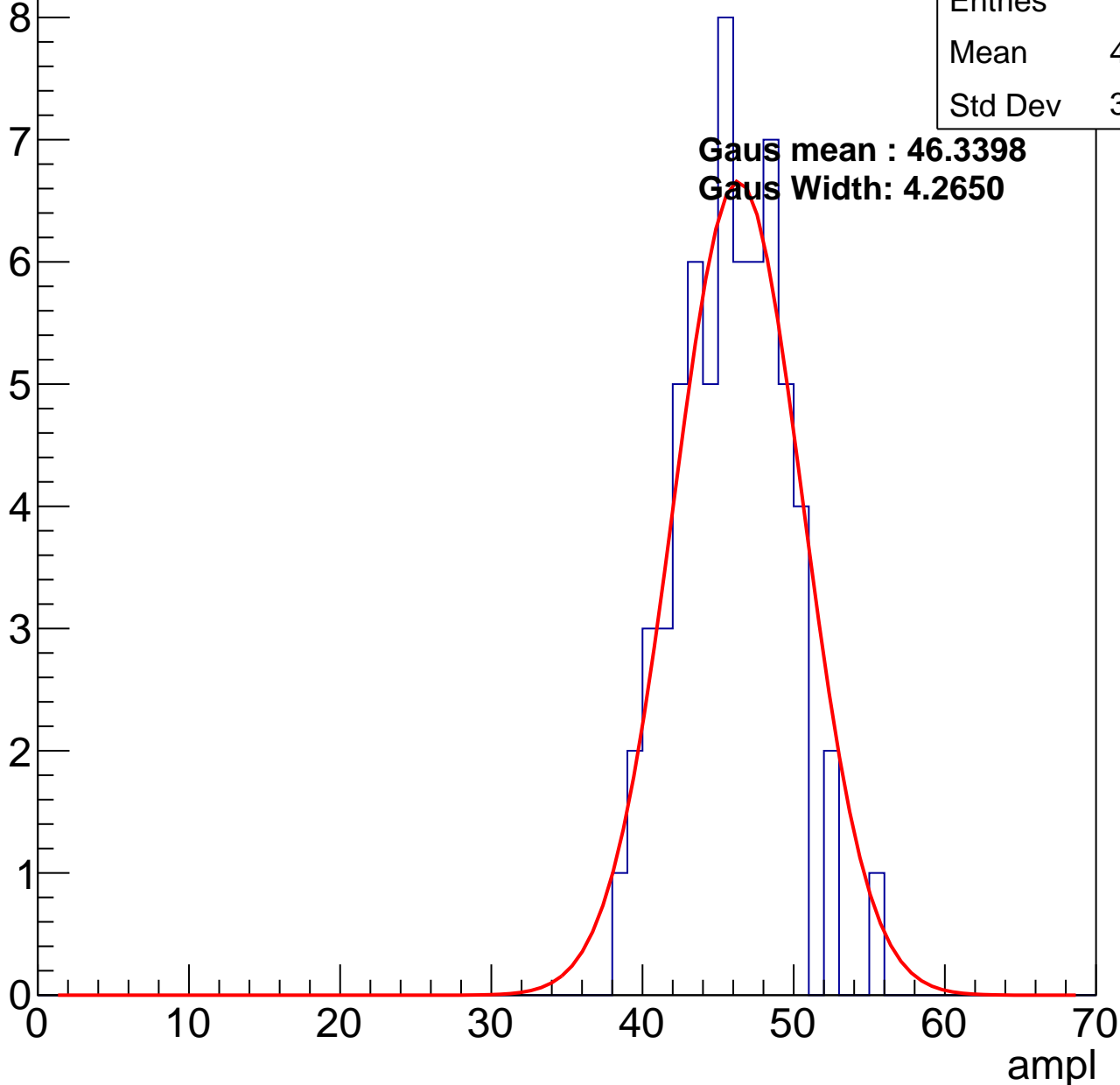
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.39
Std Dev	3.489

Gaus mean : 46.3398

Gaus Width: 4.2650

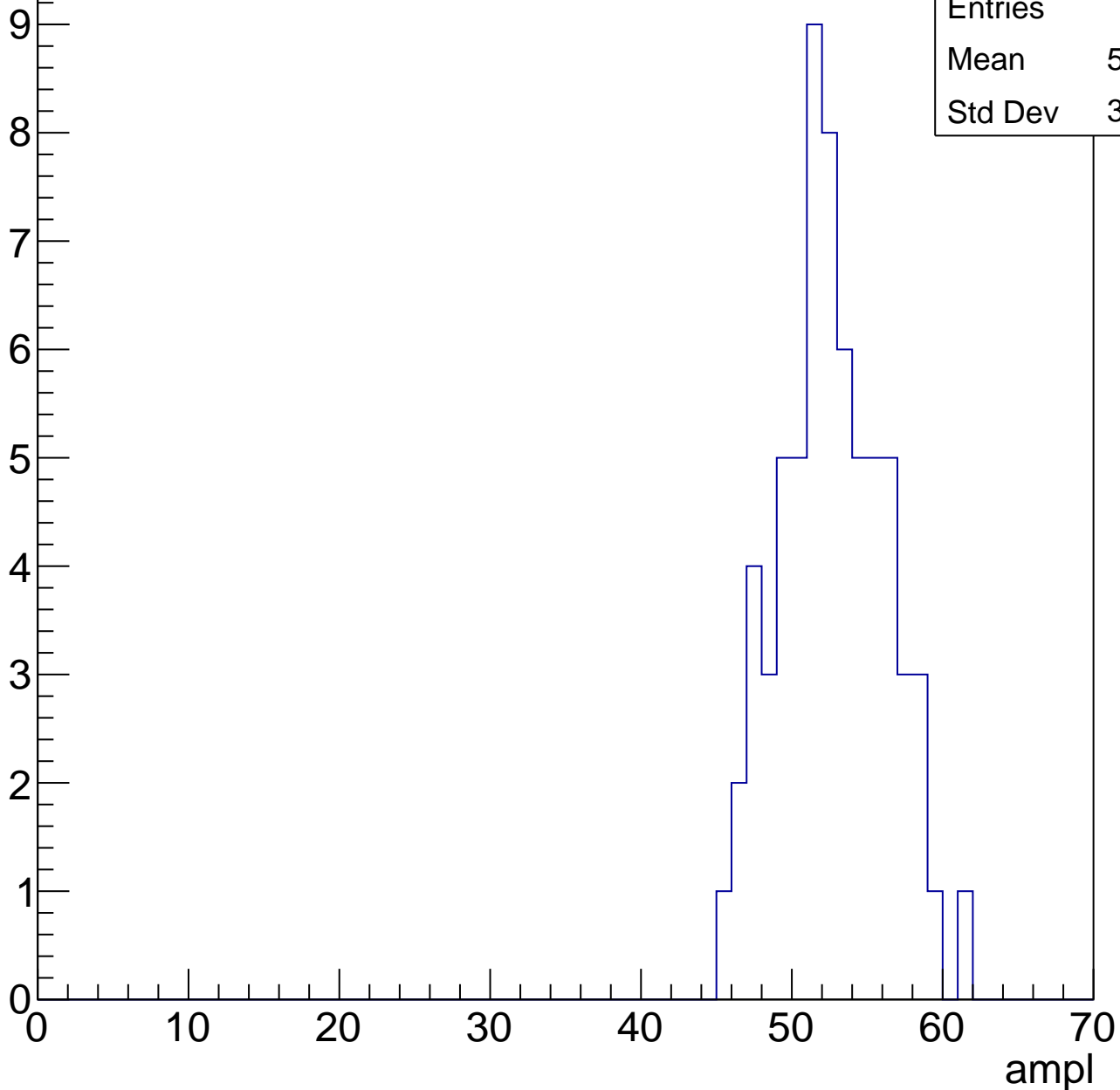


B1L101S, U12-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	52.23
Std Dev	3.489



B1L101S, U12-ch35, adc4

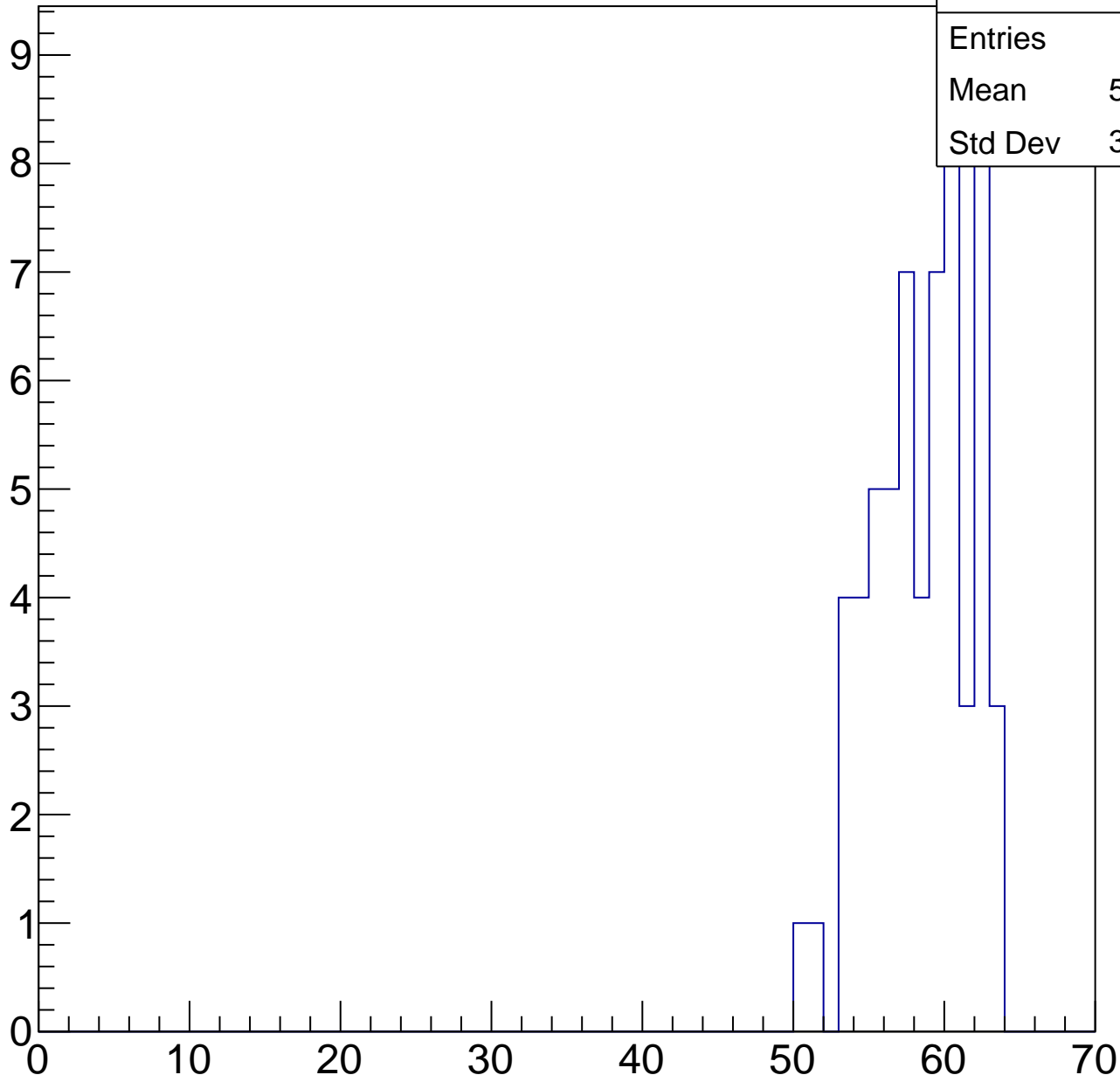
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	57.97
Std Dev	3.193

ampl

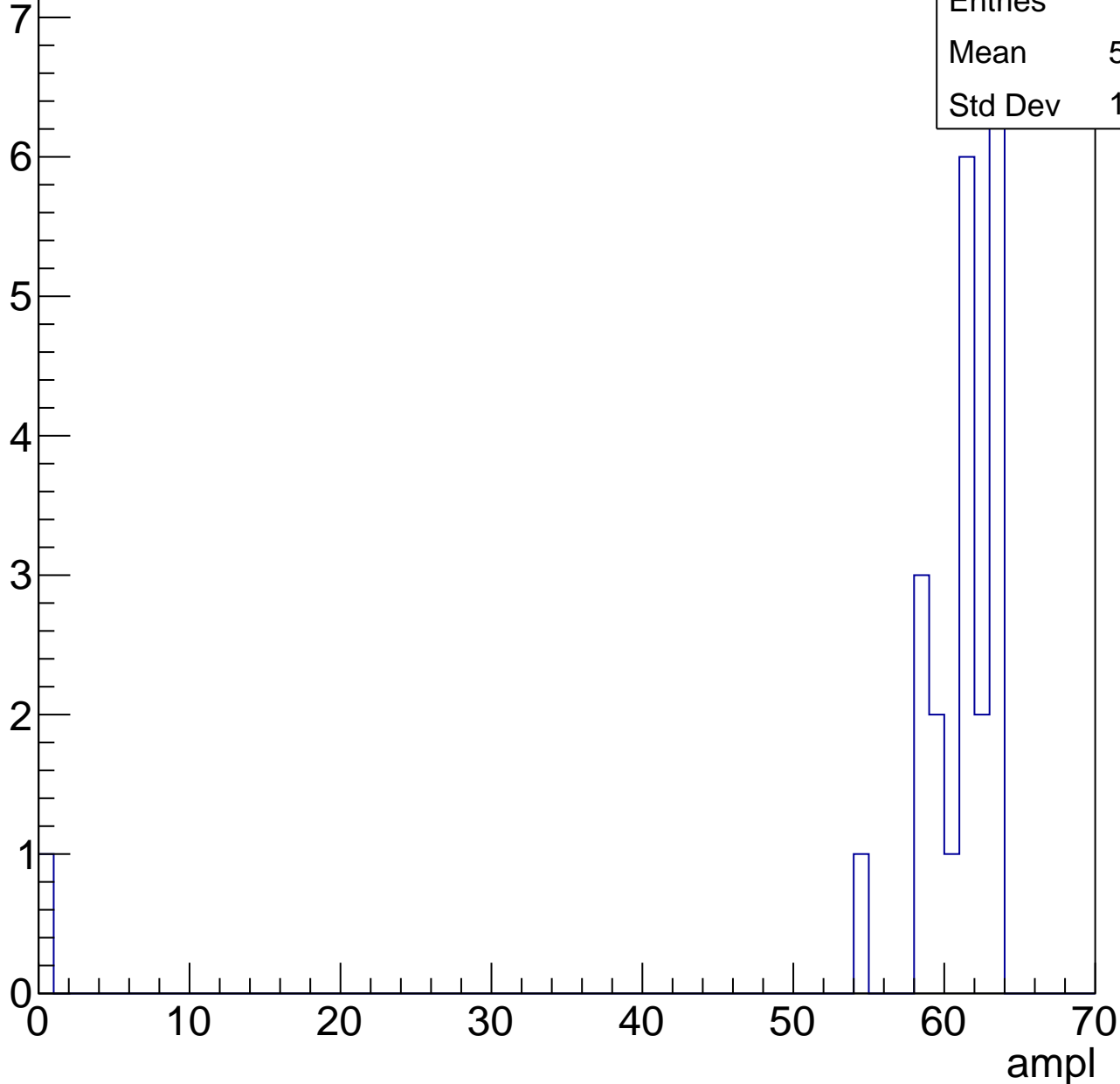


B1L101S, U12-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.13
Std Dev	12.59



B1L101S, U12-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch36, adc0

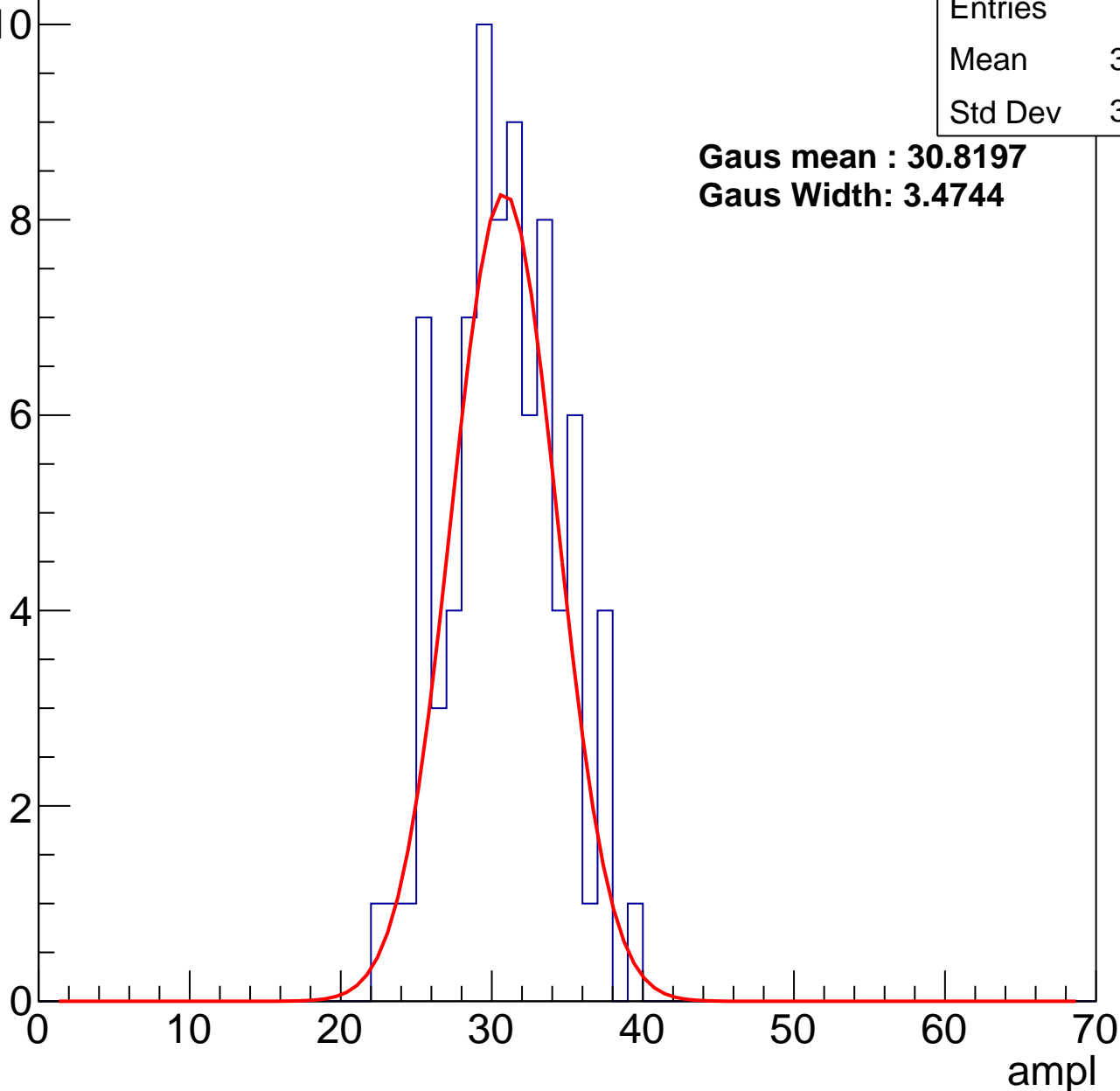
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.37
Std Dev	3.629

Gaus mean : 30.8197

Gaus Width: 3.4744



B1L101S, U12-ch36, adc1

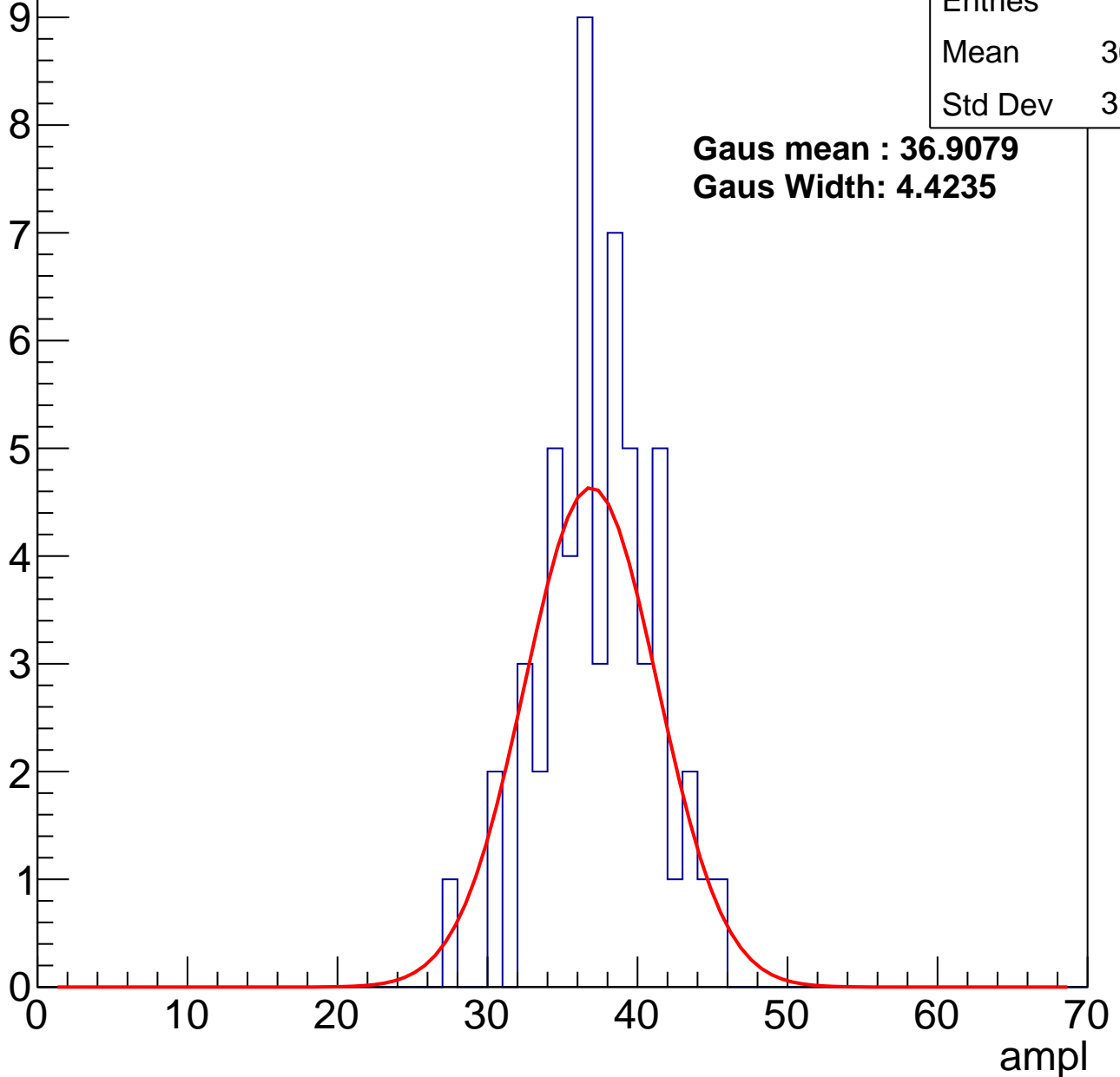
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.98
Std Dev	3.649

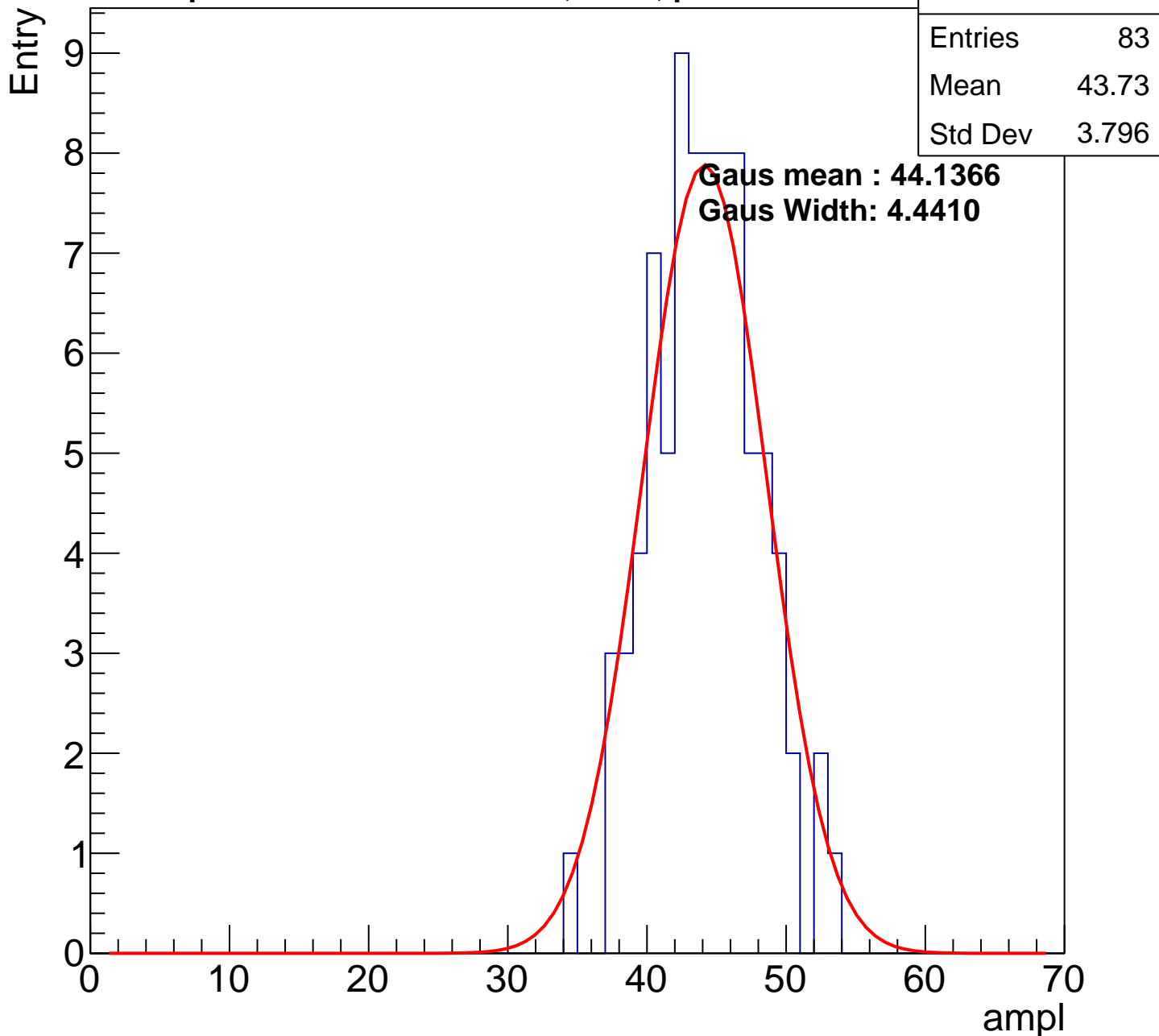
Gaus mean : 36.9079

Gaus Width: 4.4235



B1L101S, U12-ch36, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	50.83
Std Dev	2.898

Entry

10

8

6

4

2

0

0

10

20

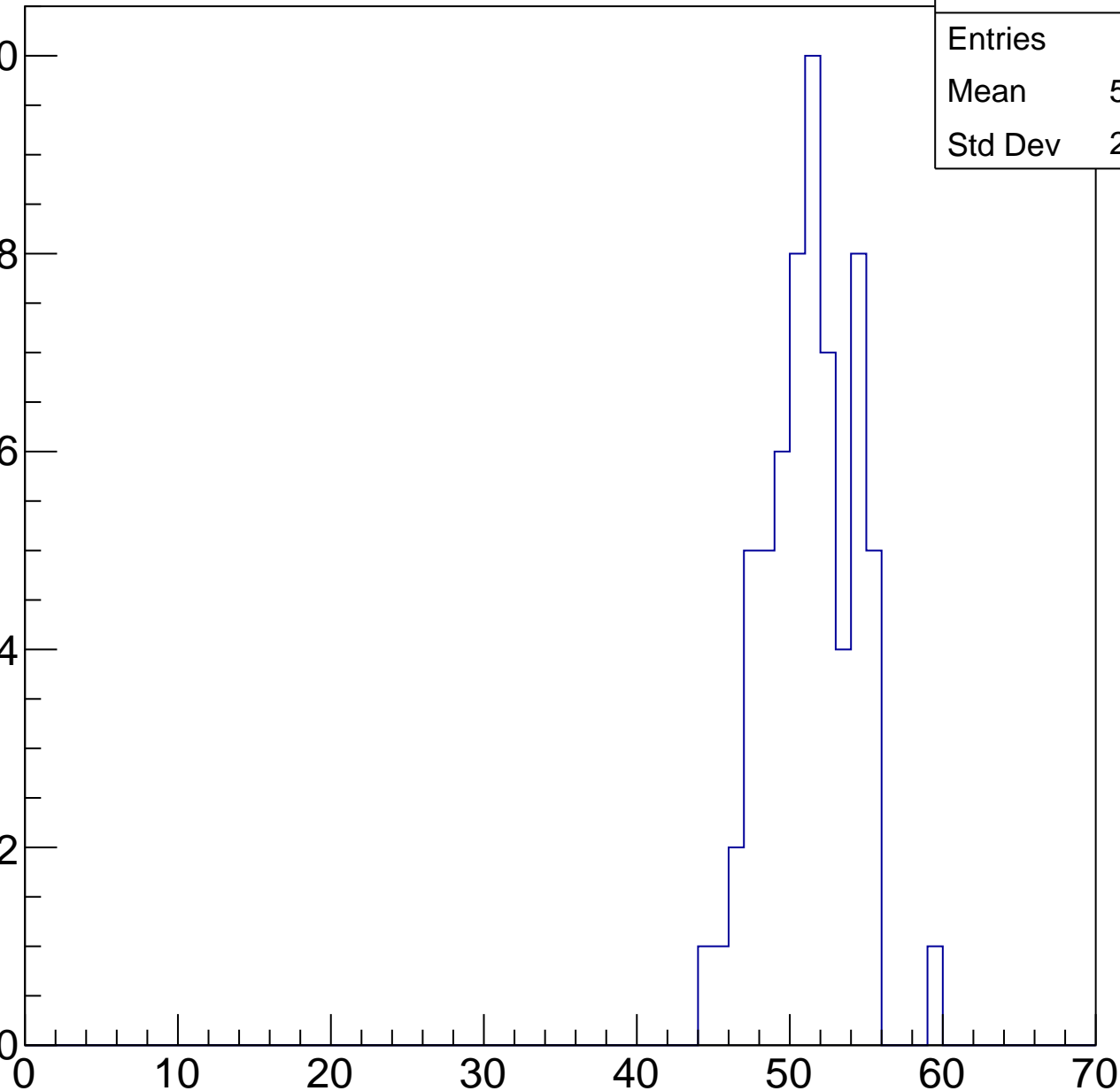
30

40

50

60

ampl

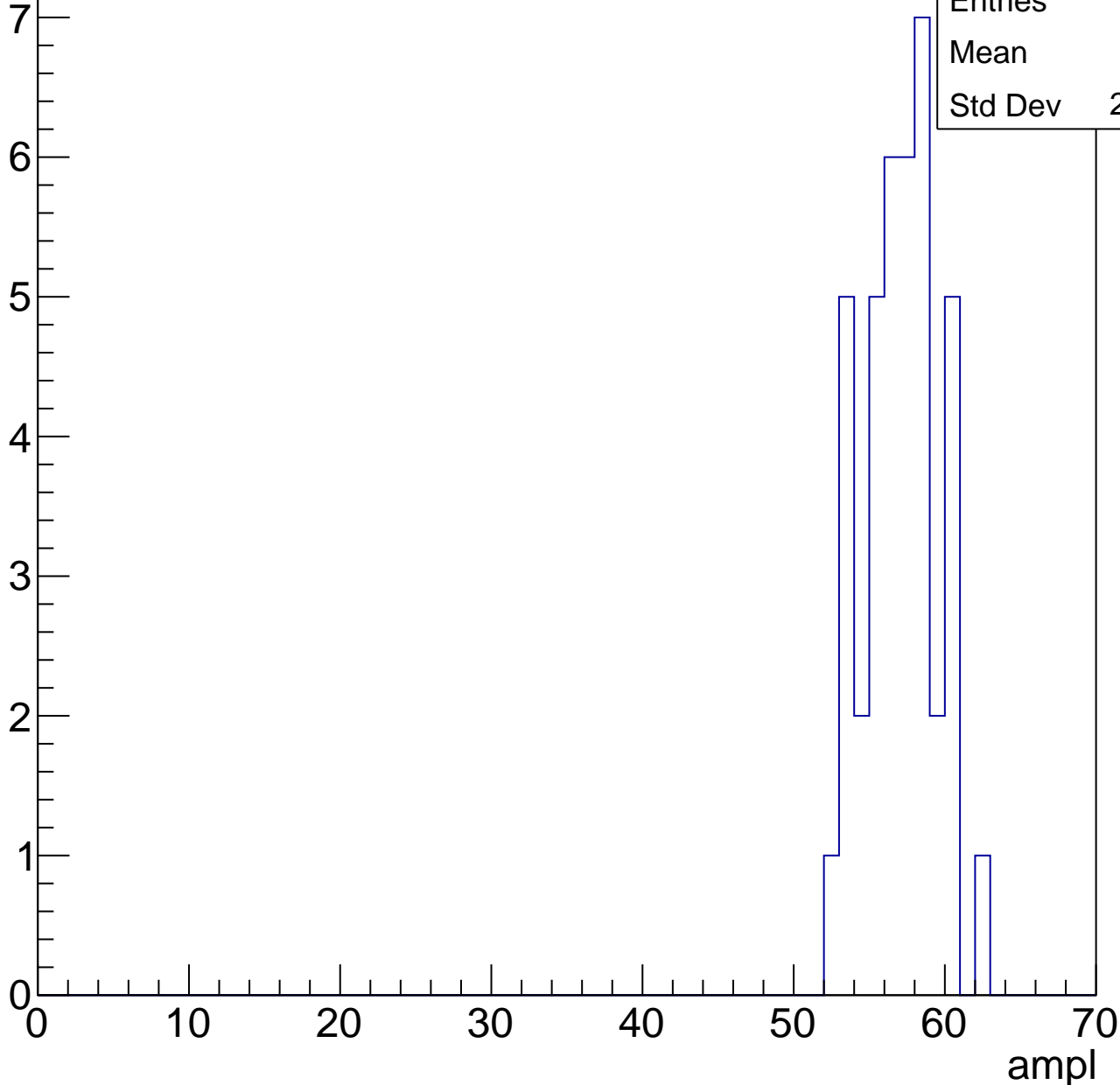


B1L101S, U12-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

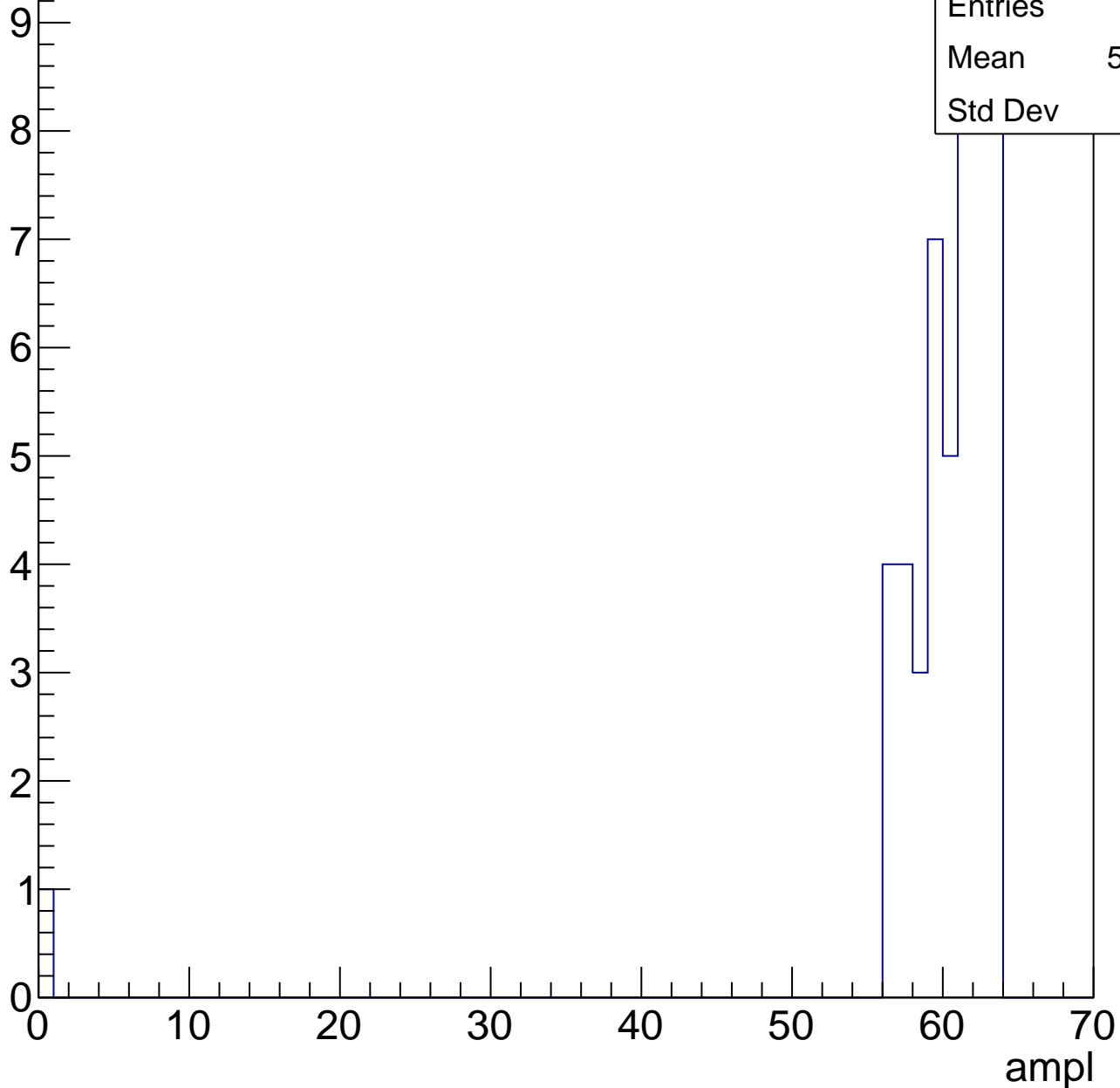
Entries	40
Mean	56.6
Std Dev	2.385



B1L101S, U12-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch37, adc0

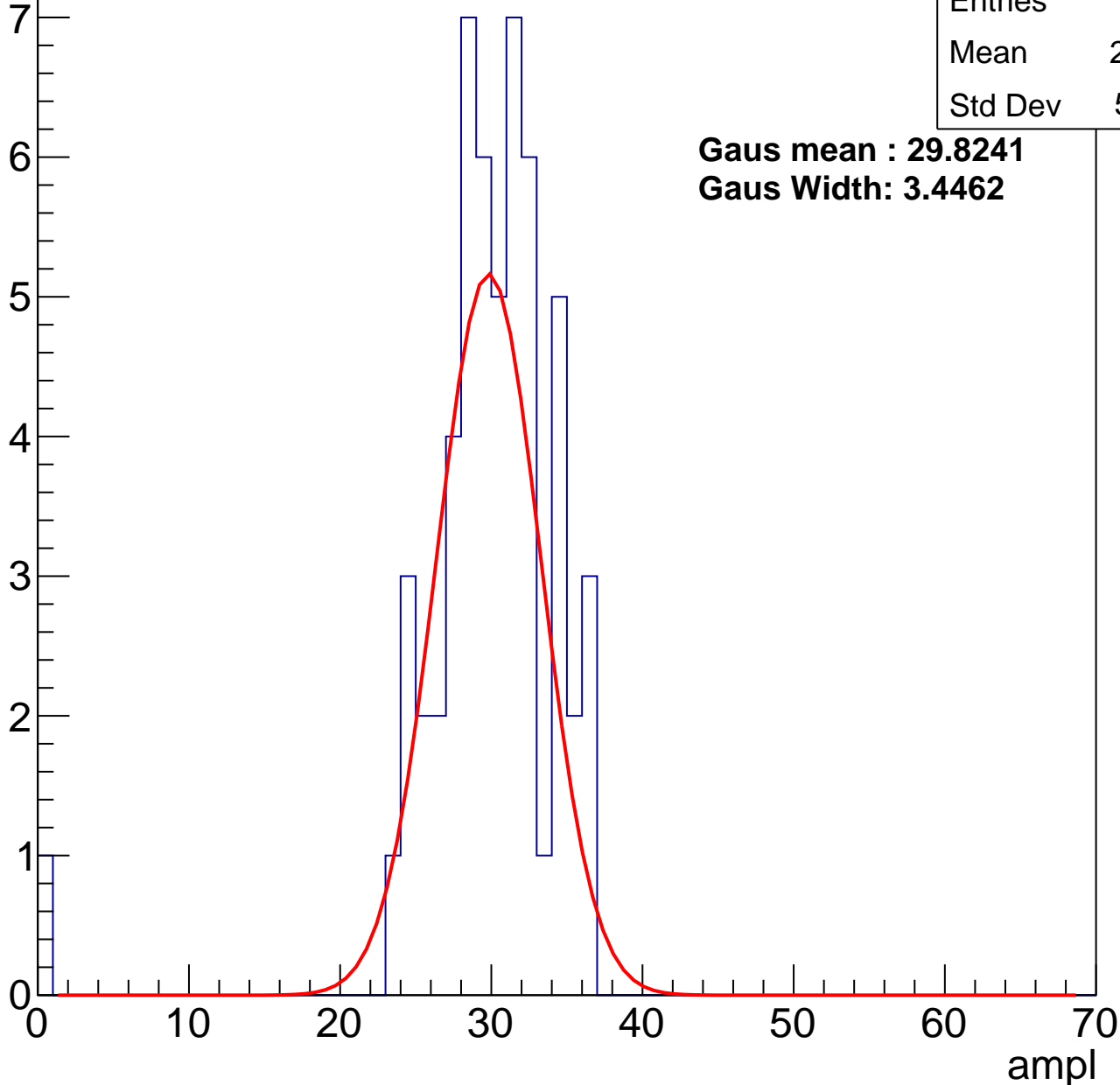
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	29.36
Std Dev	5.161

Gaus mean : 29.8241

Gaus Width: 3.4462



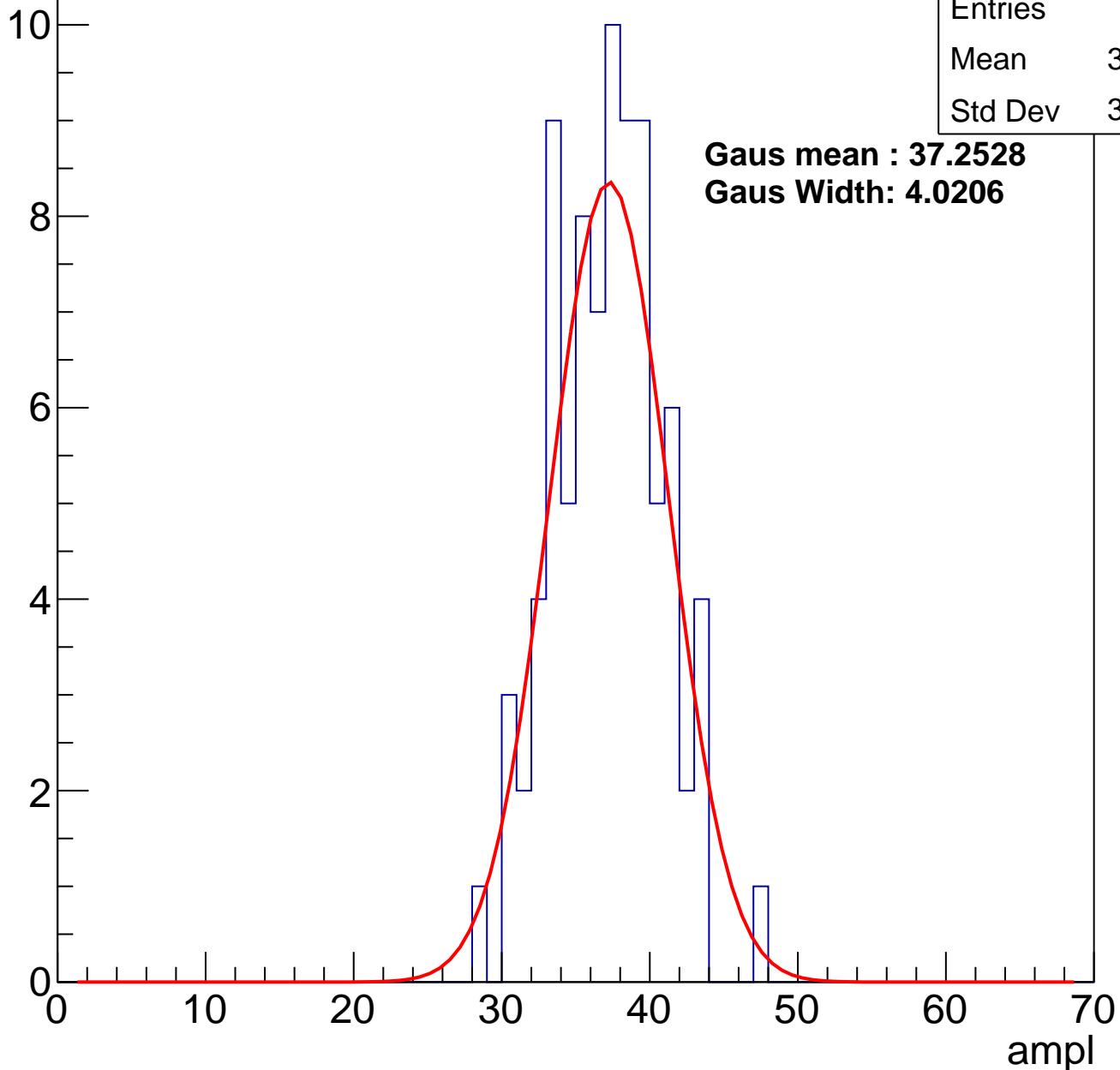
B1L101S, U12-ch37, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	36.69
Std Dev	3.597

Gaus mean : 37.2528
Gaus Width: 4.0206

Entry



B1L101S, U12-ch37, adc2

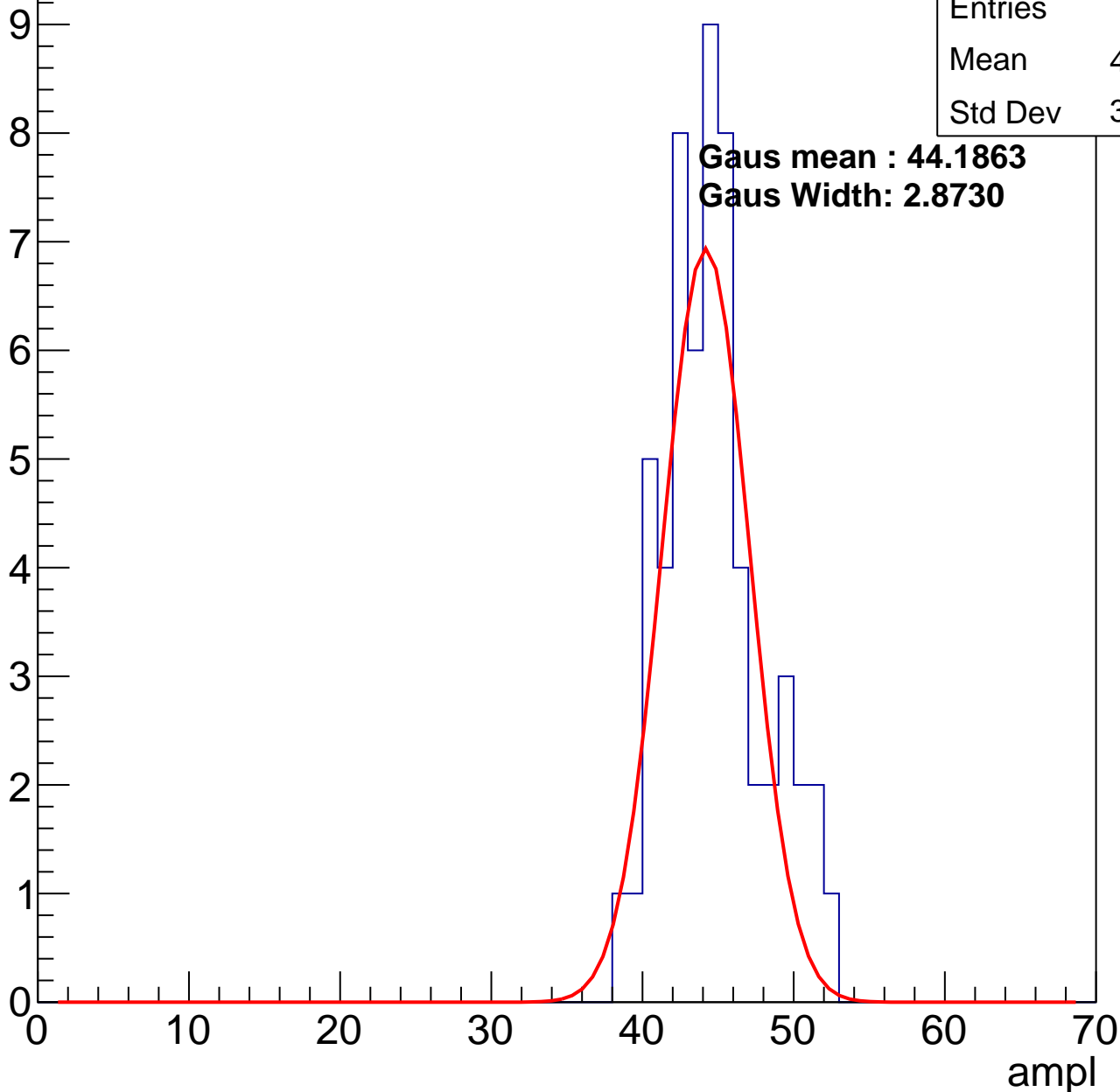
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.24
Std Dev	3.202

Gaus mean : 44.1863

Gaus Width: 2.8730

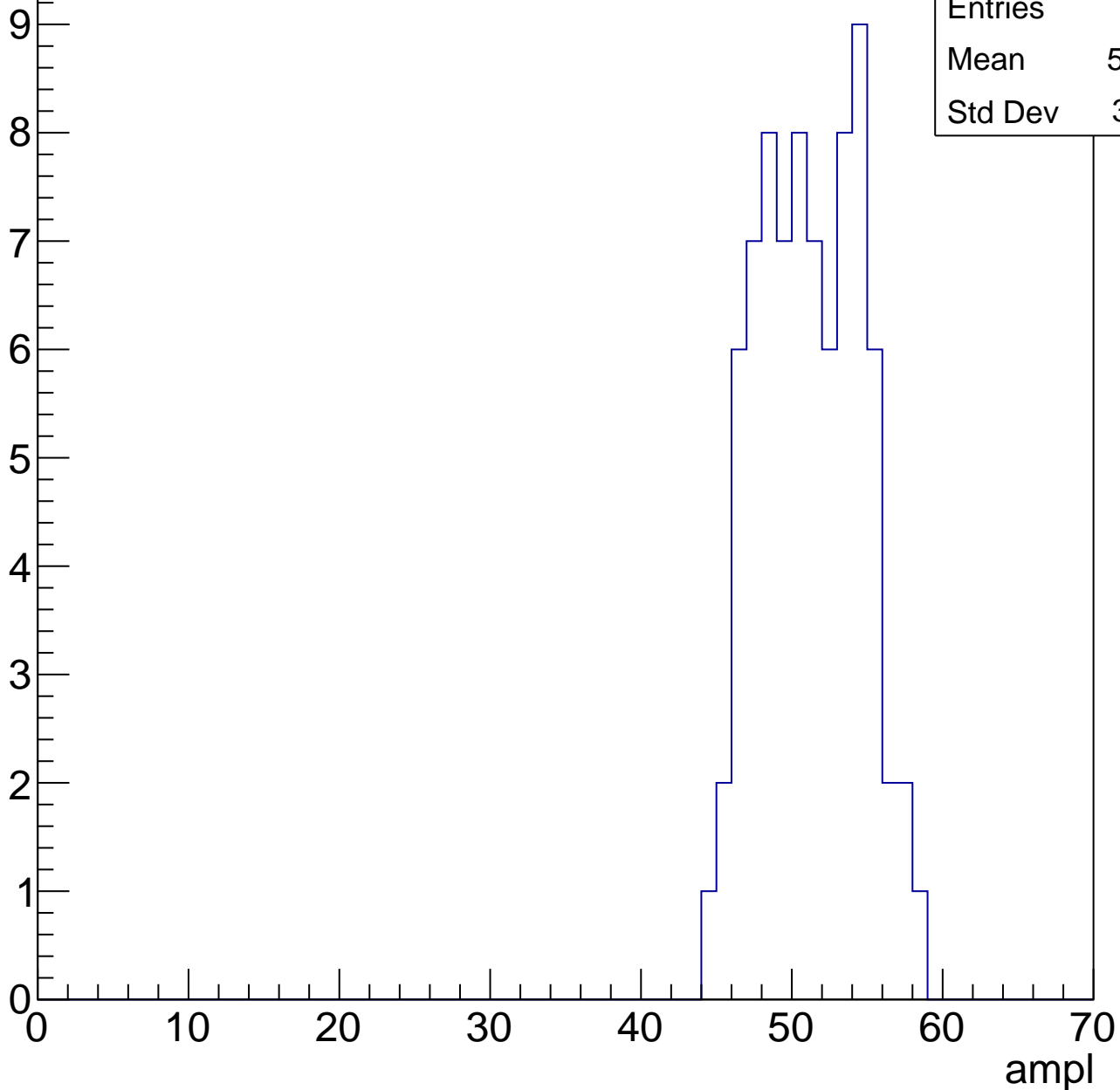


B1L101S, U12-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	50.74
Std Dev	3.301

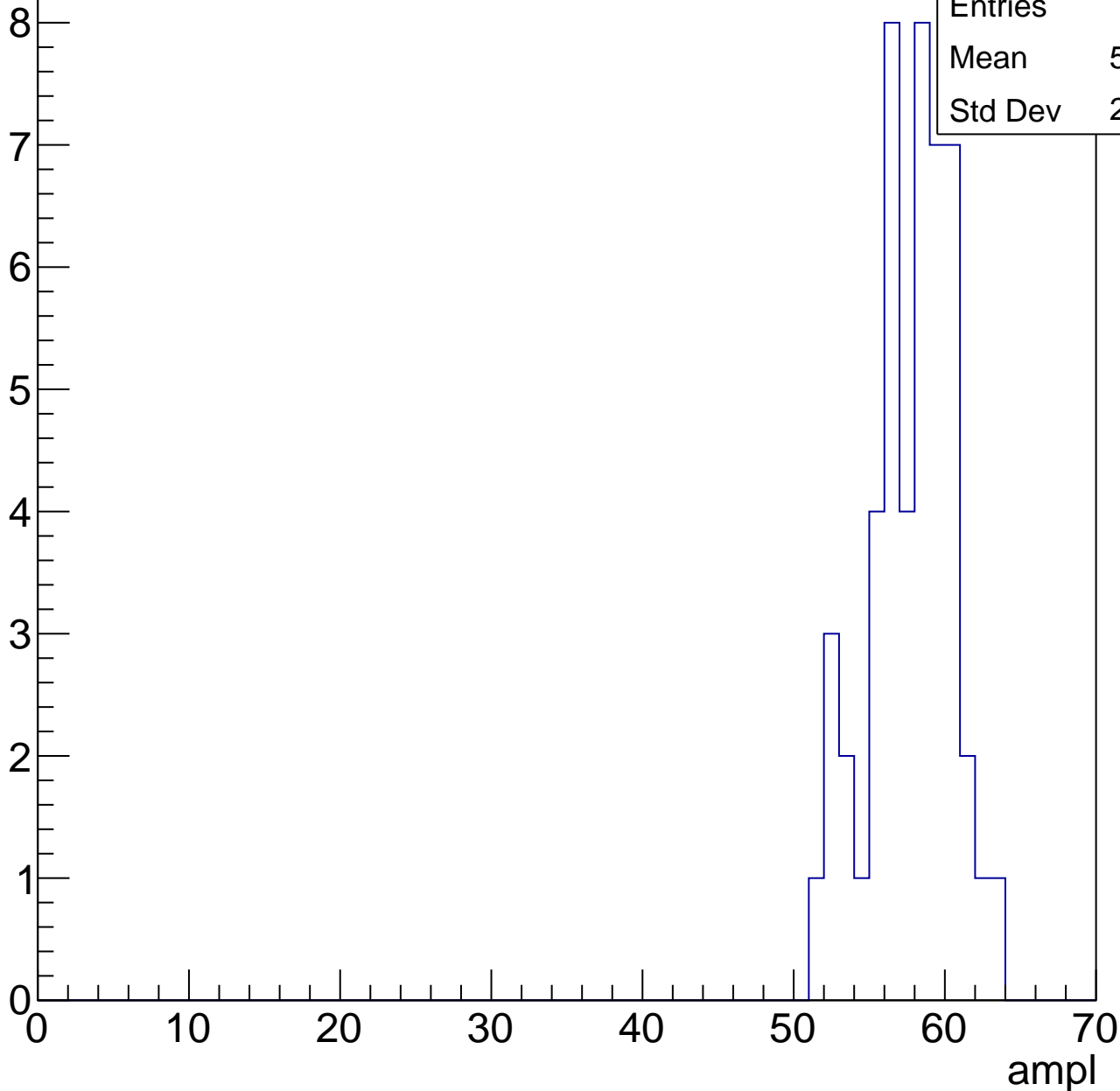


B1L101S, U12-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.29
Std Dev	2.726

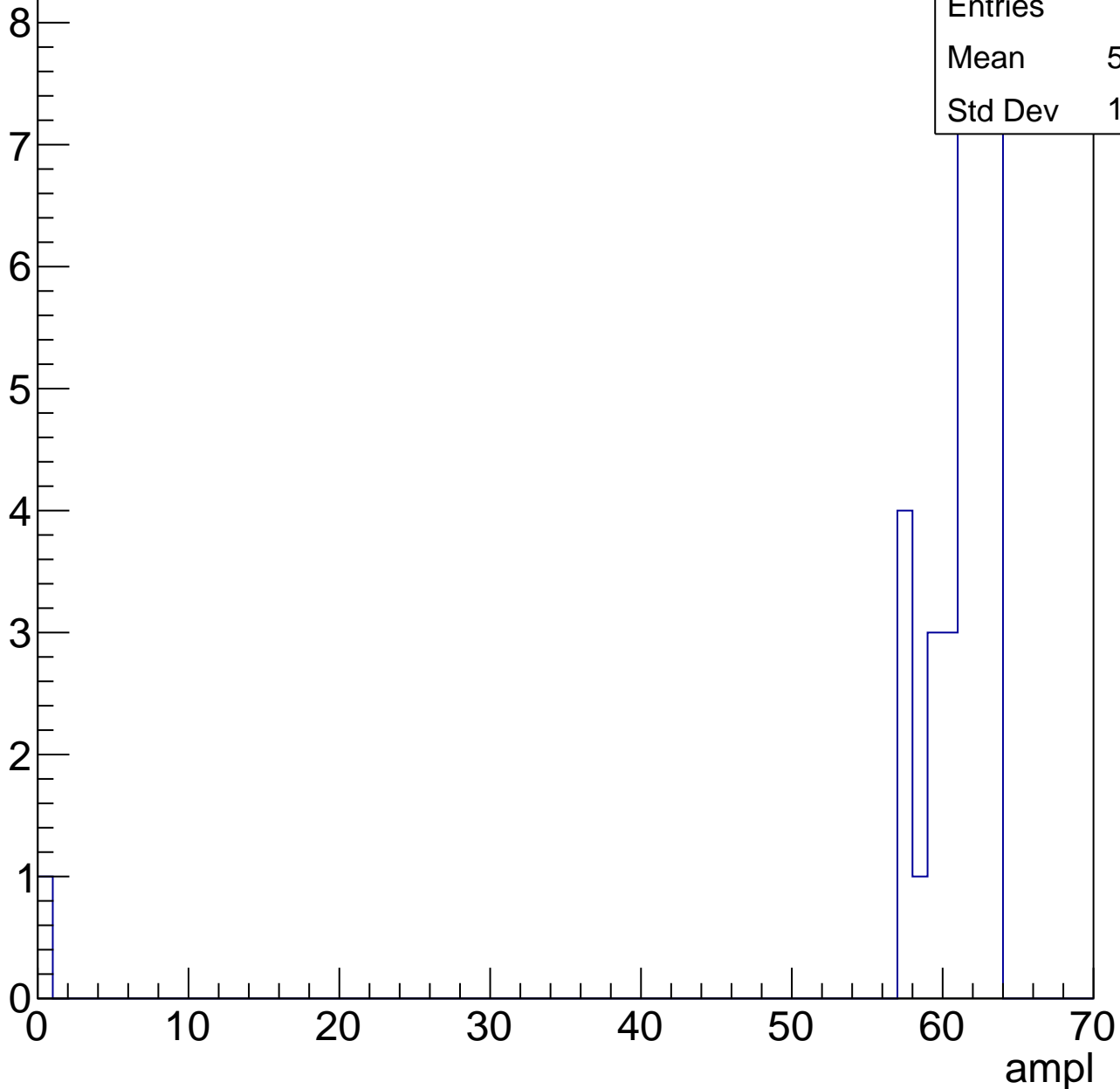


B1L101S, U12-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	59.19
Std Dev	10.18



B1L101S, U12-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch38, adc0

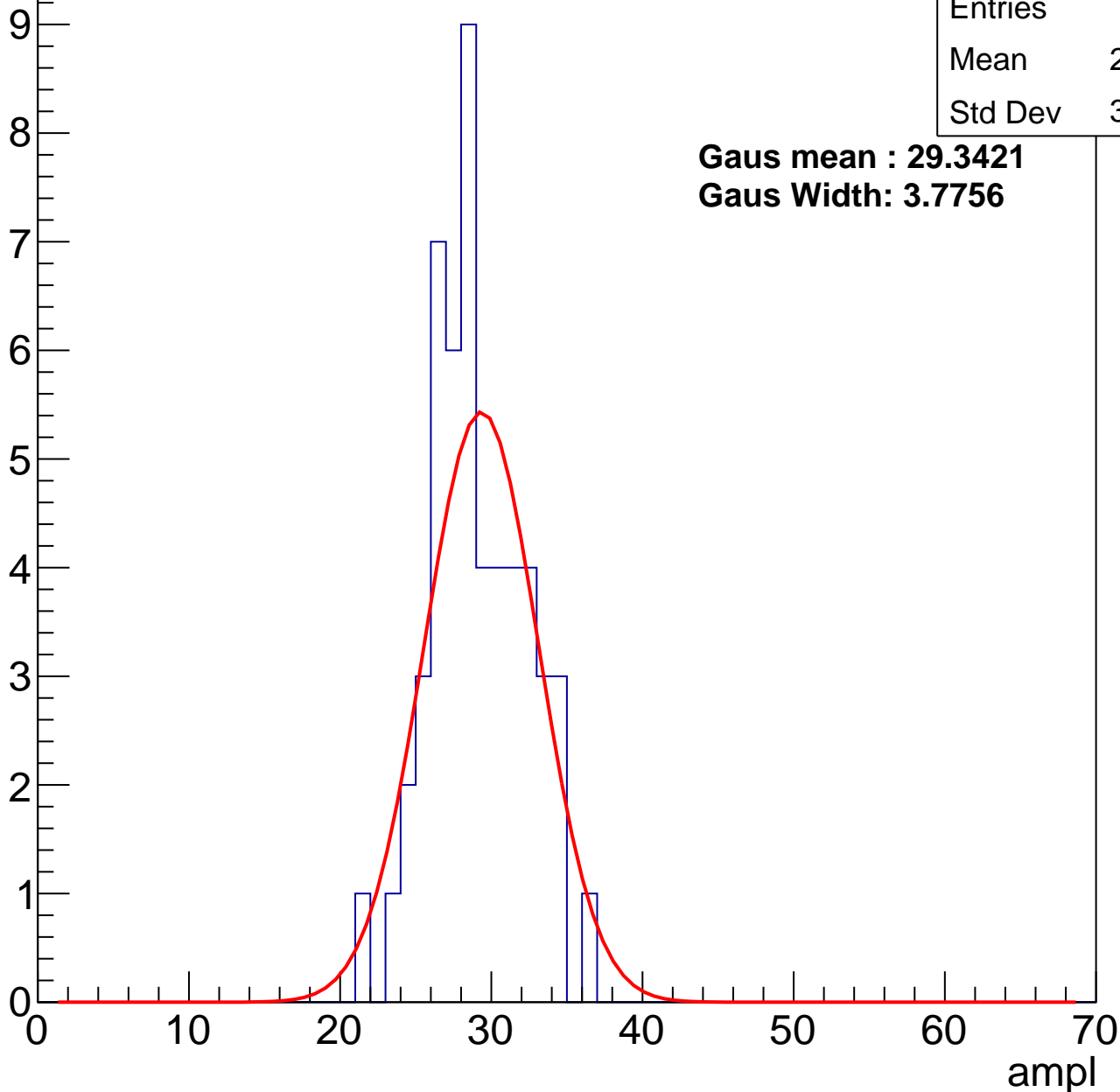
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	28.62
Std Dev	3.145

Gaus mean : 29.3421

Gaus Width: 3.7756



B1L101S, U12-ch38, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	35.86
Std Dev	3.488

Gaus mean : 36.3929

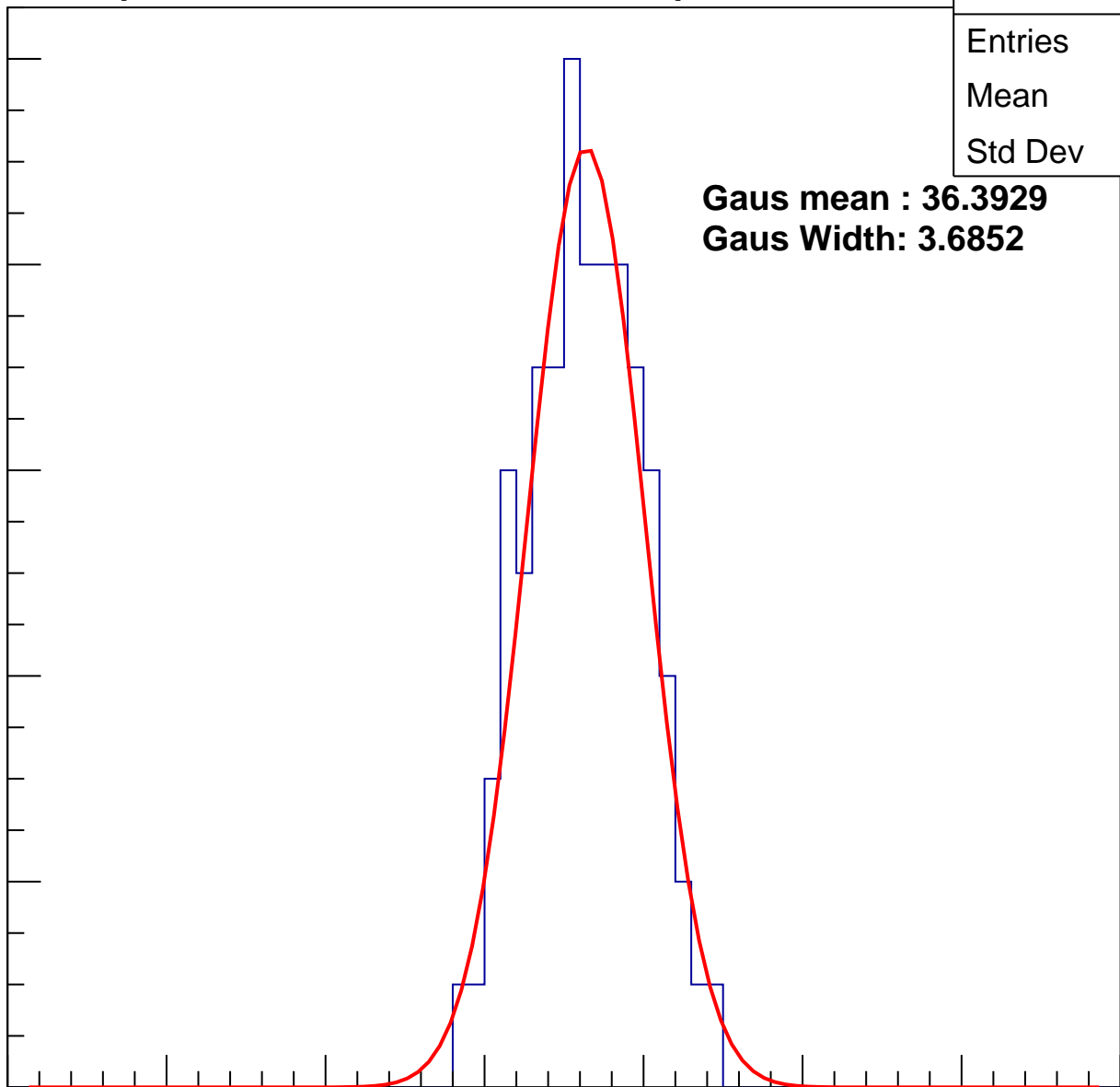
Gaus Width: 3.6852

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch38, adc2

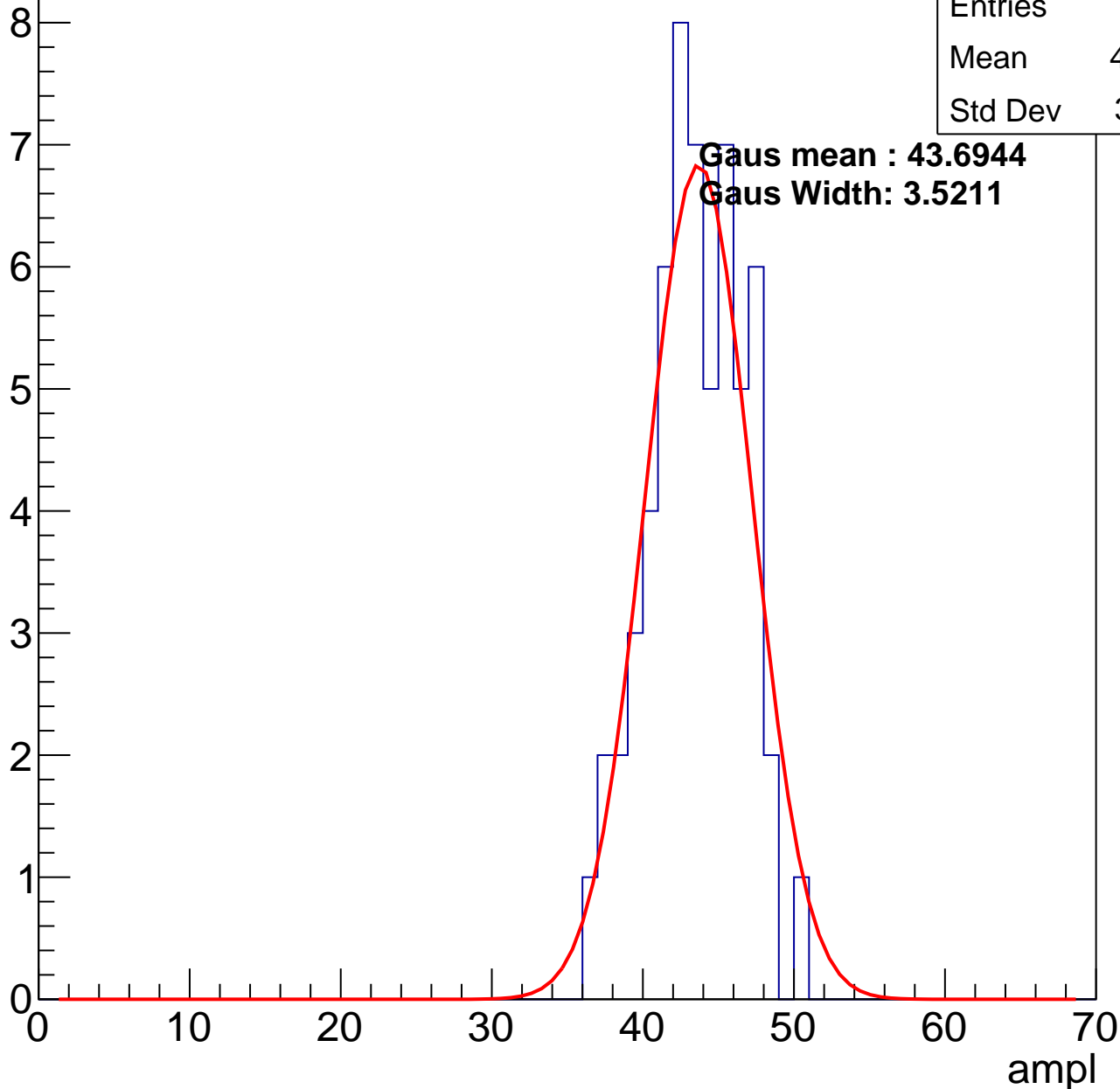
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.03
Std Dev	3.081

Gaus mean : 43.6944

Gaus Width: 3.5211

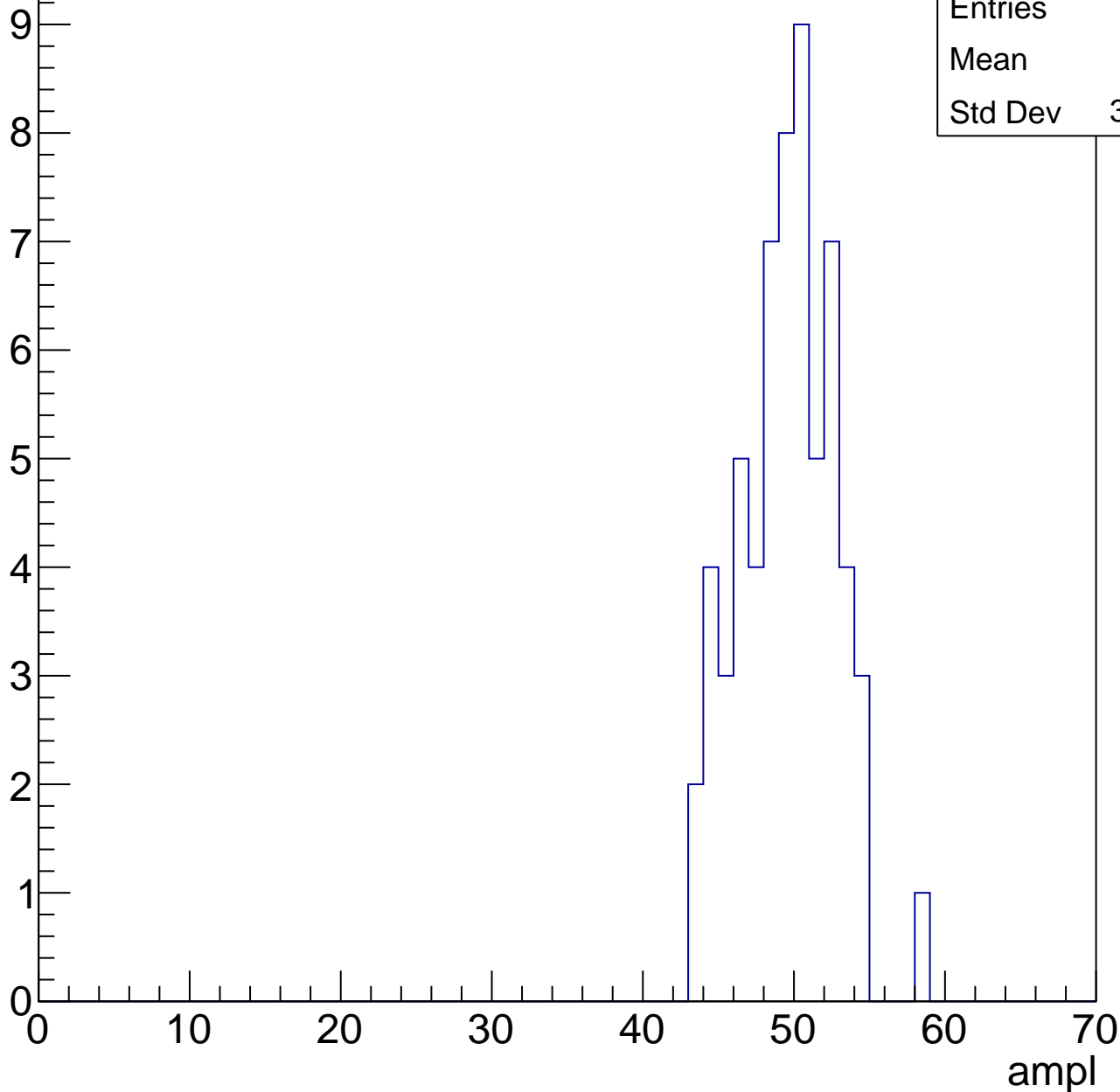


B1L101S, U12-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.1
Std Dev	3.109

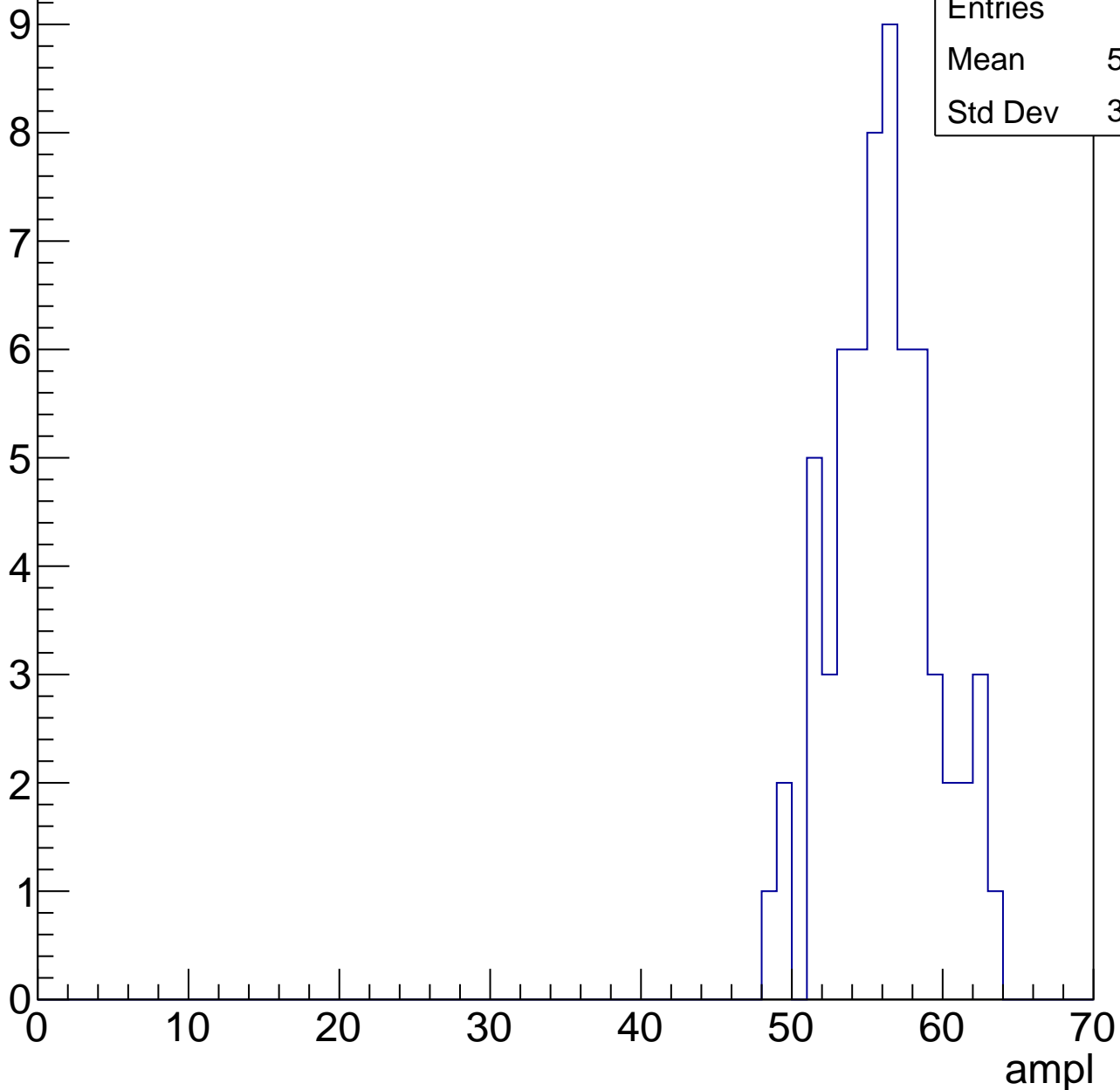


B1L101S, U12-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

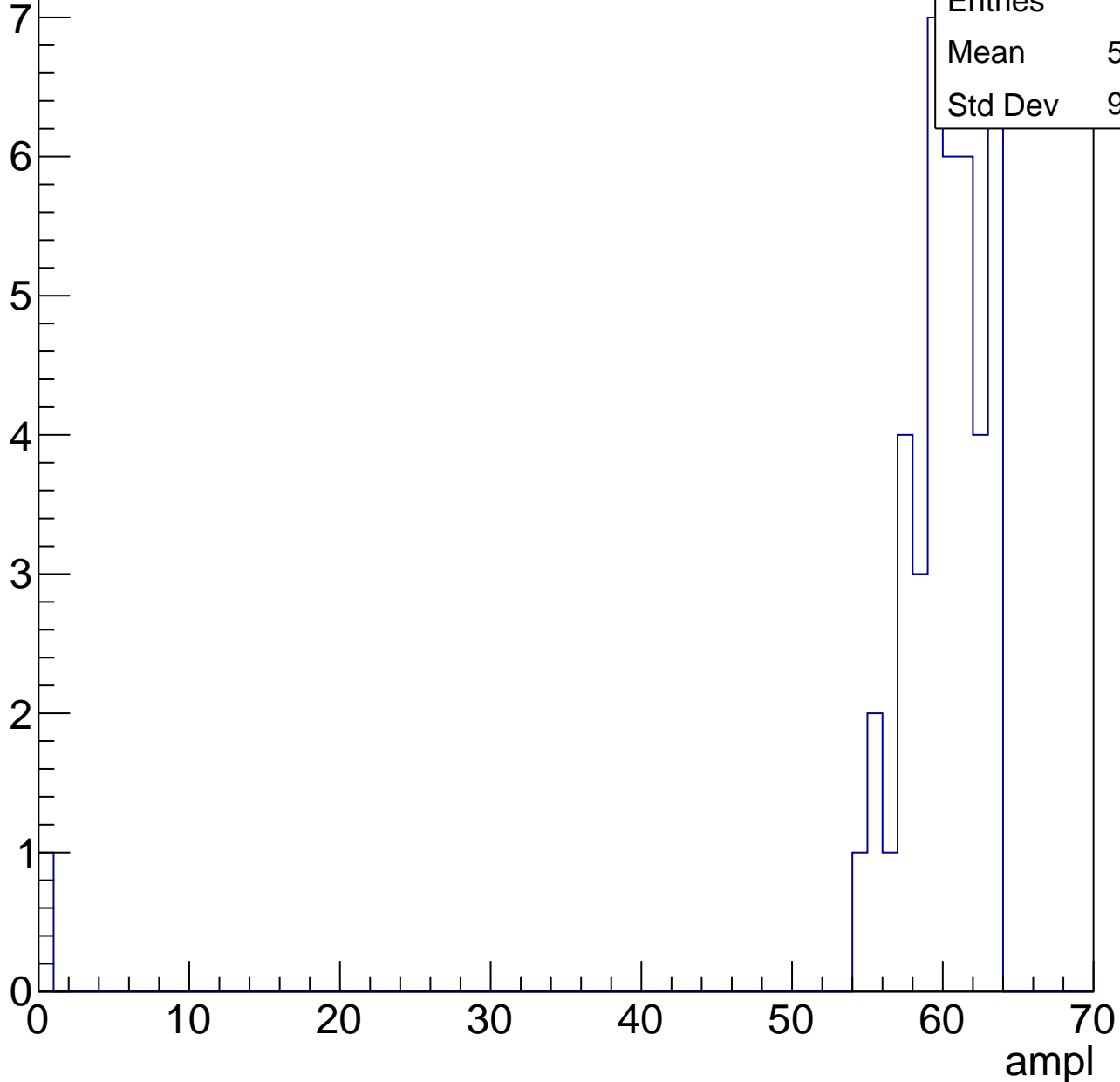
Entries	63
Mean	55.57
Std Dev	3.346



B1L101S, U12-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

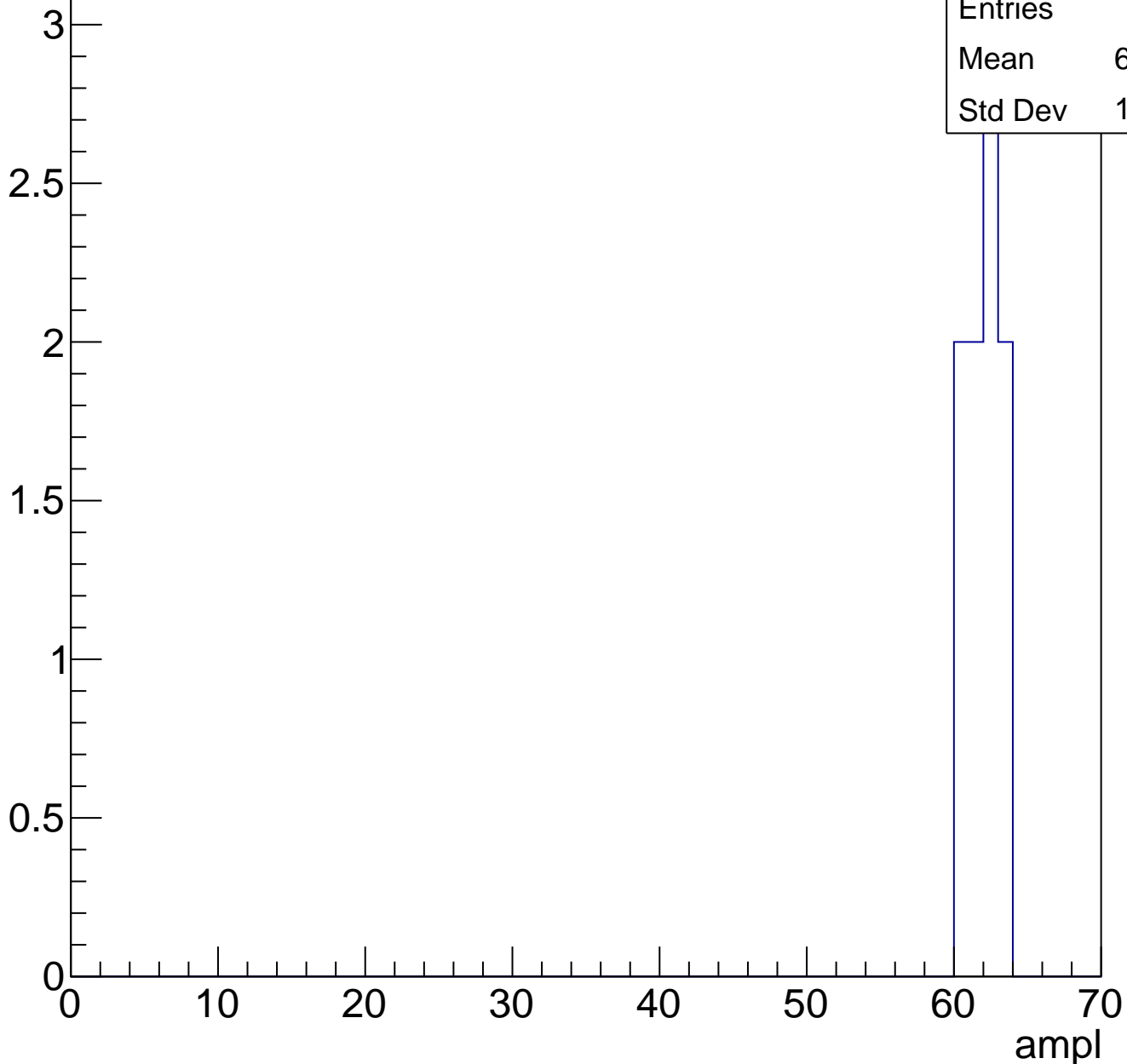
Entry



B1L101S, U12-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	9
Mean	61.56
Std Dev	1.066

B1L101S, U12-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch39, adc0

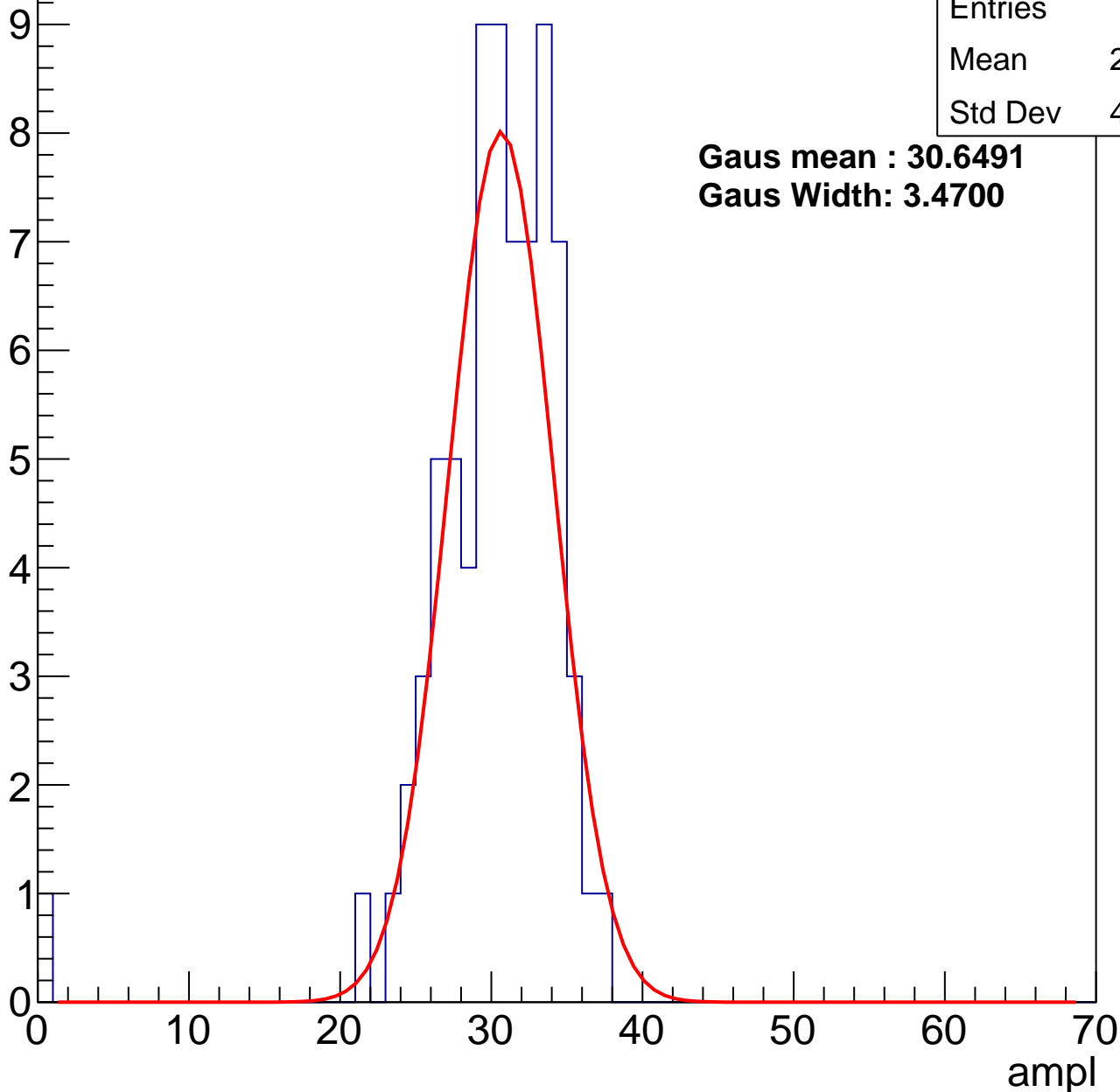
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.72
Std Dev	4.774

Gaus mean : 30.6491

Gaus Width: 3.4700



B1L101S, U12-ch39, adc1

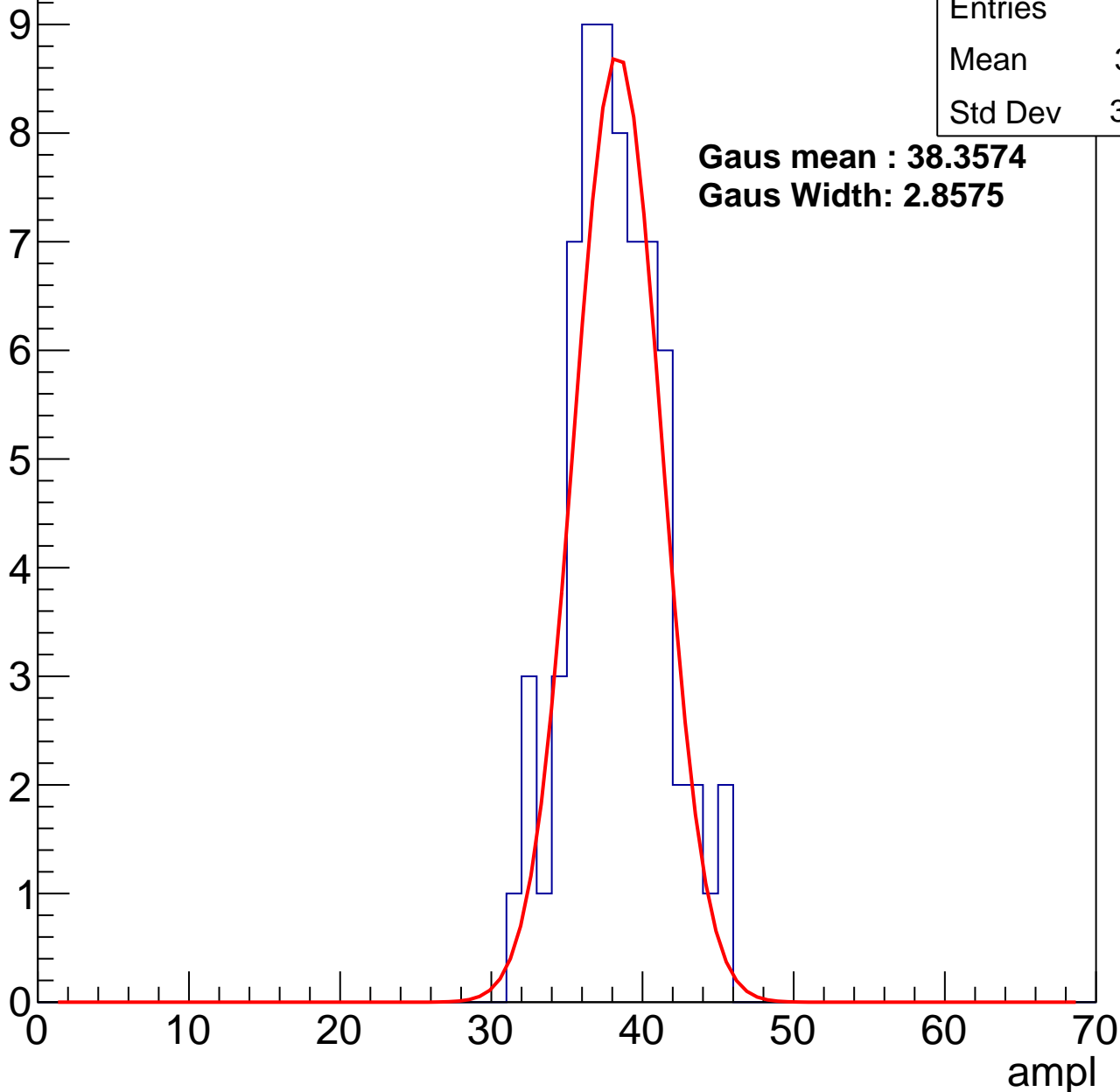
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.81
Std Dev	3.069

Gaus mean : 38.3574

Gaus Width: 2.8575



B1L101S, U12-ch39, adc2

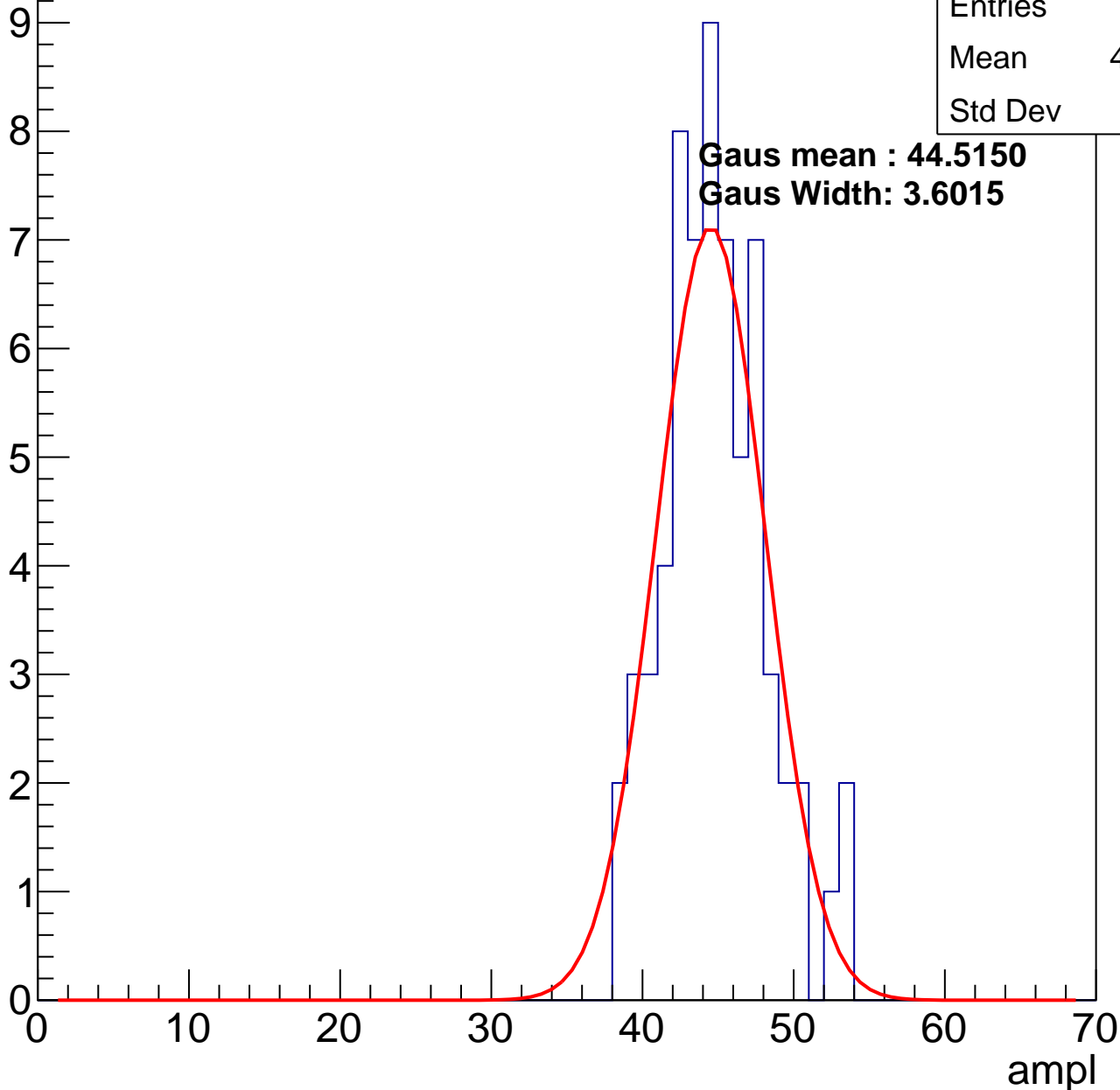
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.37
Std Dev	3.39

Gaus mean : 44.5150

Gaus Width: 3.6015

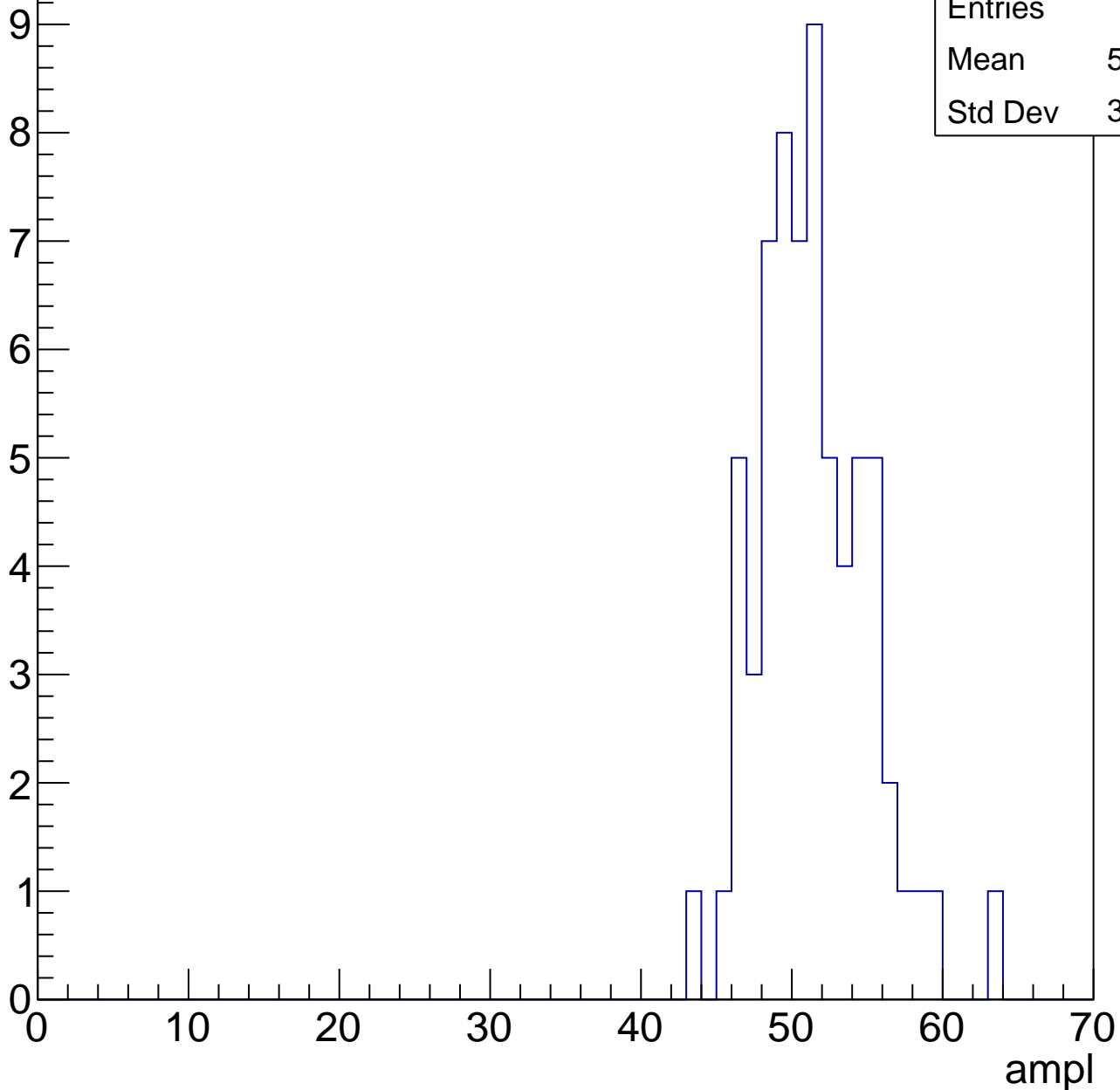


B1L101S, U12-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.94
Std Dev	3.618

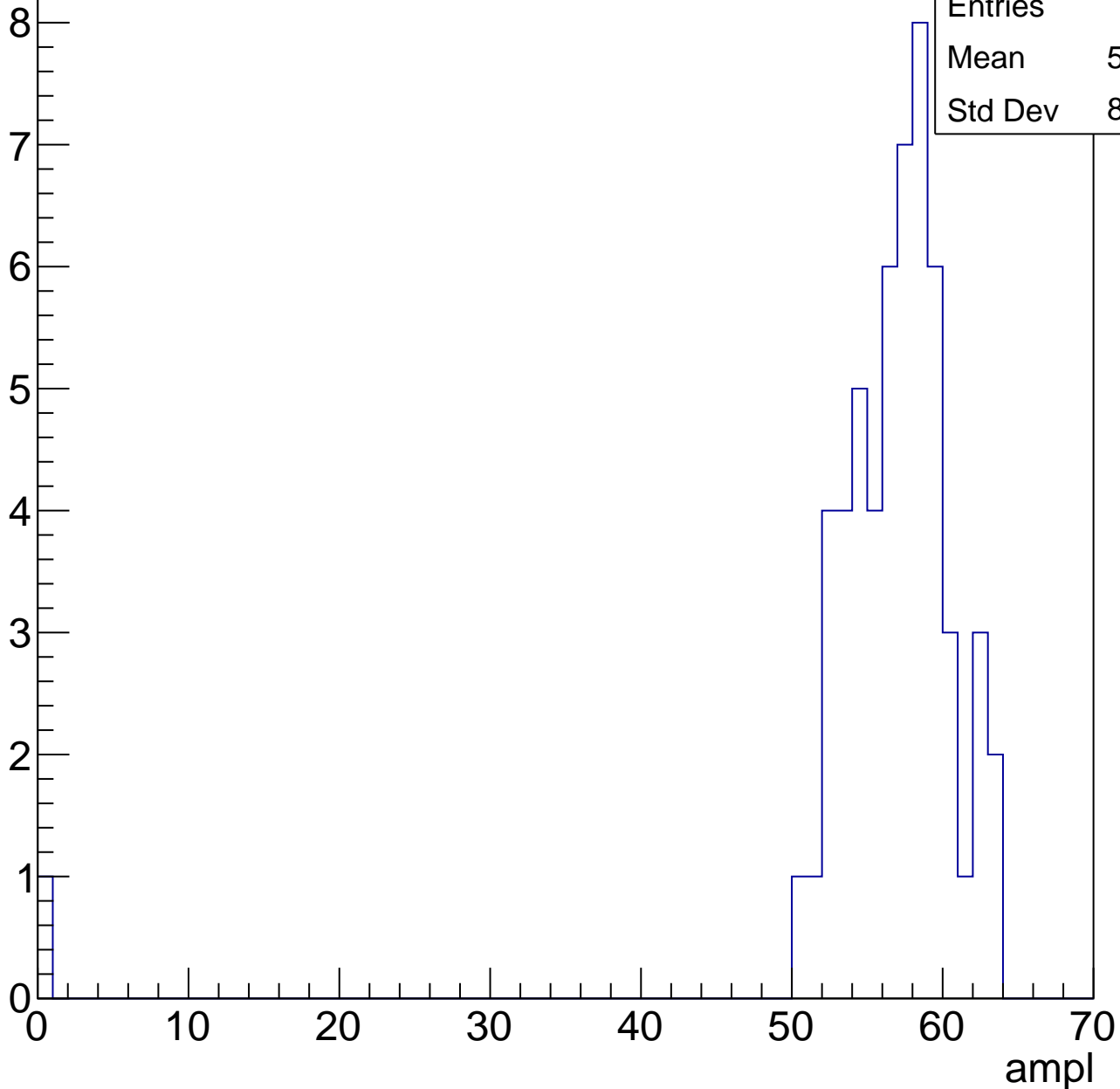


B1L101S, U12-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.66
Std Dev	8.114

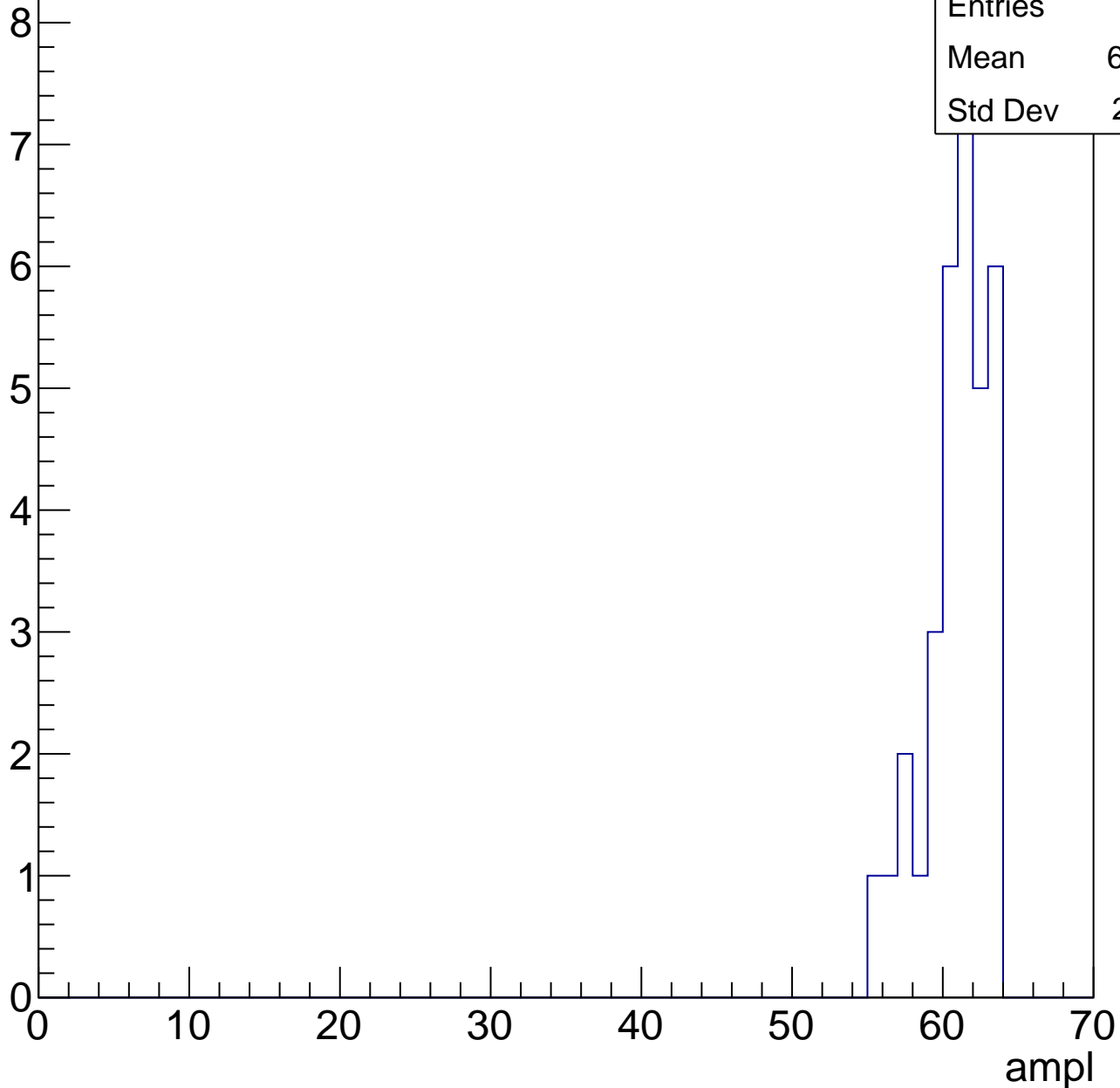


B1L101S, U12-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.48
Std Dev	2.061



B1L101S, U12-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	2.285

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch40, adc0

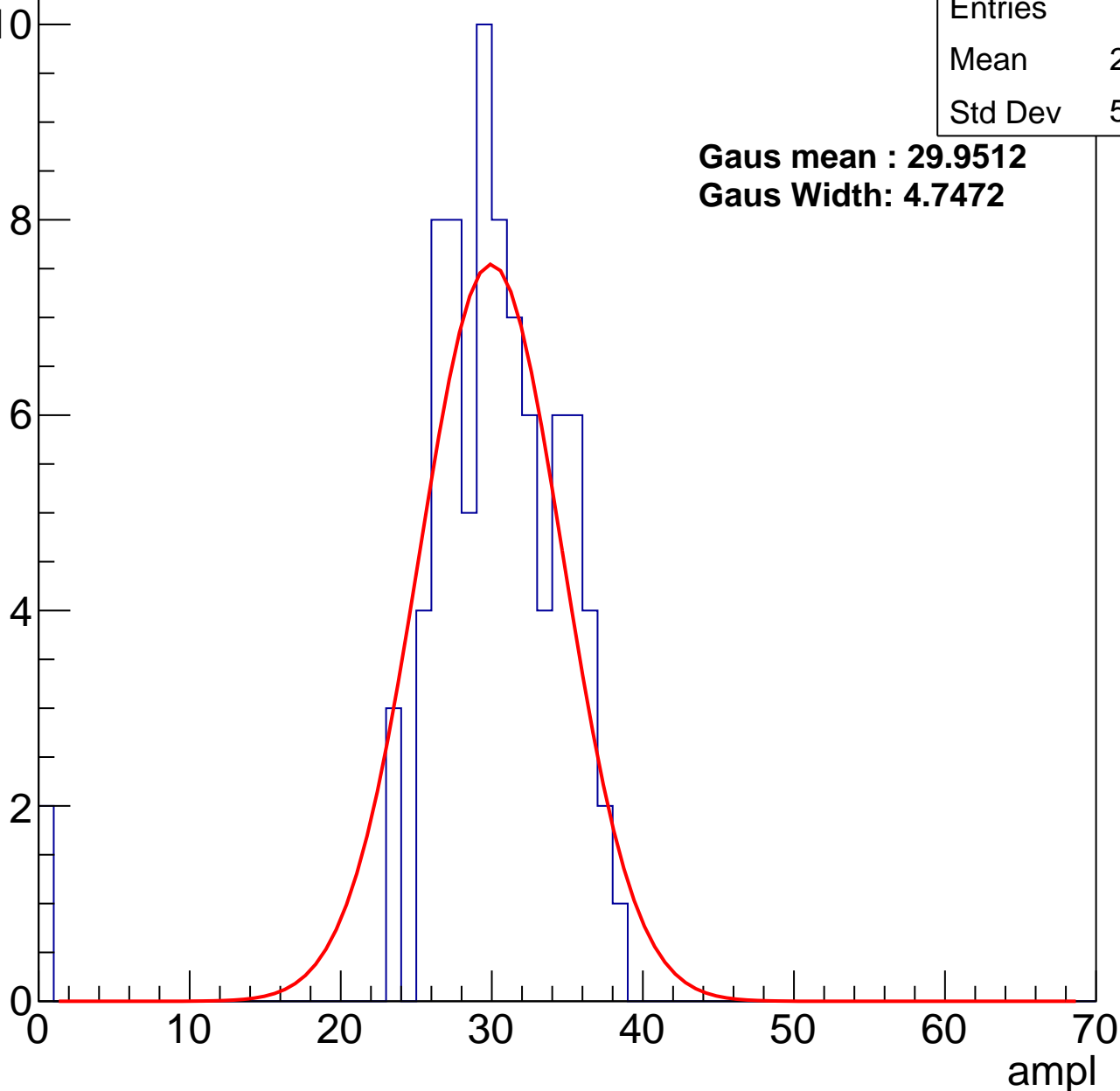
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.45
Std Dev	5.846

Gaus mean : 29.9512

Gaus Width: 4.7472



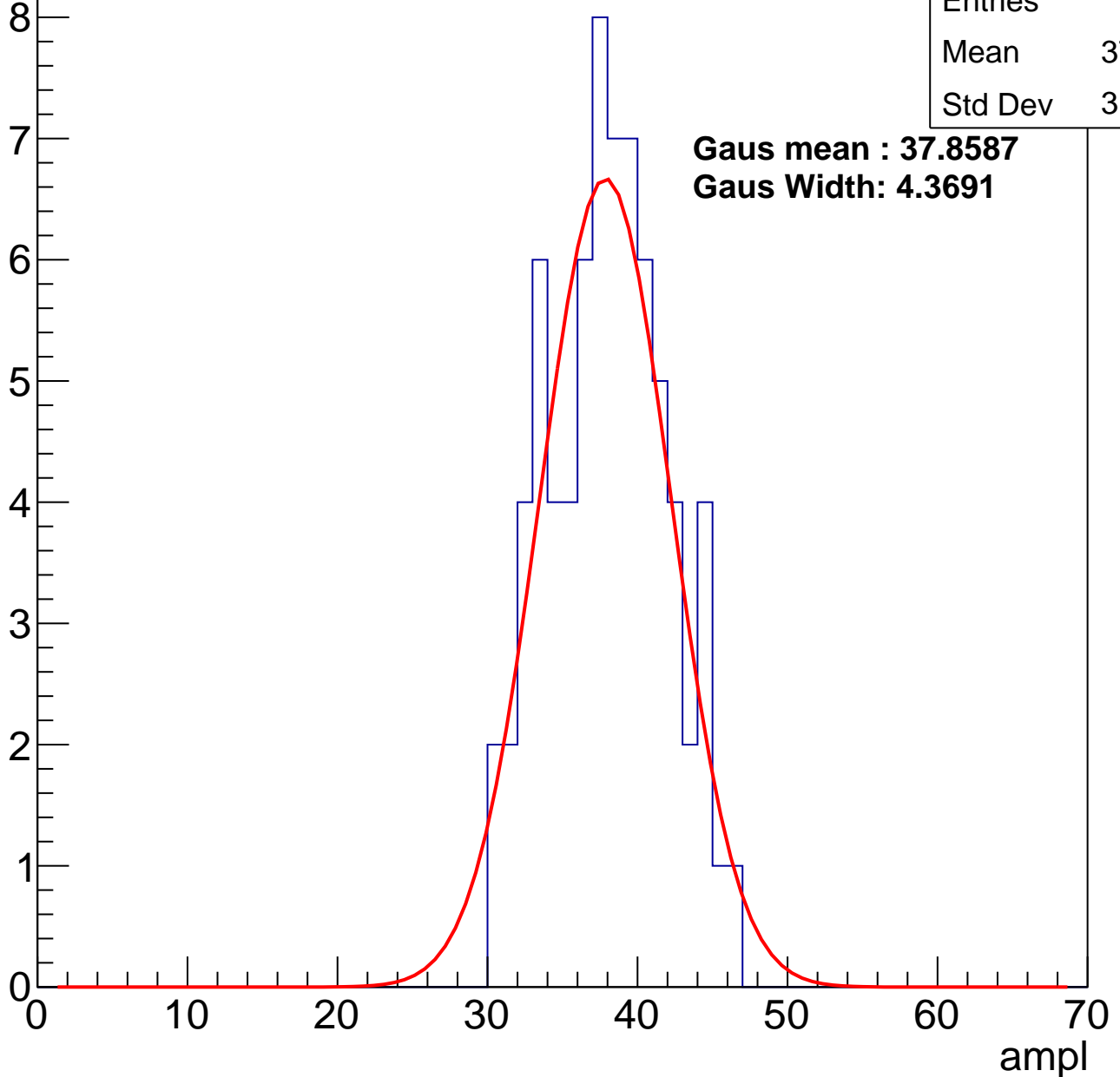
B1L101S, U12-ch40, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	37.55
Std Dev	3.854

Gaus mean : 37.8587
Gaus Width: 4.3691



B1L101S, U12-ch40, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 60

Mean 44.13

Std Dev 3.227

Gaus mean : 44.0719

Gaus Width: 3.5556

ampl

0

10

20

30

40

50

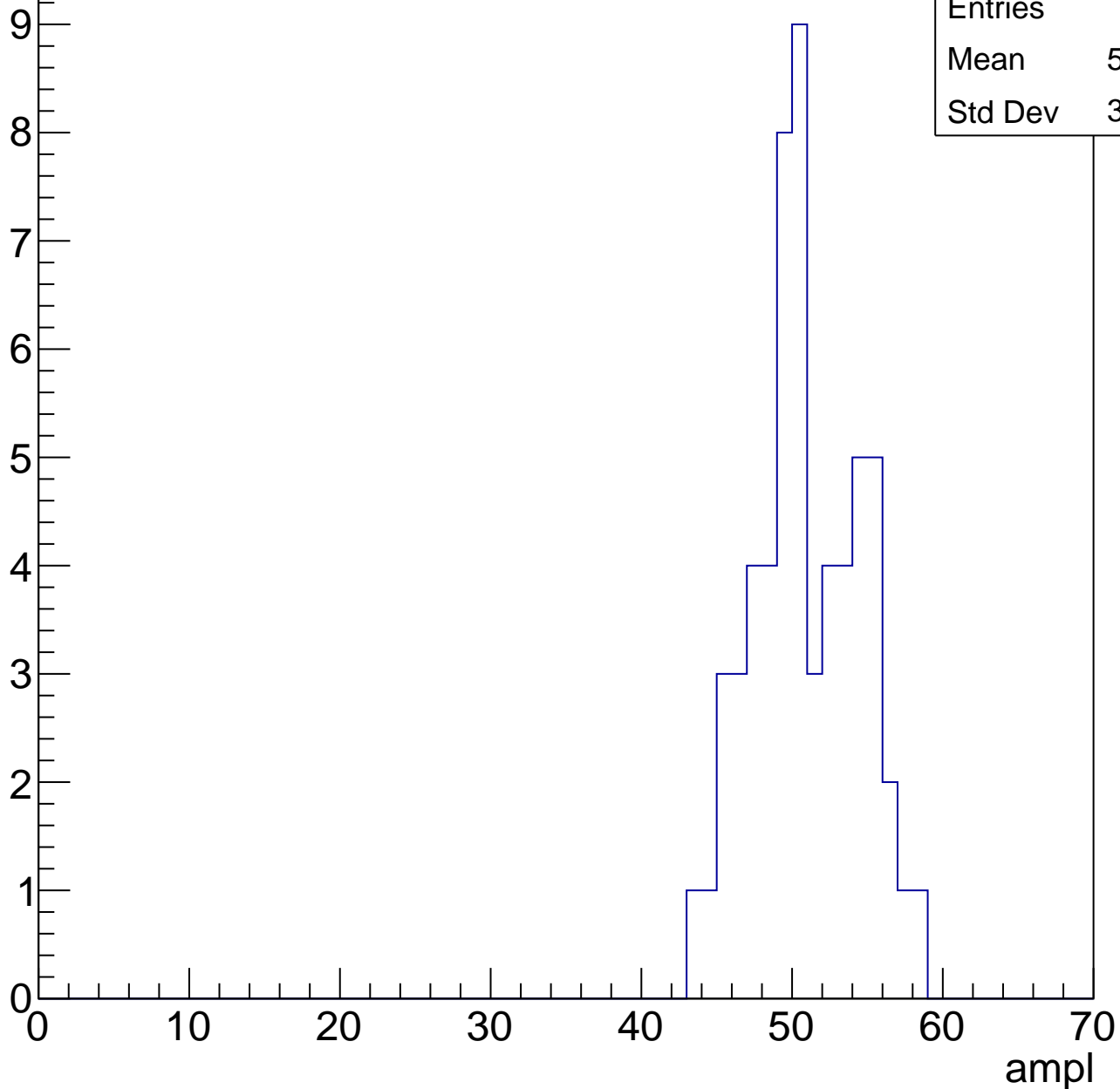
60

70

B1L101S, U12-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



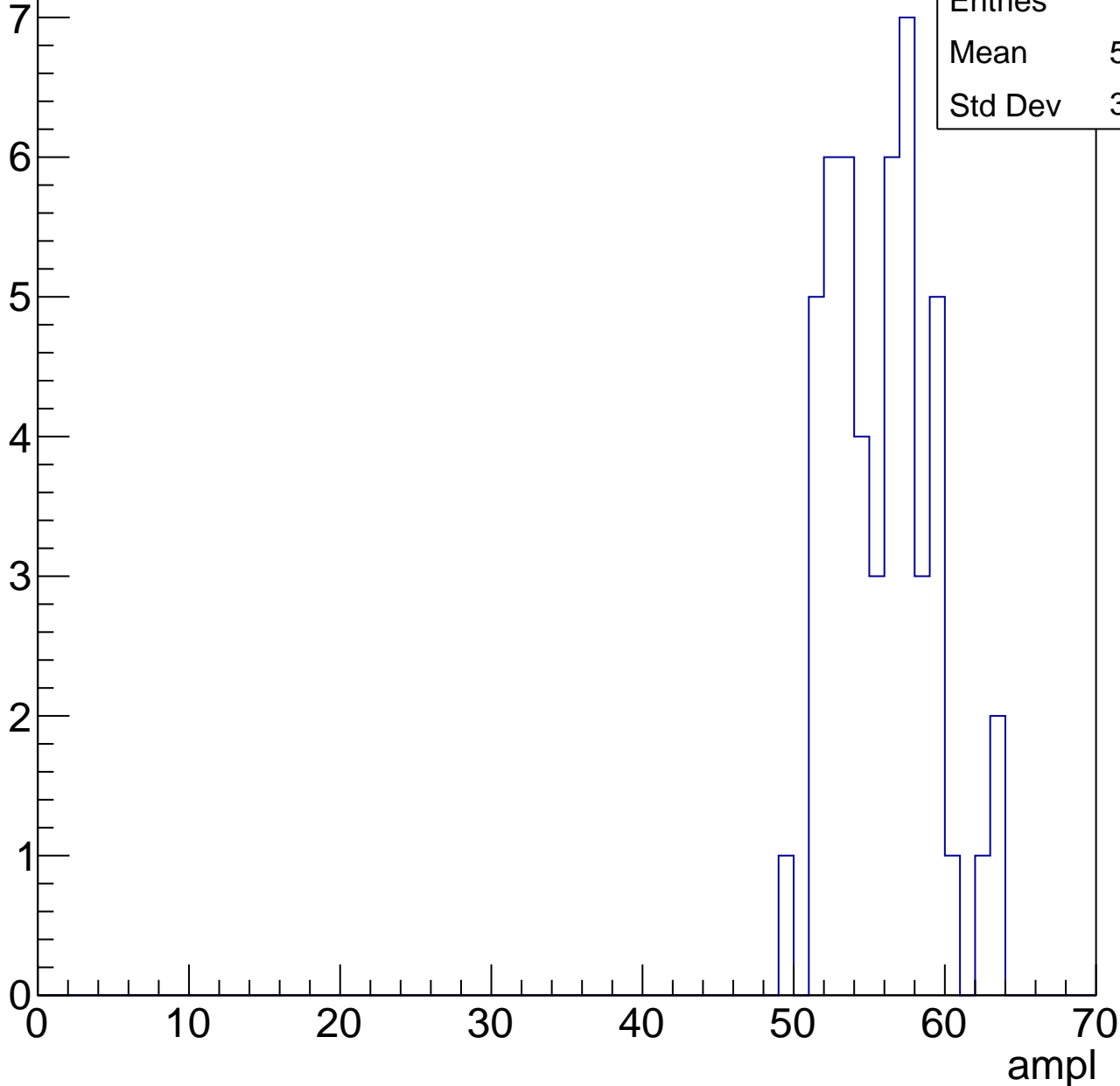
Entries	58
Mean	50.47
Std Dev	3.475

B1L101S, U12-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.34
Std Dev	3.272

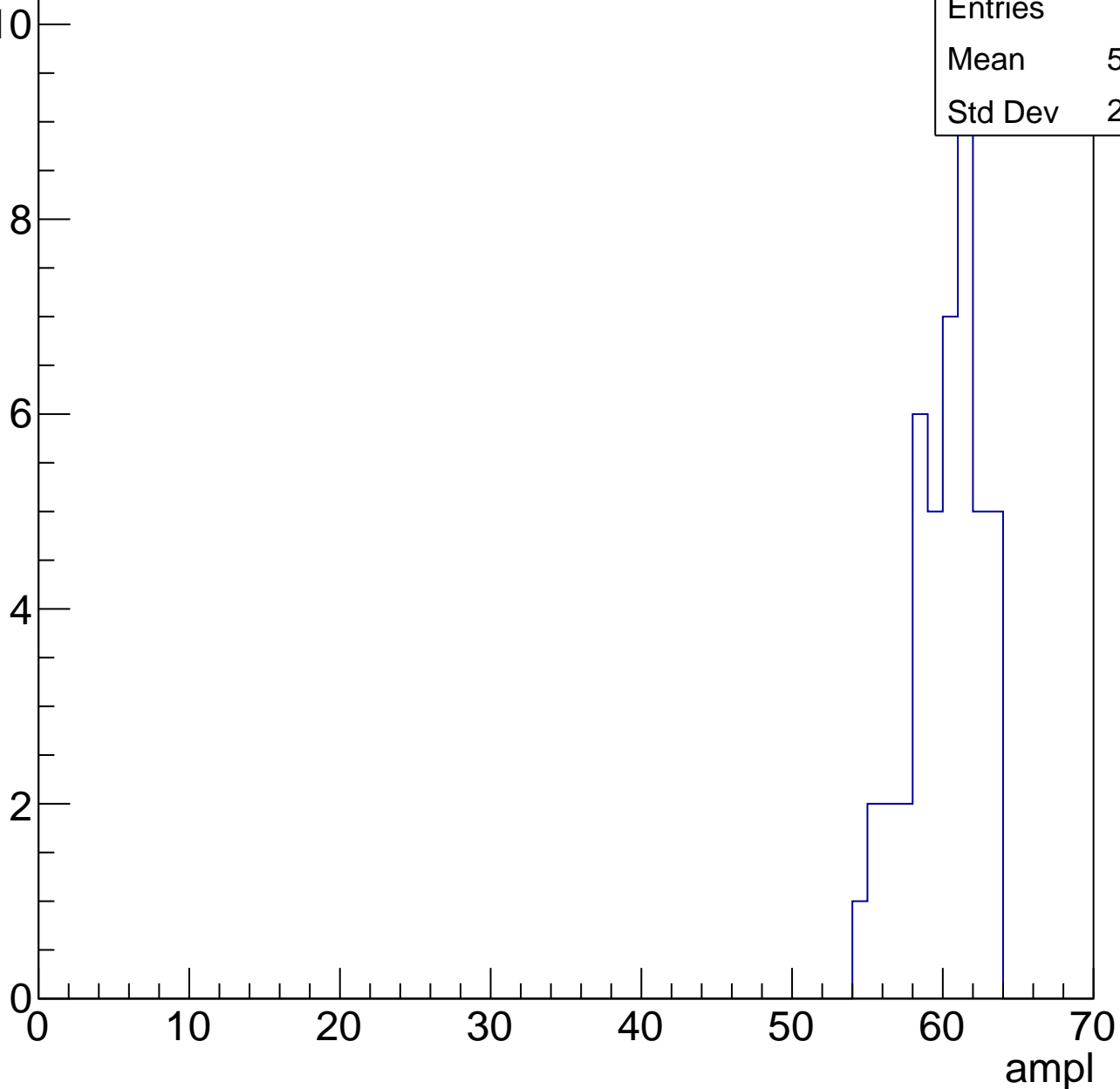


B1L101S, U12-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

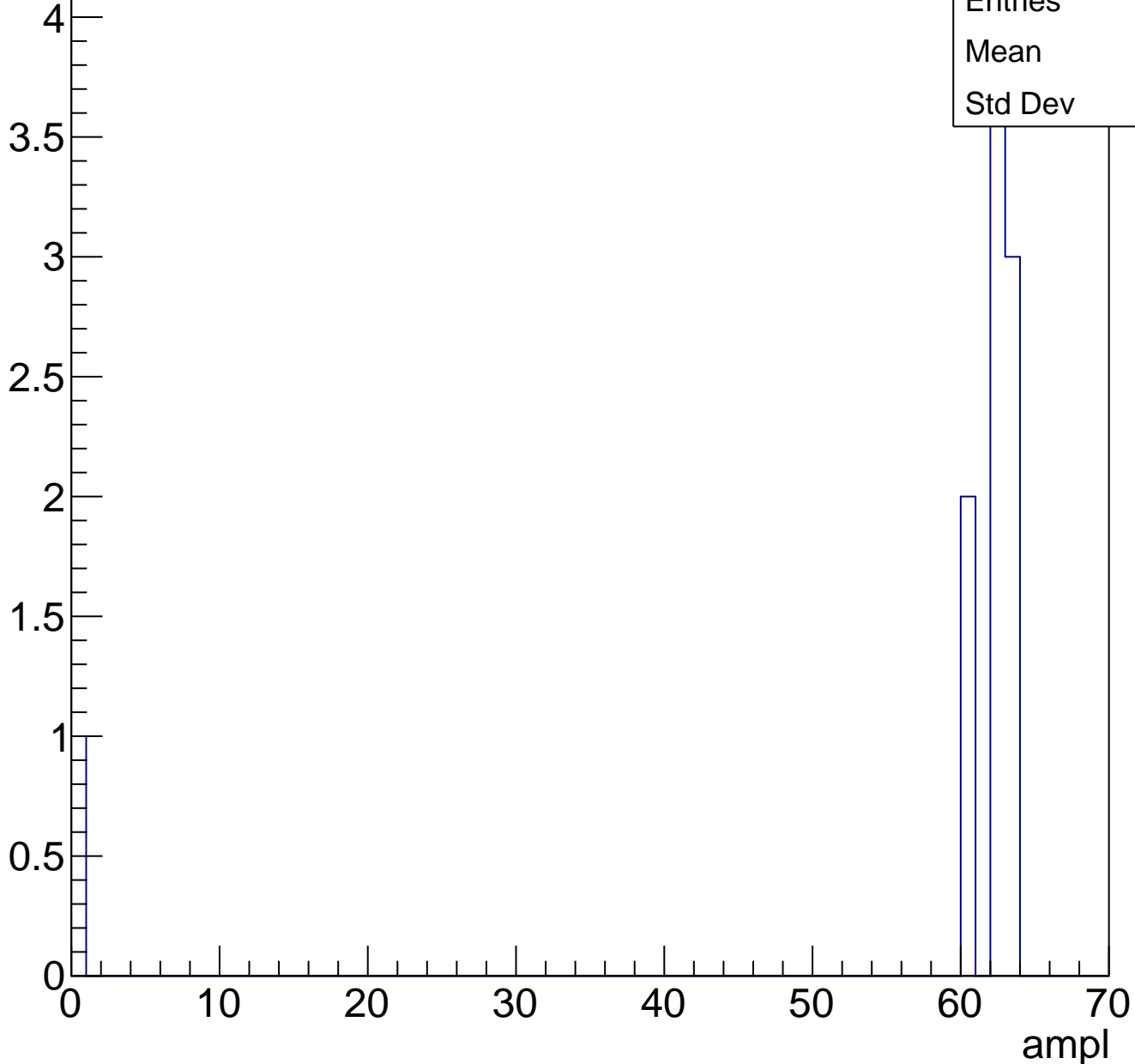
Entries	45
Mean	59.73
Std Dev	2.294



B1L101S, U12-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	55.7
Std Dev	18.6

B1L101S, U12-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch41, adc0

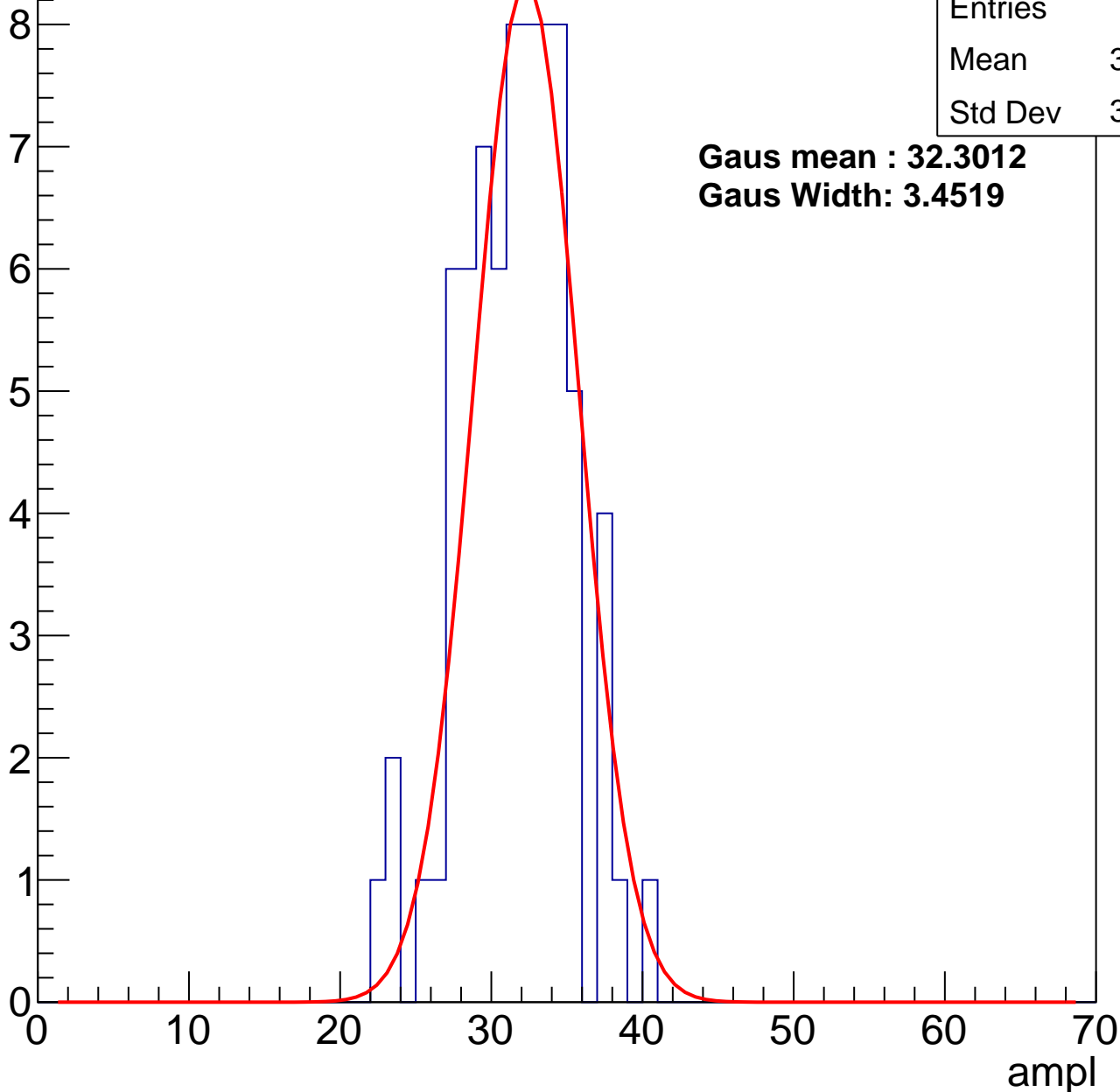
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	31.14
Std Dev	3.547

Gaus mean : 32.3012

Gaus Width: 3.4519



B1L101S, U12-ch41, adc1

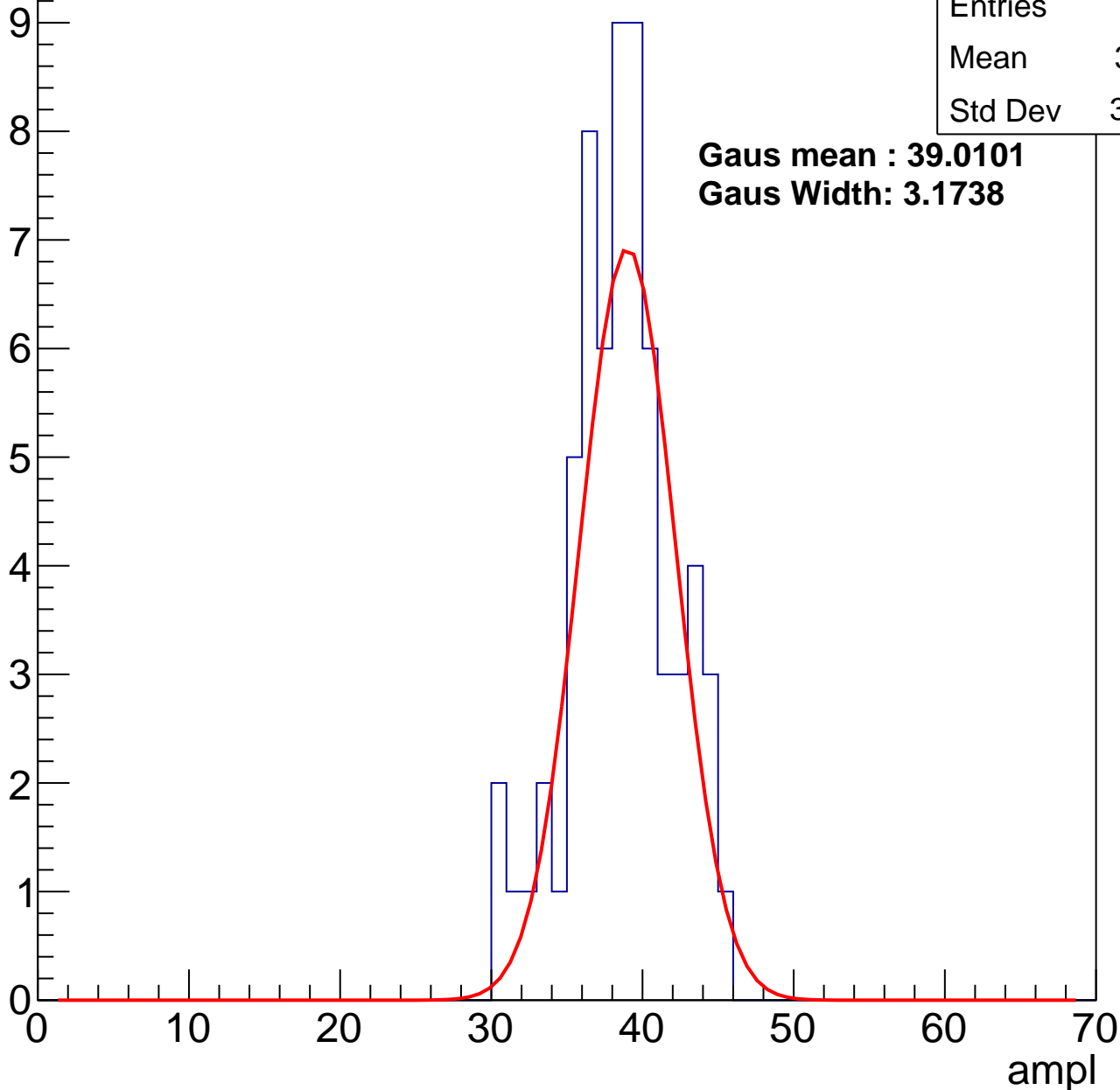
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	38.11
Std Dev	3.369

Gaus mean : 39.0101

Gaus Width: 3.1738



B1L101S, U12-ch41, adc2

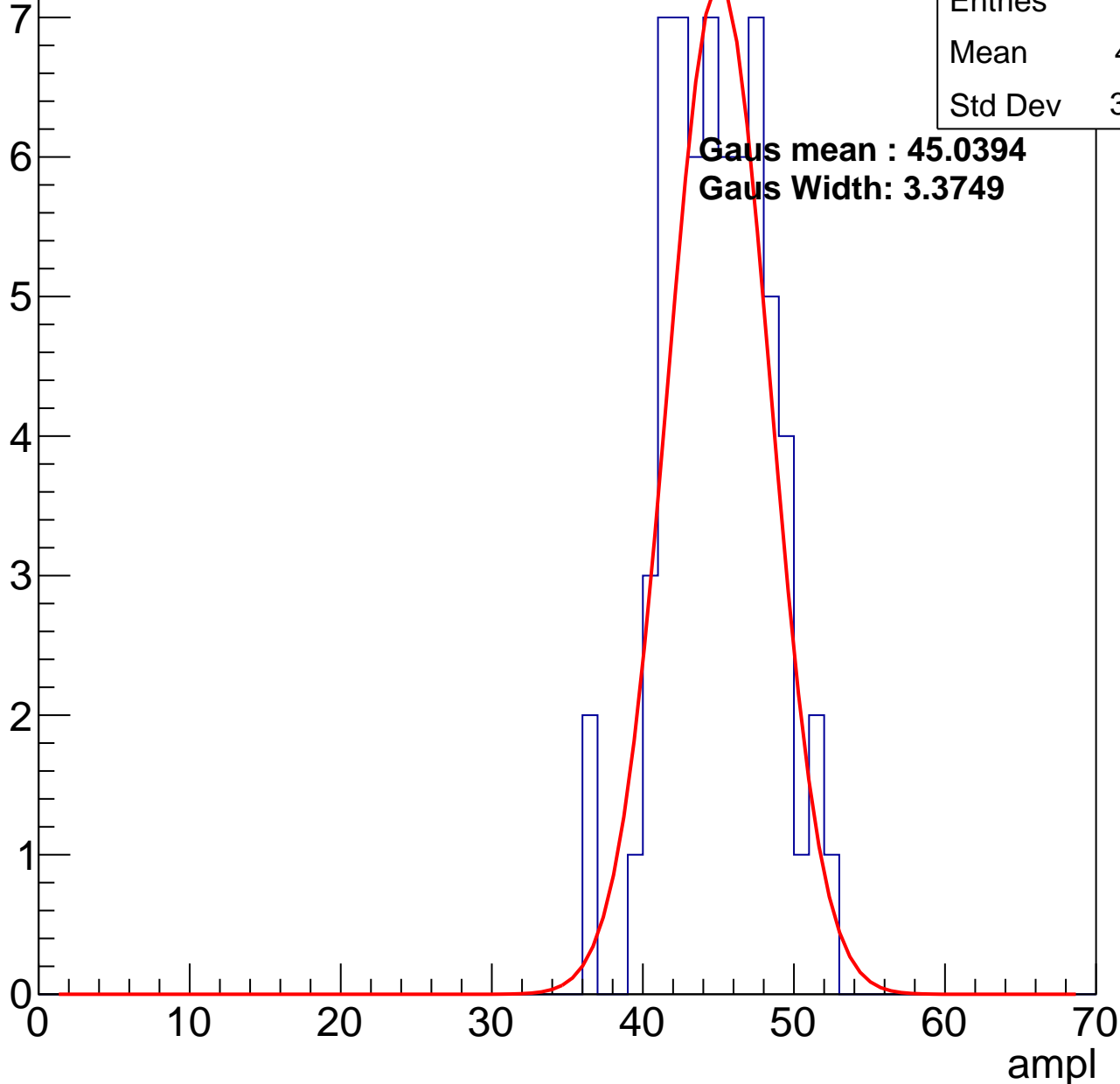
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.51
Std Dev	3.402

Gaus mean : 45.0394

Gaus Width: 3.3749

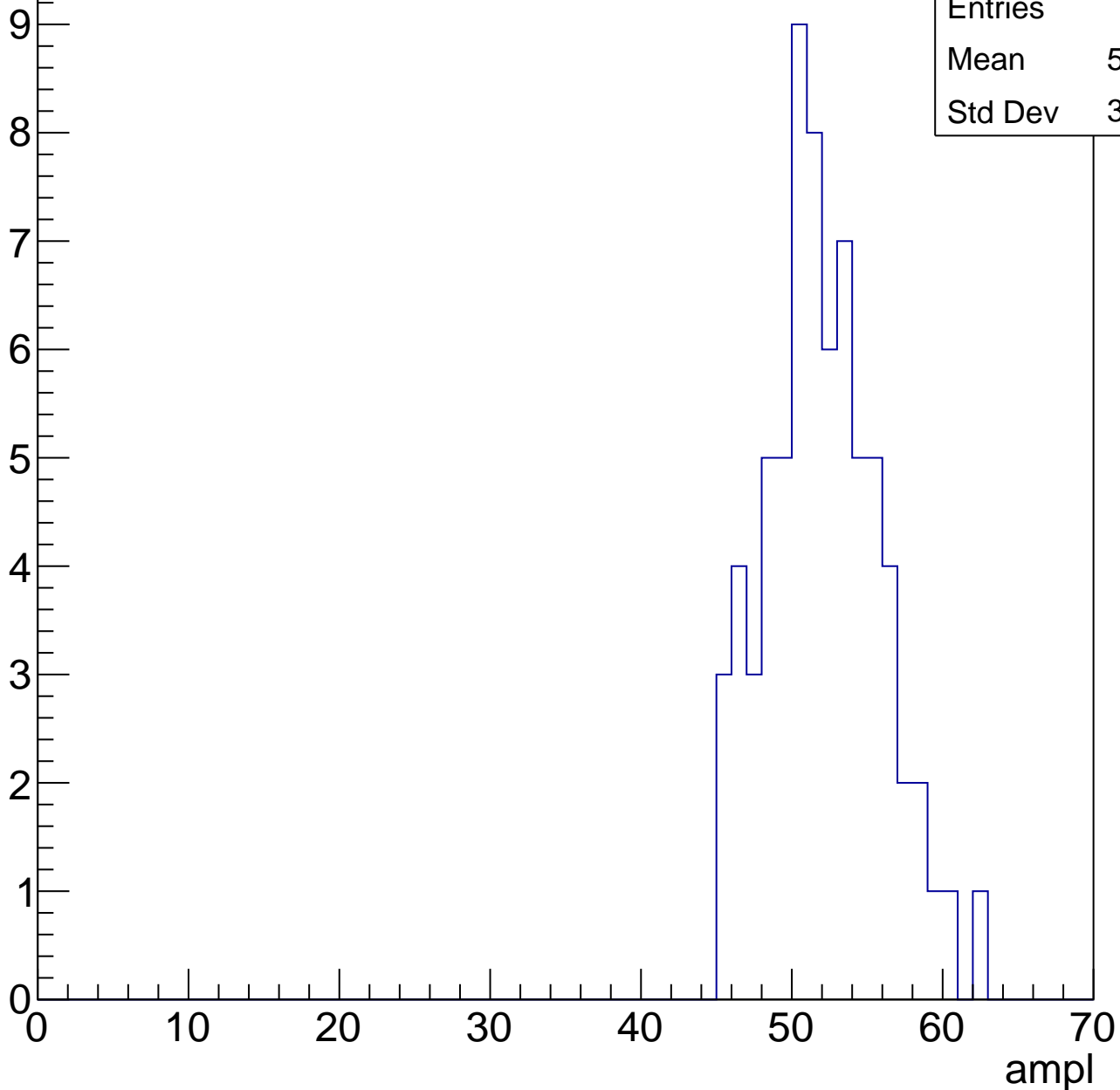


B1L101S, U12-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.63
Std Dev	3.746

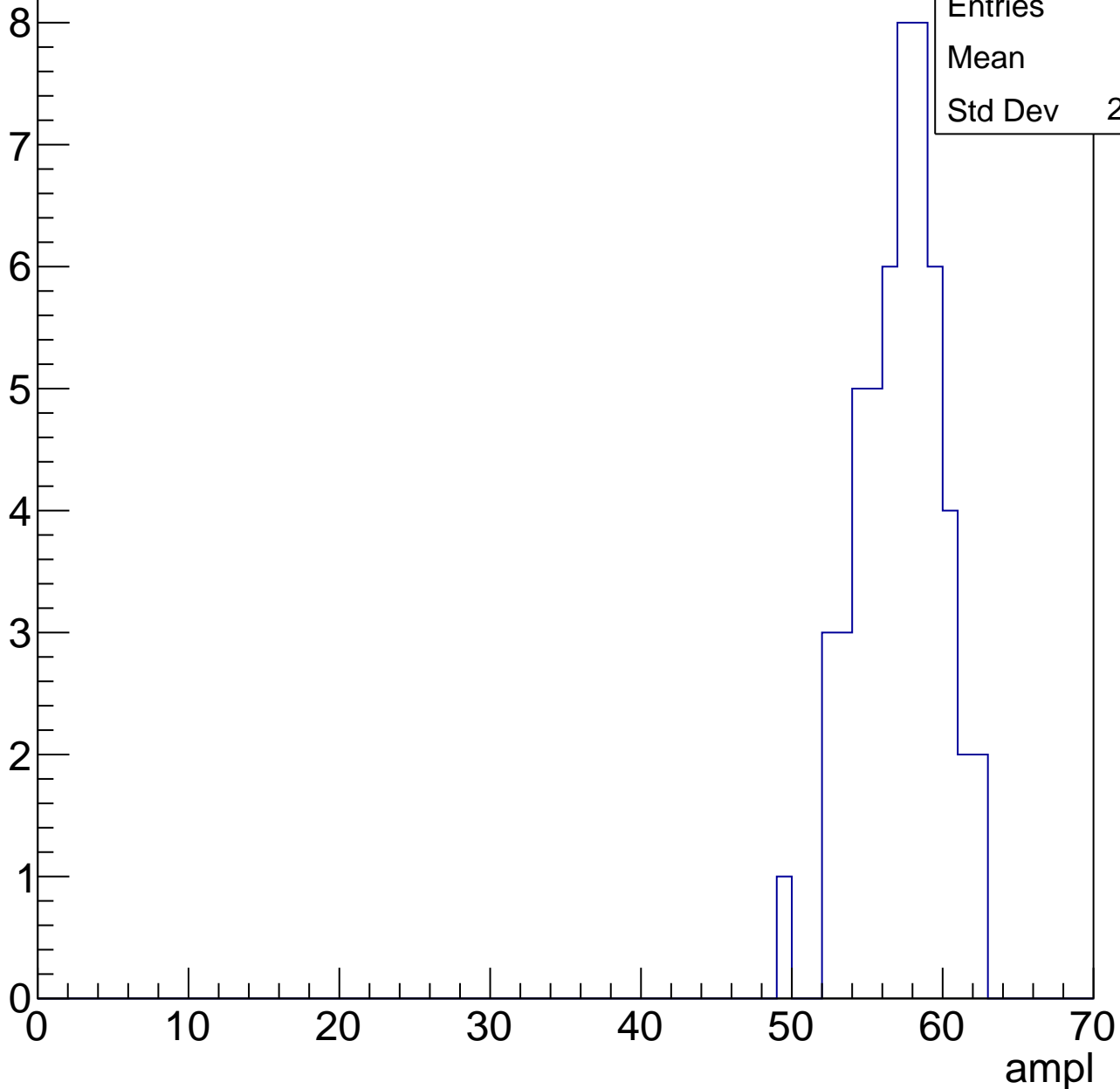


B1L101S, U12-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.7
Std Dev	2.758

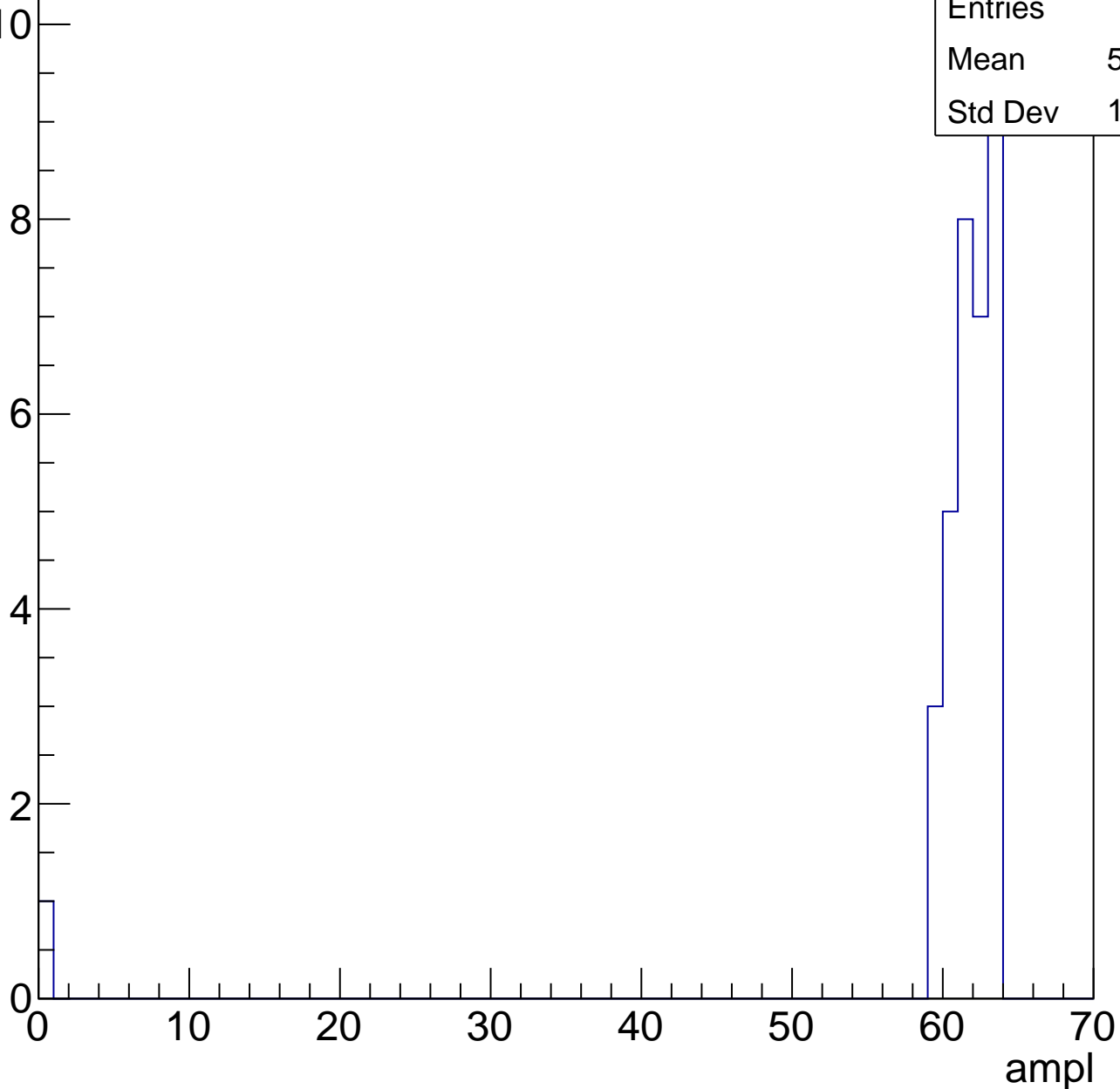


B1L101S, U12-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.68
Std Dev	10.47



B1L101S, U12-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch42, adc0

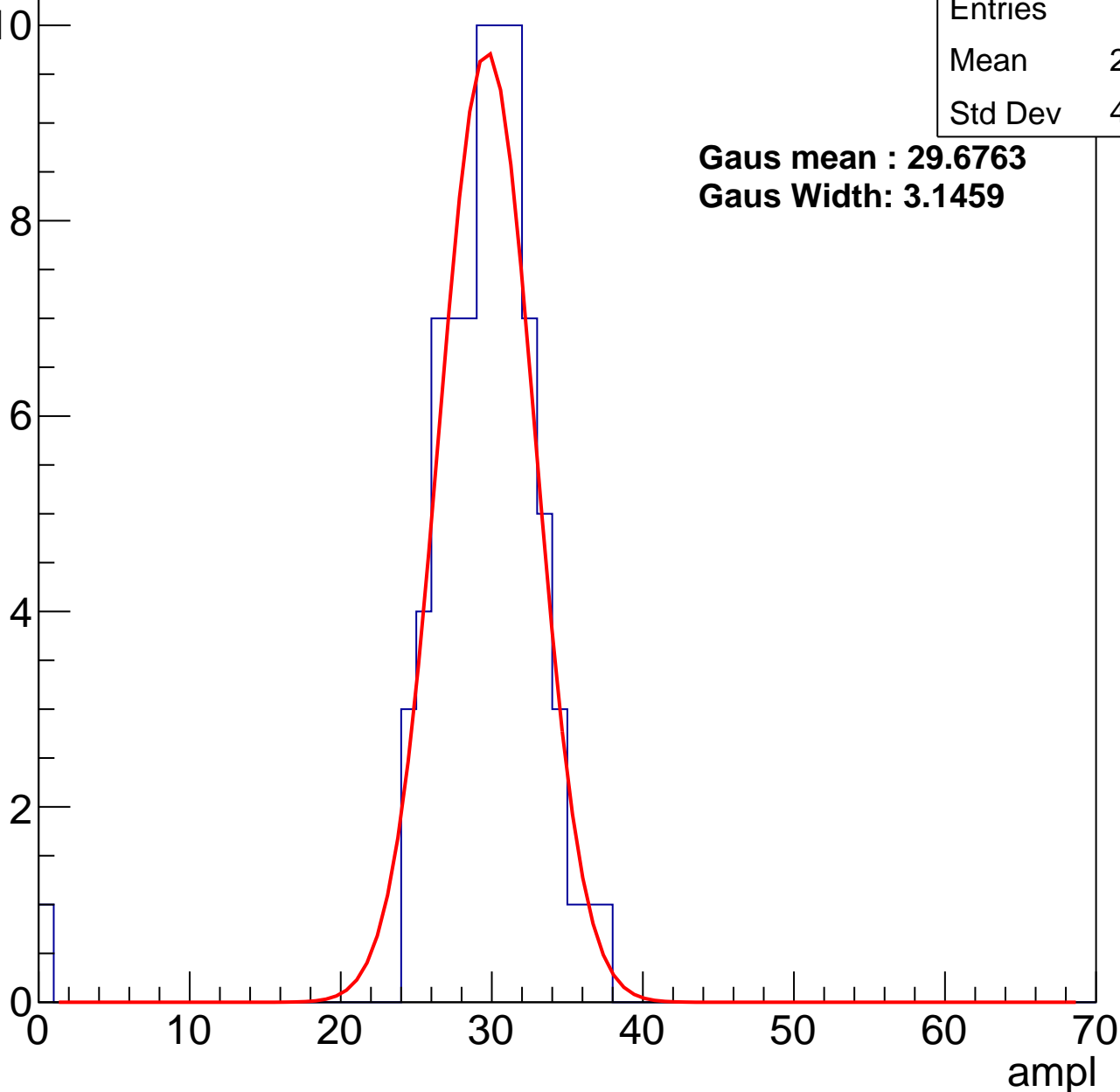
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.06
Std Dev	4.406

Gaus mean : 29.6763

Gaus Width: 3.1459



B1L101S, U12-ch42, adc1

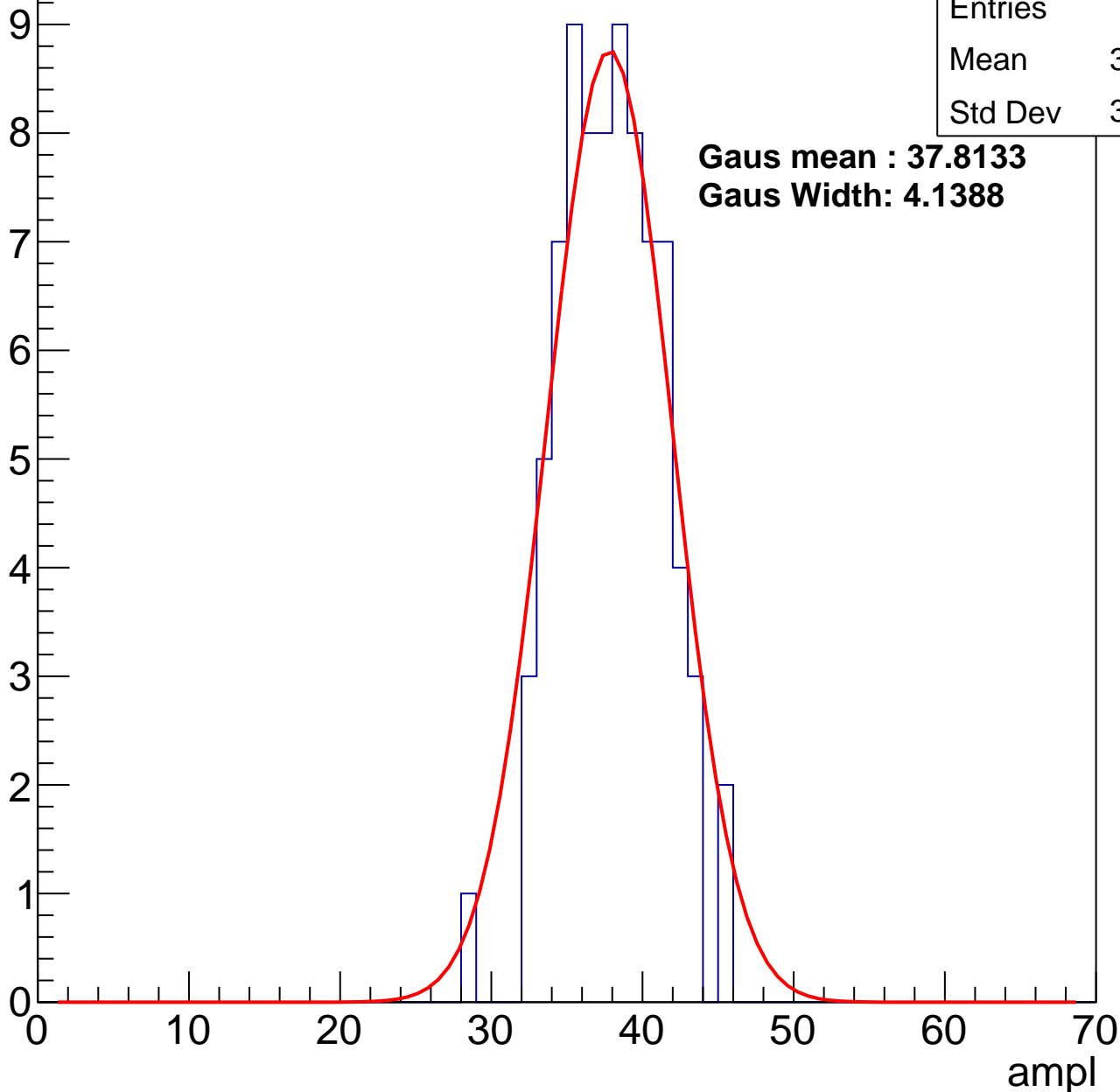
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.46
Std Dev	3.293

Gaus mean : 37.8133

Gaus Width: 4.1388



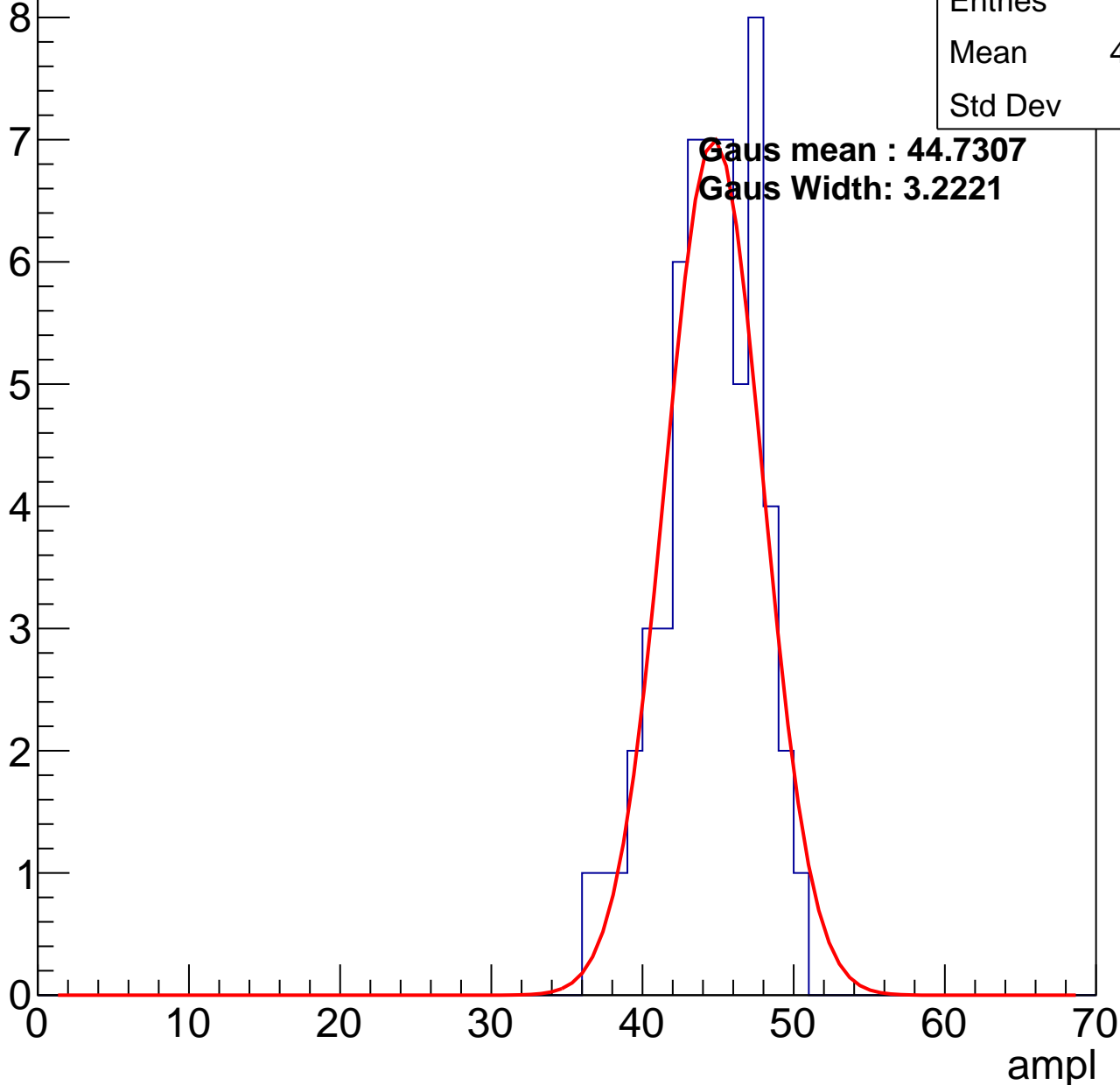
B1L101S, U12-ch42, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.03
Std Dev	3.09

Gaus mean : 44.7307
Gaus Width: 3.2221

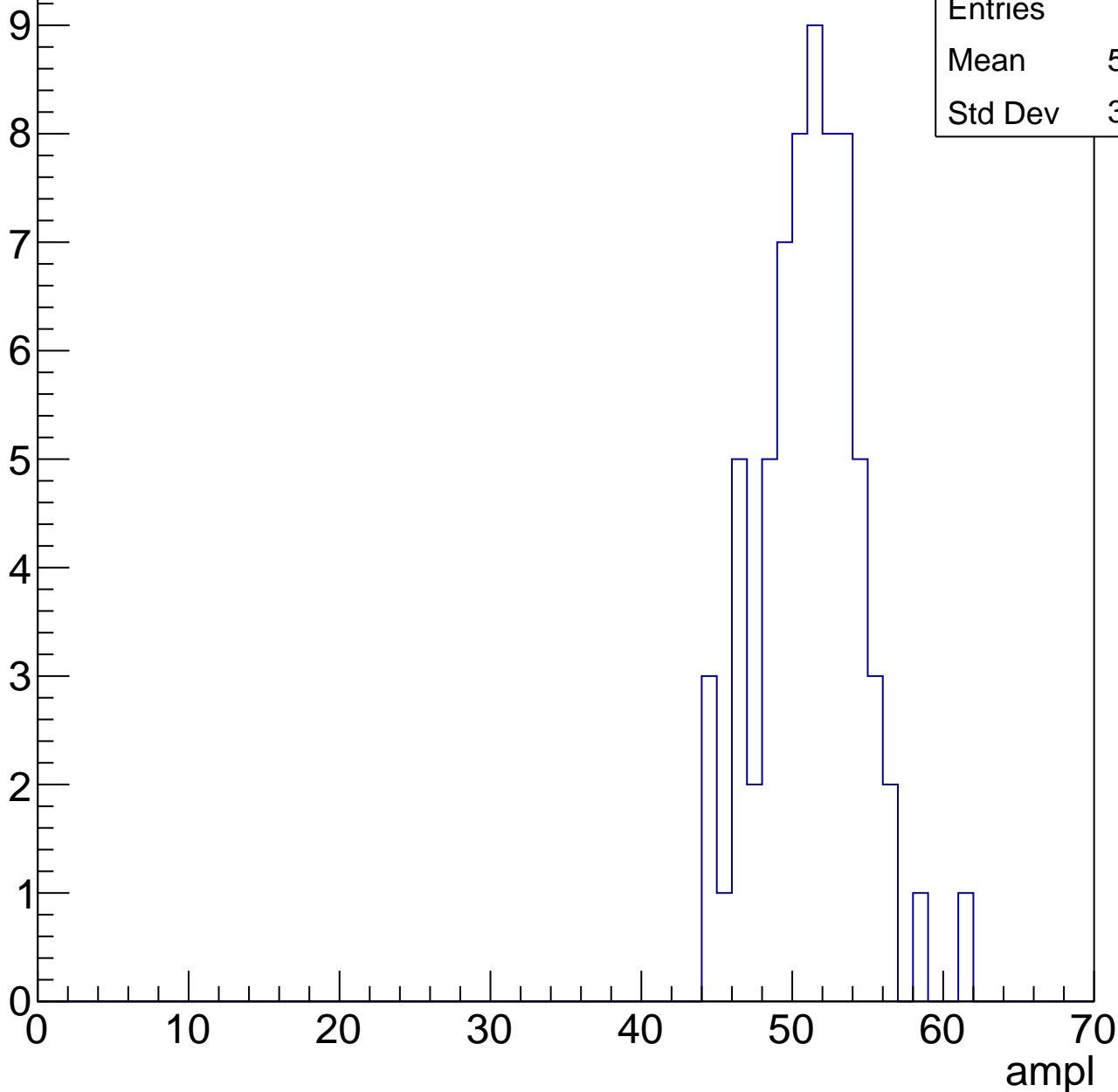


B1L101S, U12-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	50.72
Std Dev	3.316

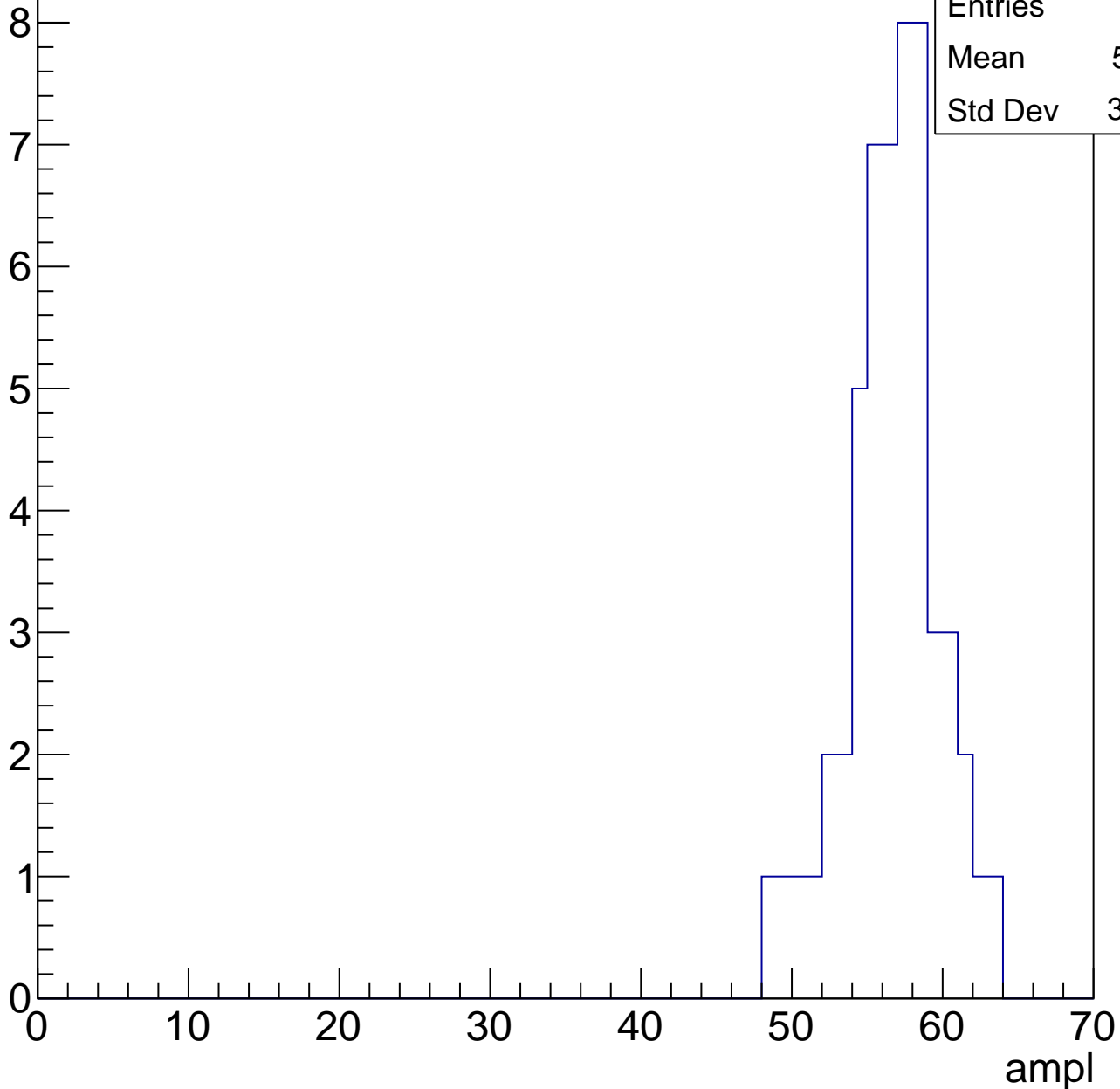


B1L101S, U12-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

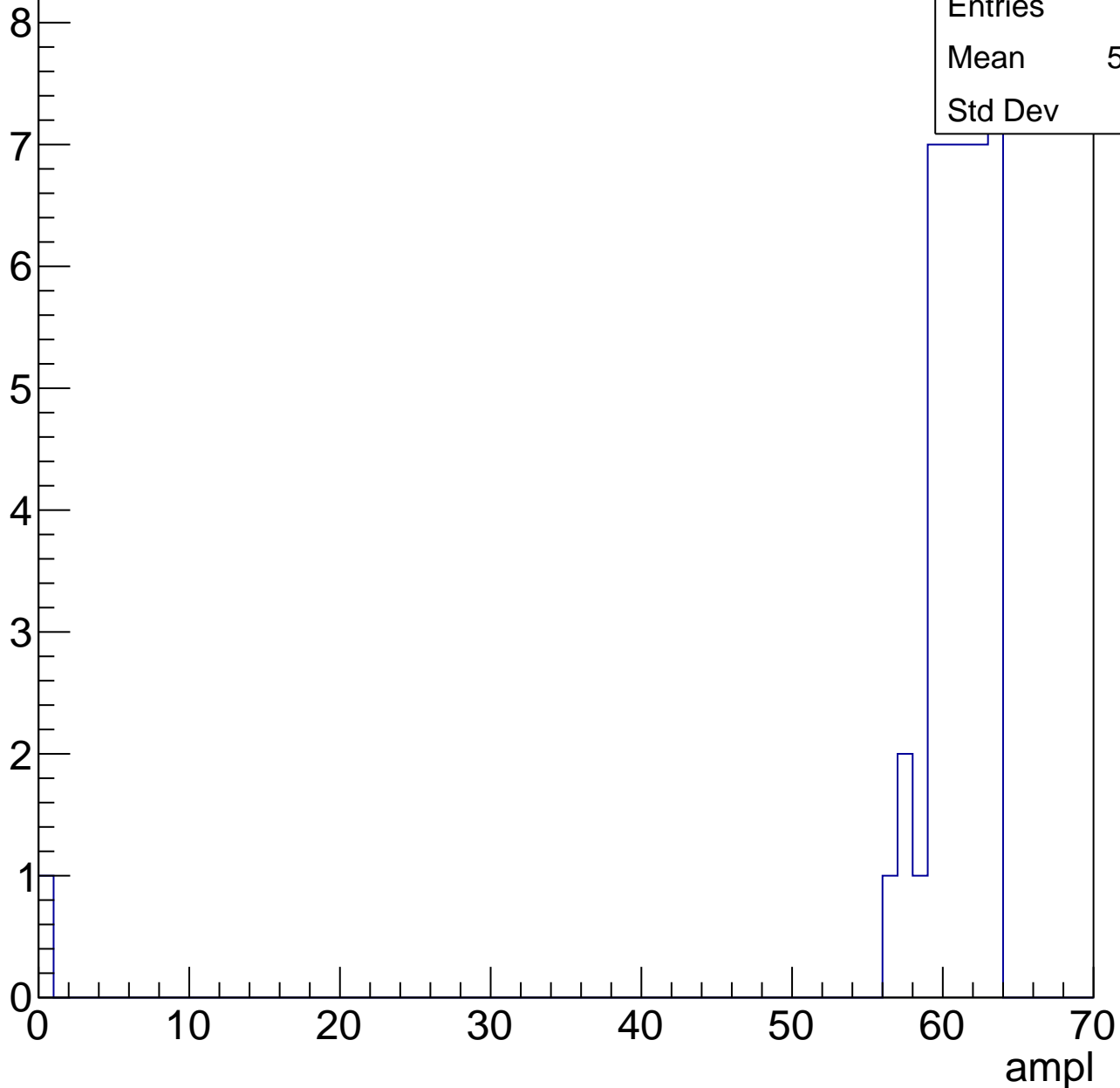
Entries	53
Mean	56.21
Std Dev	3.068



B1L101S, U12-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	41
Mean	59.17
Std Dev	9.53

B1L101S, U12-ch42, adc6

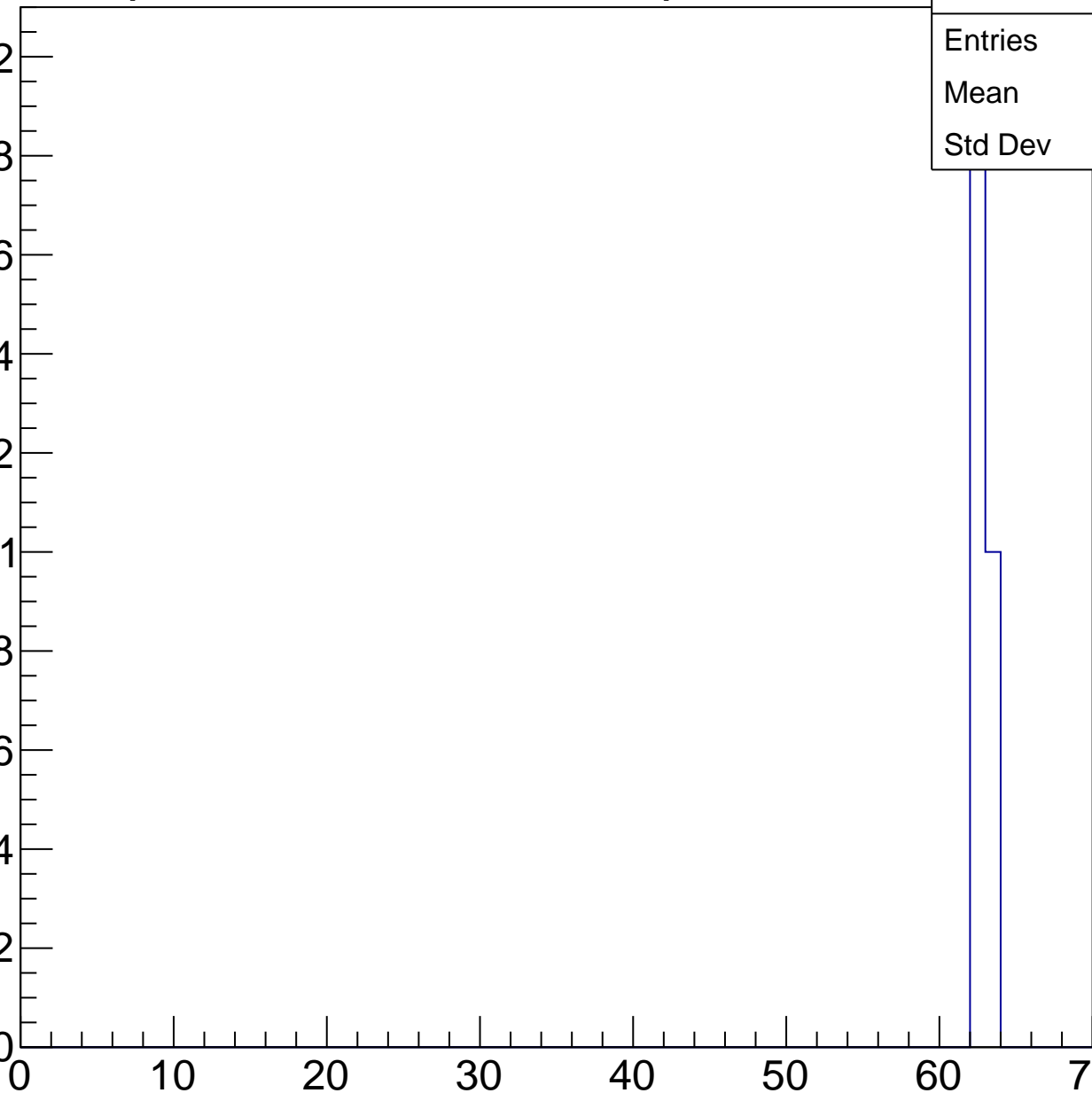
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L101S, U12-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch43, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	28.88
Std Dev	5.849

Gaus mean : 30.2114

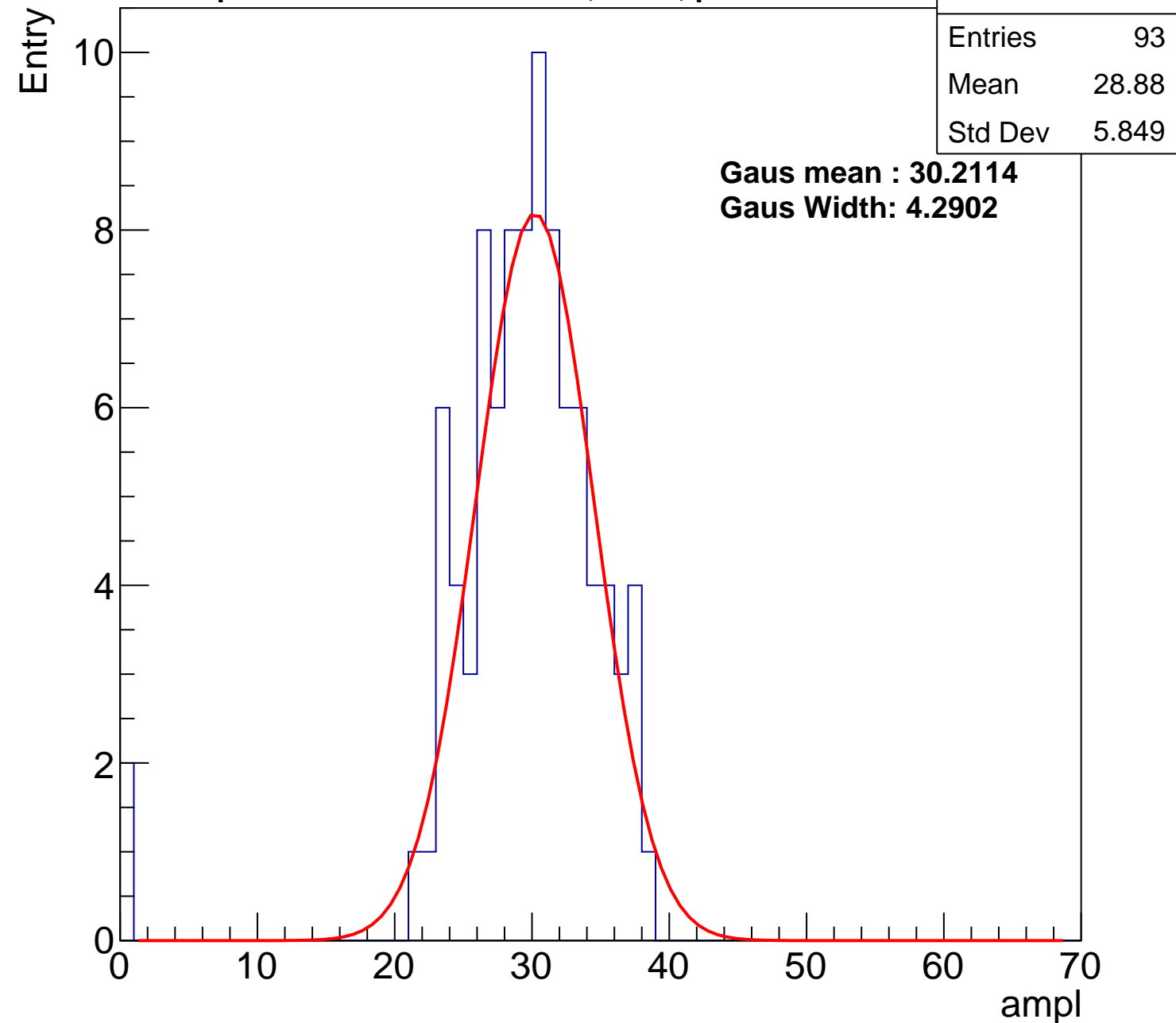
Gaus Width: 4.2902

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



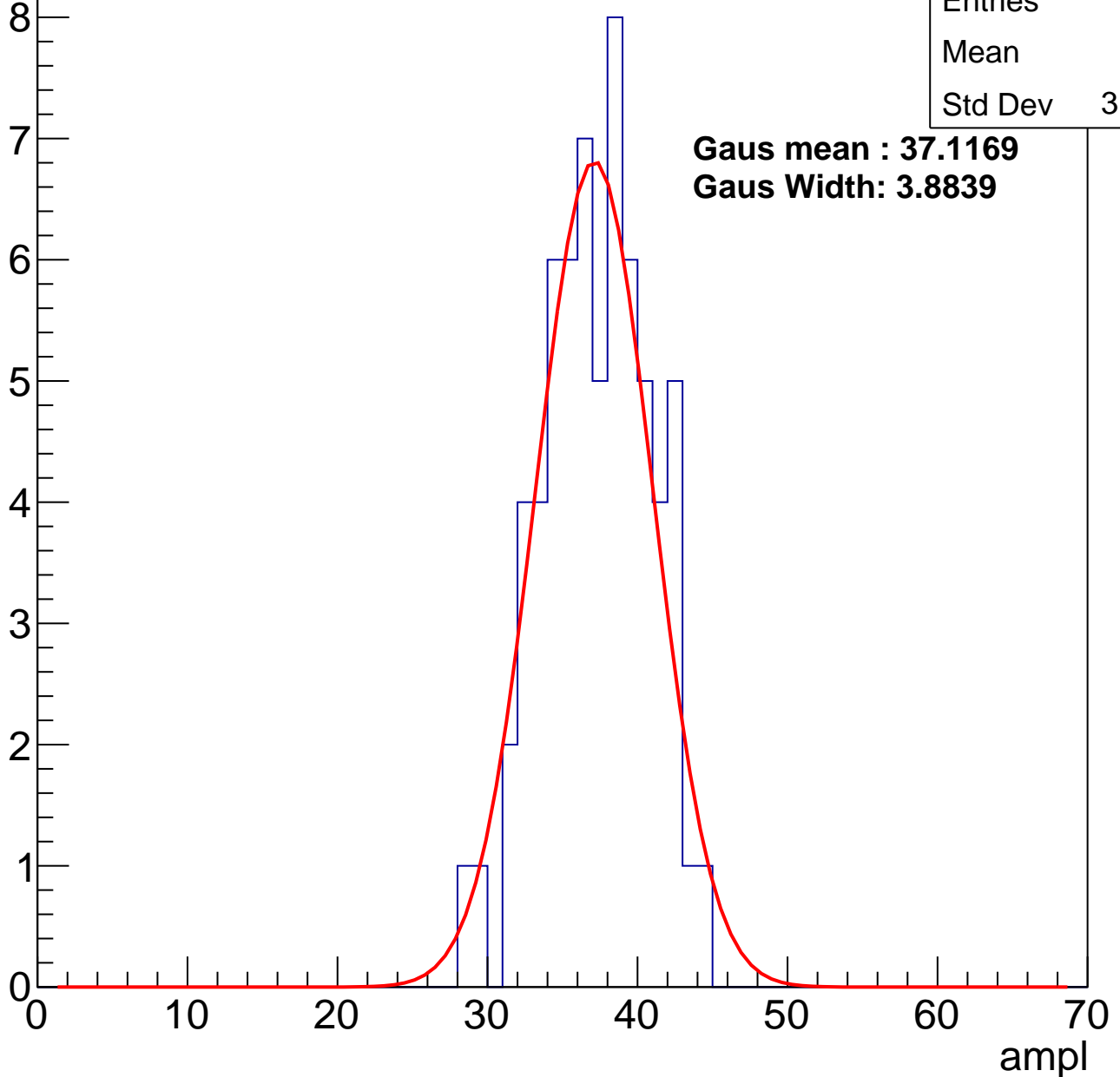
B1L101S, U12-ch43, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.8
Std Dev	3.513

Gaus mean : 37.1169
Gaus Width: 3.8839



B1L101S, U12-ch43, adc2

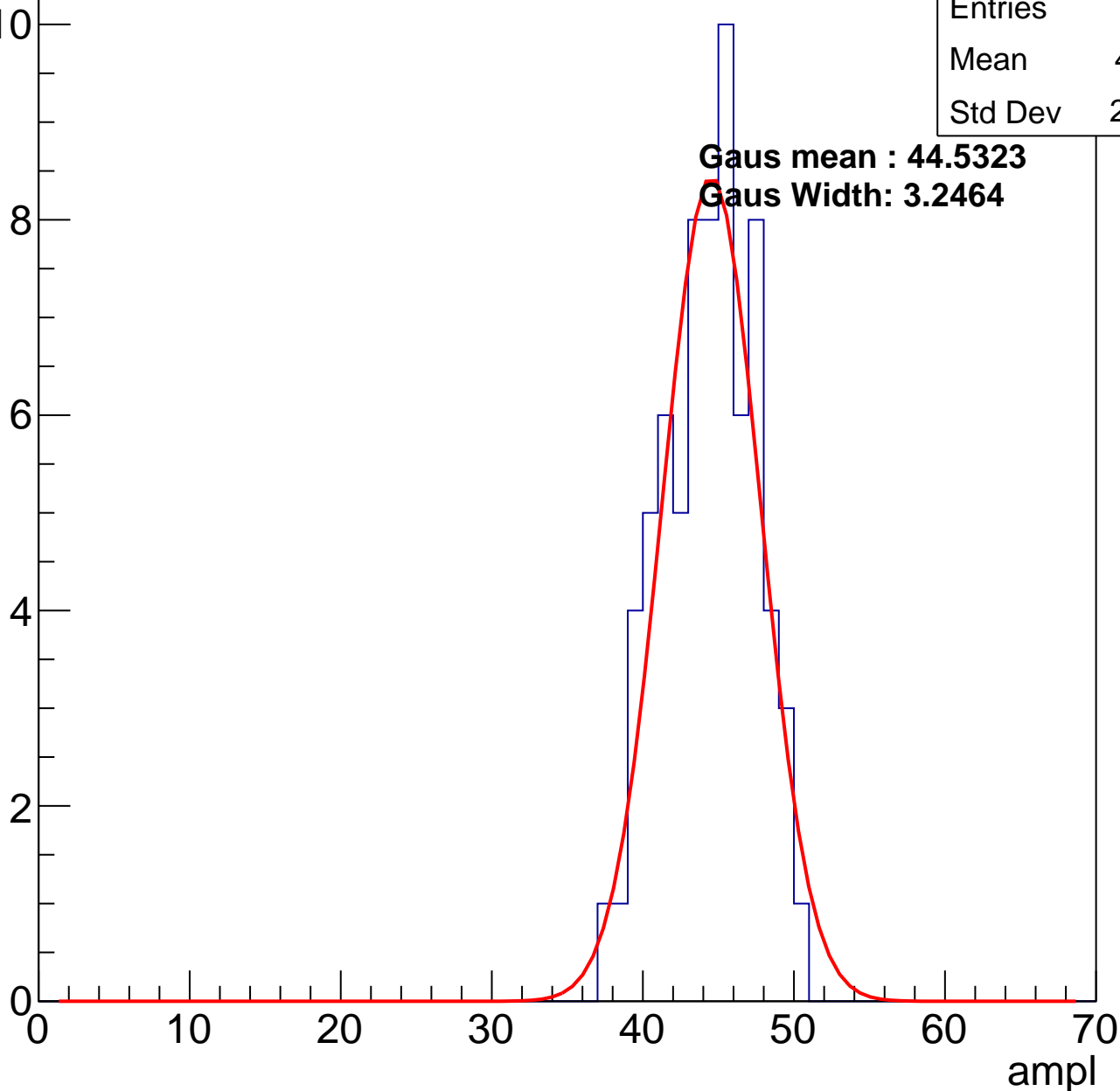
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.91
Std Dev	2.994

Gaus mean : 44.5323

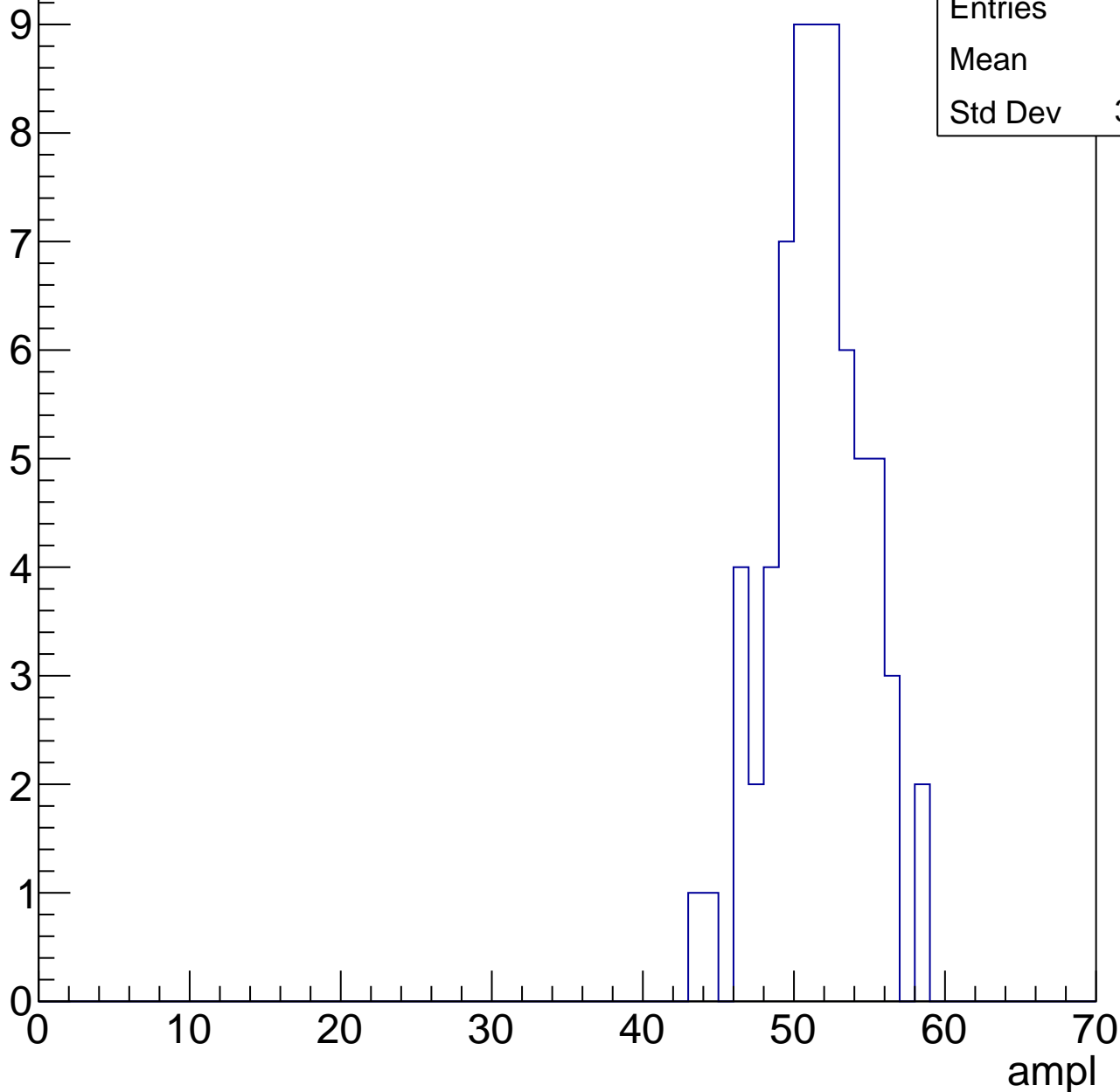
Gaus Width: 3.2464



B1L101S, U12-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	67
Mean	51.1
Std Dev	3.111

B1L101S, U12-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	53
Mean	56.81
Std Dev	3.228

Entry

10

8

6

4

2

0

0

10

20

30

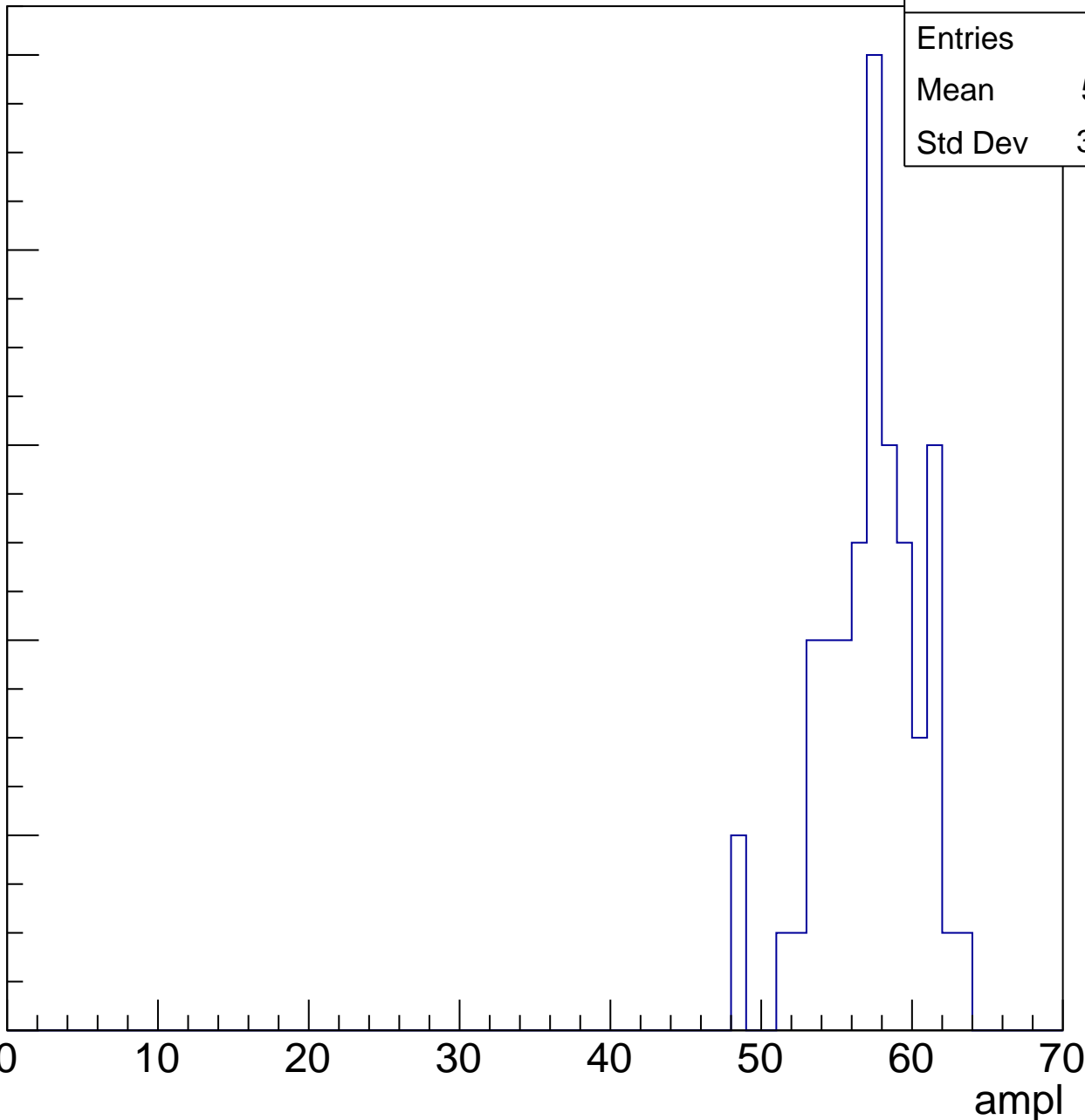
40

50

60

70

ampl

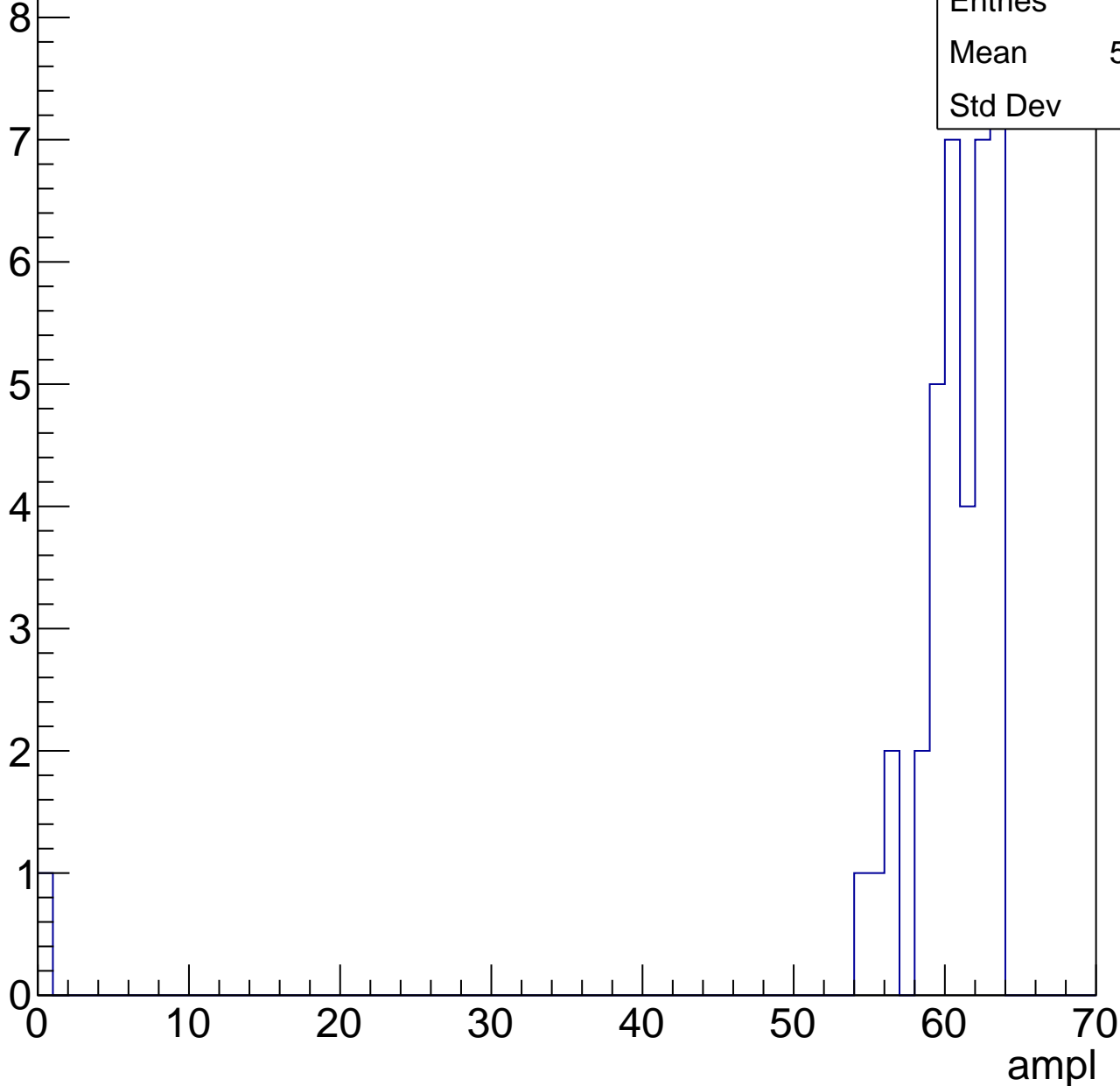


B1L101S, U12-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.79
Std Dev	9.94



B1L101S, U12-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch44, adc0

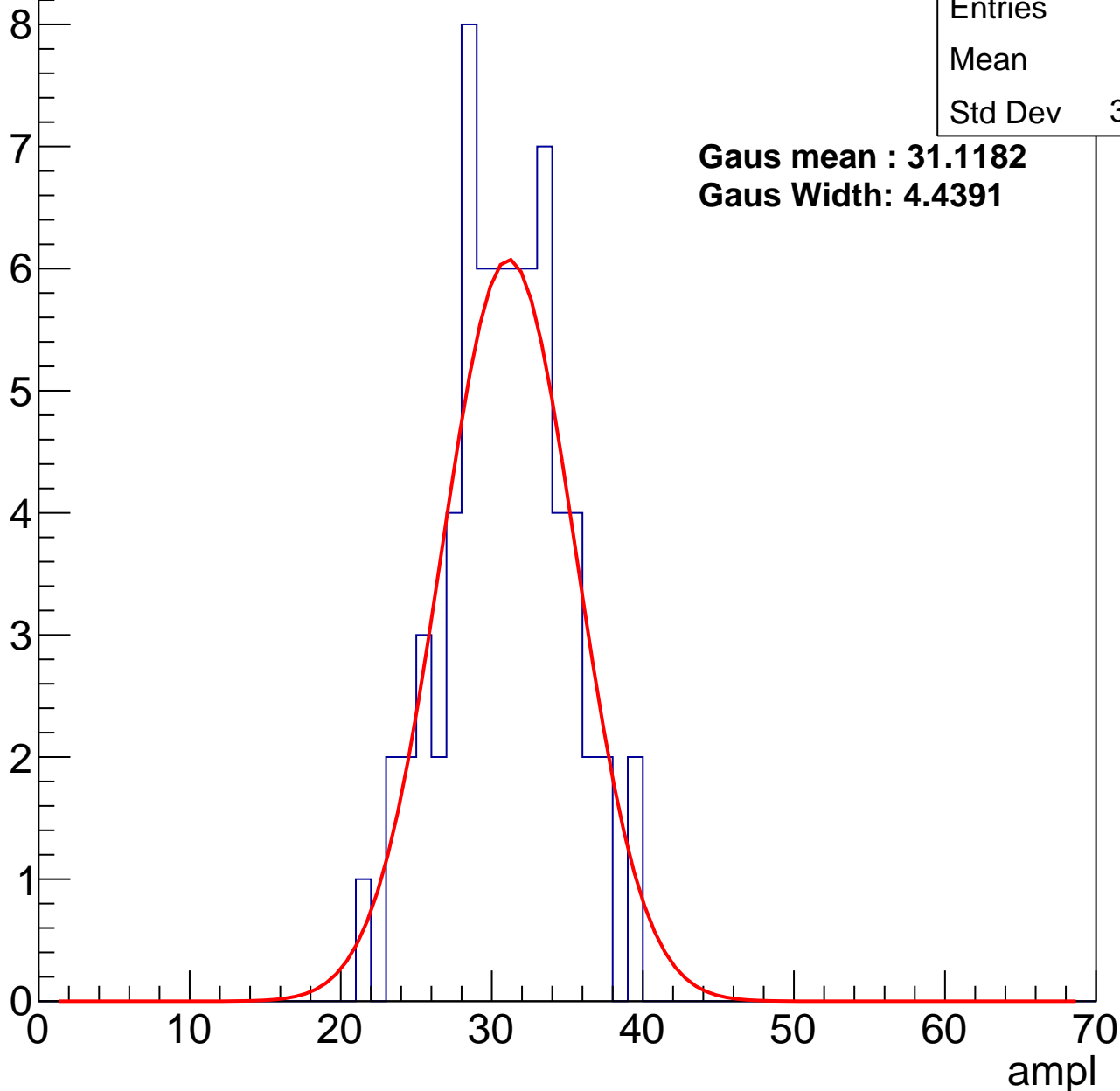
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.4
Std Dev	3.883

Gaus mean : 31.1182

Gaus Width: 4.4391



B1L101S, U12-ch44, adc1

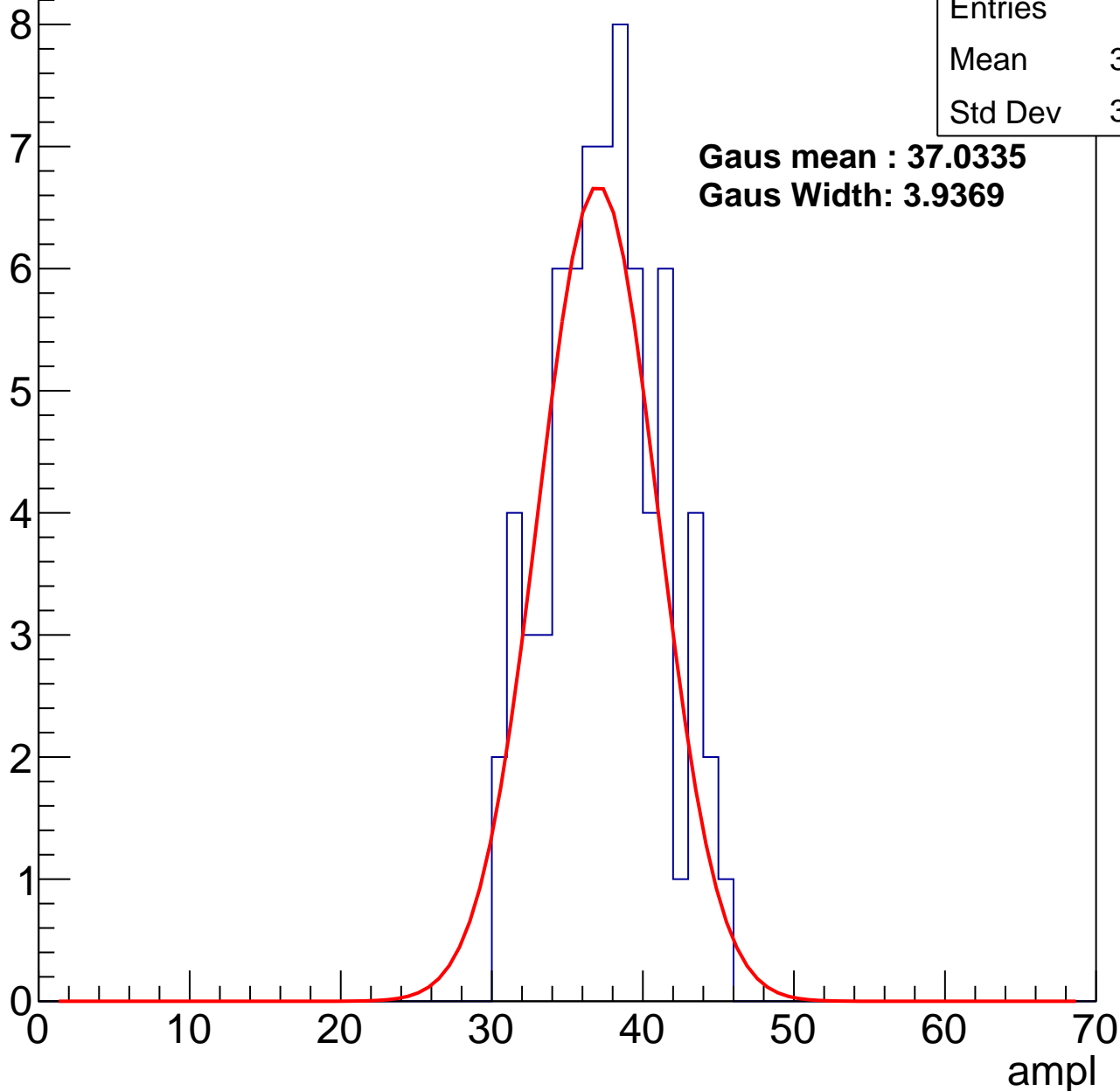
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.07
Std Dev	3.674

Gaus mean : 37.0335

Gaus Width: 3.9369



B1L101S, U12-ch44, adc2

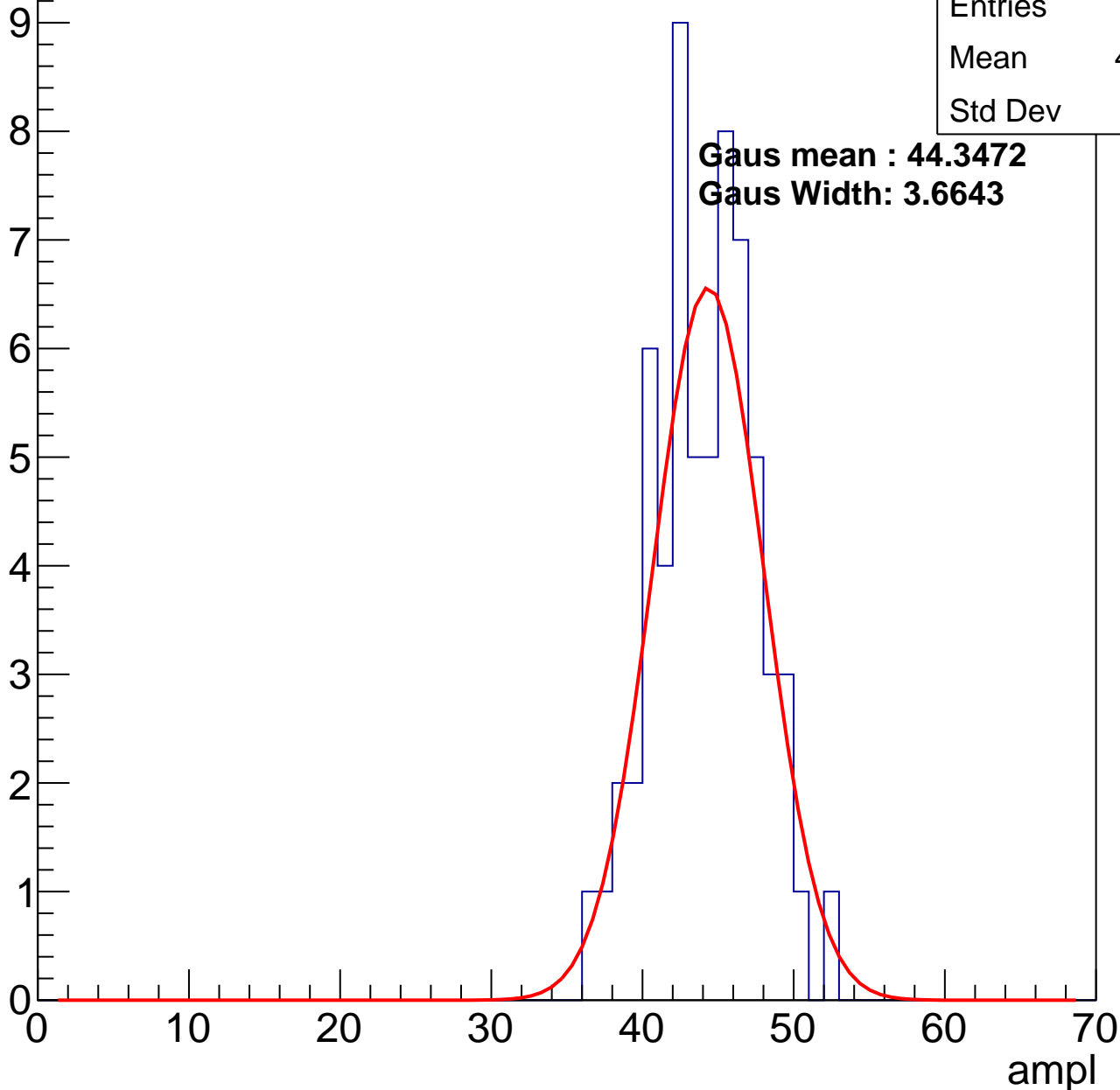
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.71
Std Dev	3.35

Gaus mean : 44.3472

Gaus Width: 3.6643

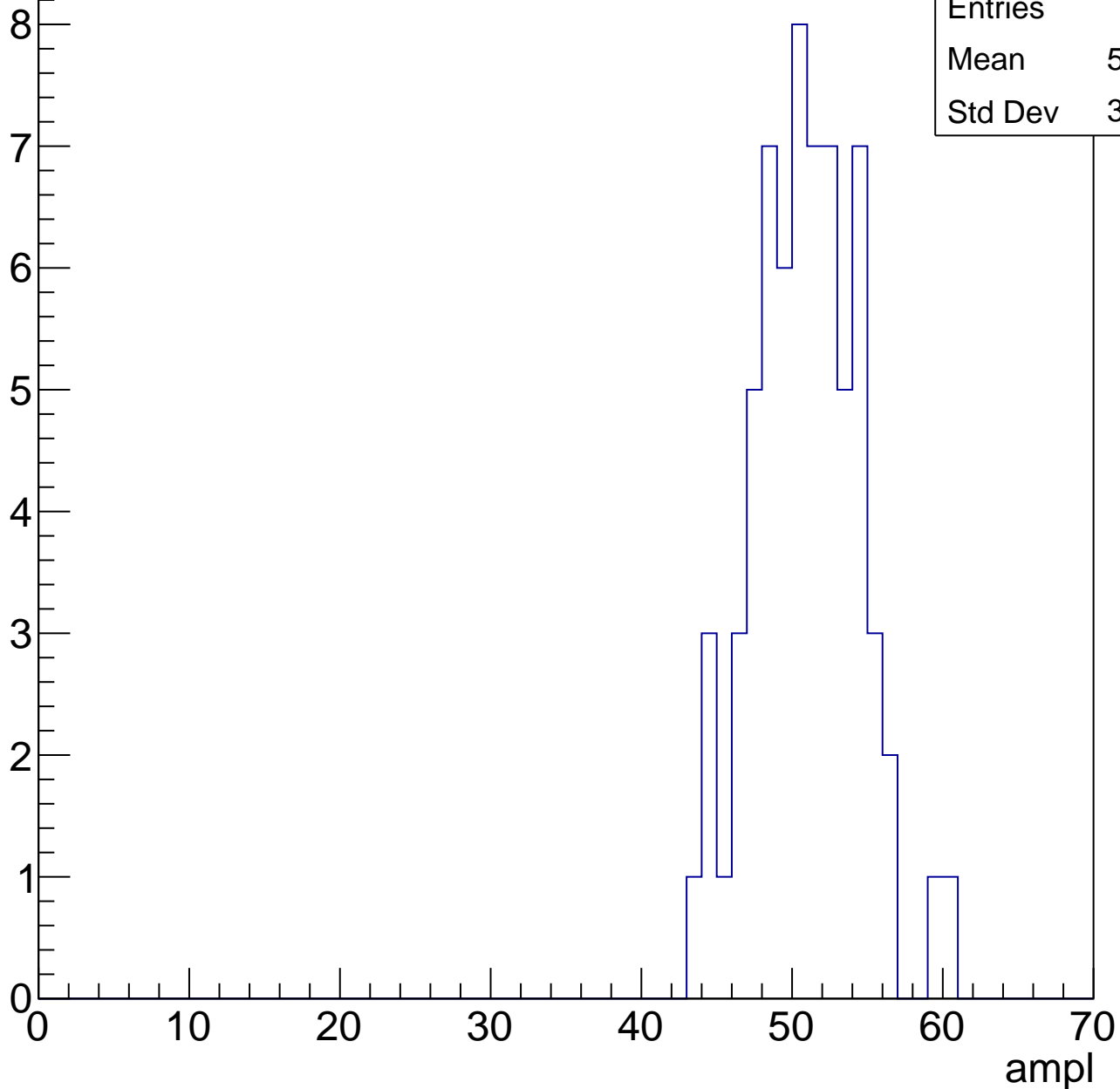


B1L101S, U12-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.49
Std Dev	3.483

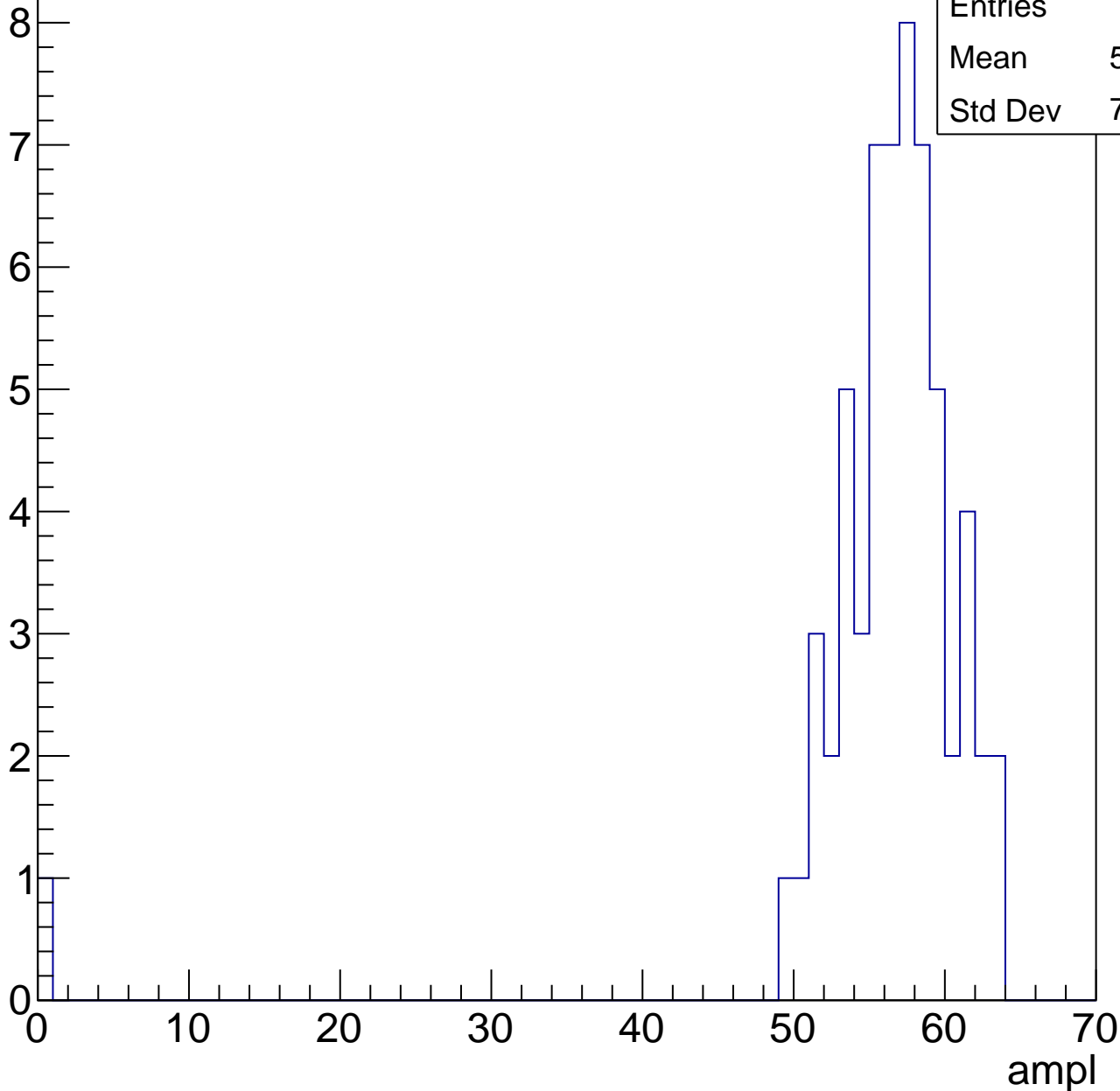


B1L101S, U12-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.52
Std Dev	7.917

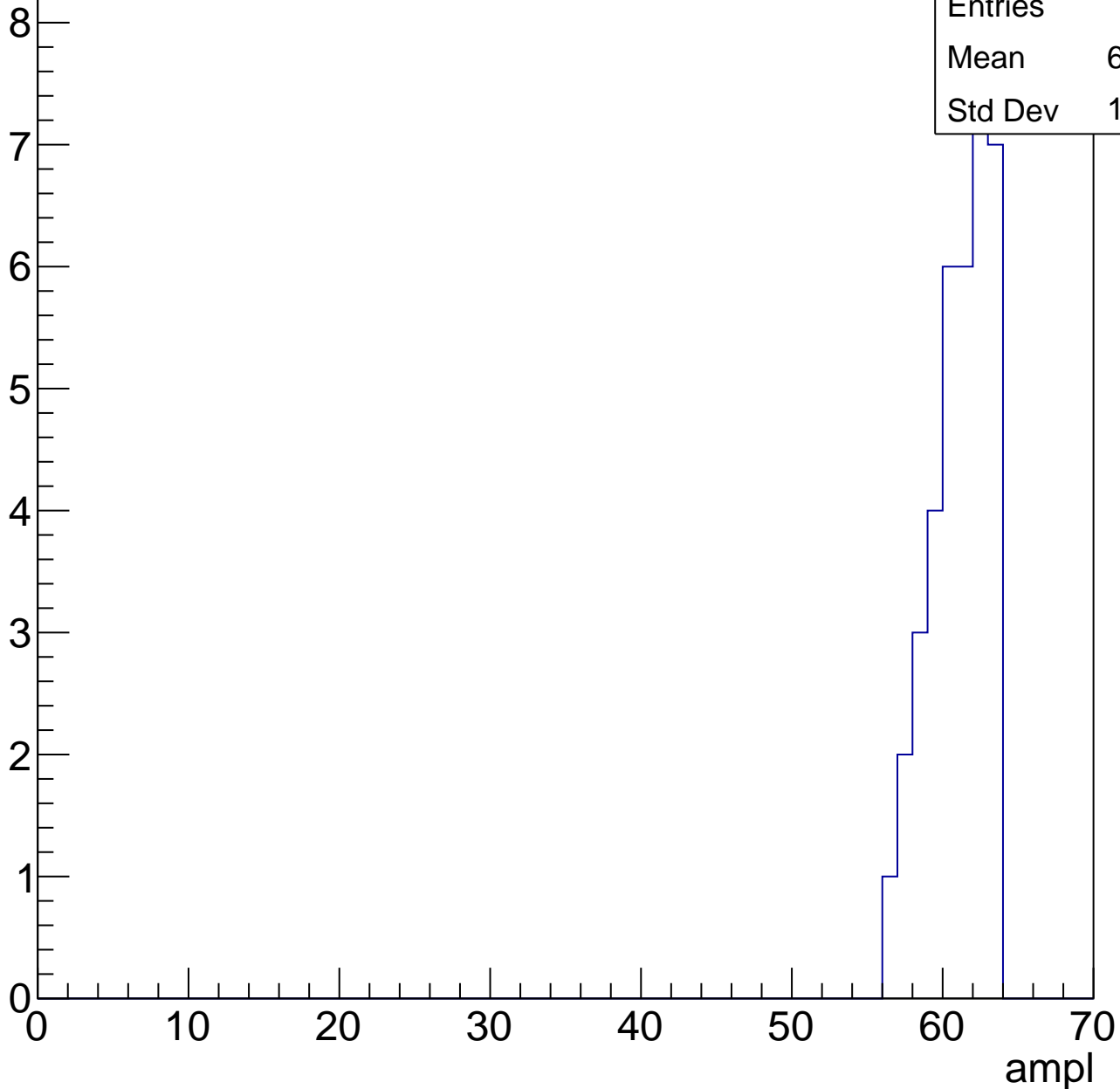


B1L101S, U12-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	60.62
Std Dev	1.922



B1L101S, U12-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch45, adc0

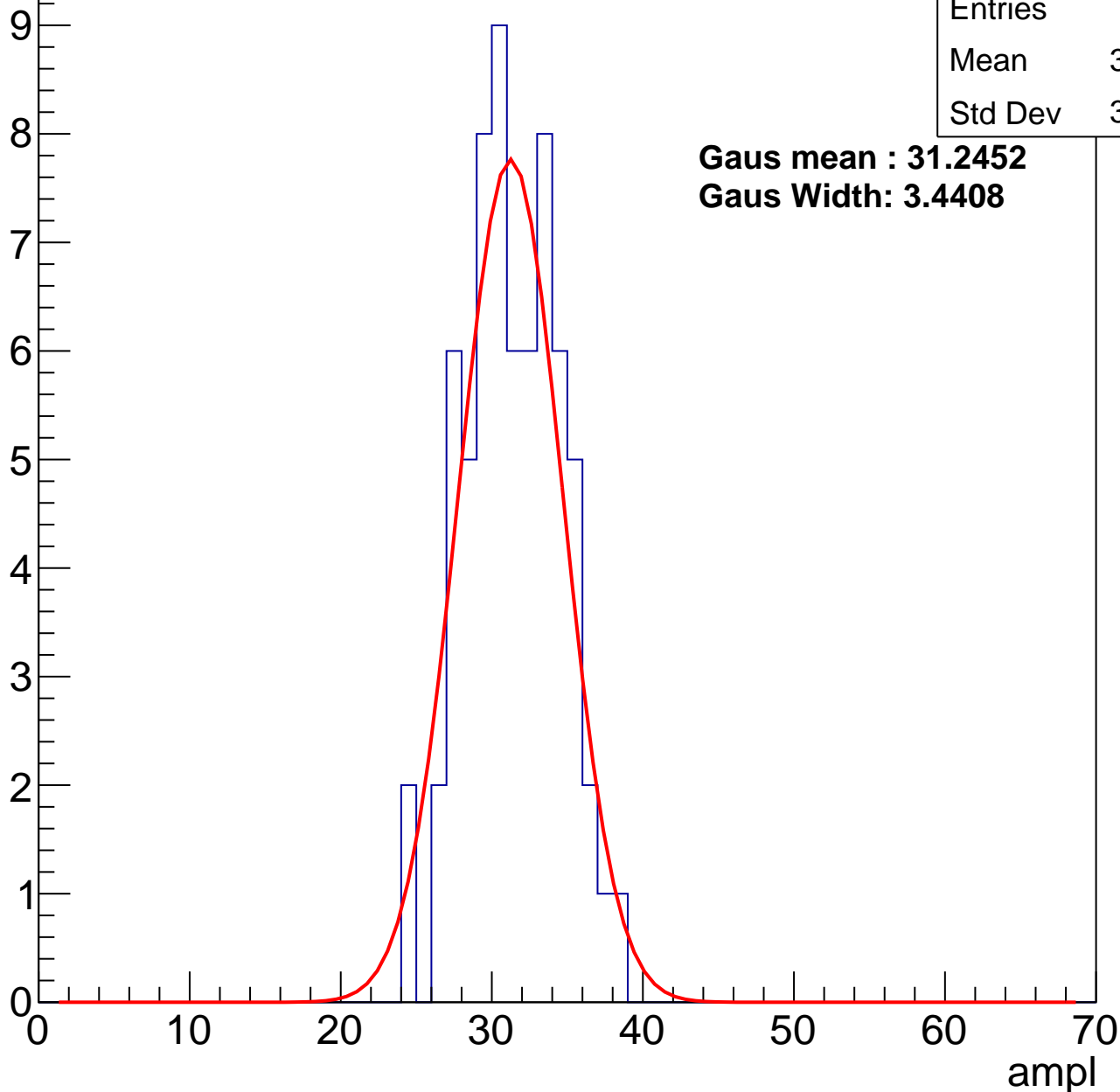
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.93
Std Dev	3.083

Gaus mean : 31.2452

Gaus Width: 3.4408



B1L101S, U12-ch45, adc1

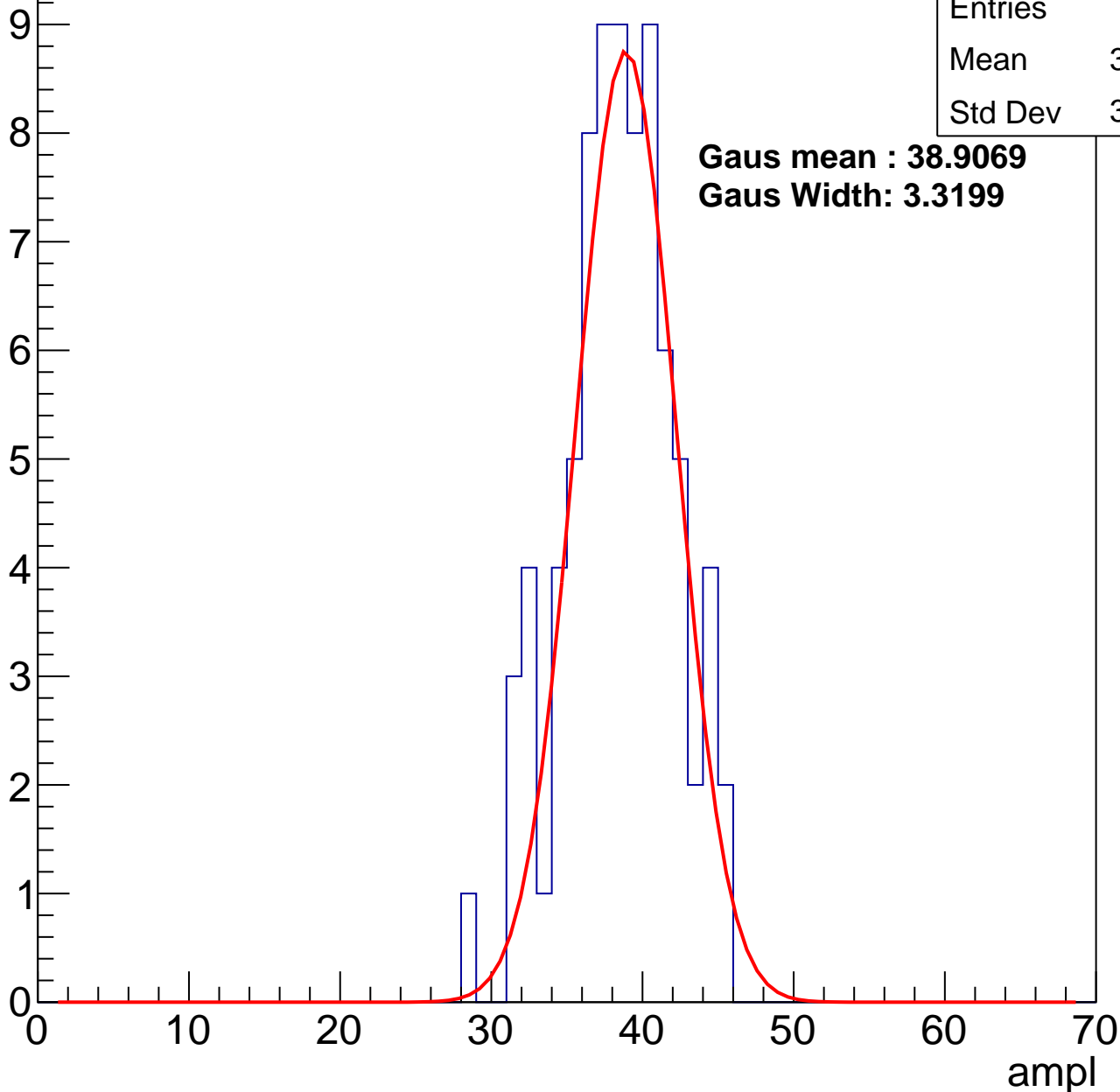
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	37.95
Std Dev	3.598

Gaus mean : 38.9069

Gaus Width: 3.3199



B1L101S, U12-ch45, adc2

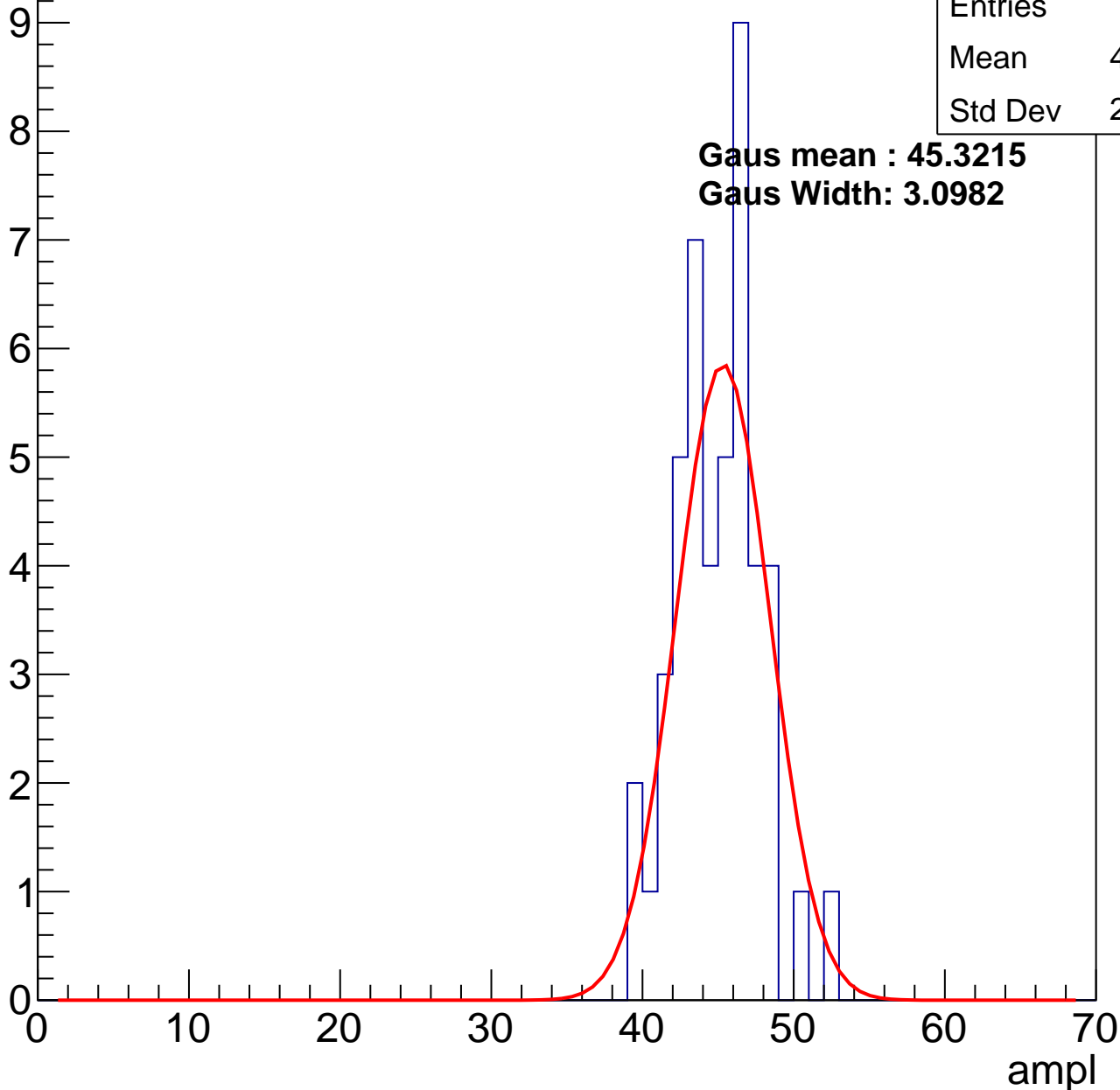
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	44.54
Std Dev	2.748

Gaus mean : 45.3215

Gaus Width: 3.0982



B1L101S, U12-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	51.28
Std Dev	3.981

Entry

10

8

6

4

2

0

0

10

20

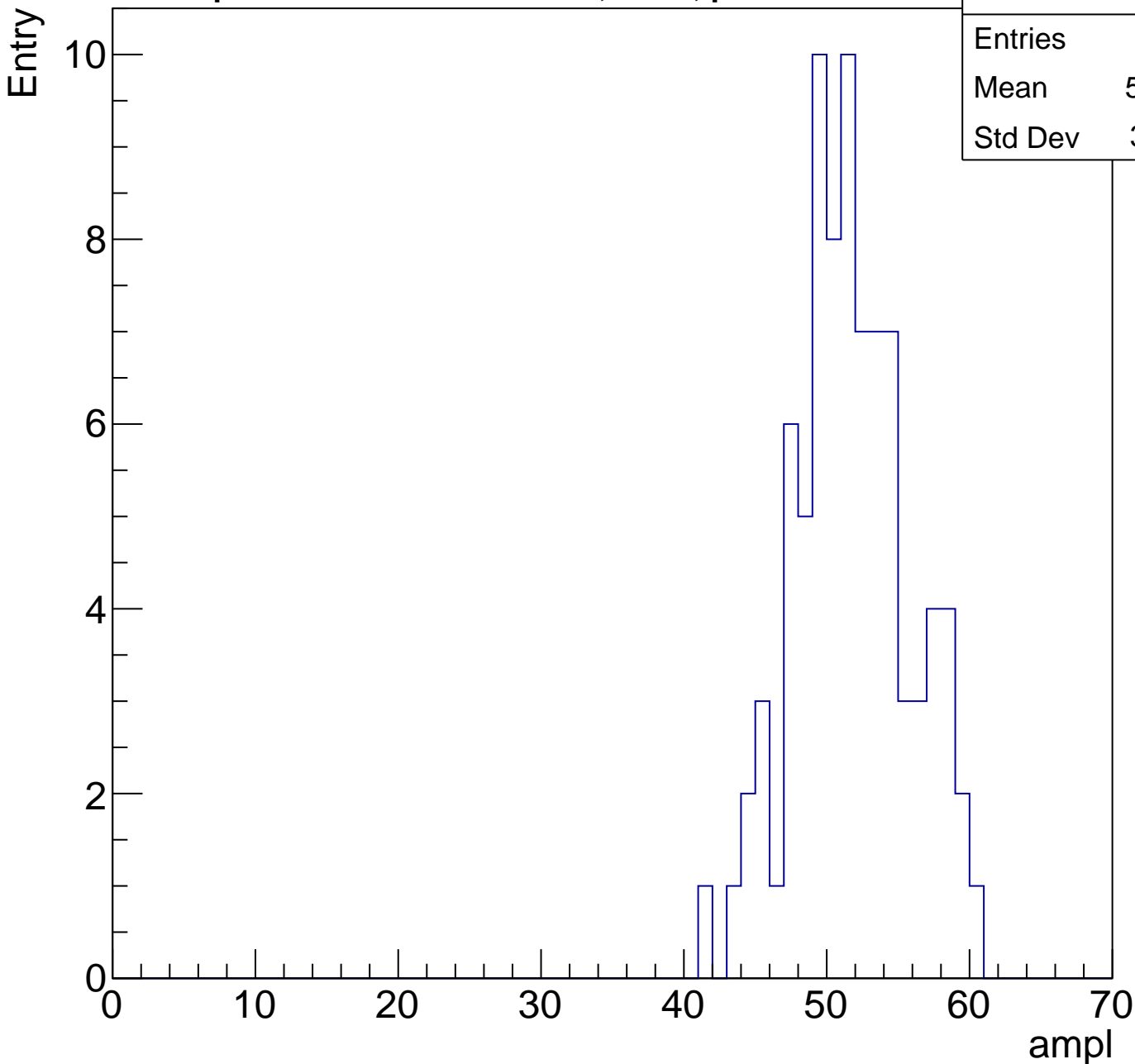
30

40

50

60

ampl



B1L101S, U12-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 55.81

Std Dev 8.29

ampl

0

10

20

30

40

50

60

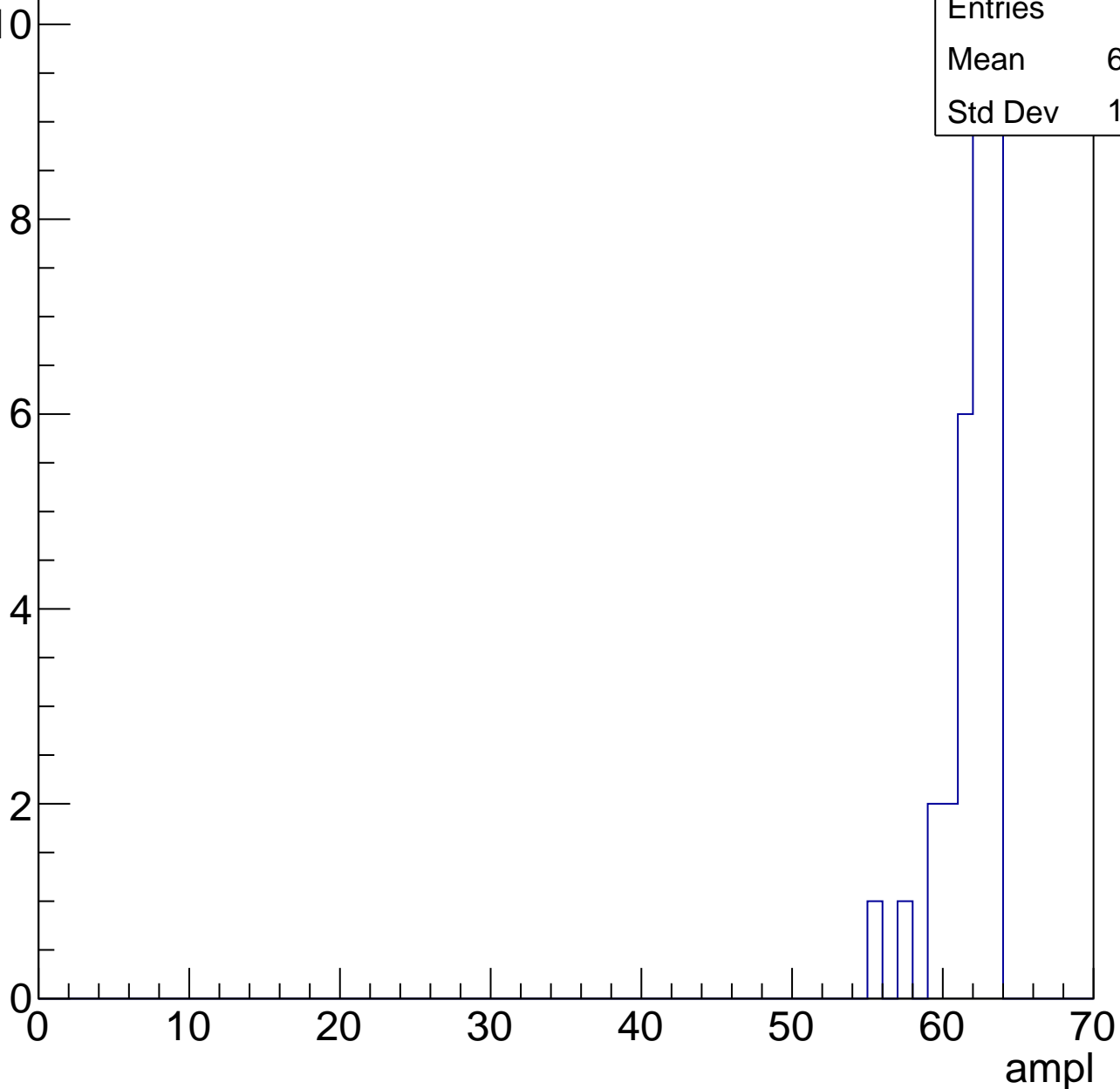
70

B1L101S, U12-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	61.42
Std Dev	1.845



B1L101S, U12-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch46, adc0

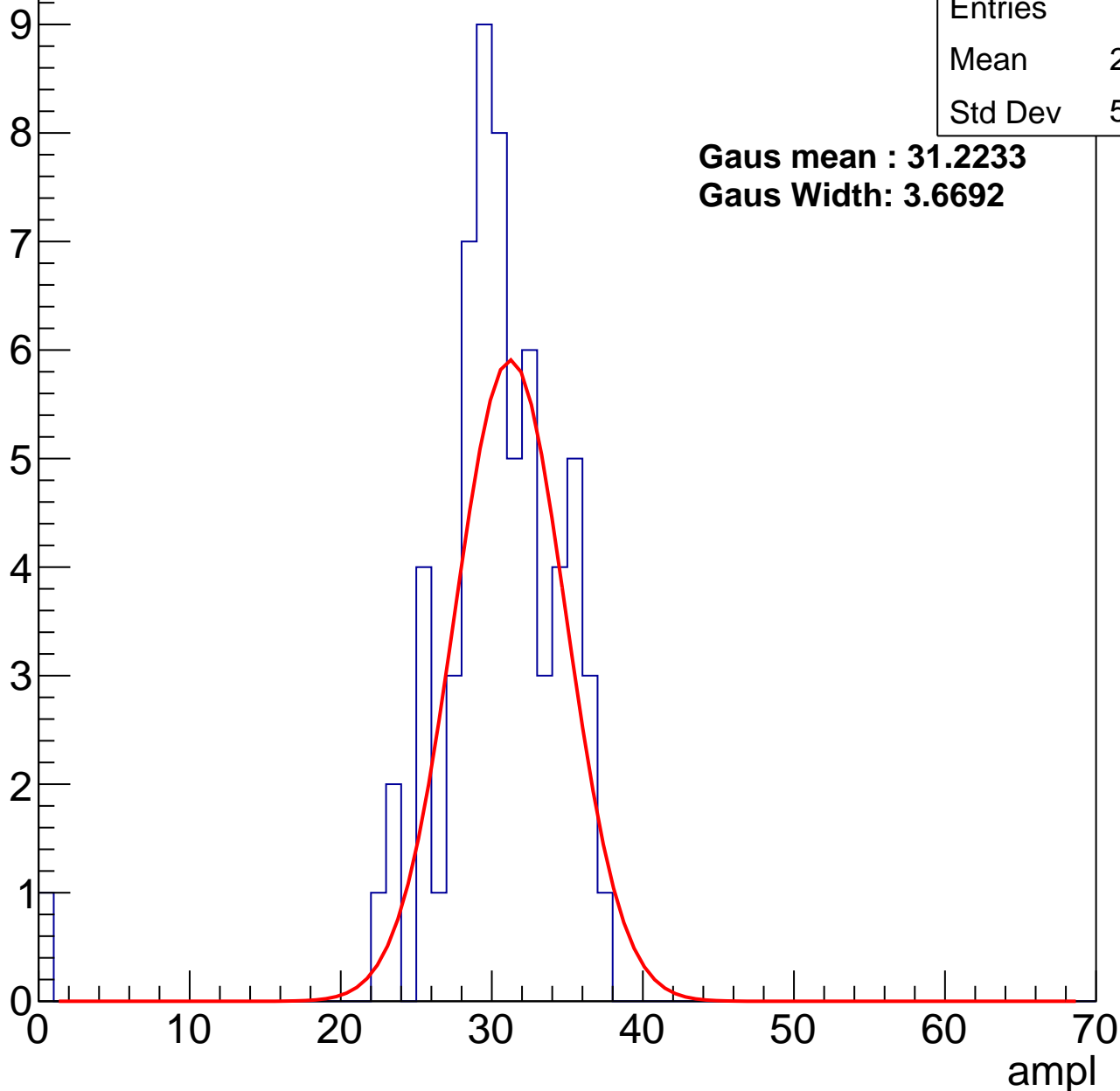
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.75
Std Dev	5.102

Gaus mean : 31.2233

Gaus Width: 3.6692



B1L101S, U12-ch46, adc1

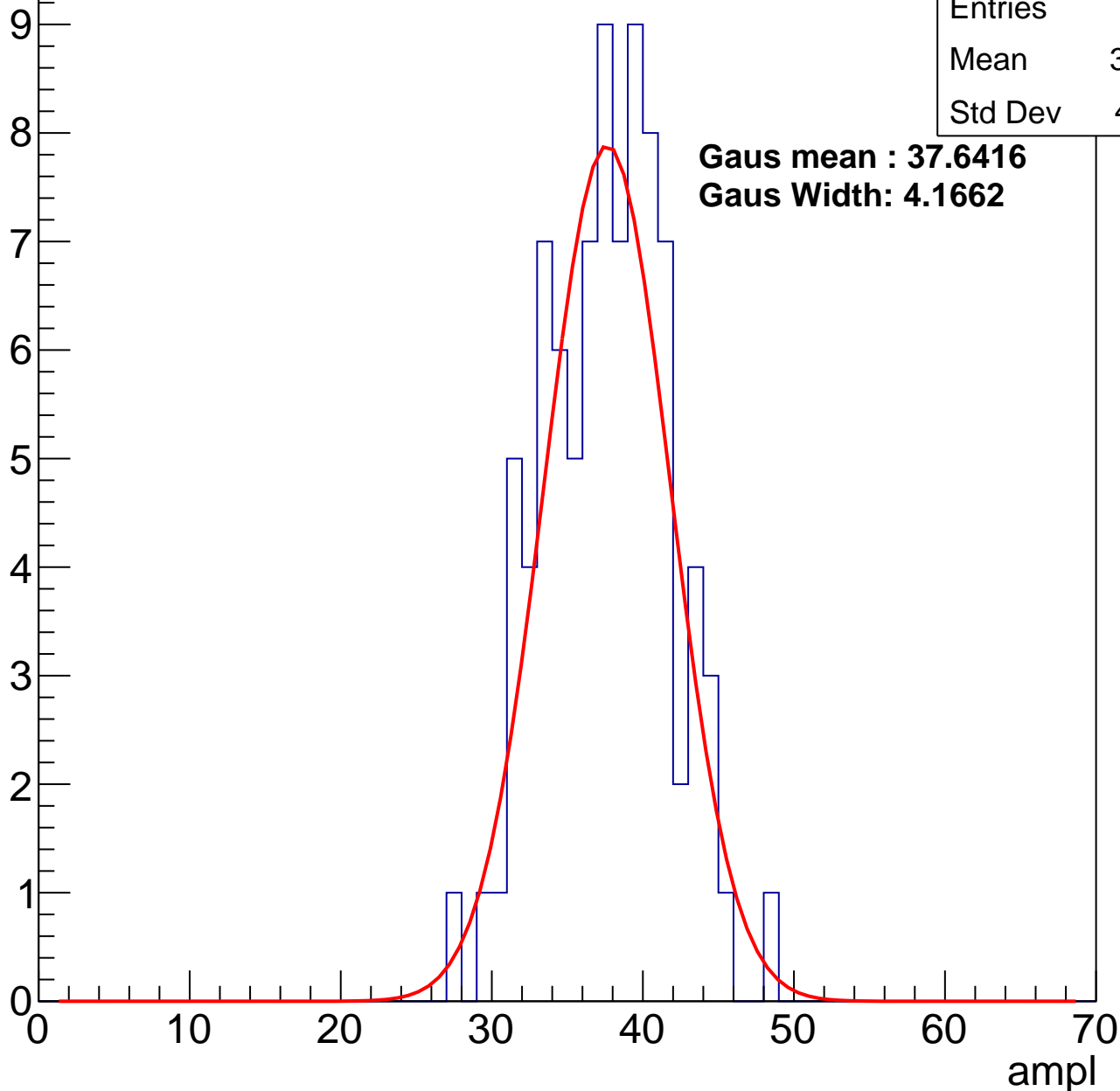
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	37.15
Std Dev	4.041

Gaus mean : 37.6416

Gaus Width: 4.1662

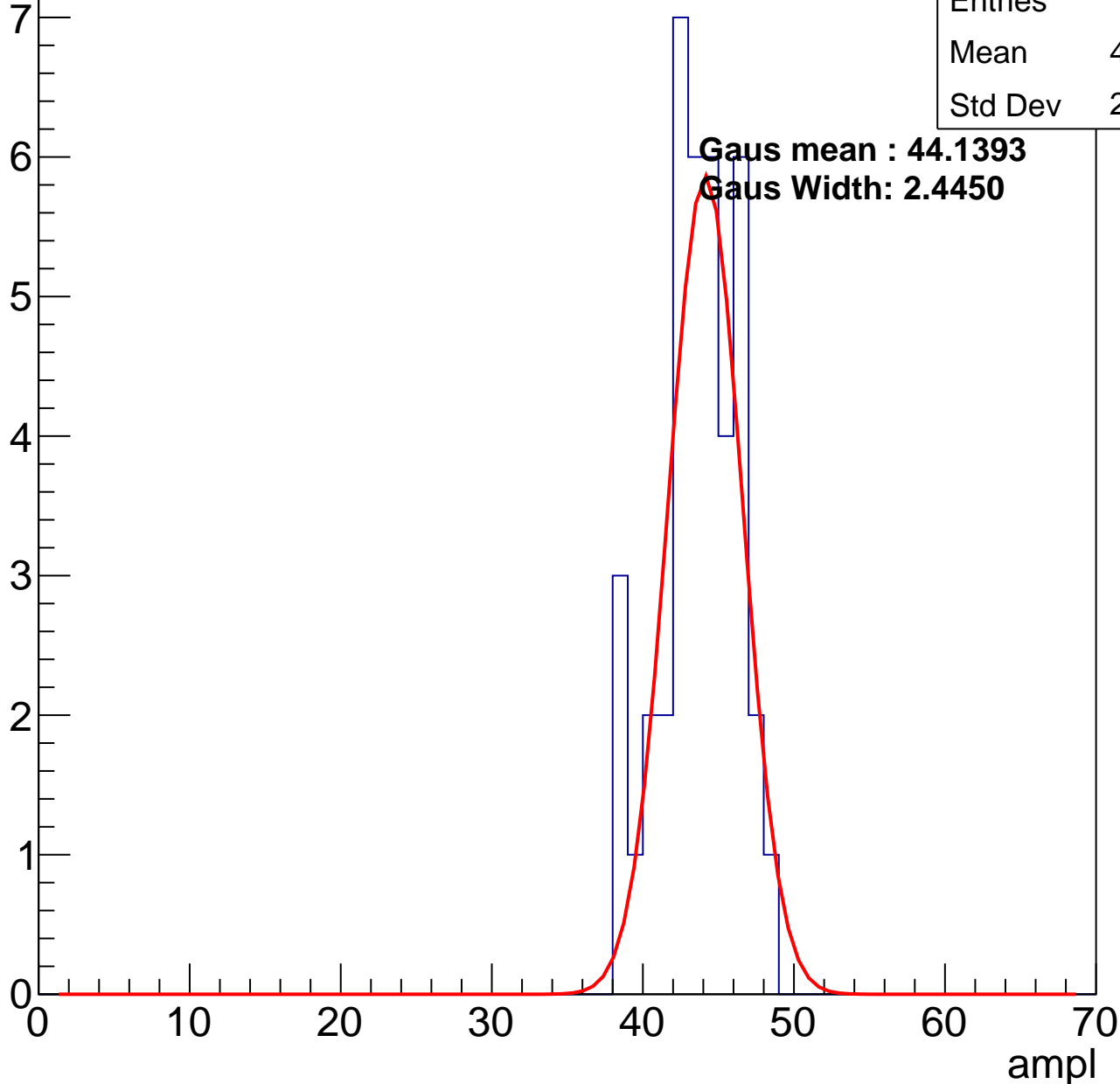


B1L101S, U12-ch46, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	43.23
Std Dev	2.525



B1L101S, U12-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	49.68
Std Dev	3.396

Entry

10

8

6

4

2

0

0

10

20

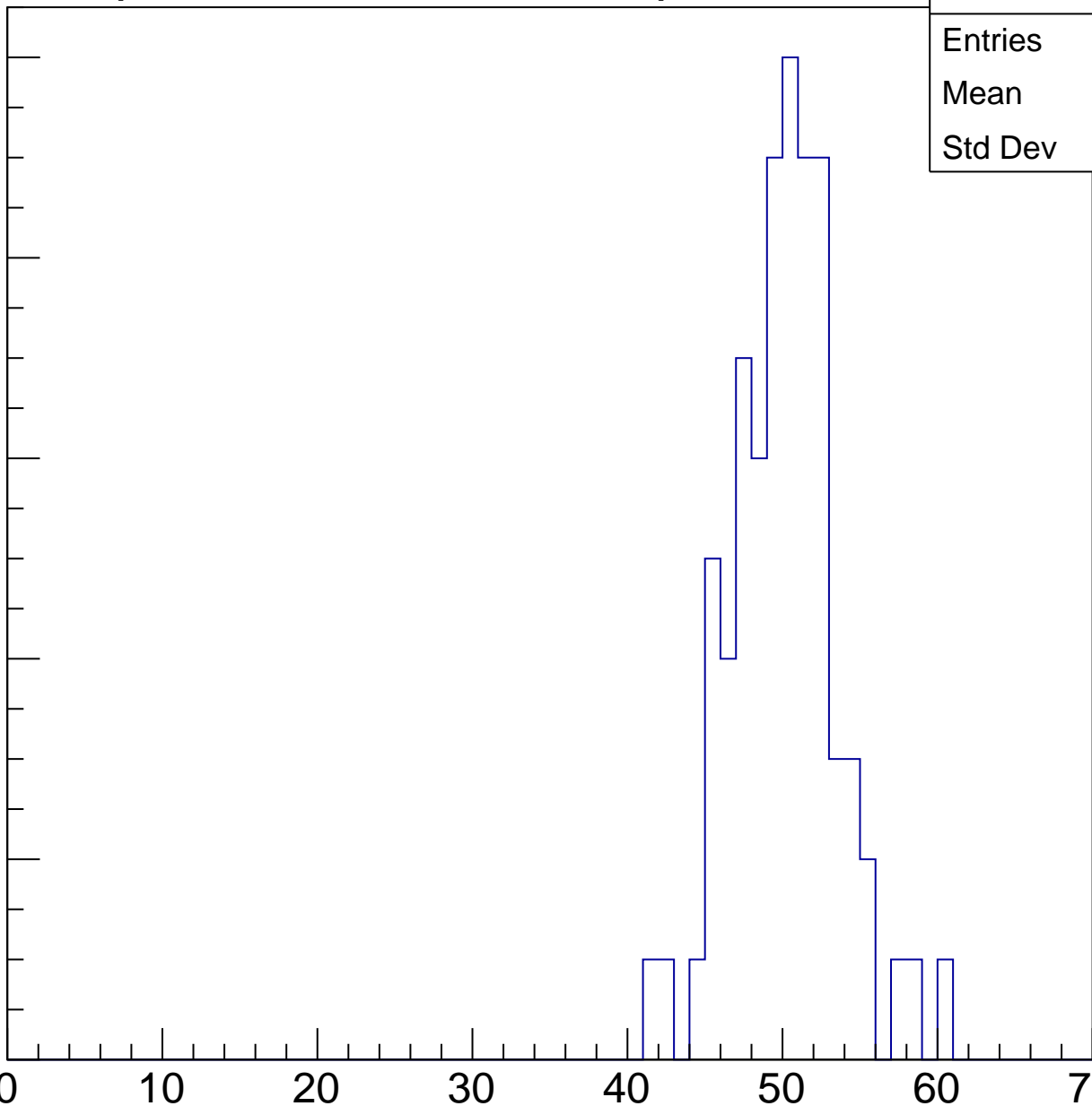
30

40

50

60

ampl

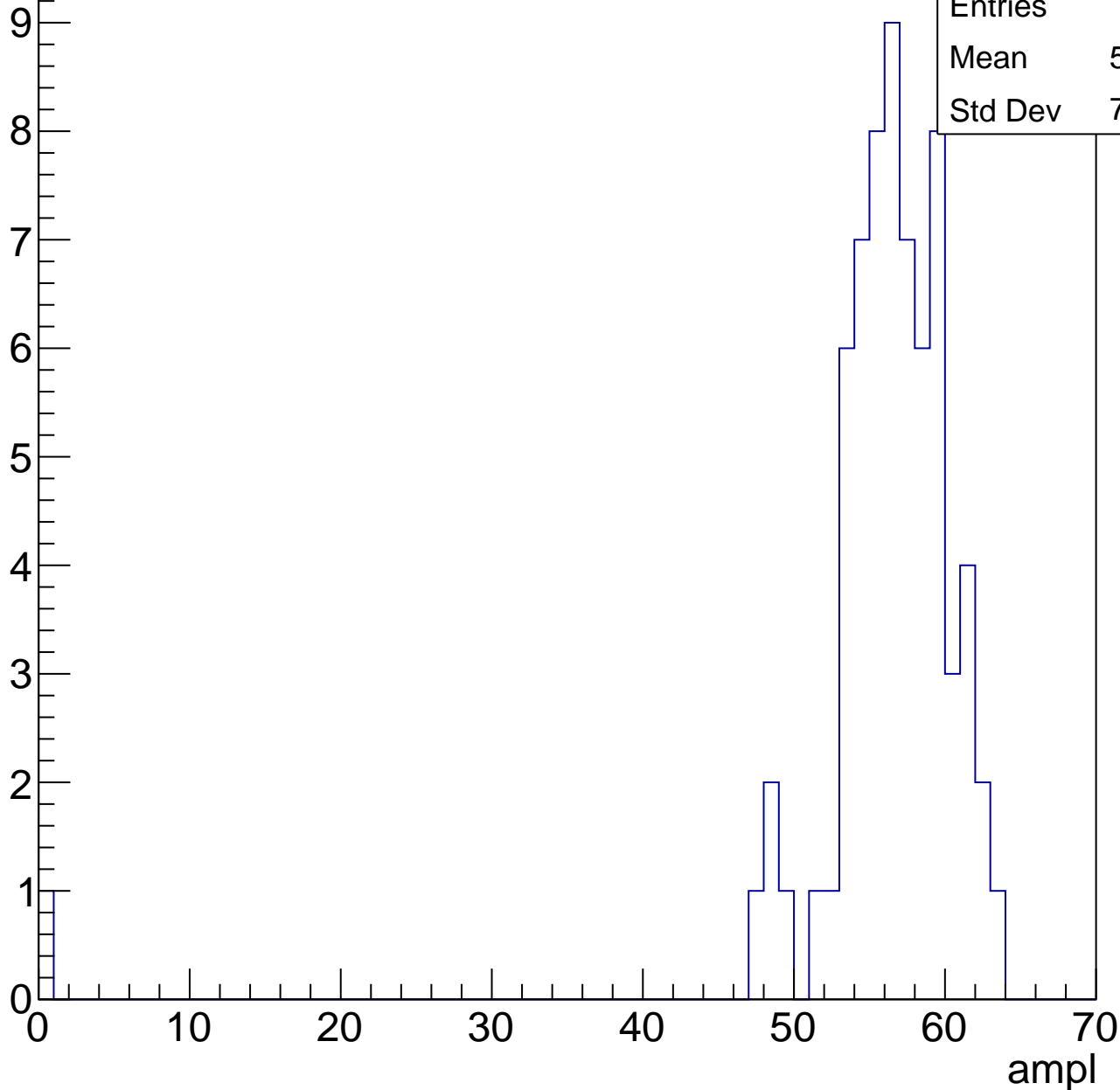


B1L101S, U12-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	55.37
Std Dev	7.544

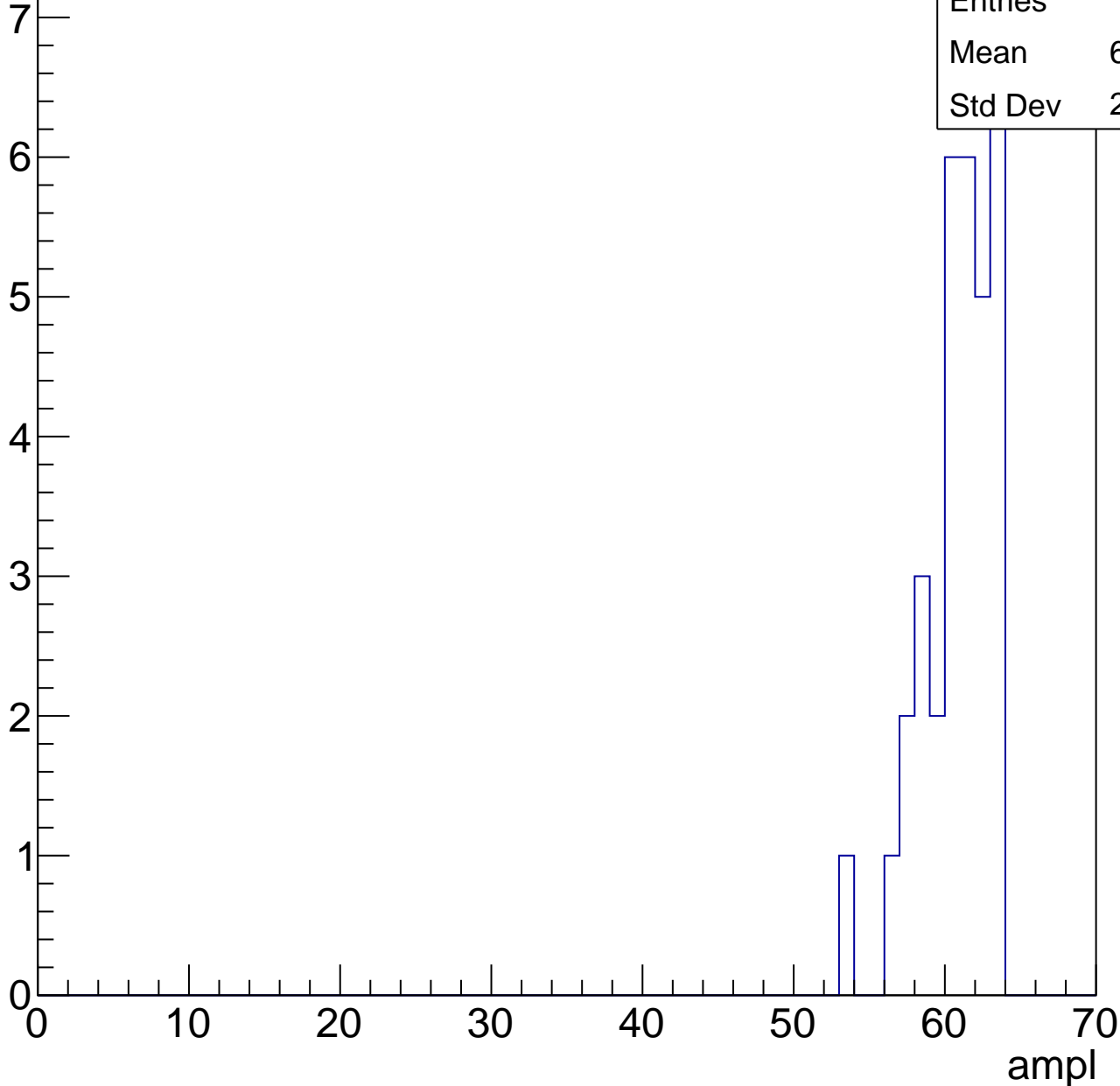


B1L101S, U12-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.36
Std Dev	2.346



B1L101S, U12-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L101S, U12-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch47, adc0

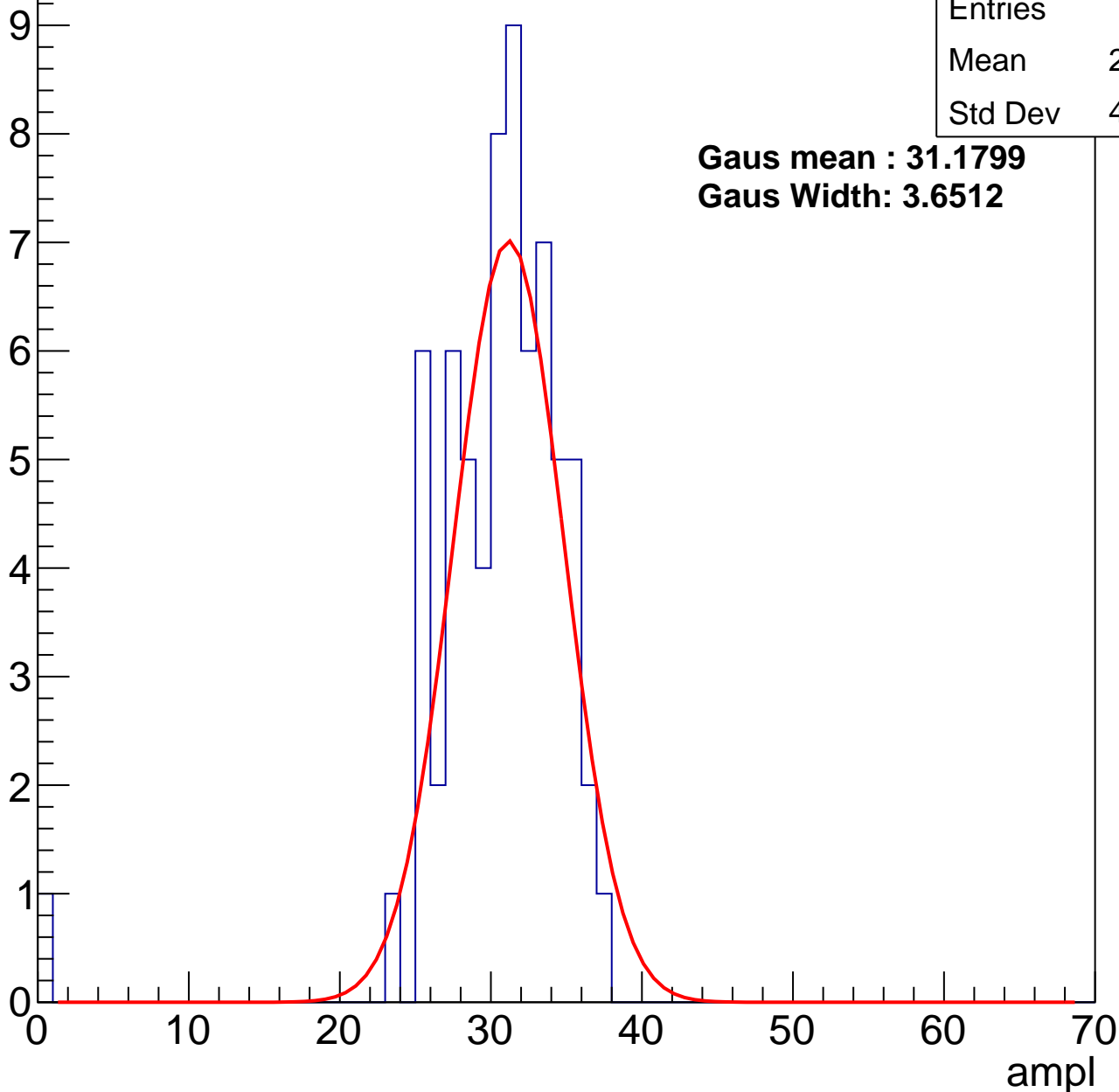
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.99
Std Dev	4.897

Gaus mean : 31.1799

Gaus Width: 3.6512



B1L101S, U12-ch47, adc1

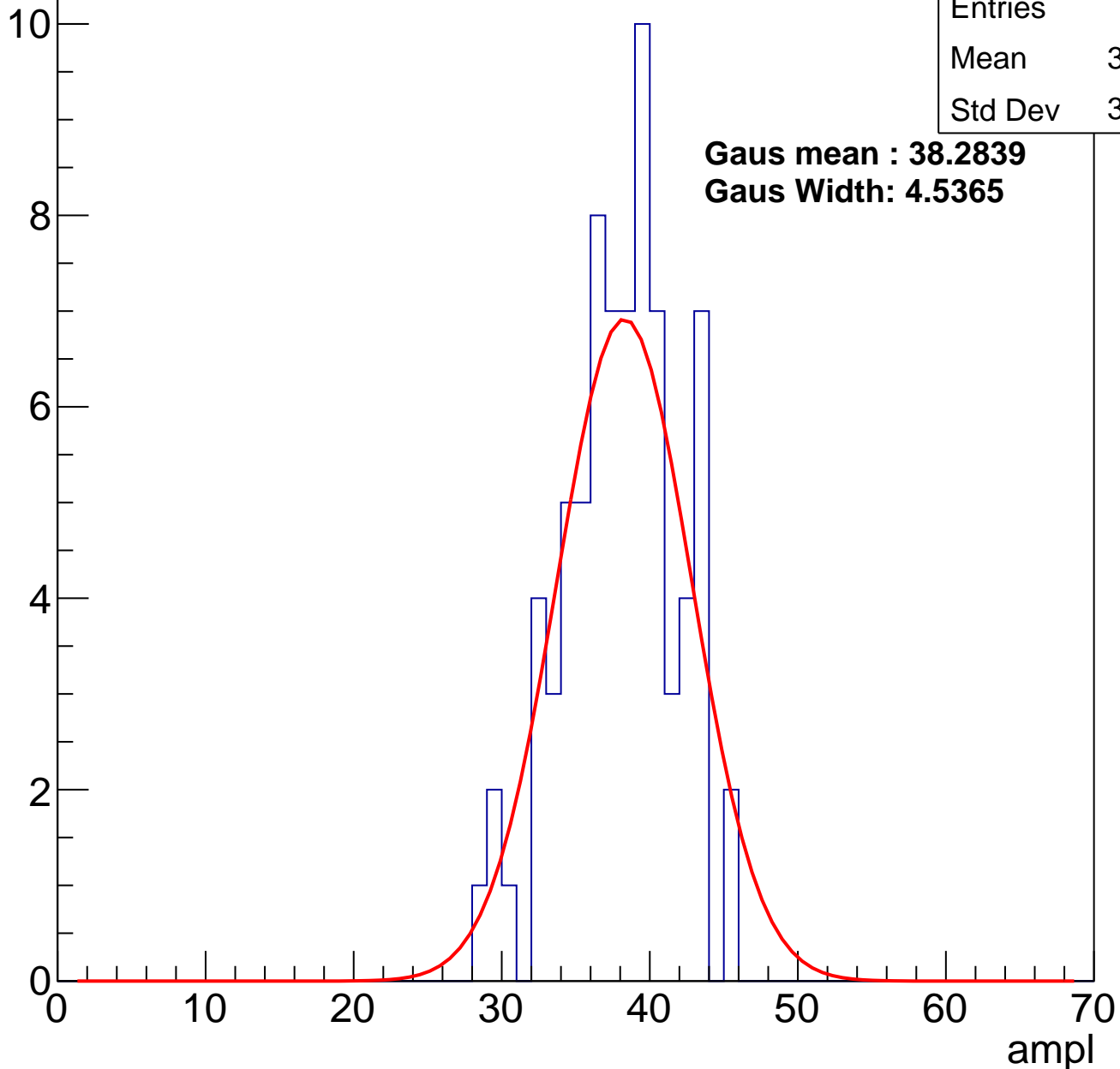
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	37.54
Std Dev	3.796

Gaus mean : 38.2839

Gaus Width: 4.5365

Entry



B1L101S, U12-ch47, adc2

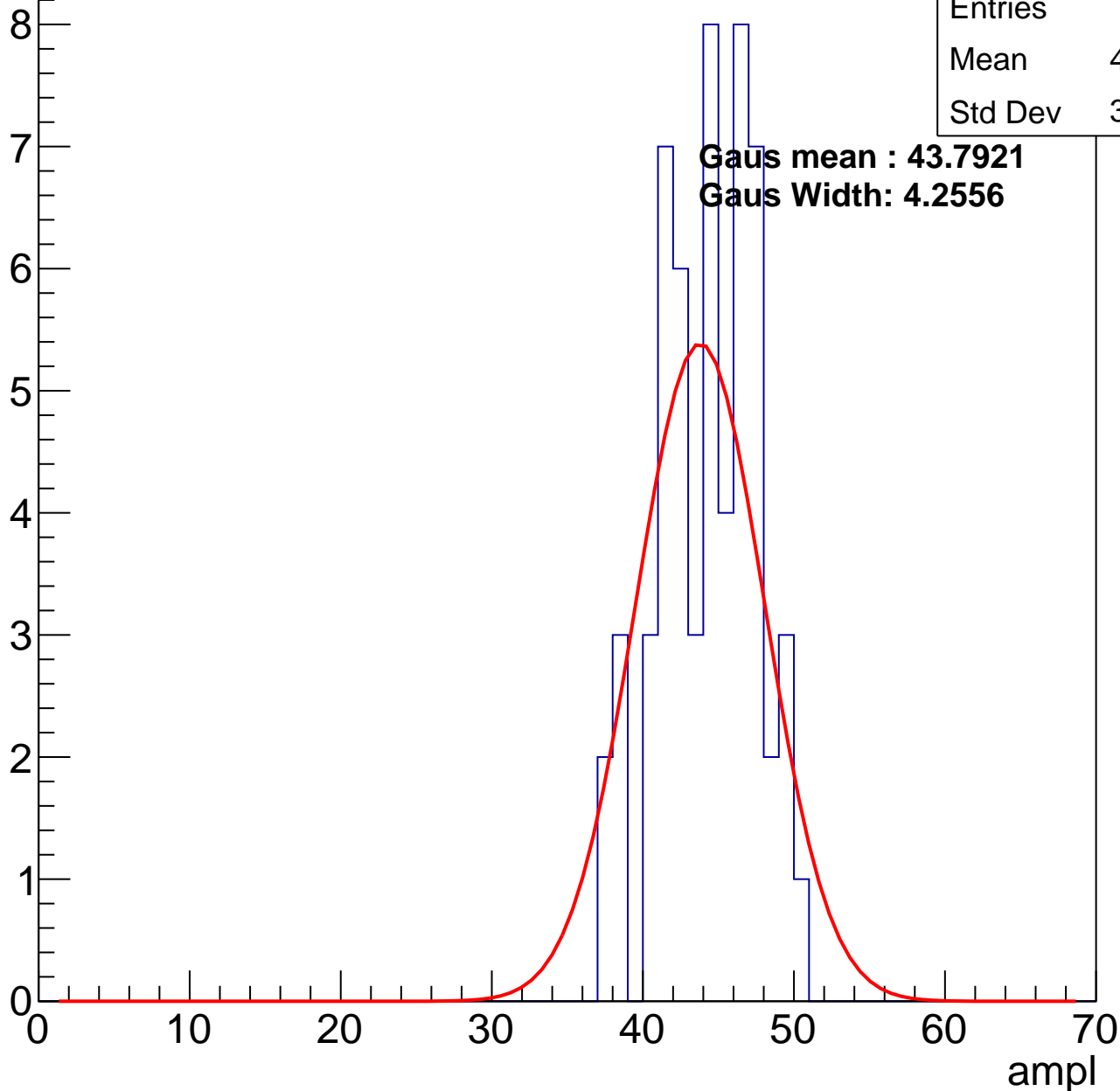
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.82
Std Dev	3.202

Gaus mean : 43.7921

Gaus Width: 4.2556

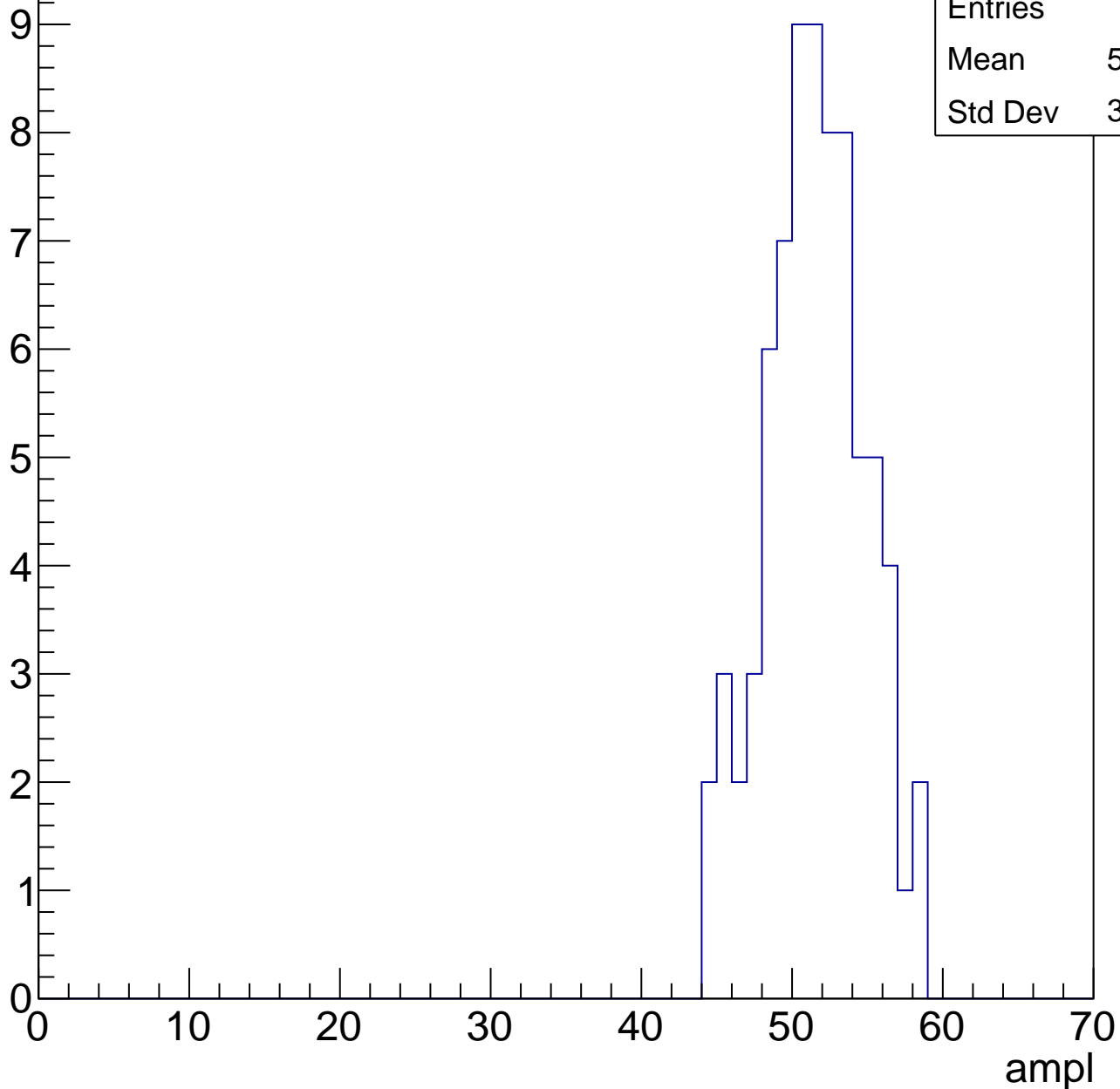


B1L101S, U12-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	51.05
Std Dev	3.275

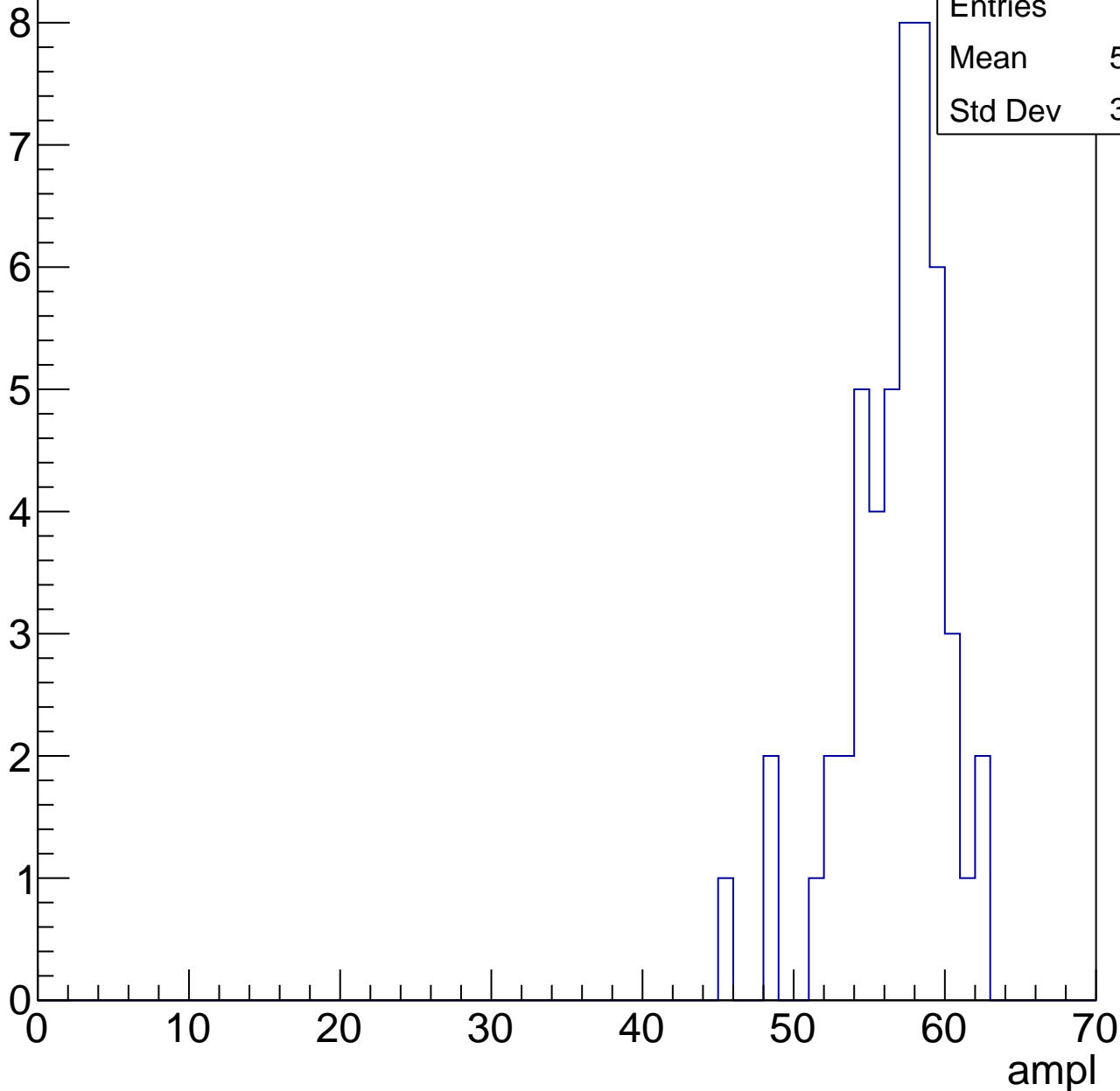


B1L101S, U12-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	56.22
Std Dev	3.419



B1L101S, U12-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	59.41
Std Dev	9.579

ampl

0

10

20

30

40

50

60

70

B1L101S, U12-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch48, adc0

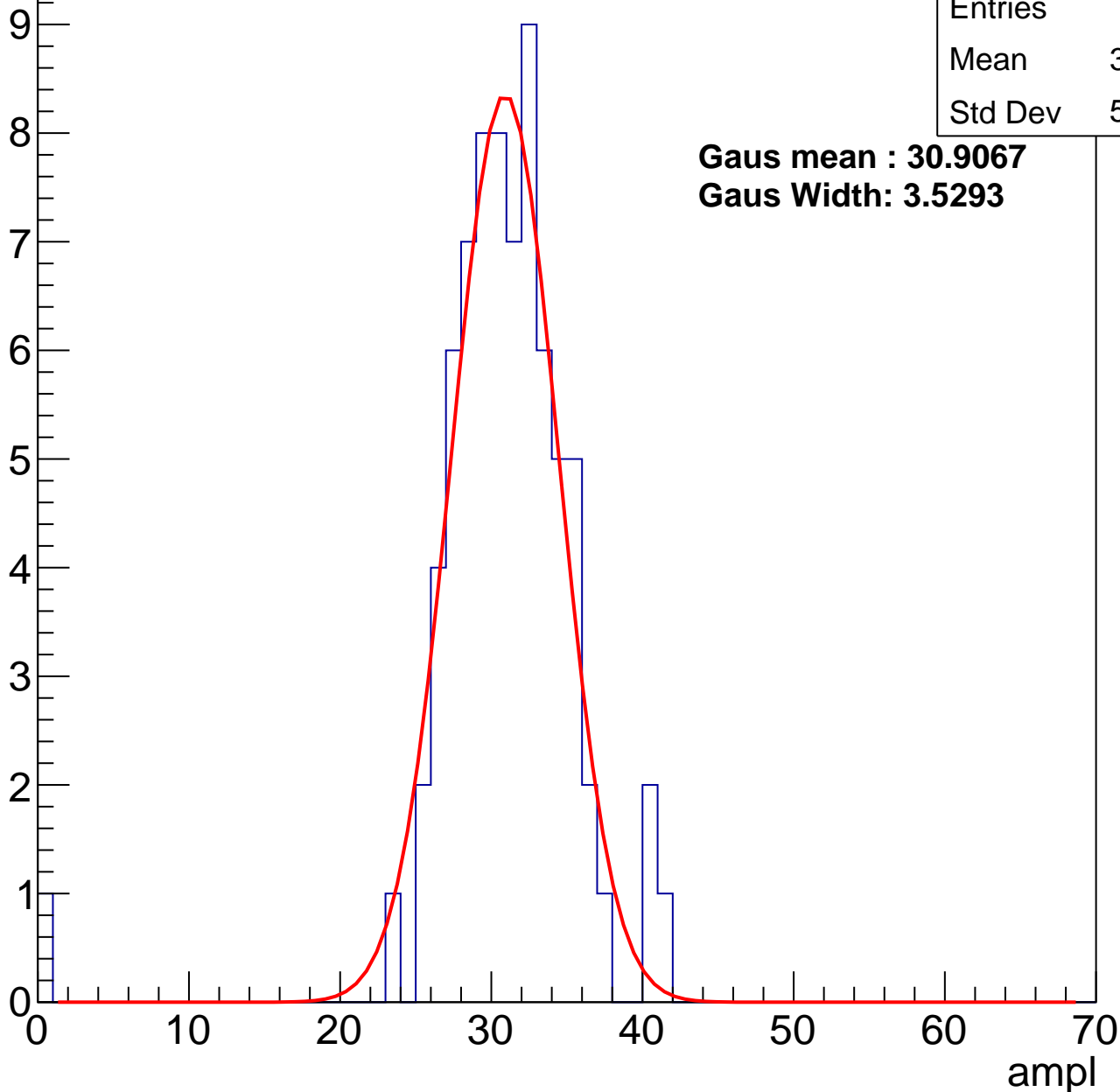
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.47
Std Dev	5.008

Gaus mean : 30.9067

Gaus Width: 3.5293



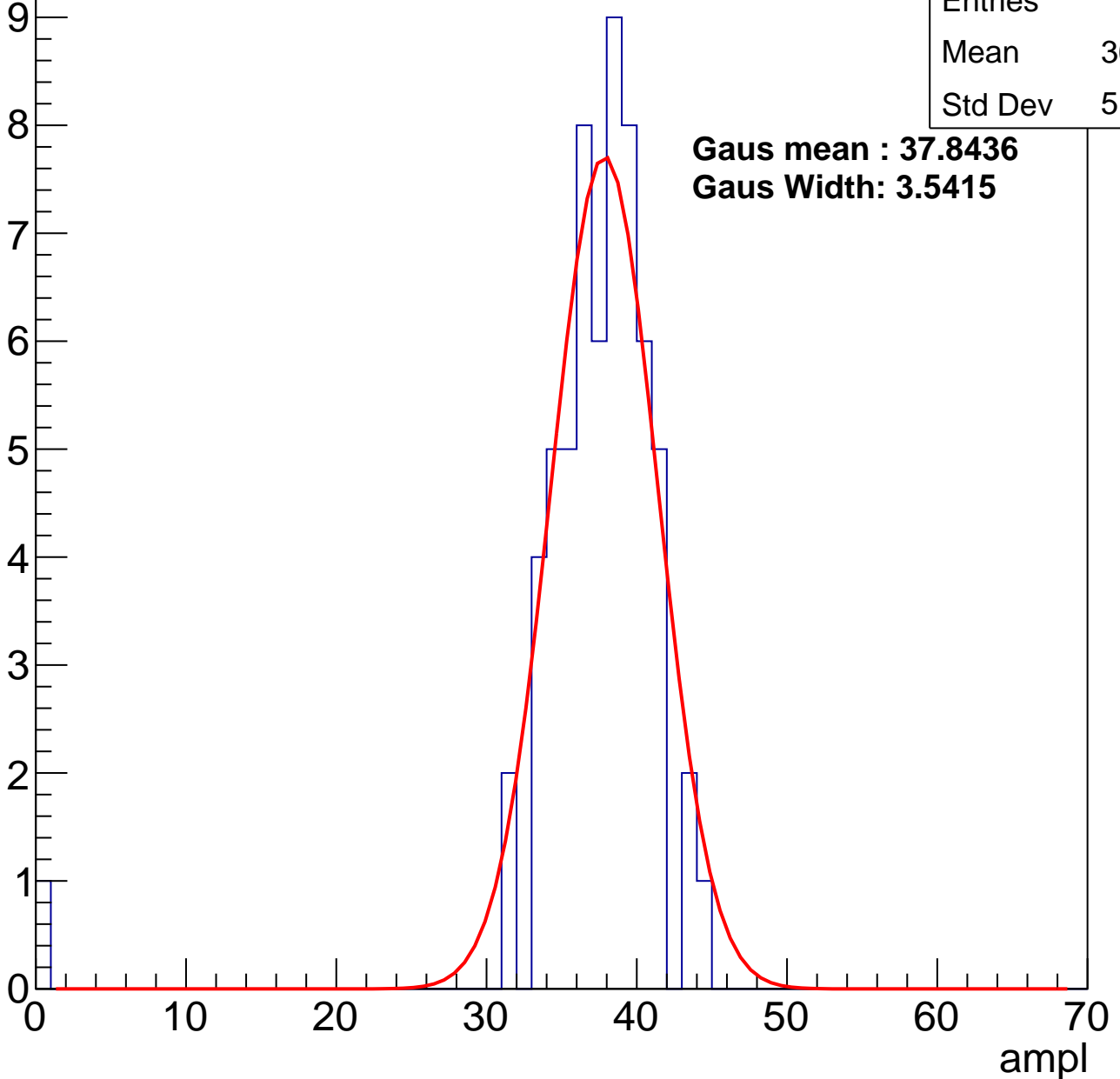
B1L101S, U12-ch48, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.74
Std Dev	5.492

Gaus mean : 37.8436
Gaus Width: 3.5415



B1L101S, U12-ch48, adc2

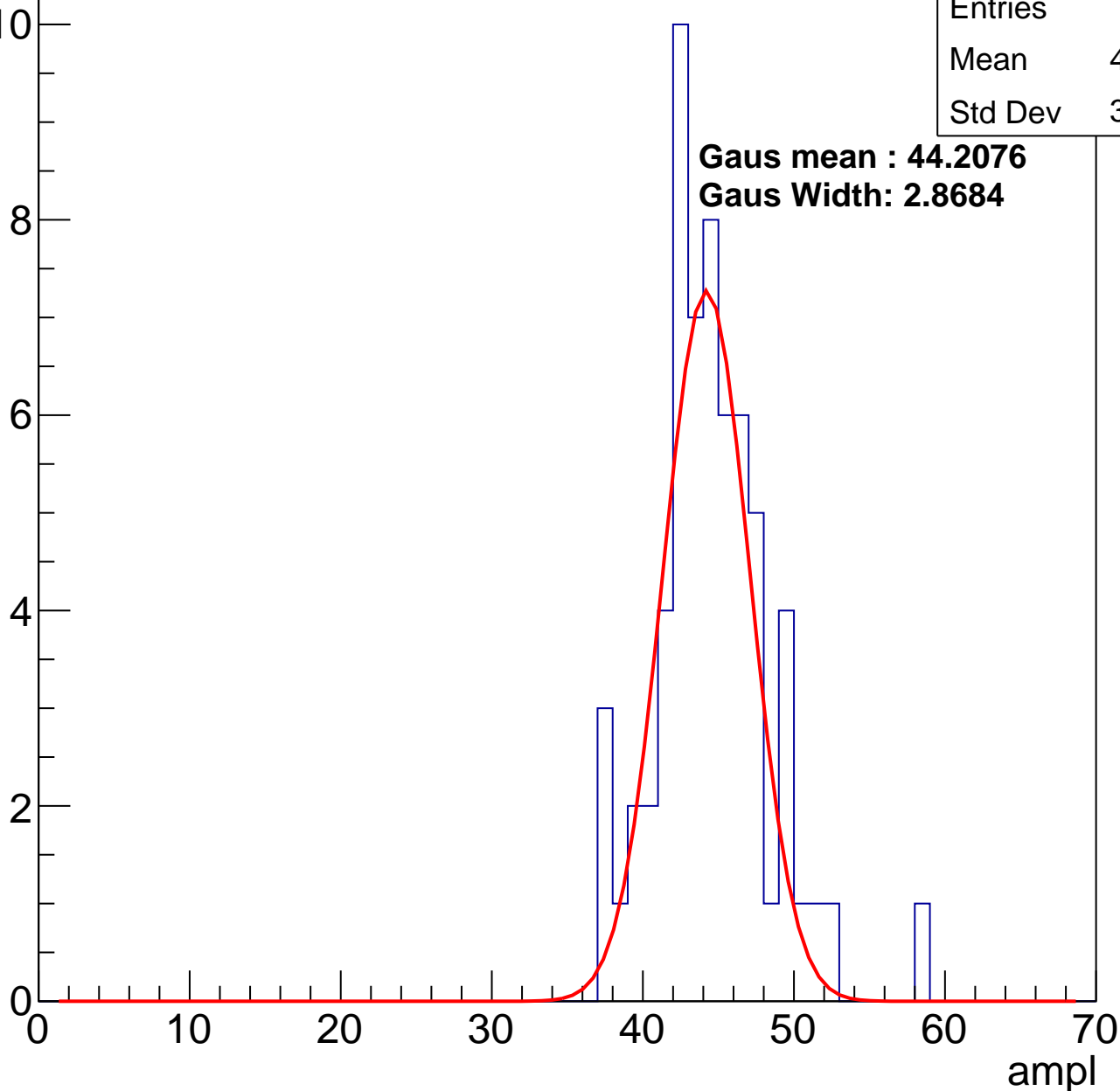
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.13
Std Dev	3.744

Gaus mean : 44.2076

Gaus Width: 2.8684

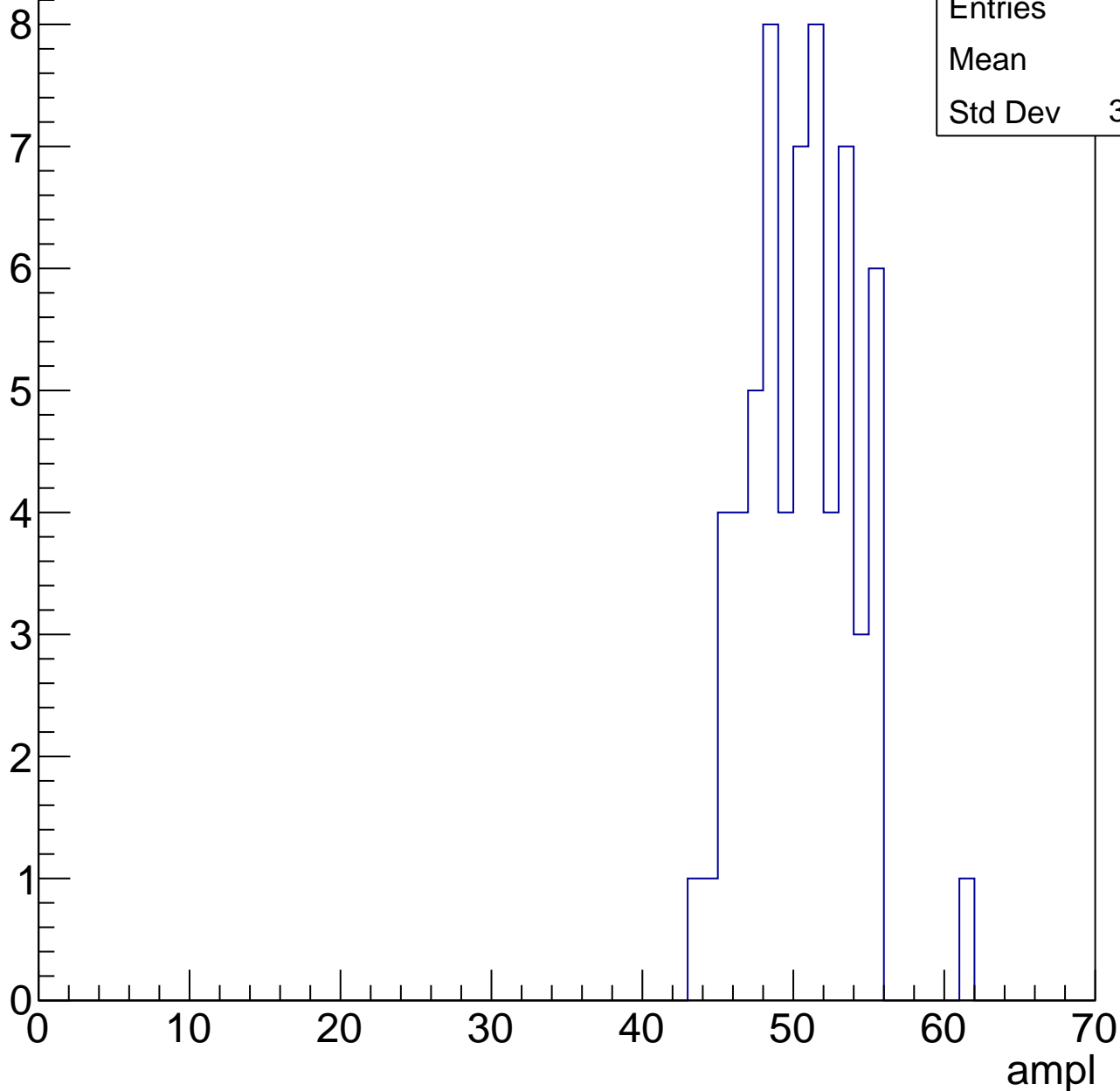


B1L101S, U12-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.1
Std Dev	3.417

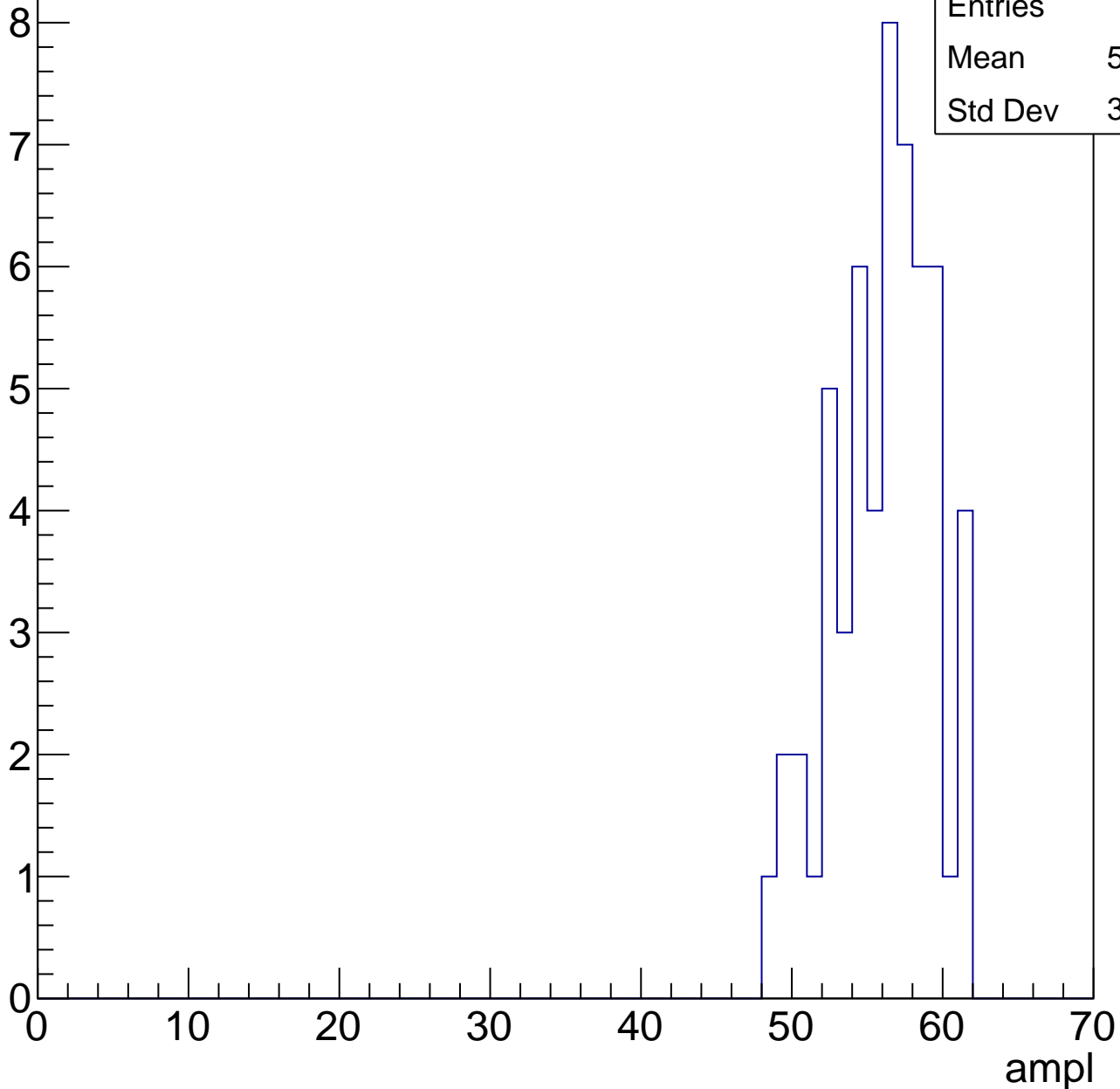


B1L101S, U12-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.59
Std Dev	3.234

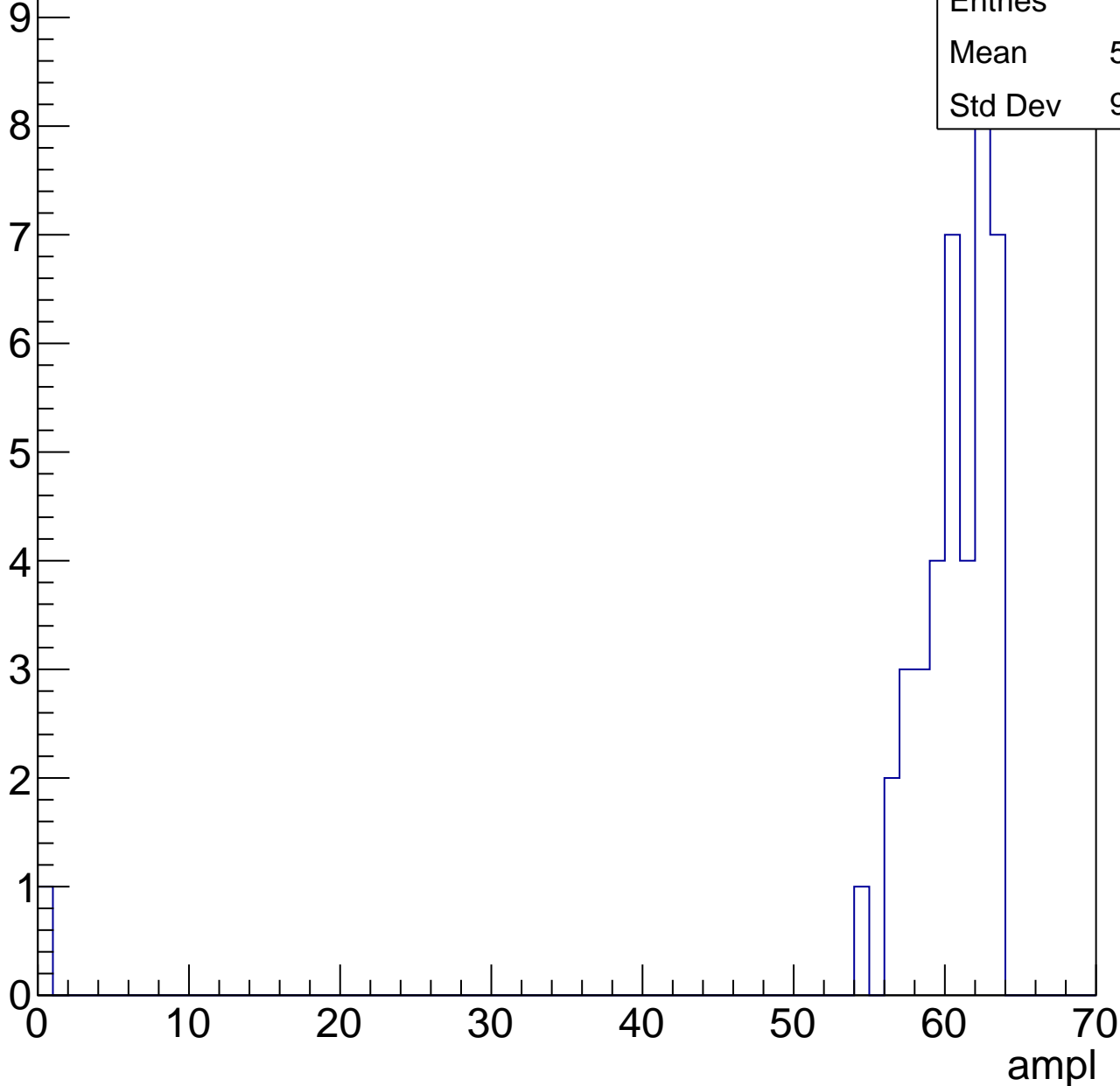


B1L101S, U12-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

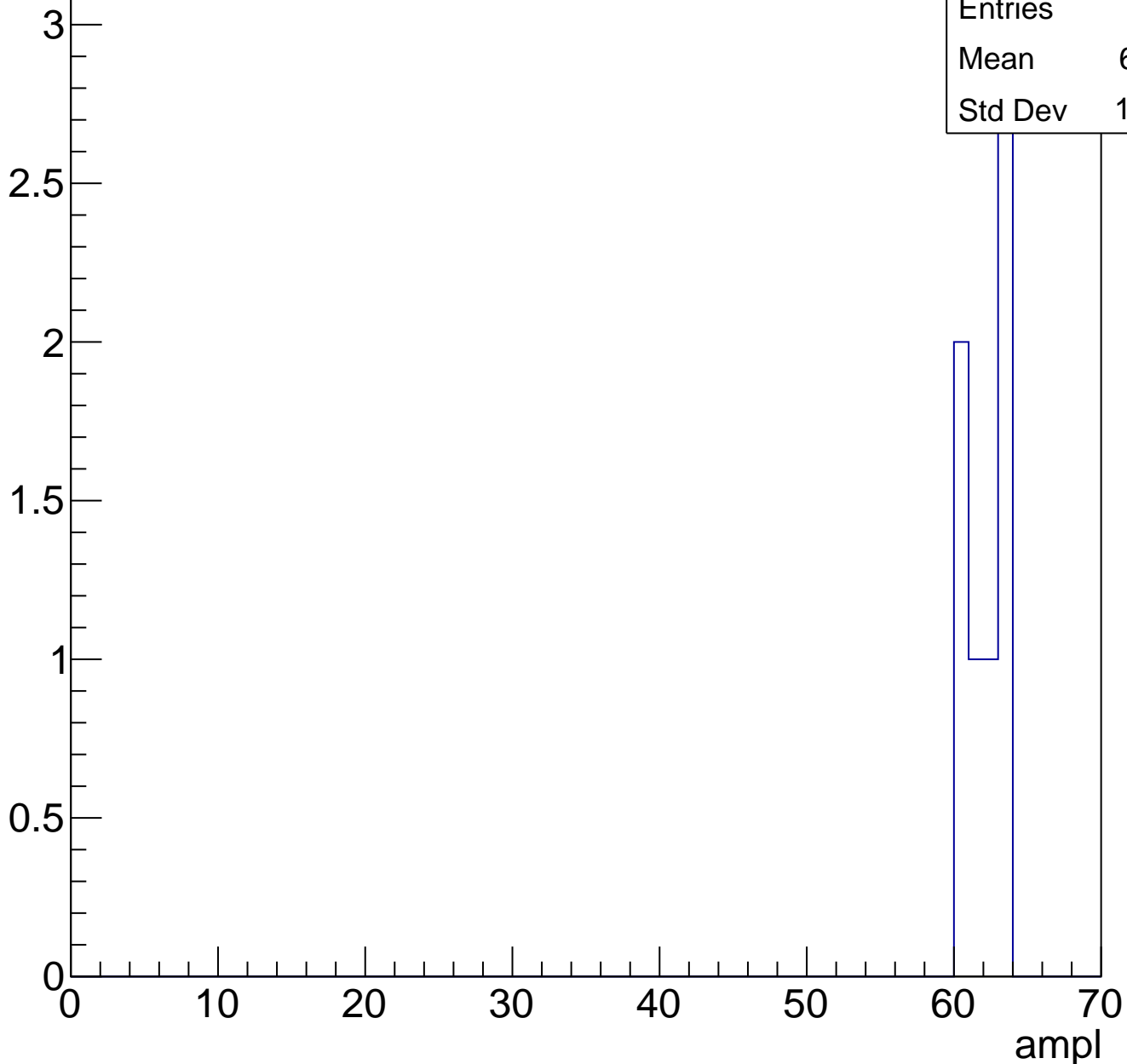
Entries	41
Mean	58.78
Std Dev	9.568



B1L101S, U12-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U12-ch49, adc0

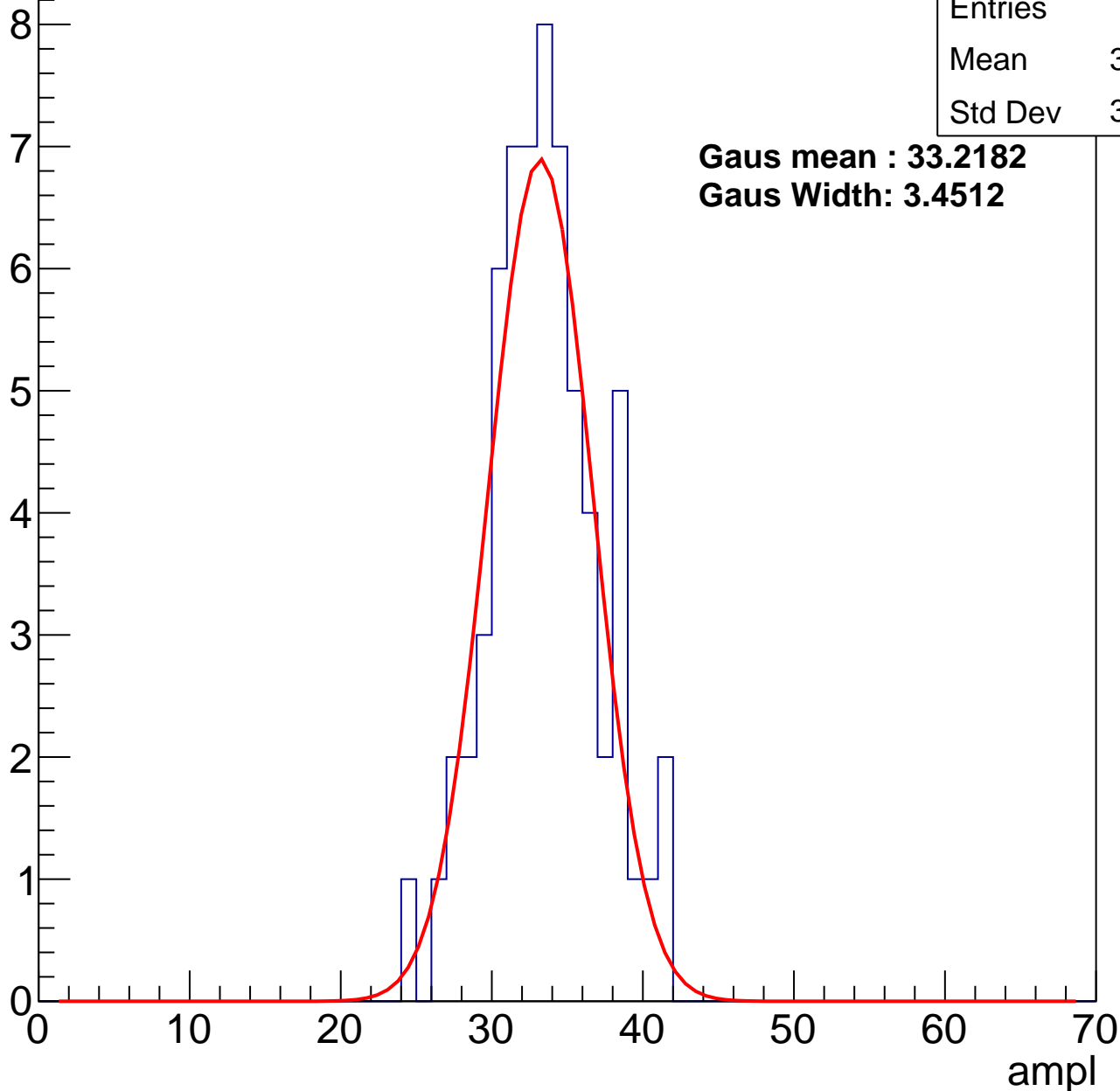
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	33.03
Std Dev	3.584

Gaus mean : 33.2182

Gaus Width: 3.4512



B1L101S, U12-ch49, adc1

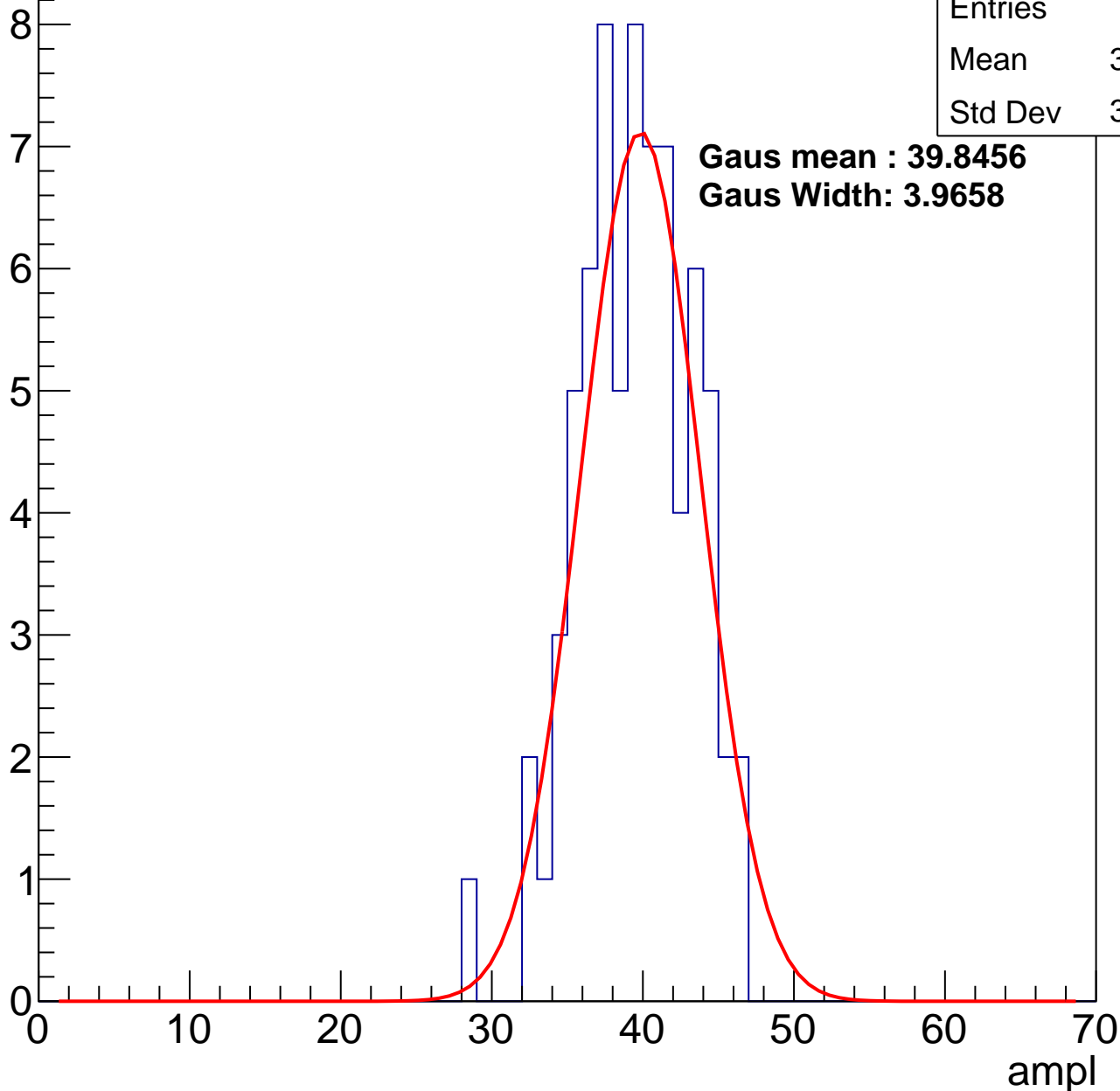
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	39.04
Std Dev	3.657

Gaus mean : 39.8456

Gaus Width: 3.9658



B1L101S, U12-ch49, adc2

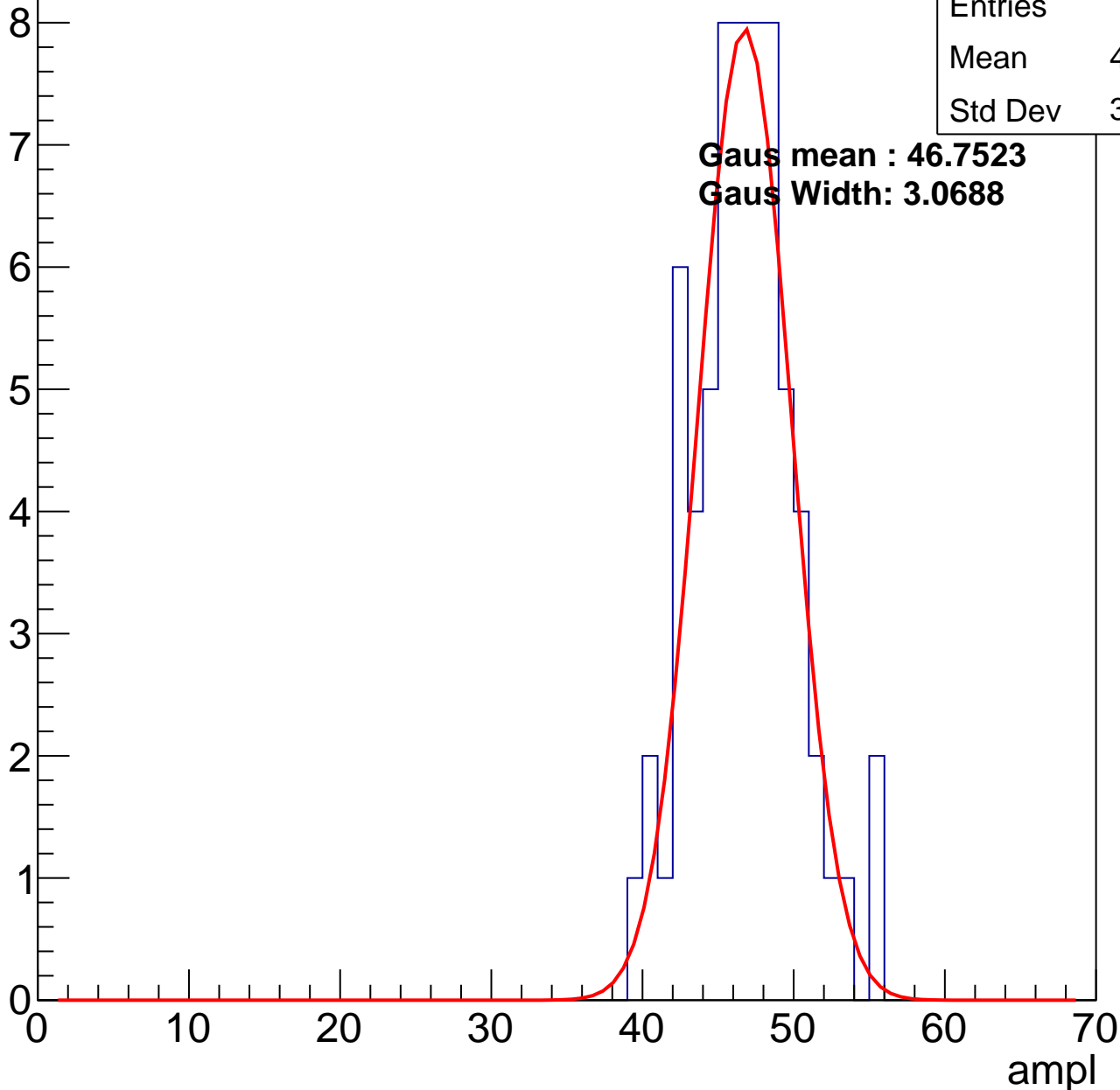
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	46.27
Std Dev	3.364

Gaus mean : 46.7523

Gaus Width: 3.0688

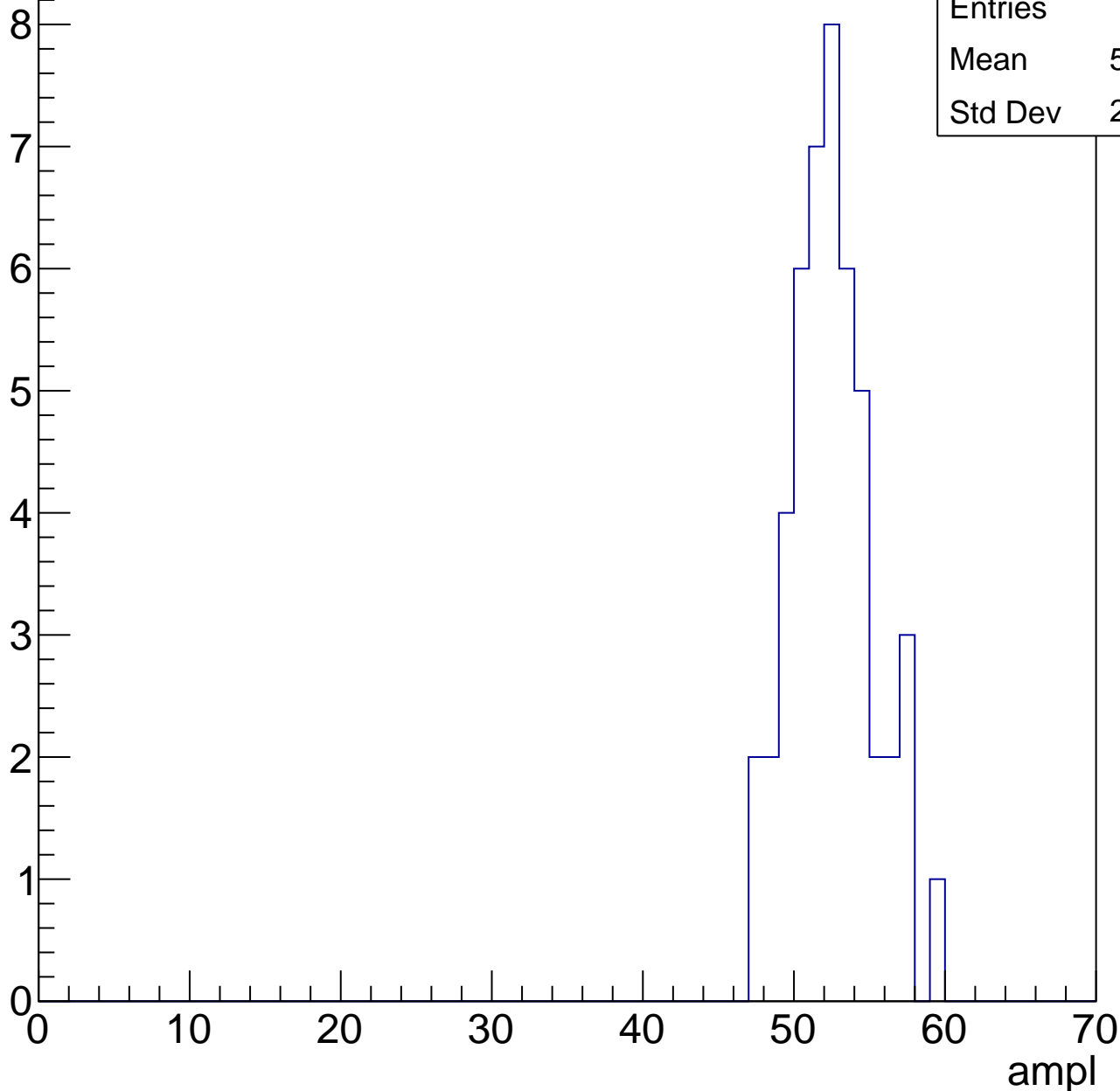


B1L101S, U12-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

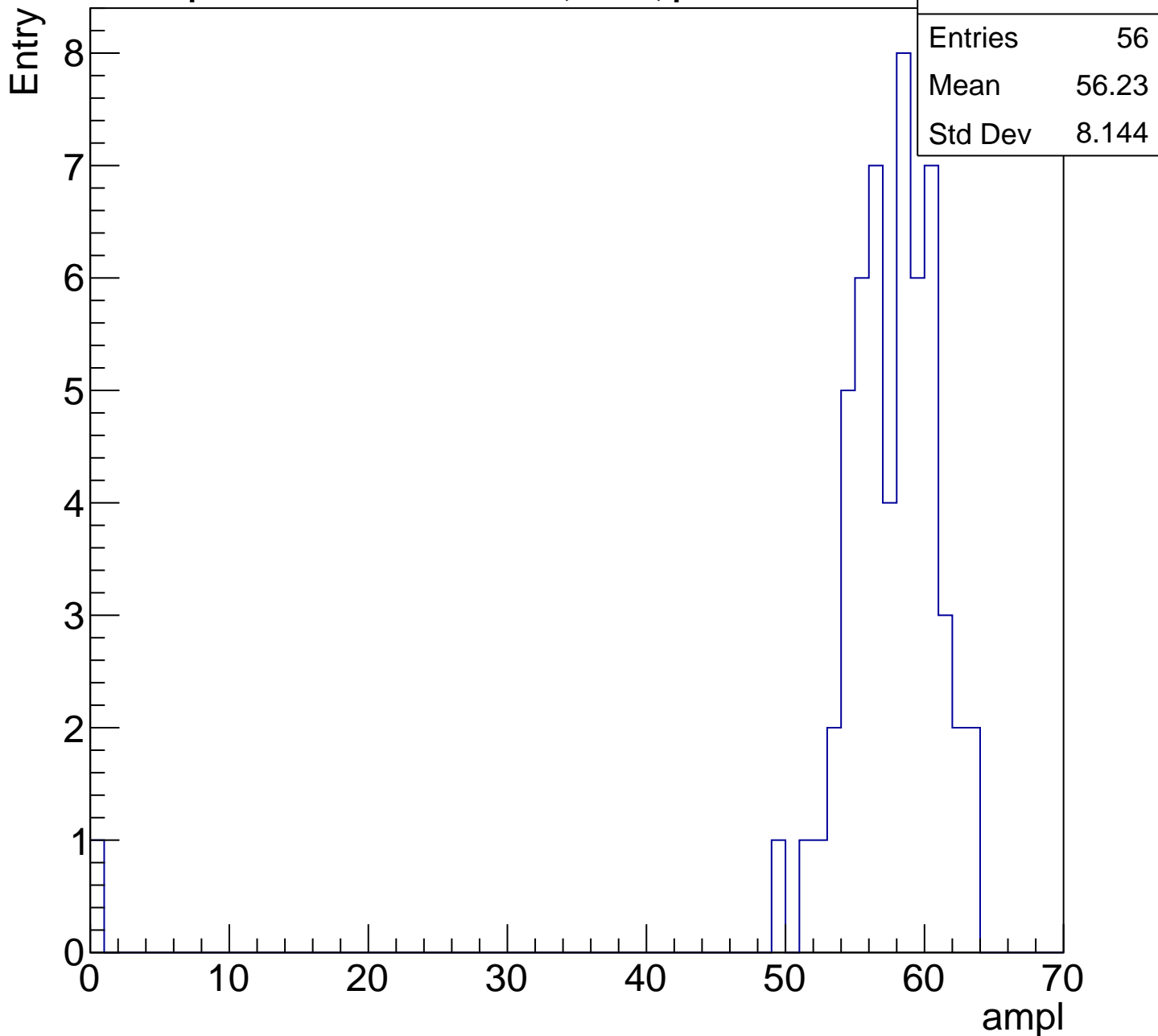
Entry

Entries	48
Mean	52.06
Std Dev	2.696



B1L101S, U12-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

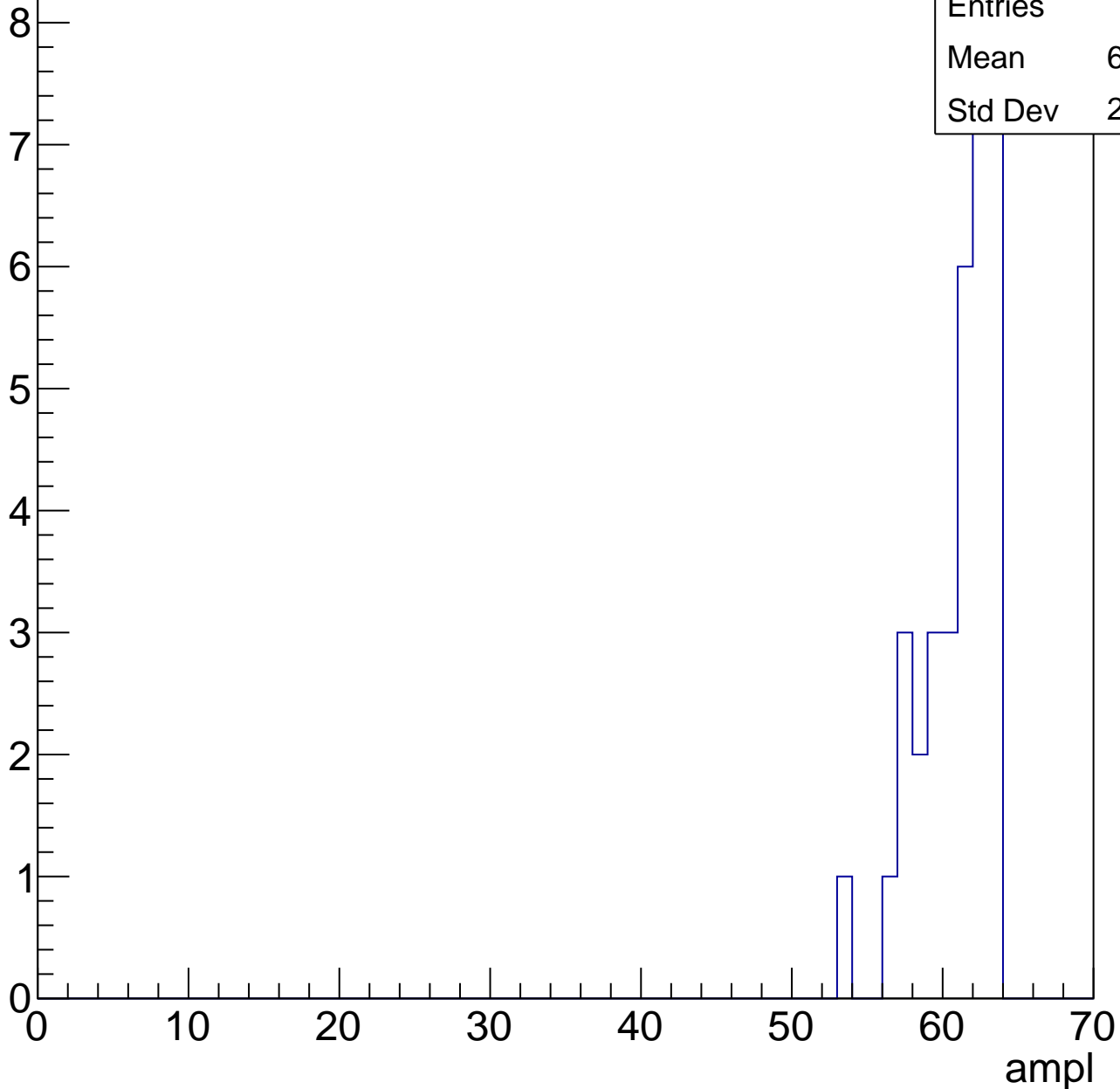


B1L101S, U12-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.54
Std Dev	2.406



B1L101S, U12-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch50, adc0

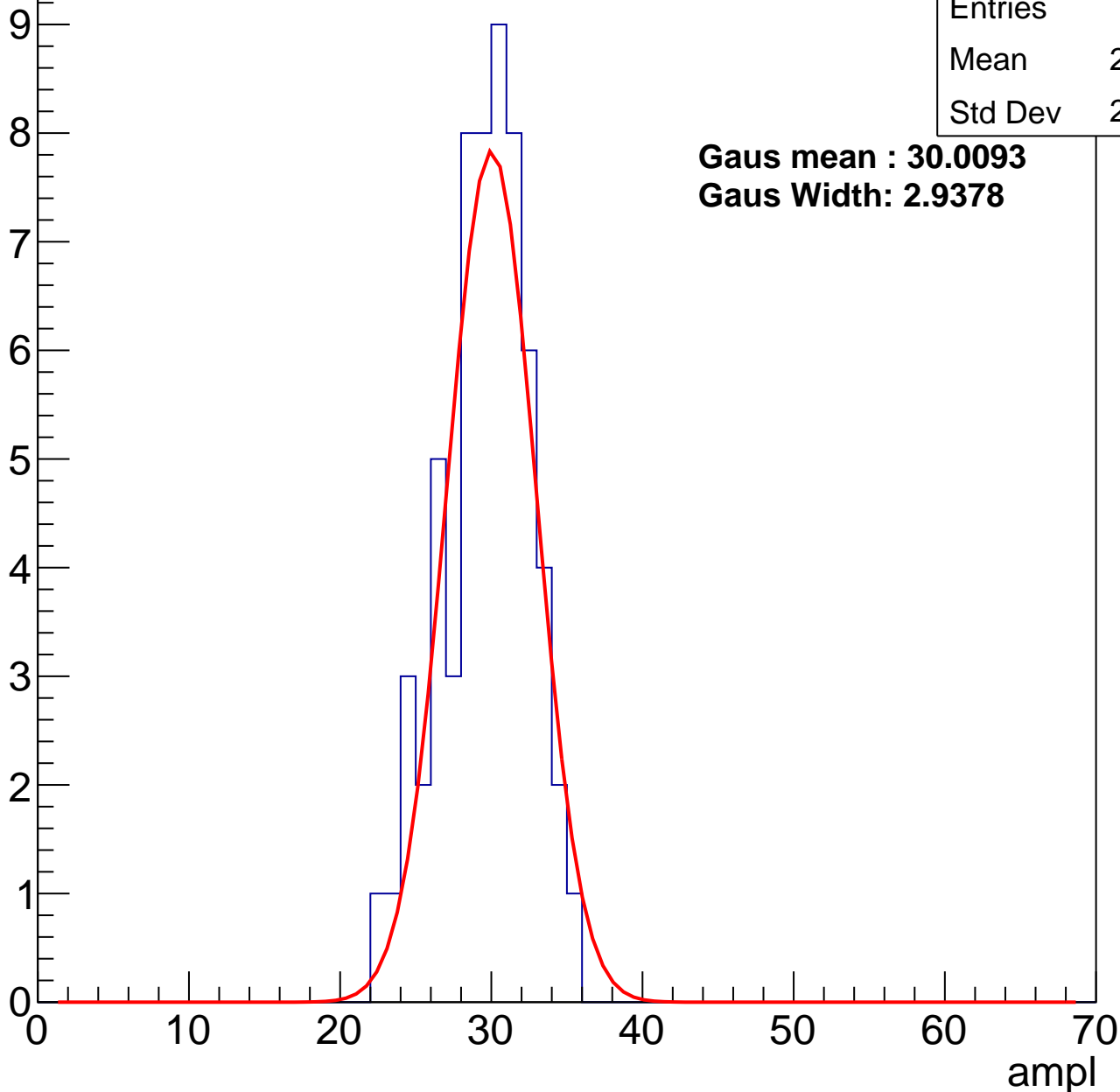
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.16
Std Dev	2.864

Gaus mean : 30.0093

Gaus Width: 2.9378



B1L101S, U12-ch50, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	36.19
Std Dev	3.045

Gaus mean : 36.7419

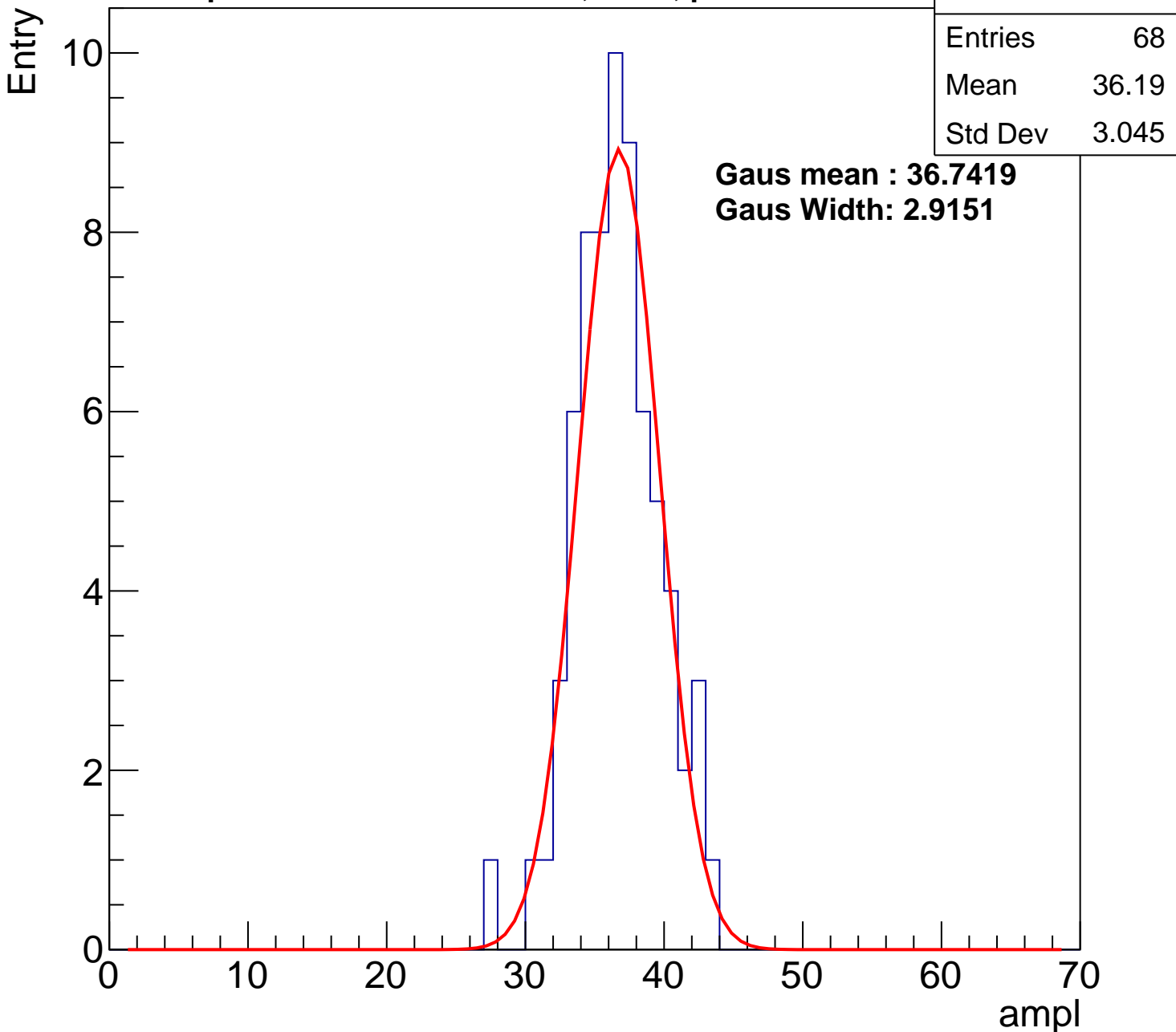
Gaus Width: 2.9151

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch50, adc2

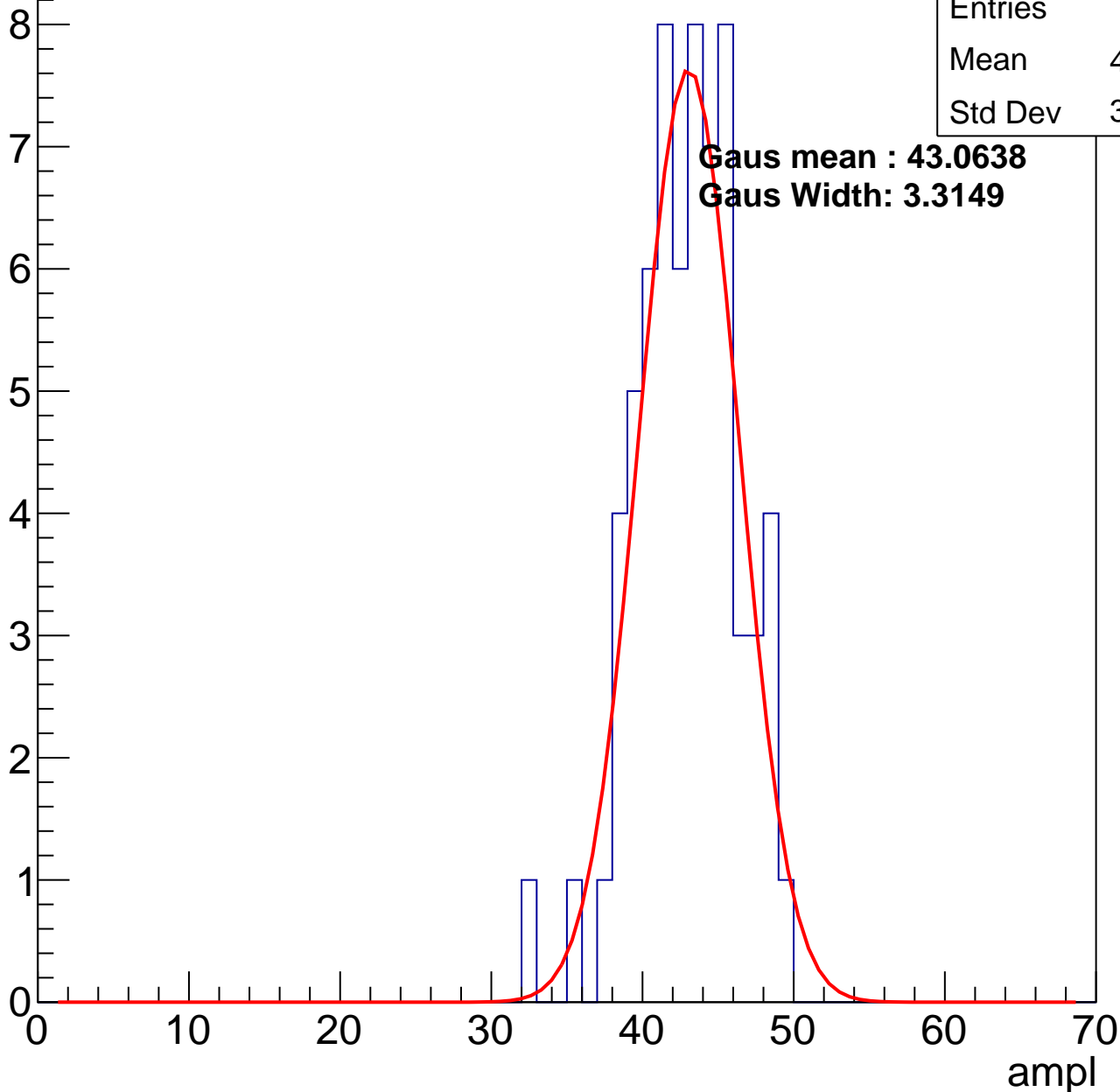
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.47
Std Dev	3.313

Gaus mean : 43.0638

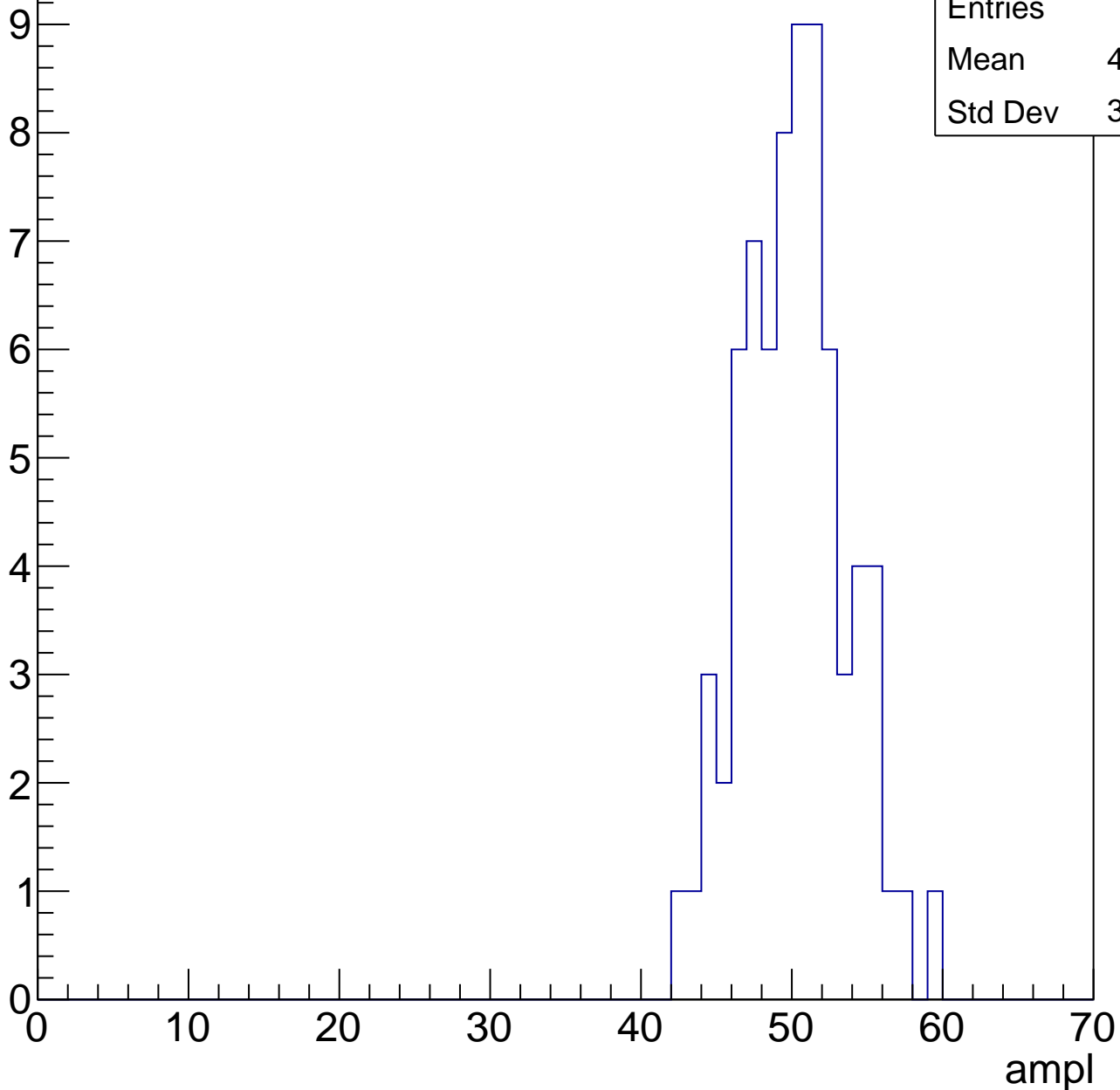
Gaus Width: 3.3149



B1L101S, U12-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

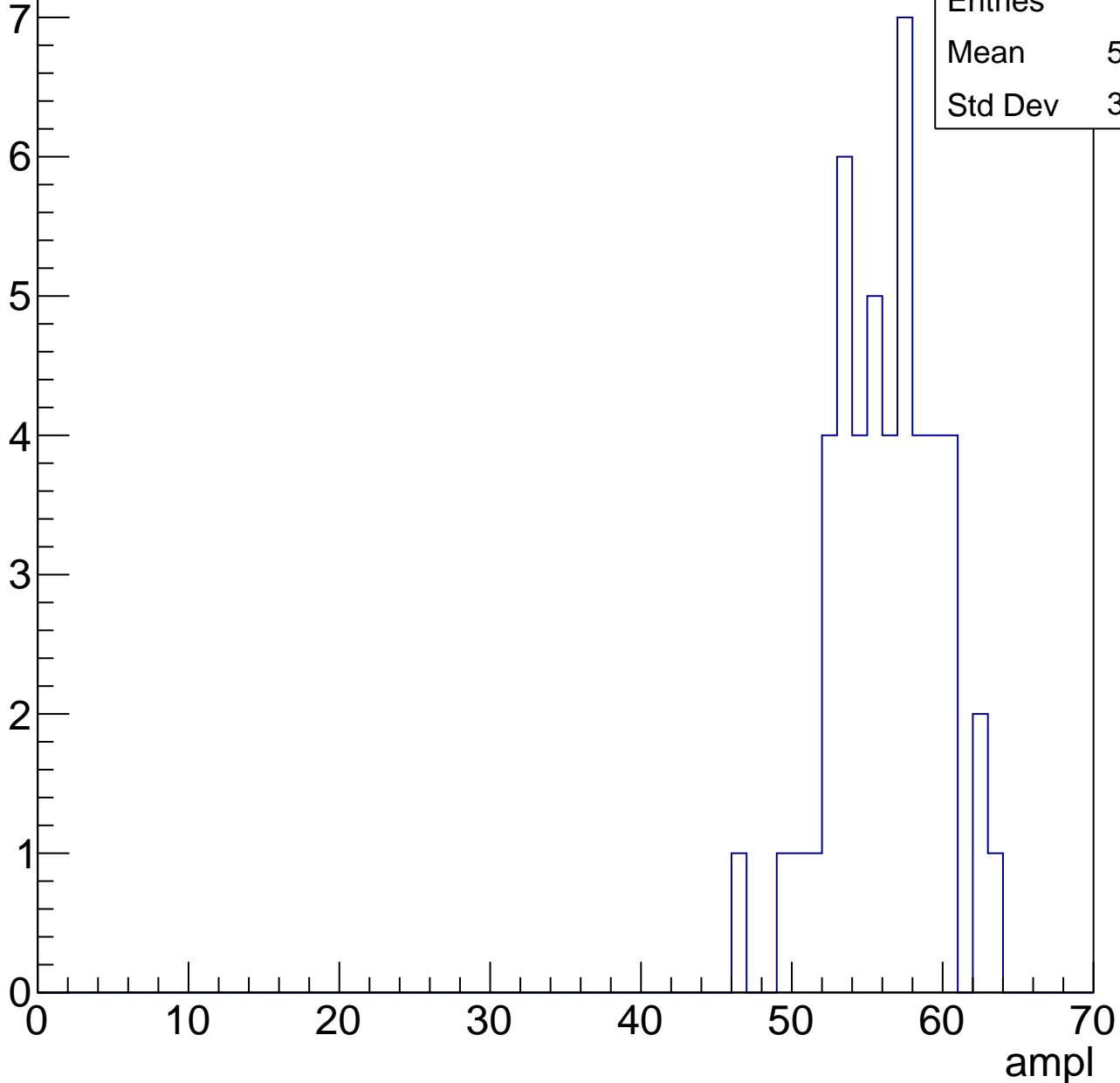


B1L101S, U12-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.73
Std Dev	3.469

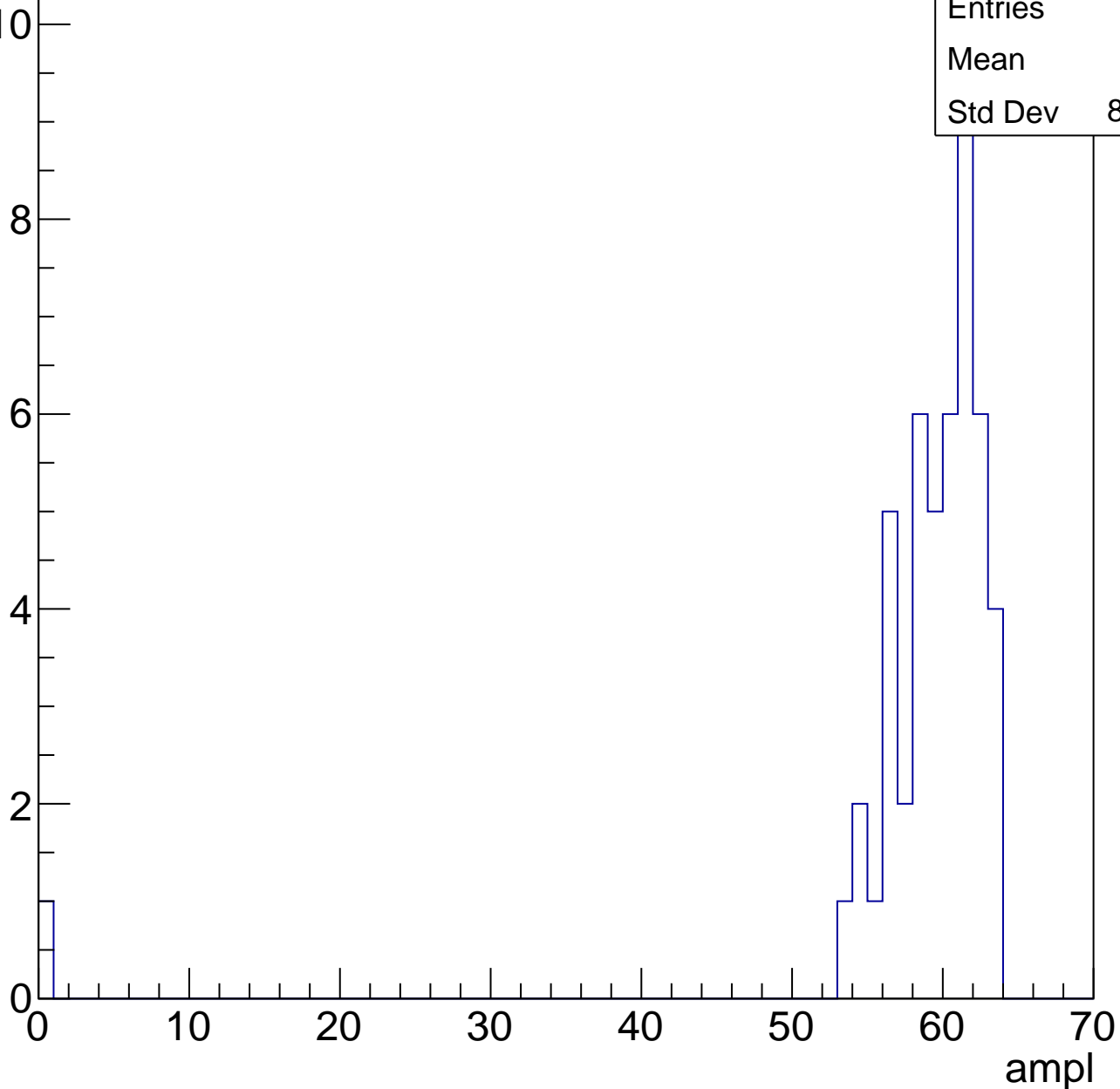


B1L101S, U12-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

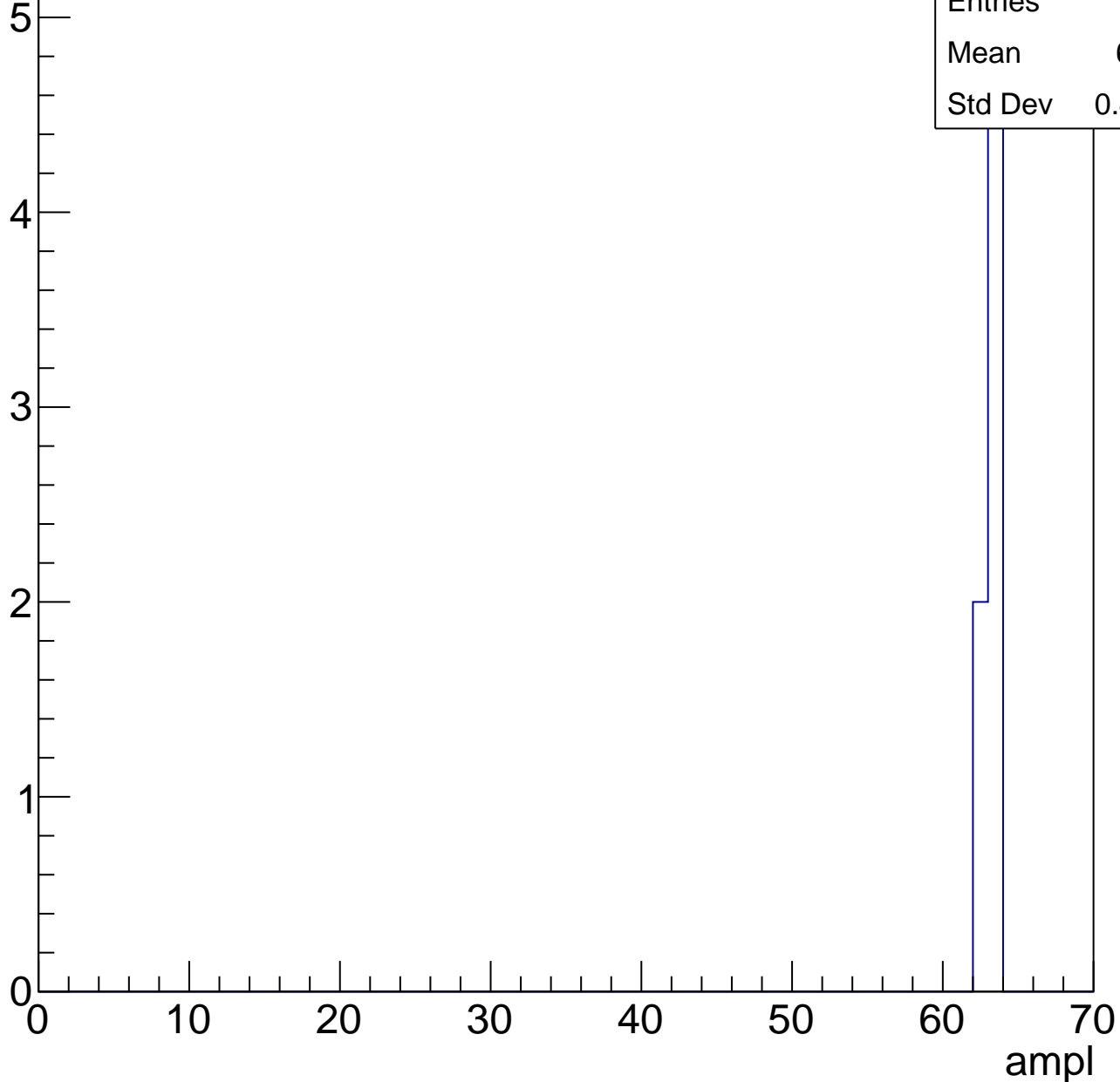
Entries	49
Mean	58.1
Std Dev	8.767



B1L101S, U12-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch51, adc0

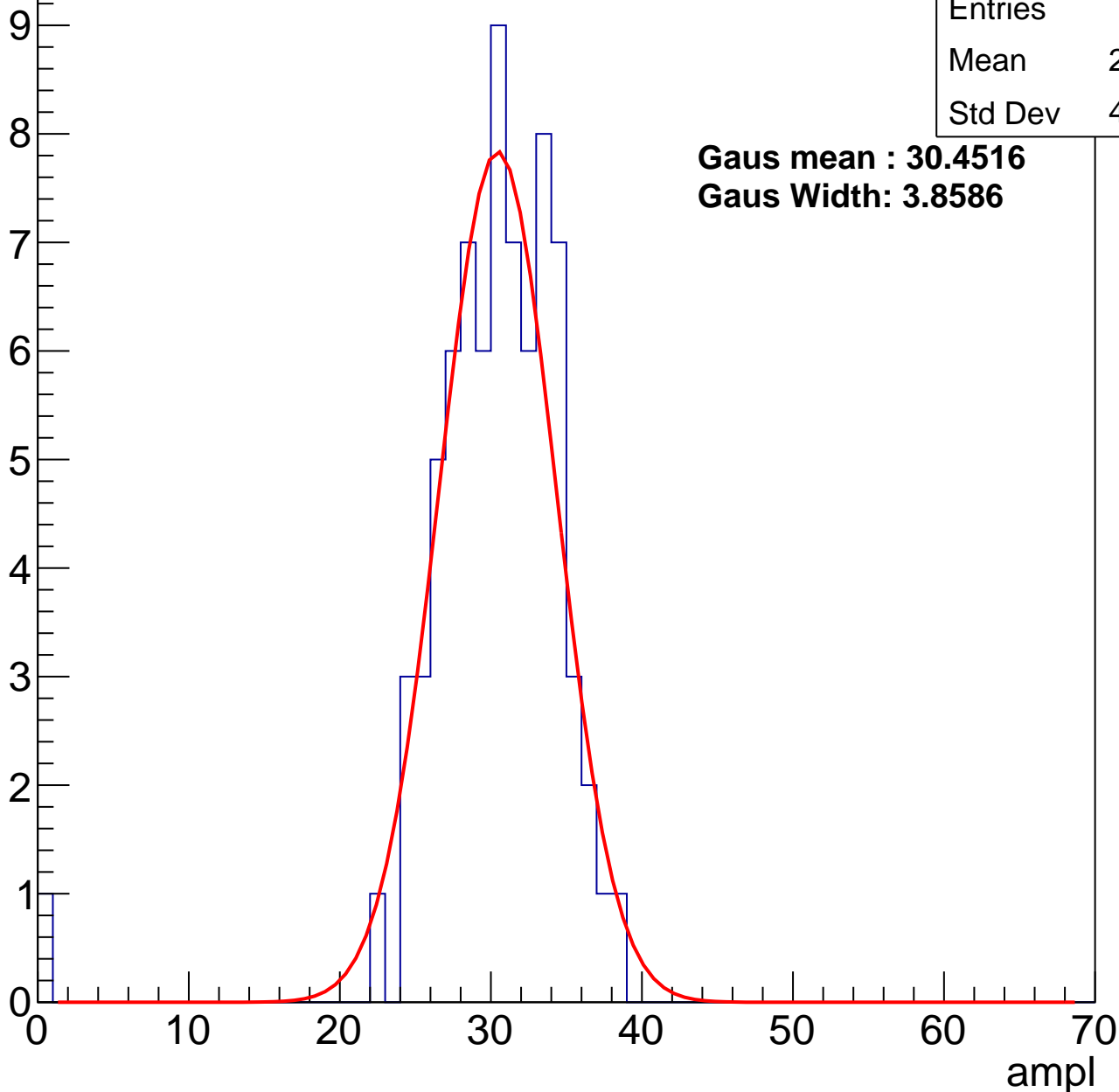
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.79
Std Dev	4.849

Gaus mean : 30.4516

Gaus Width: 3.8586



B1L101S, U12-ch51, adc1

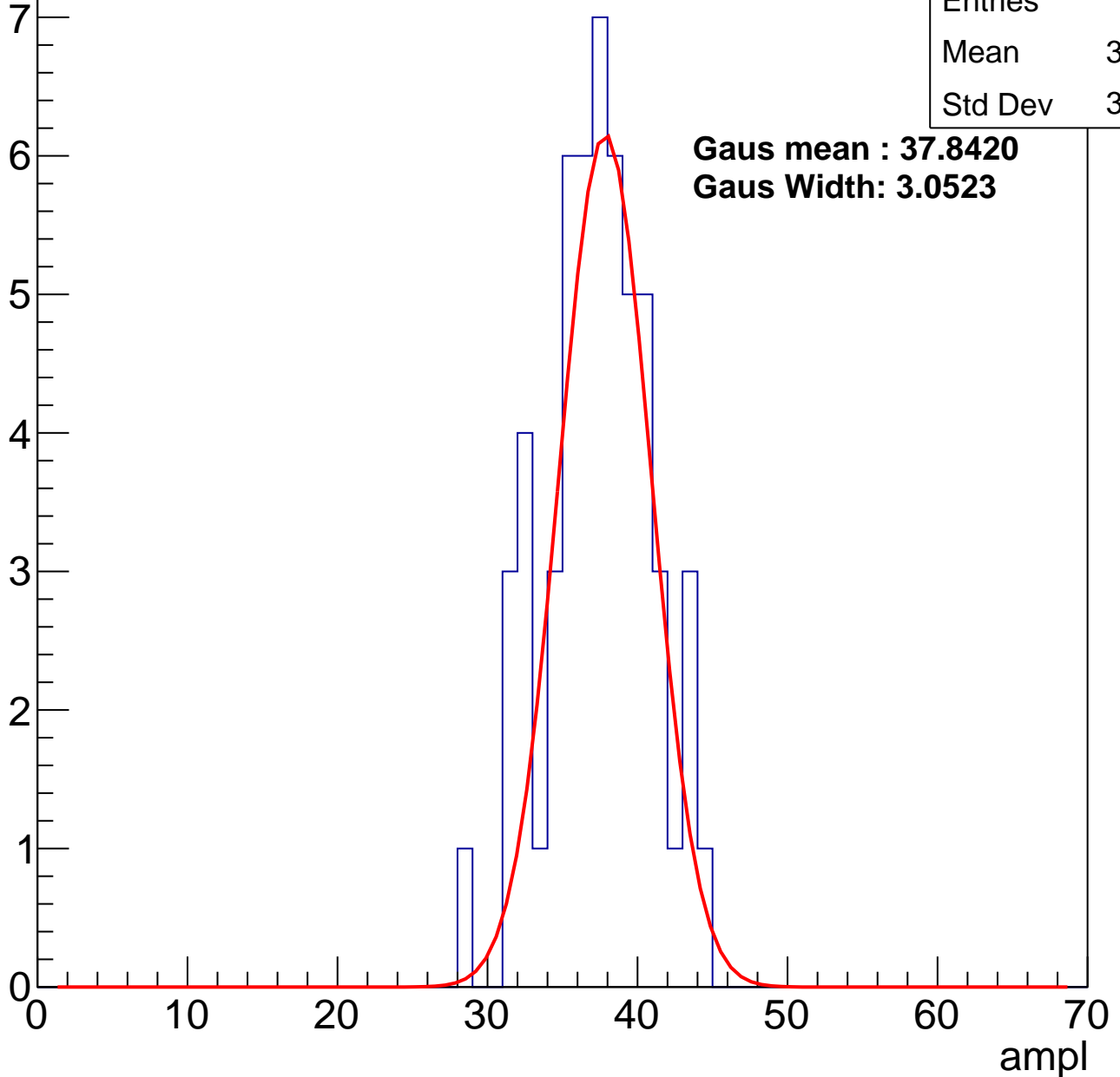
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.91
Std Dev	3.471

Gaus mean : 37.8420

Gaus Width: 3.0523



B1L101S, U12-ch51, adc2

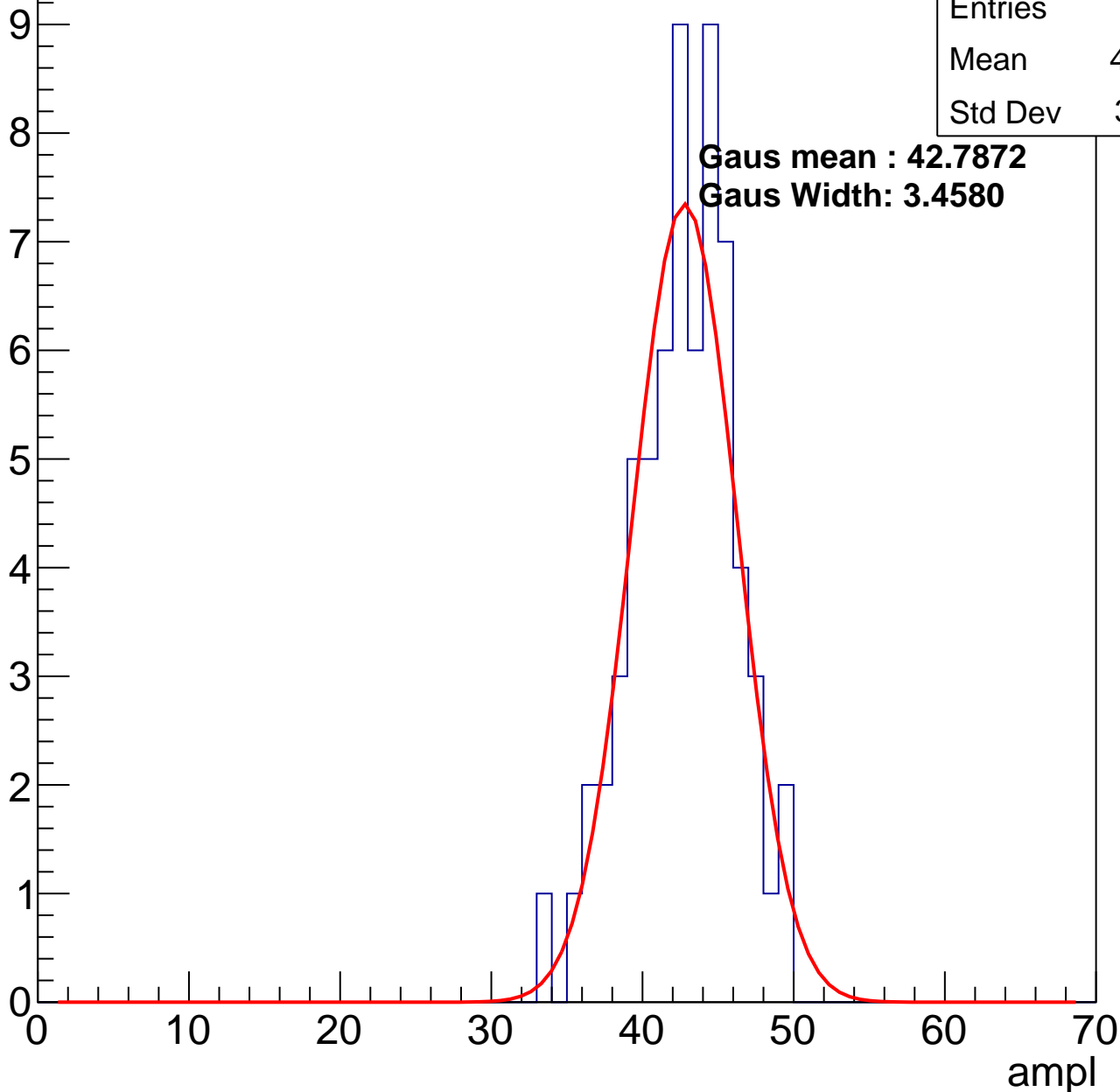
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.23
Std Dev	3.361

Gaus mean : 42.7872

Gaus Width: 3.4580

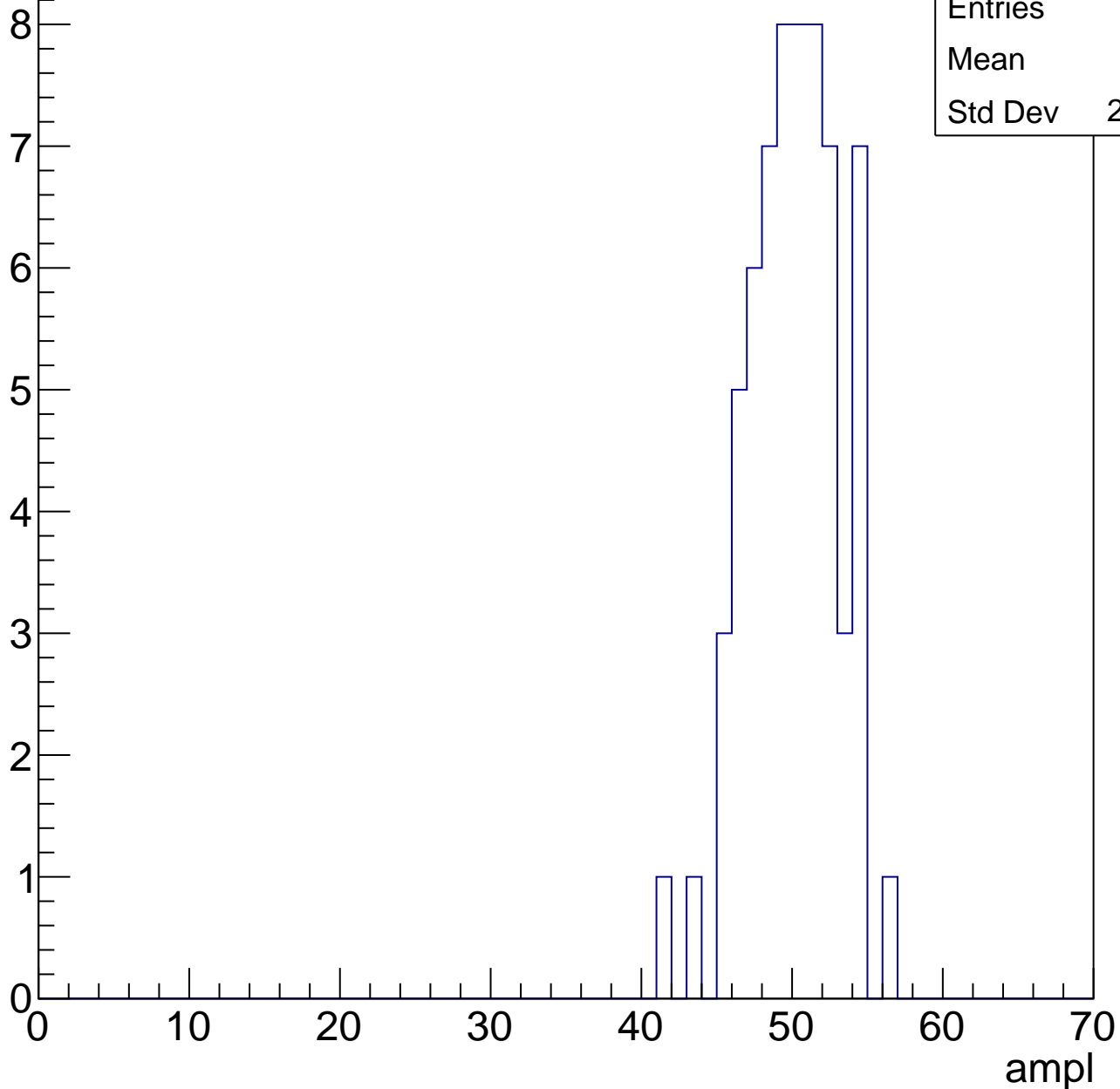


B1L101S, U12-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.6
Std Dev	2.976

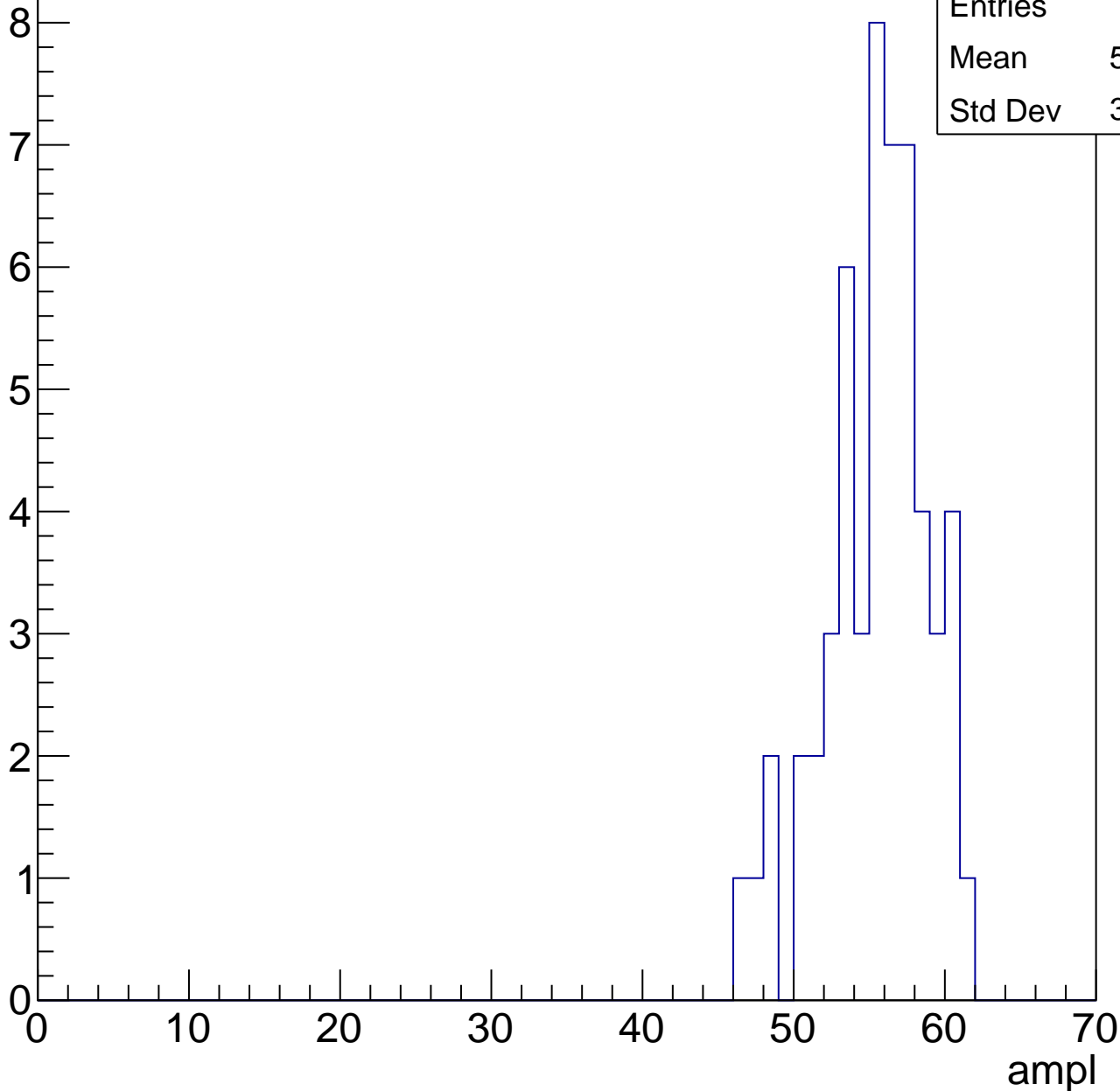


B1L101S, U12-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

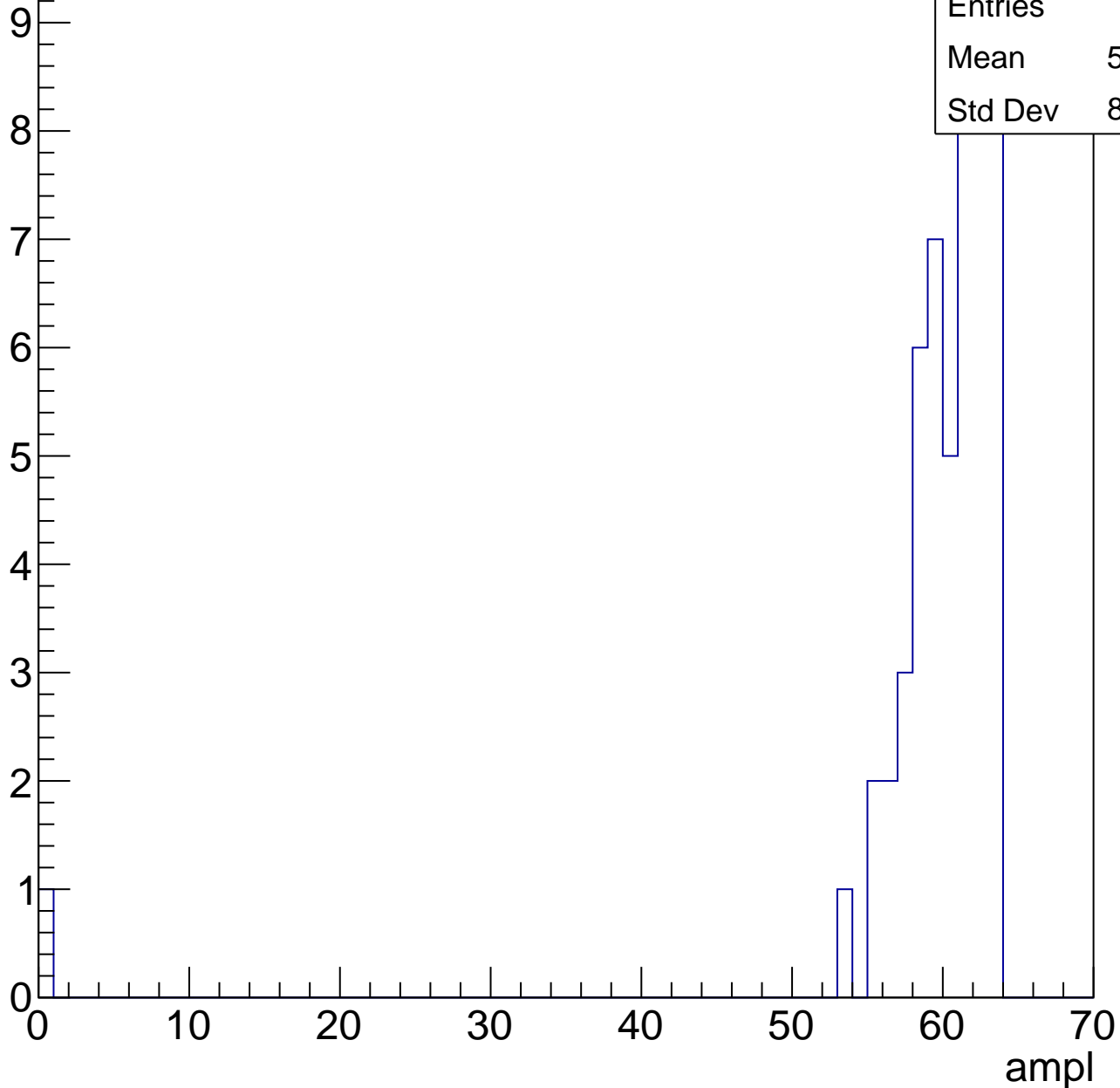
Entries	54
Mean	54.96
Std Dev	3.426



B1L101S, U12-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L101S, U12-ch52, adc0

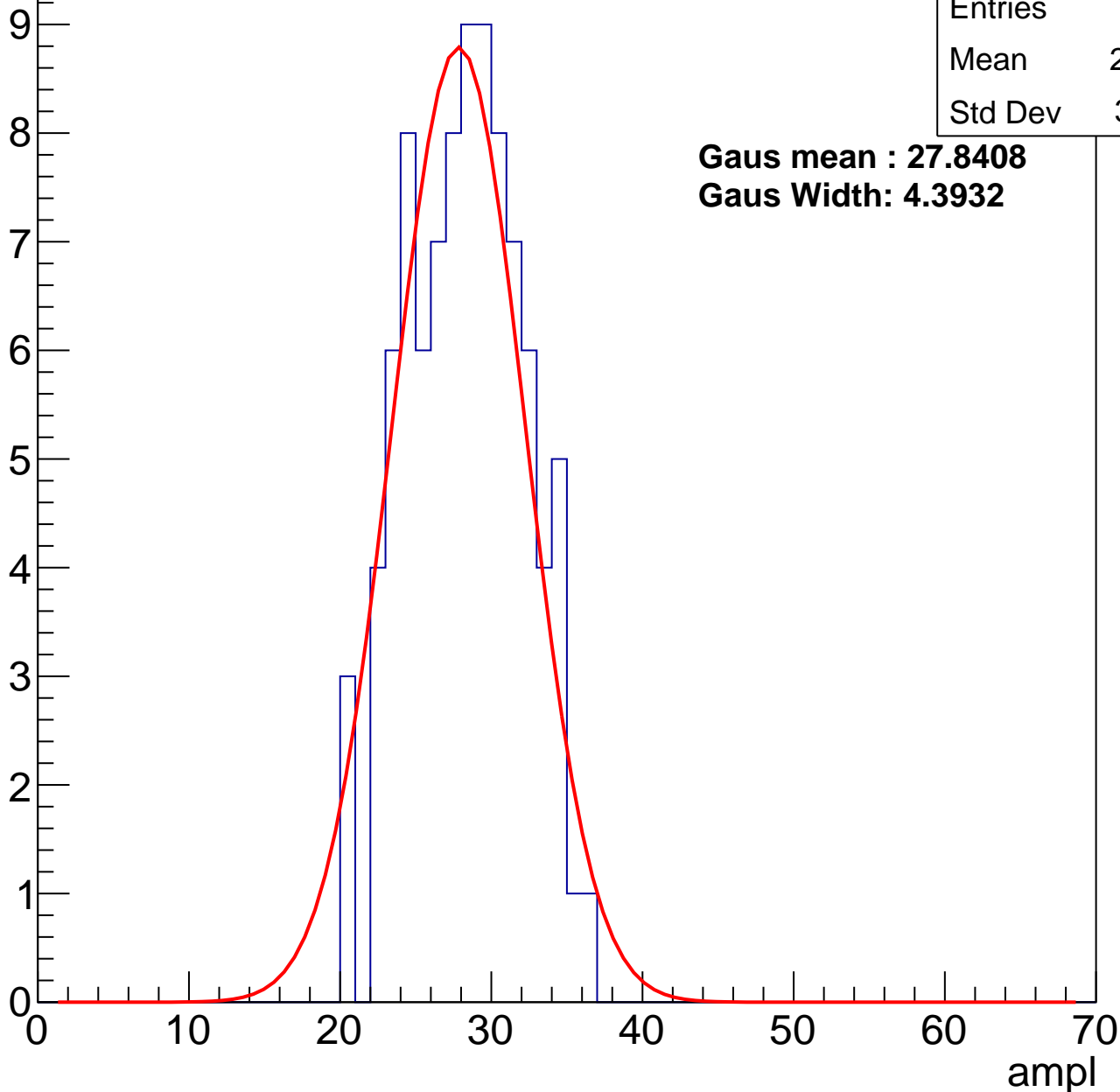
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	92
Mean	27.84
Std Dev	3.751

Gaus mean : 27.8408

Gaus Width: 4.3932



B1L101S, U12-ch52, adc1

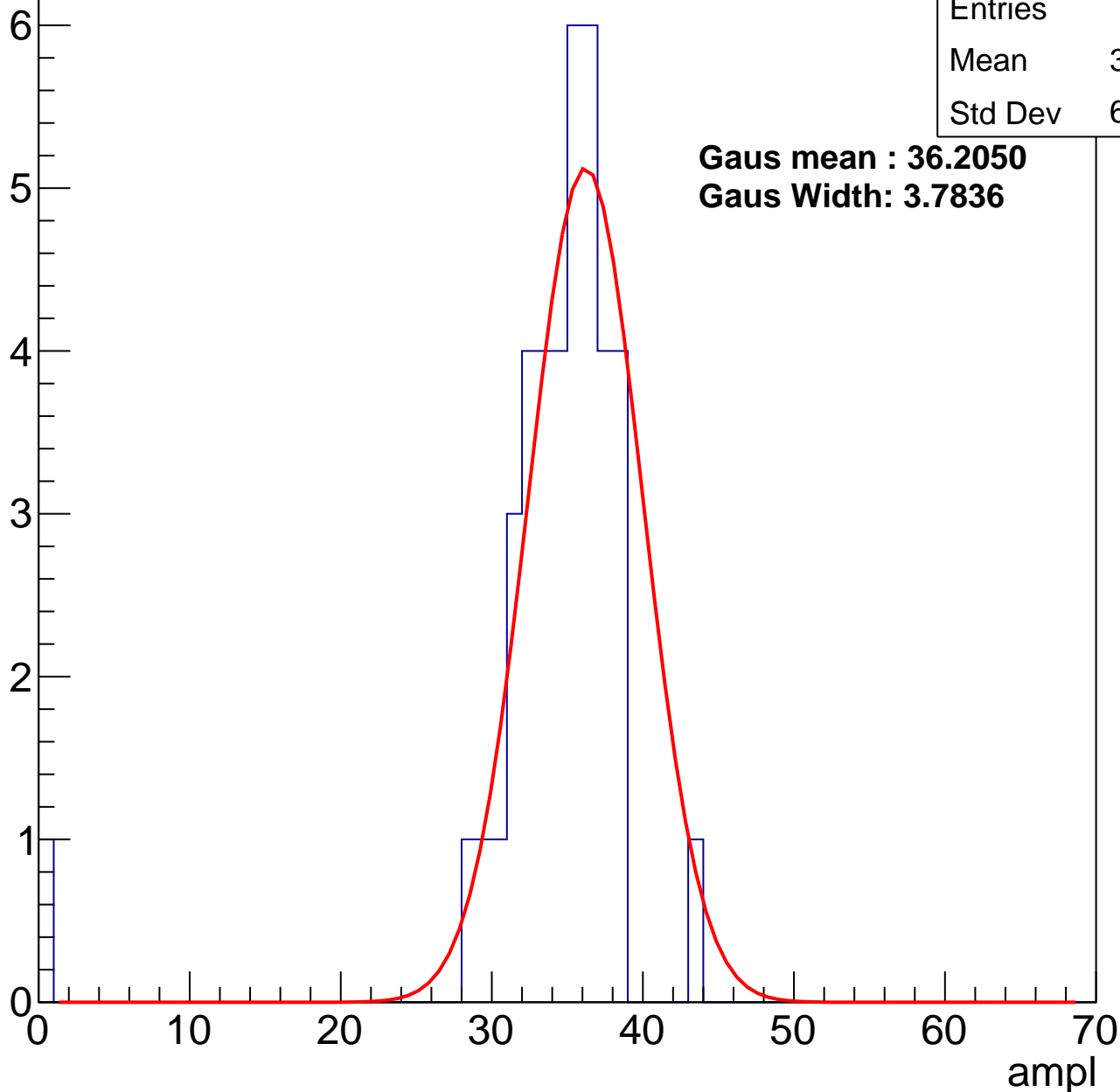
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	33.62
Std Dev	6.094

Gaus mean : 36.2050

Gaus Width: 3.7836



B1L101S, U12-ch52, adc2

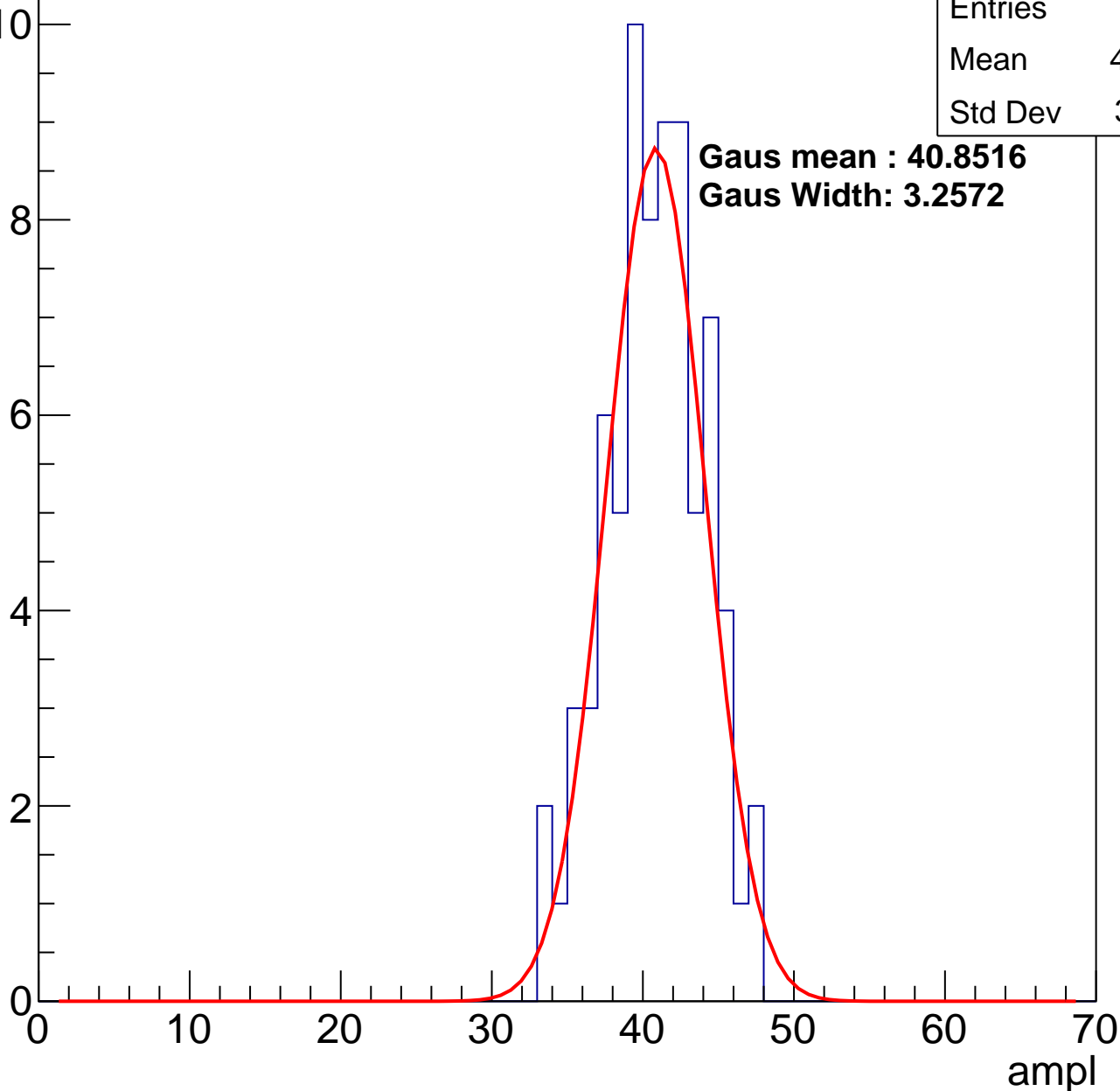
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	40.33
Std Dev	3.201

Gaus mean : 40.8516

Gaus Width: 3.2572

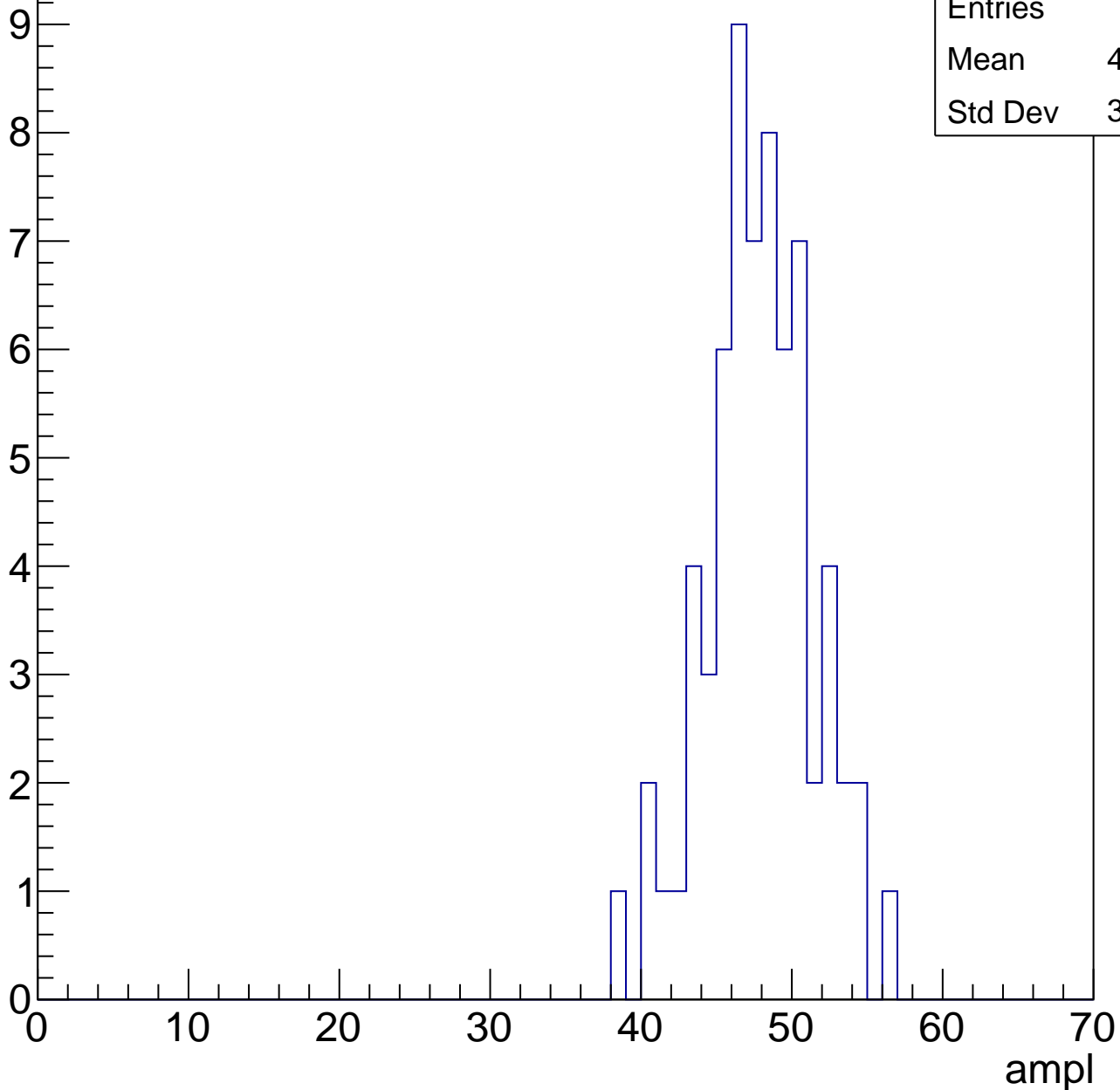


B1L101S, U12-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	47.36
Std Dev	3.553

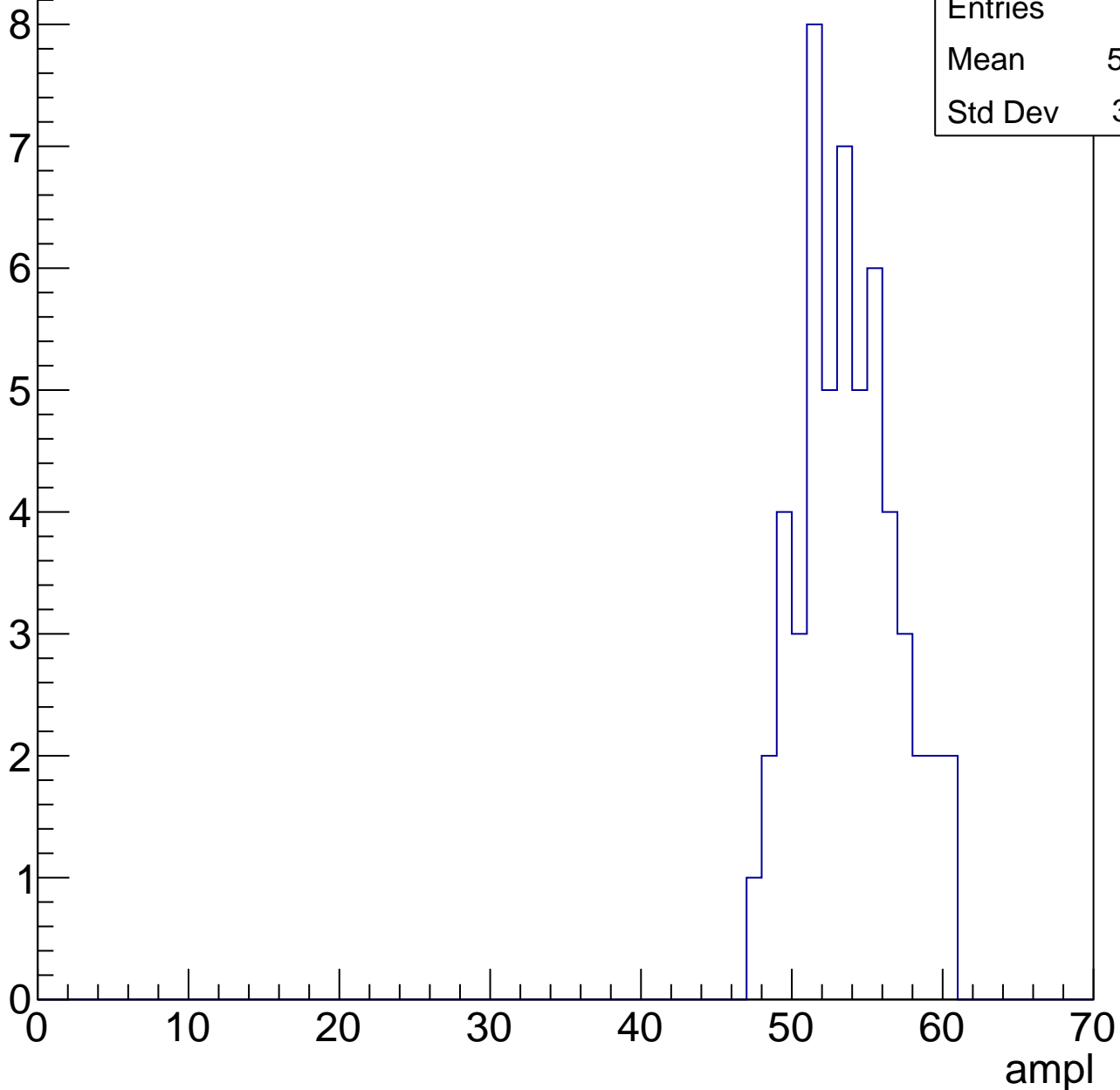


B1L101S, U12-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	53.28
Std Dev	3.171

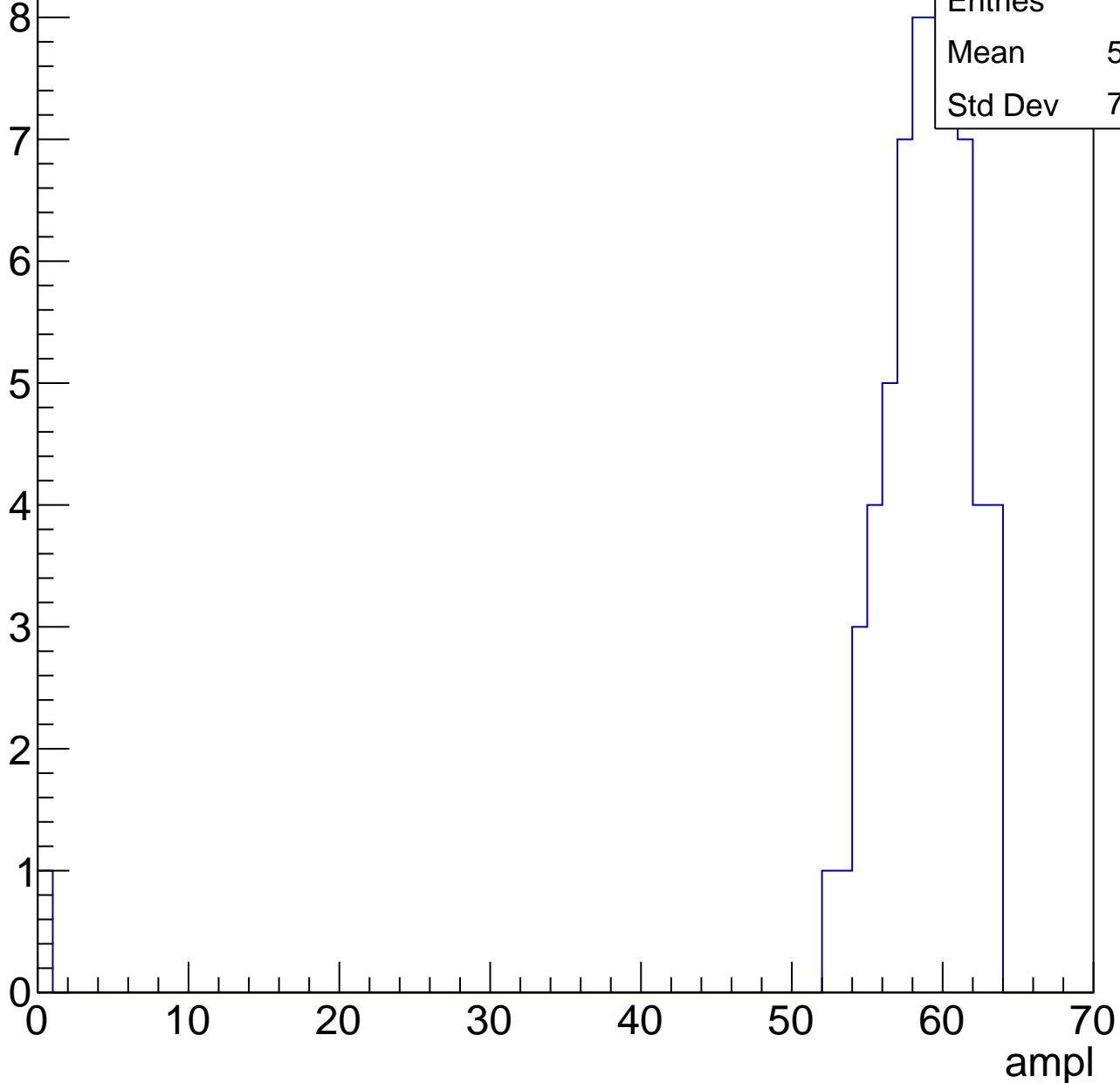


B1L101S, U12-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	57.52
Std Dev	7.882

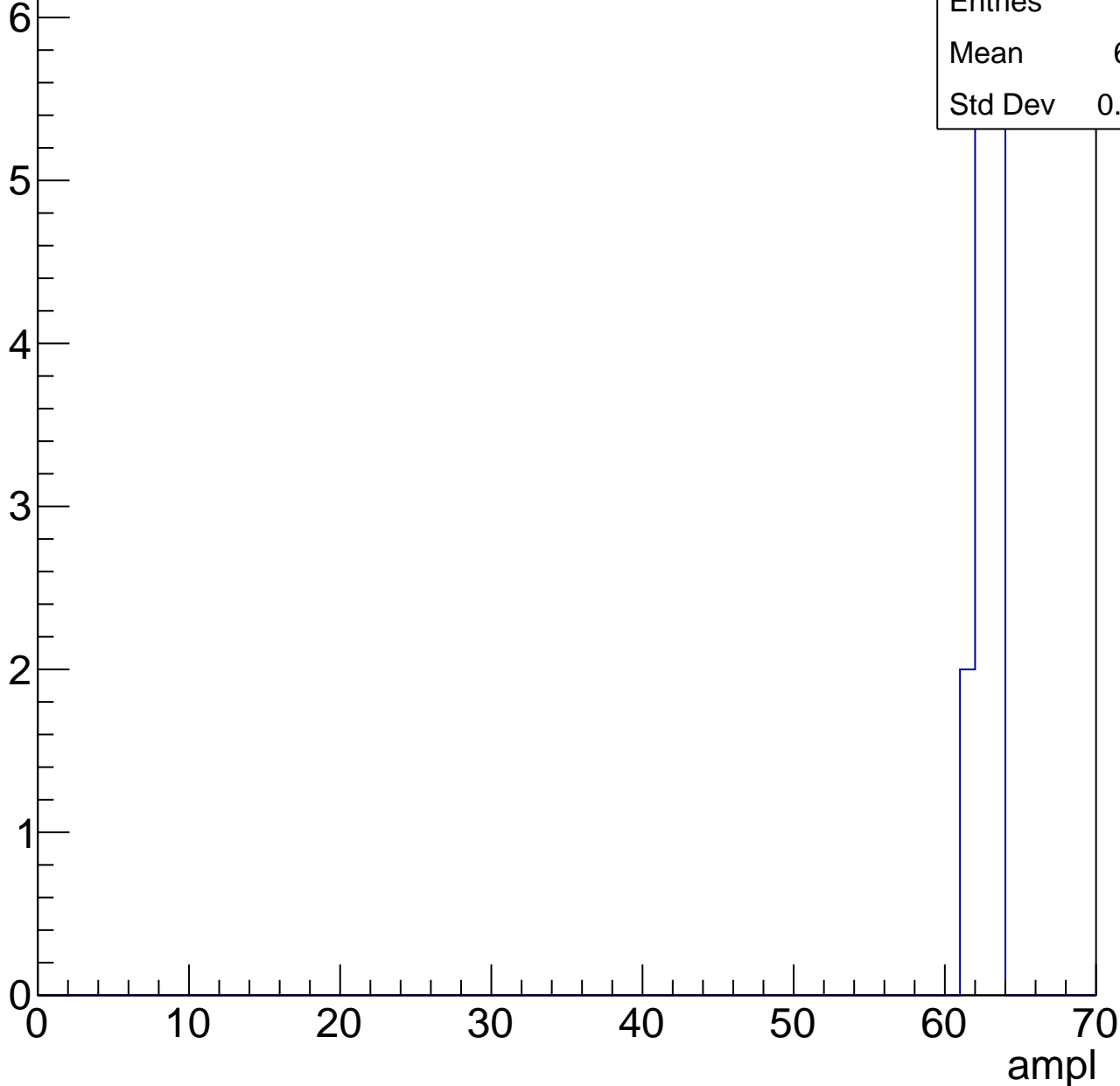


B1L101S, U12-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	62.29
Std Dev	0.6999



B1L101S, U12-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch53, adc0

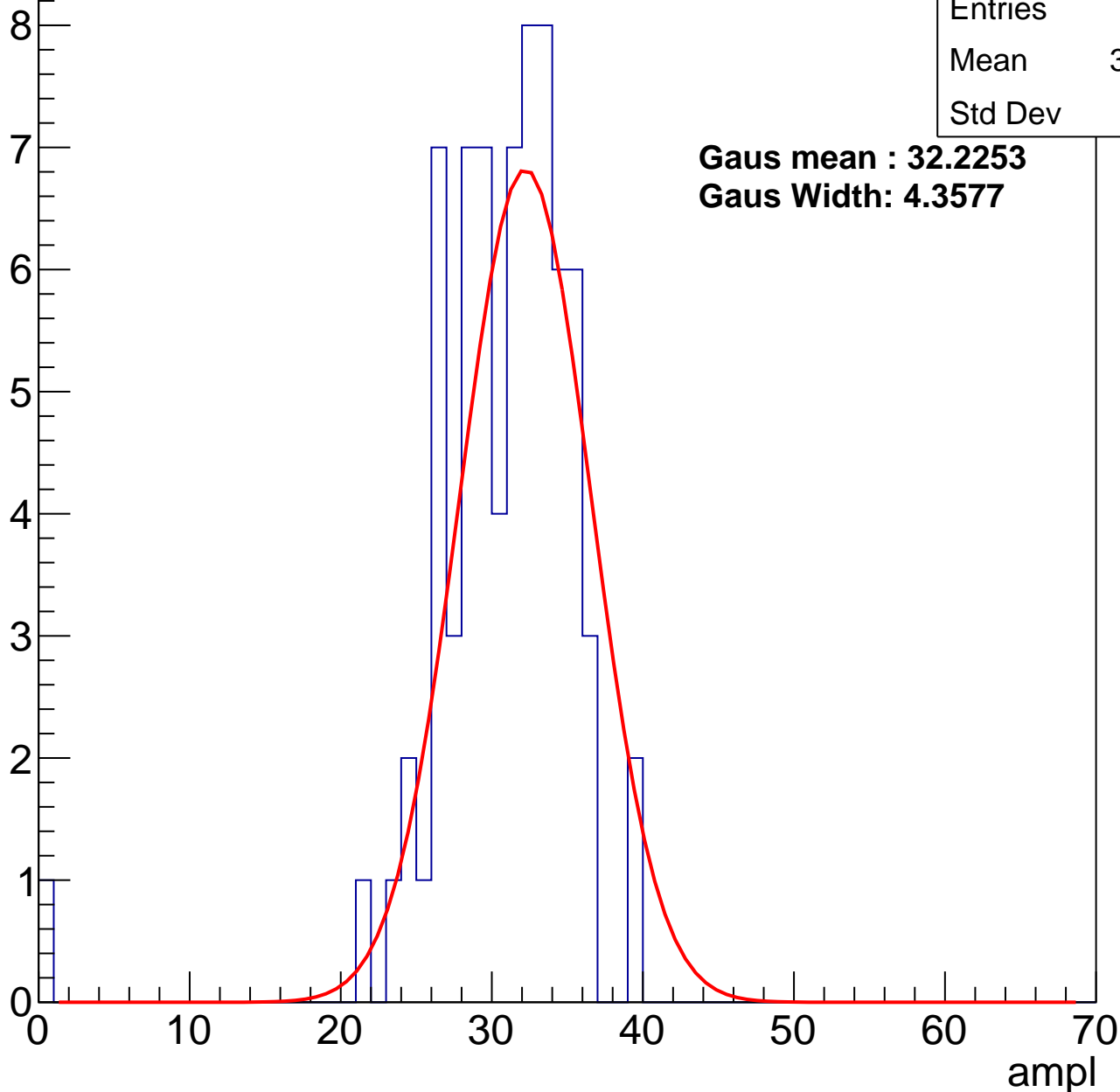
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.22
Std Dev	5.1

Gaus mean : 32.2253

Gaus Width: 4.3577



B1L101S, U12-ch53, adc1

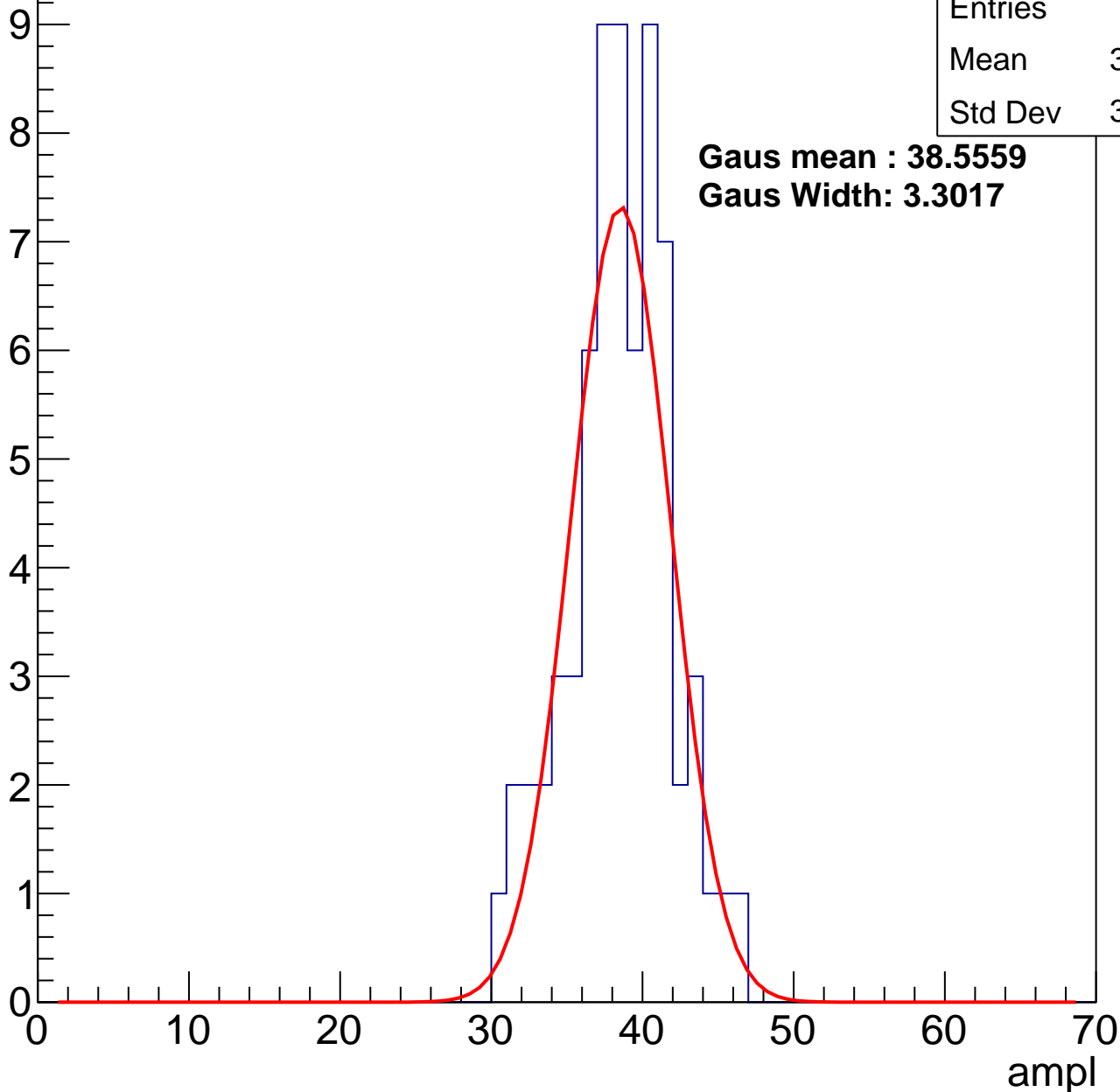
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	38.04
Std Dev	3.352

Gaus mean : 38.5559

Gaus Width: 3.3017



B1L101S, U12-ch53, adc2

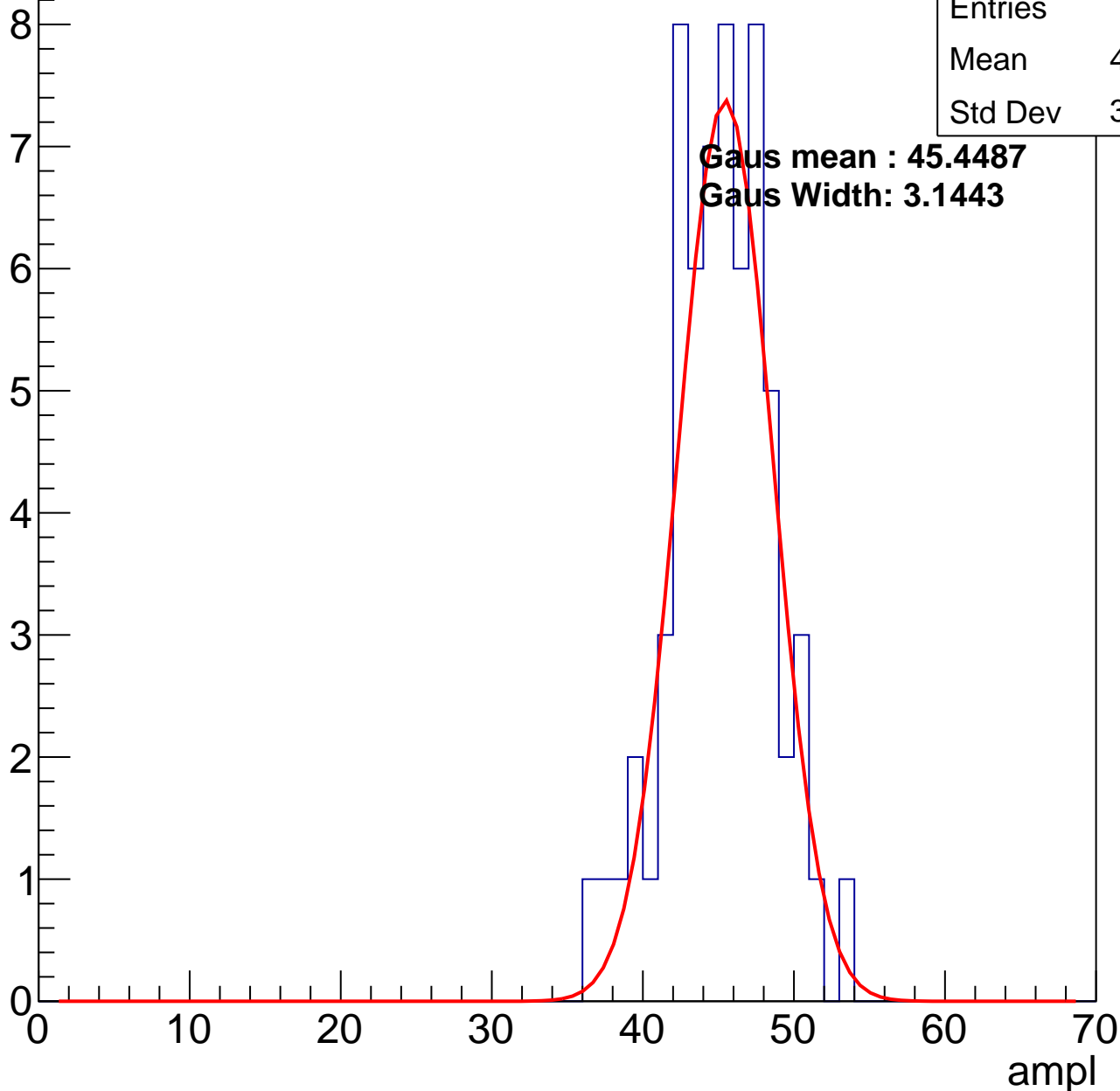
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.66
Std Dev	3.364

Gaus mean : 45.4487

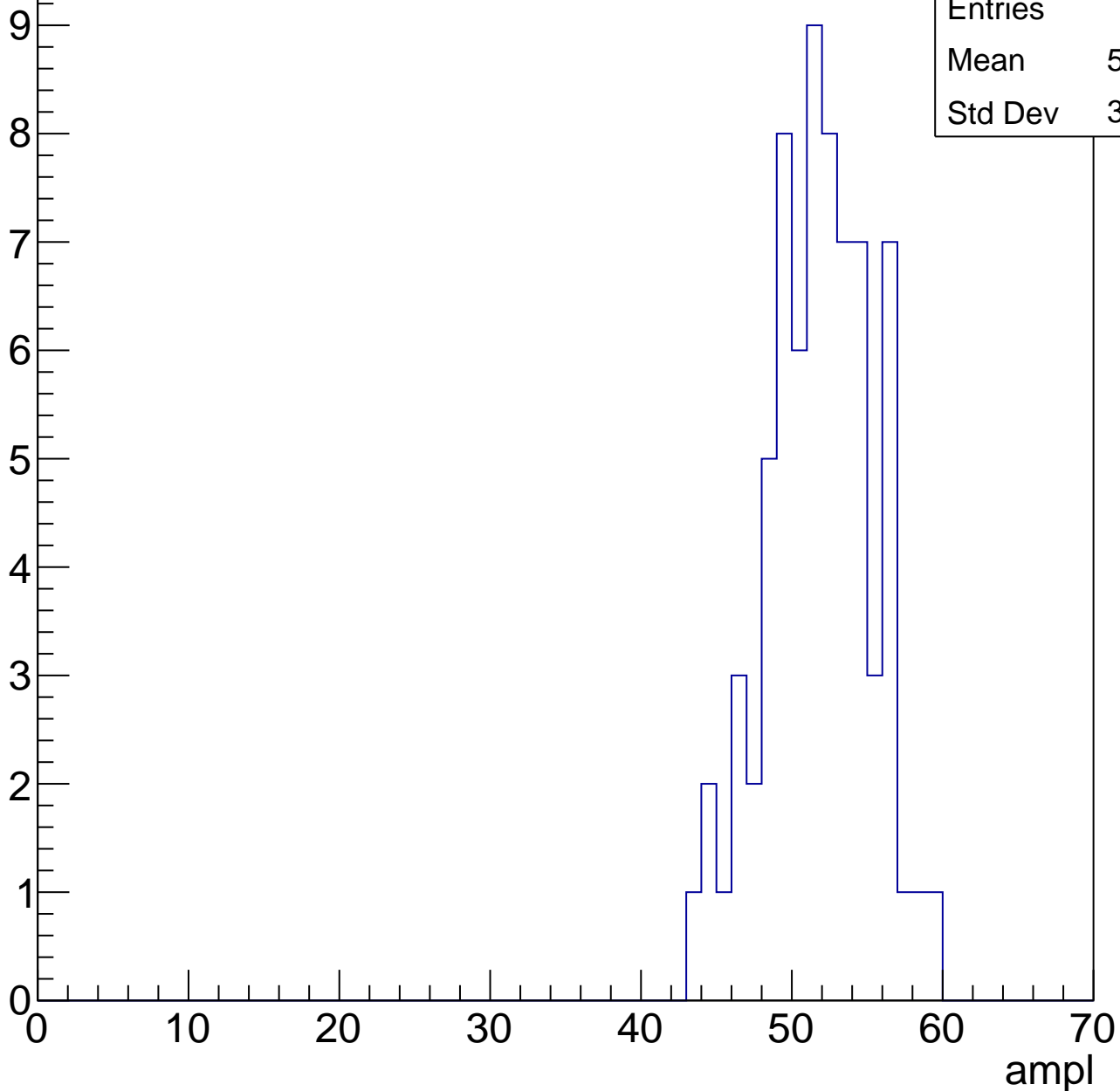
Gaus Width: 3.1443



B1L101S, U12-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

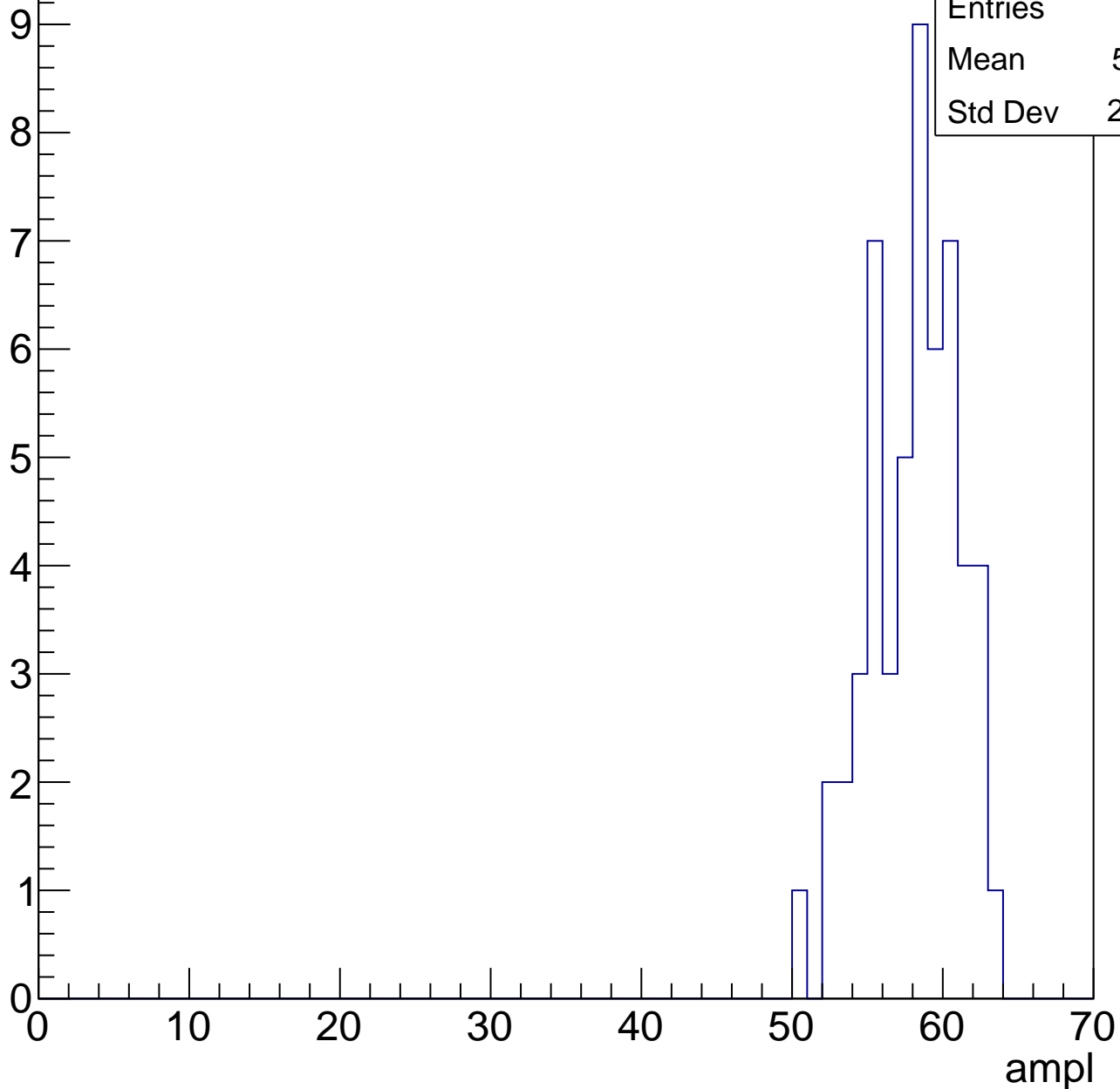


Entries	72
Mean	51.32
Std Dev	3.439

B1L101S, U12-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

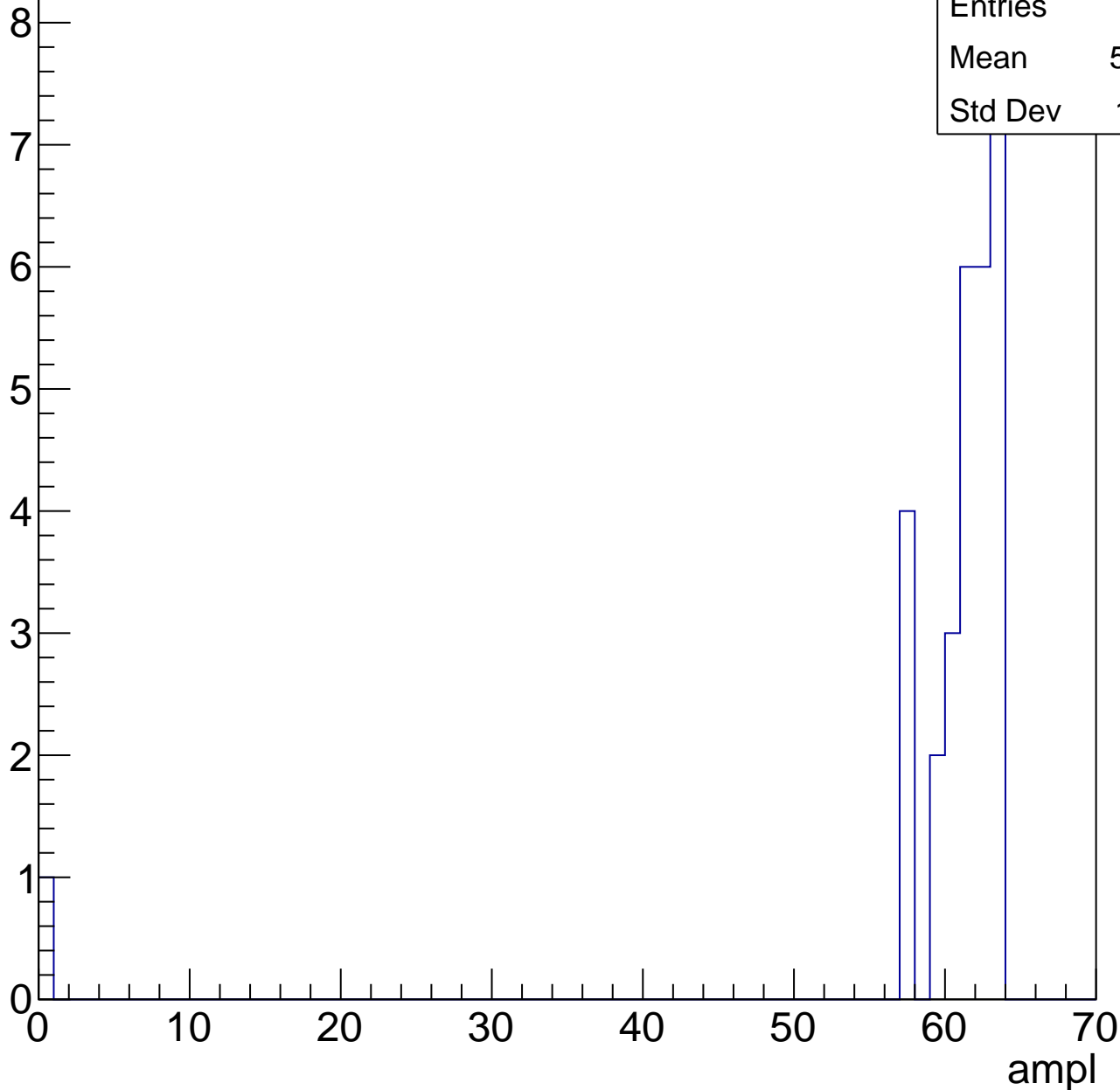


B1L101S, U12-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

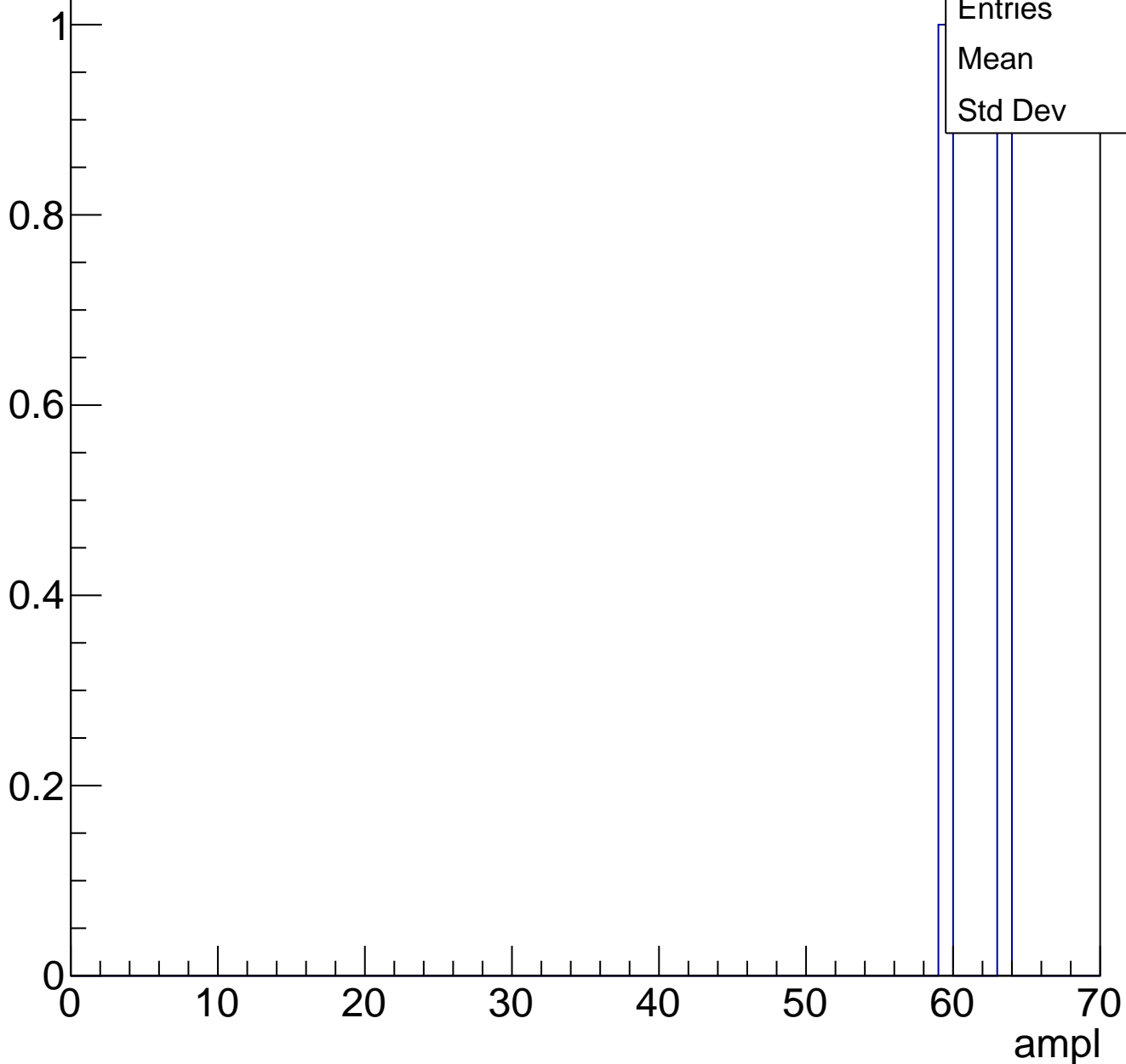
Entries	30
Mean	58.93
Std Dev	11.11



B1L101S, U12-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch54, adc0

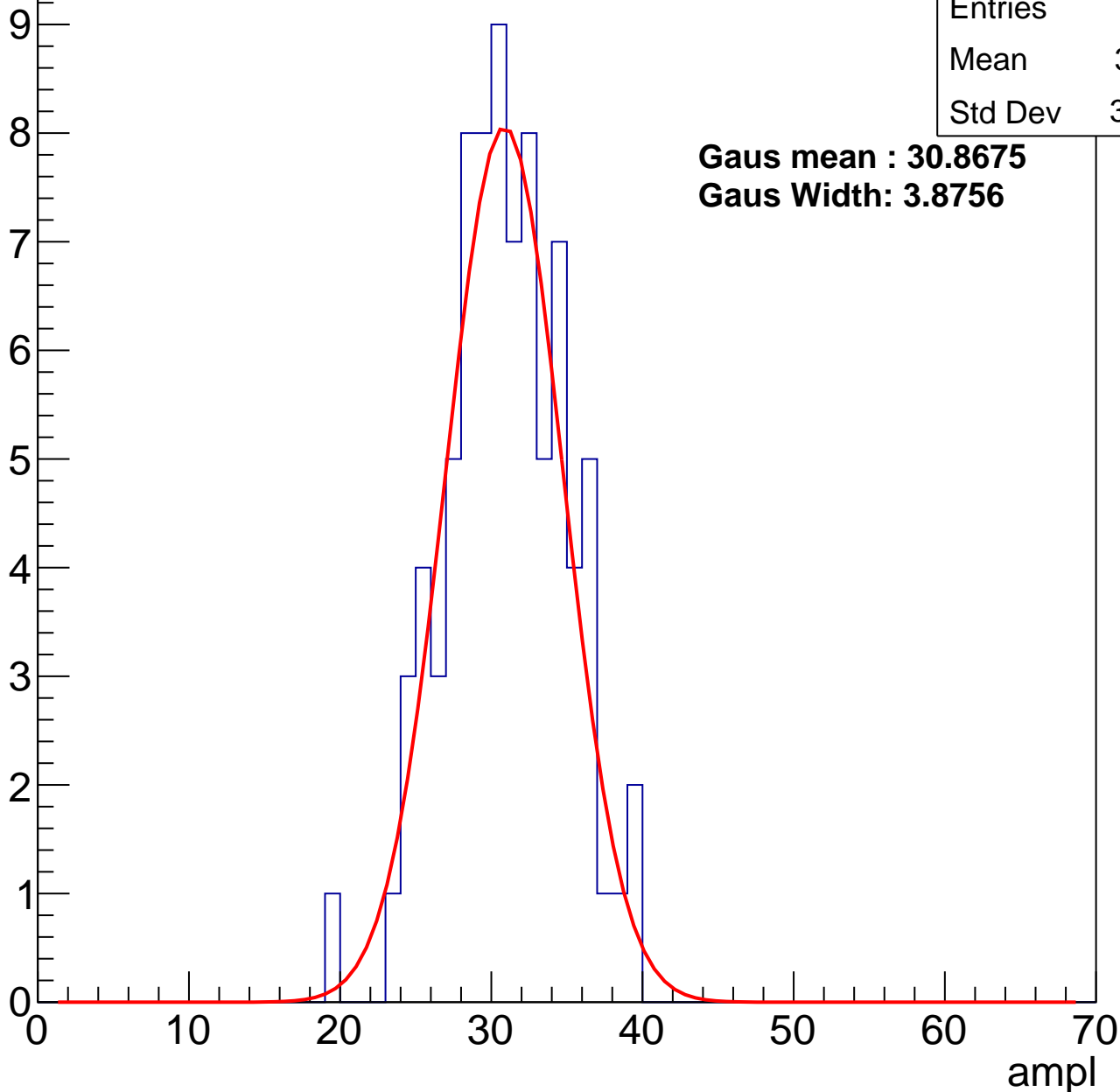
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.51
Std Dev	3.889

Gaus mean : 30.8675

Gaus Width: 3.8756



B1L101S, U12-ch54, adc1

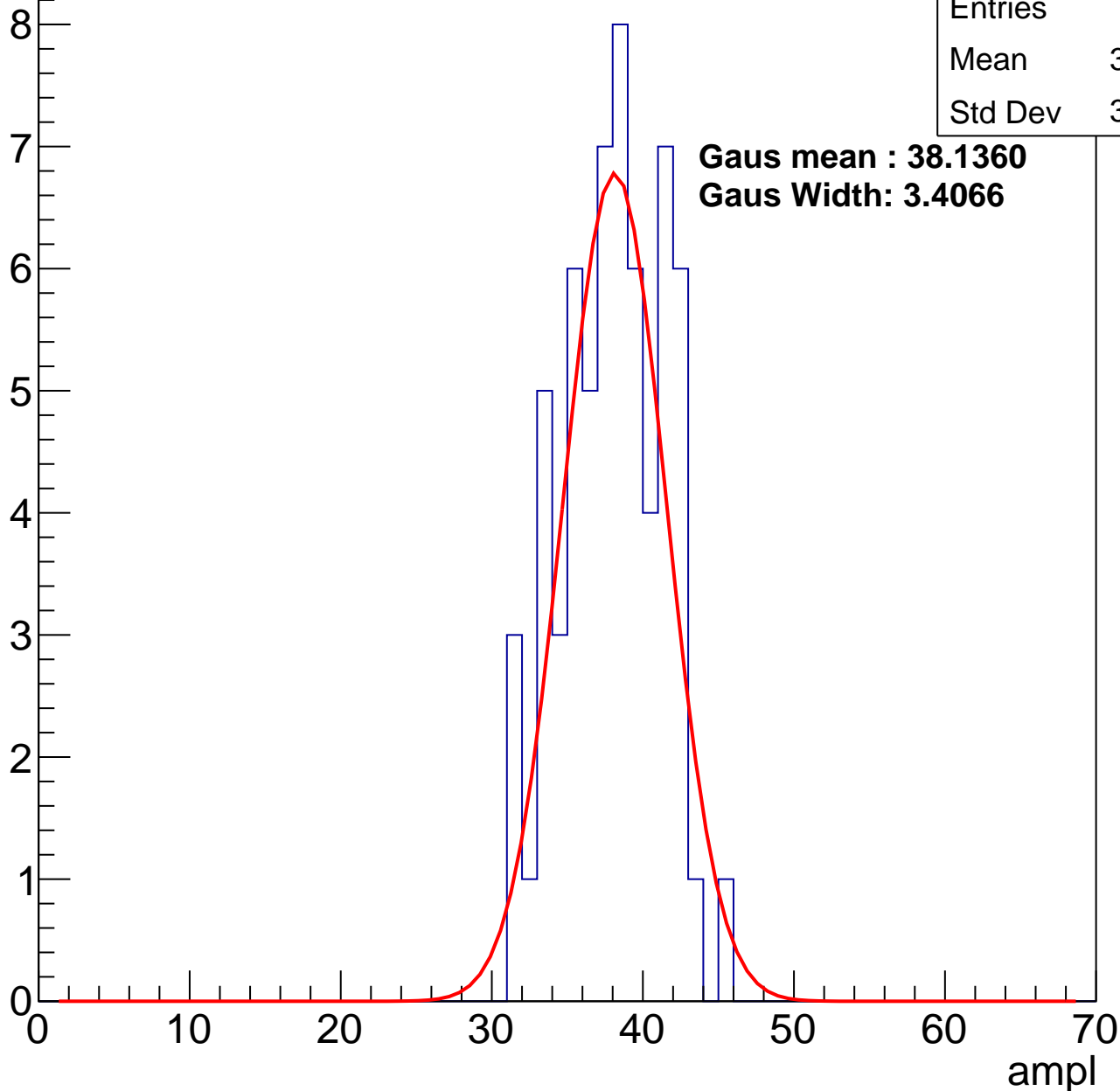
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.56
Std Dev	3.284

Gaus mean : 38.1360

Gaus Width: 3.4066



B1L101S, U12-ch54, adc2

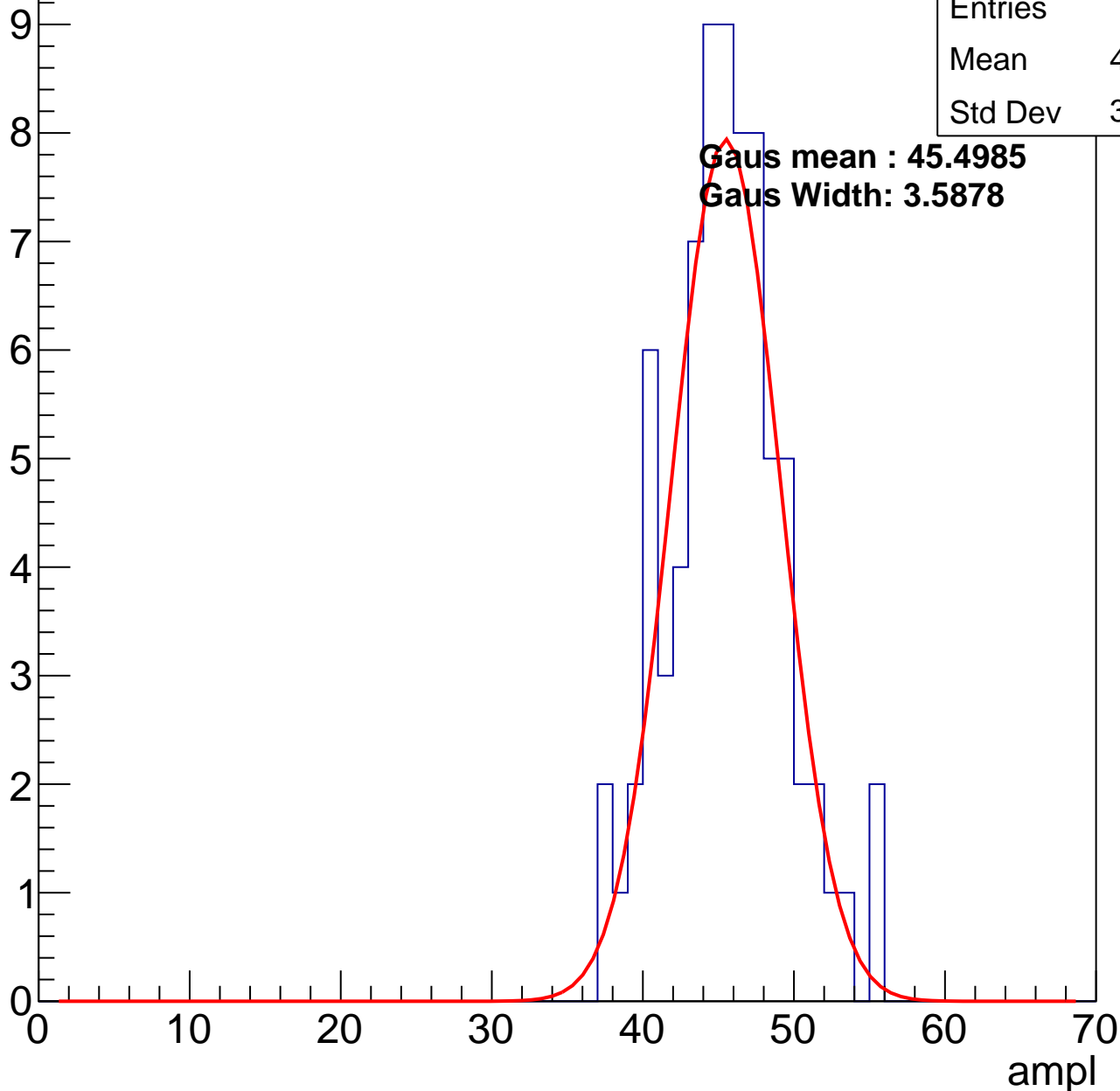
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	45.05
Std Dev	3.813

Gaus mean : 45.4985

Gaus Width: 3.5878

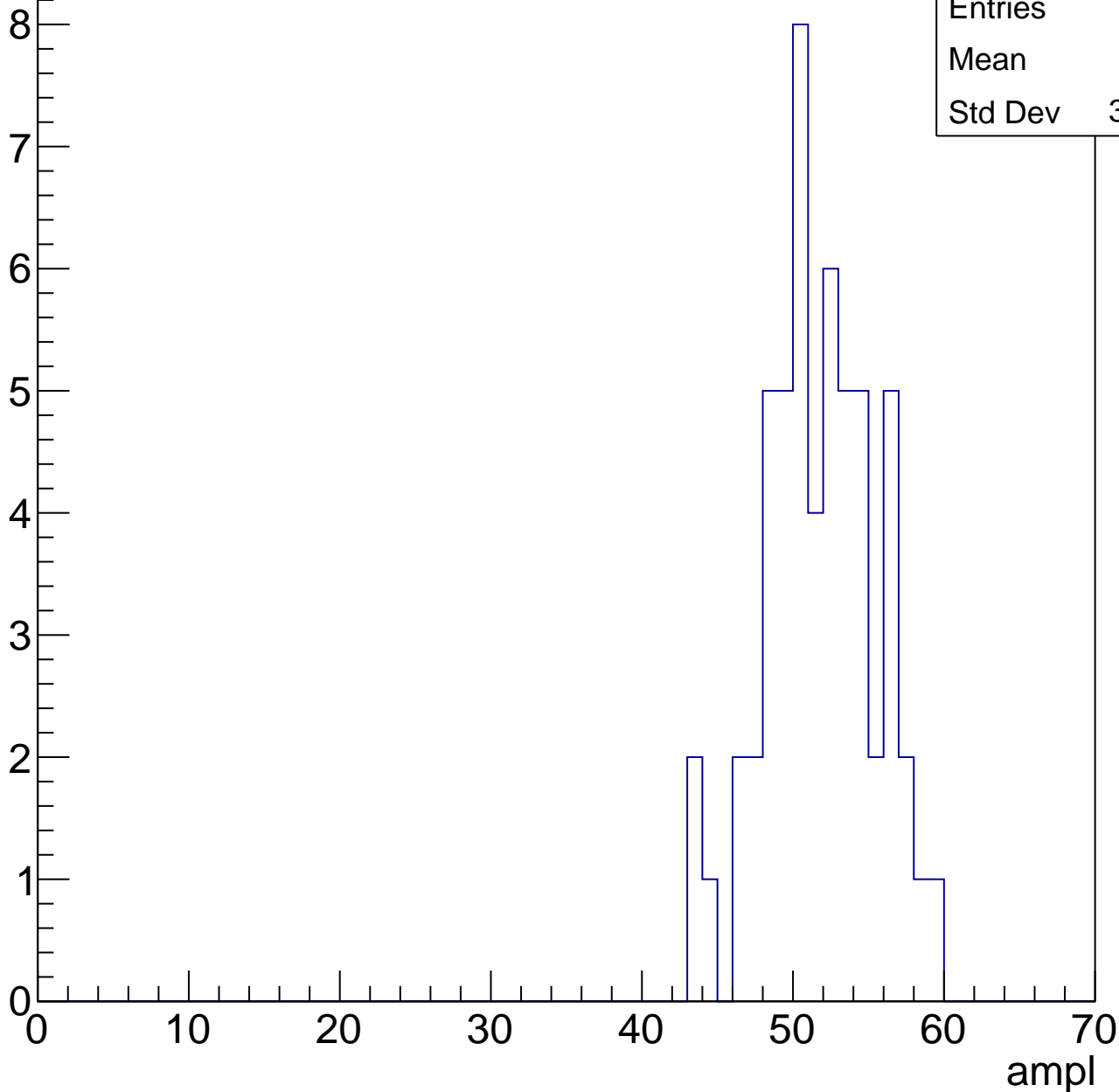


B1L101S, U12-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	51.3
Std Dev	3.635



B1L101S, U12-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 58

Mean 56.53

Std Dev 3.053

ampl

0

10

20

30

40

50

60

70

B1L101S, U12-ch54, adc5

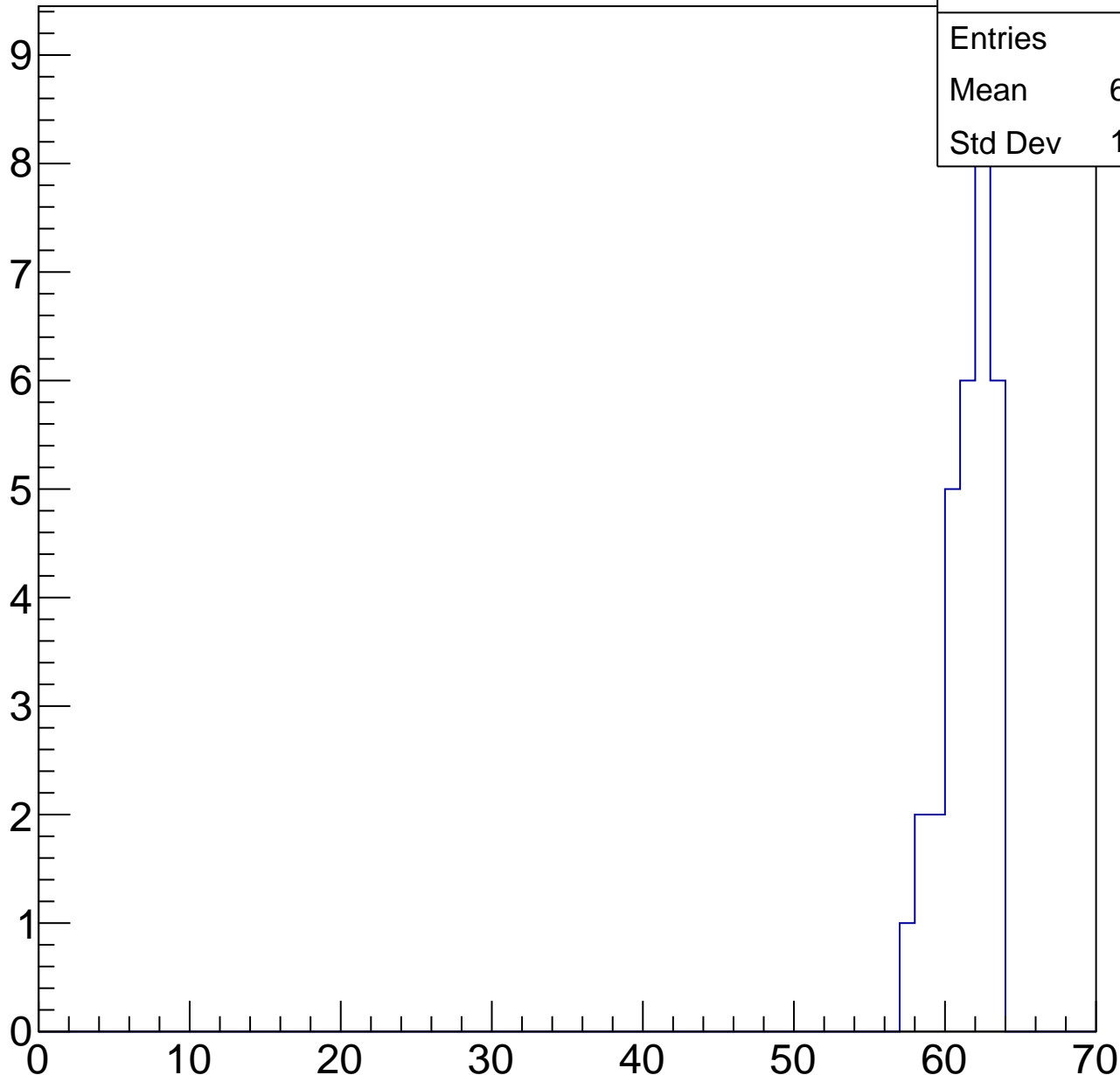
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	31
Mean	61.06
Std Dev	1.605

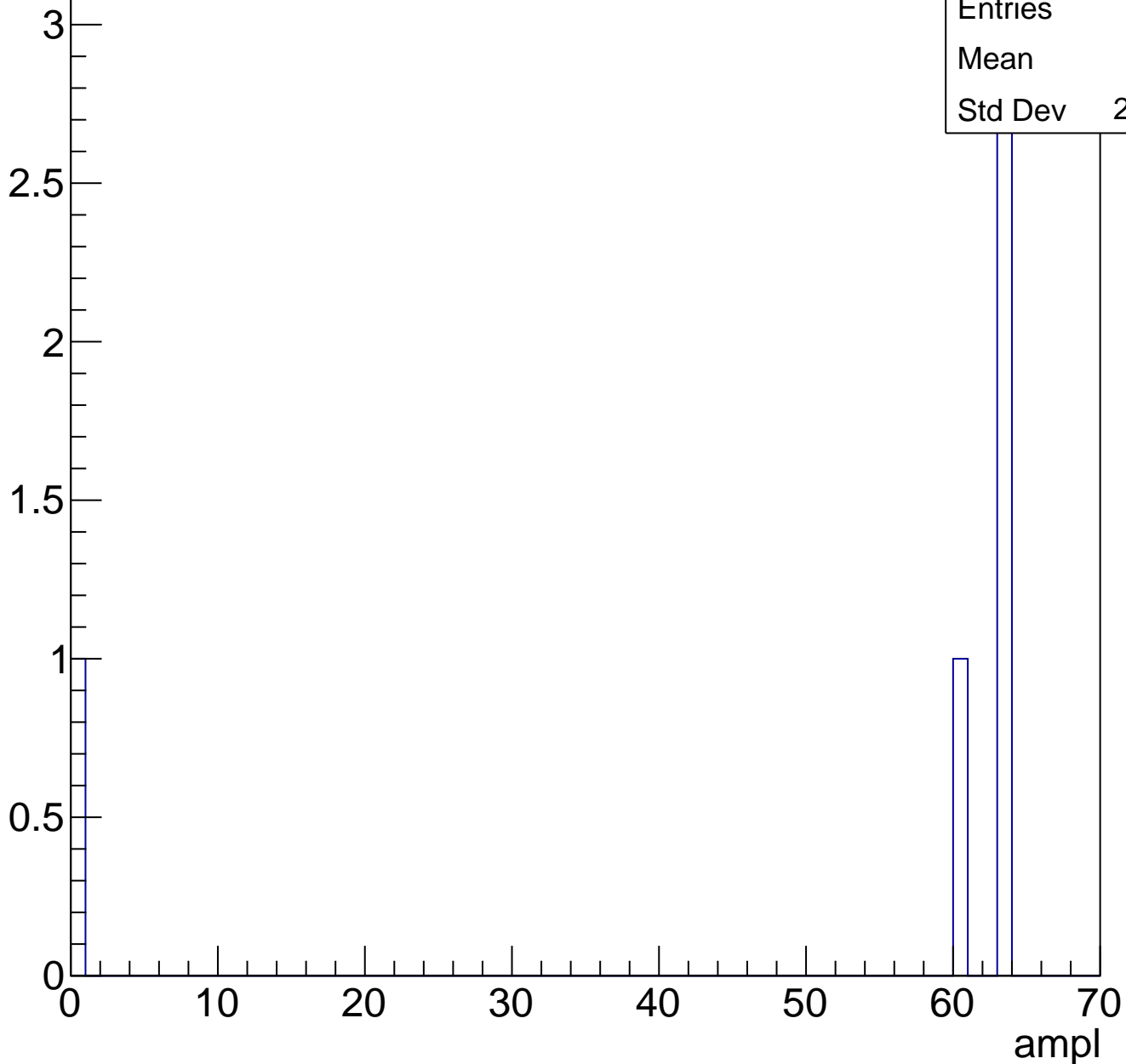
ampl



B1L101S, U12-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch55, adc0

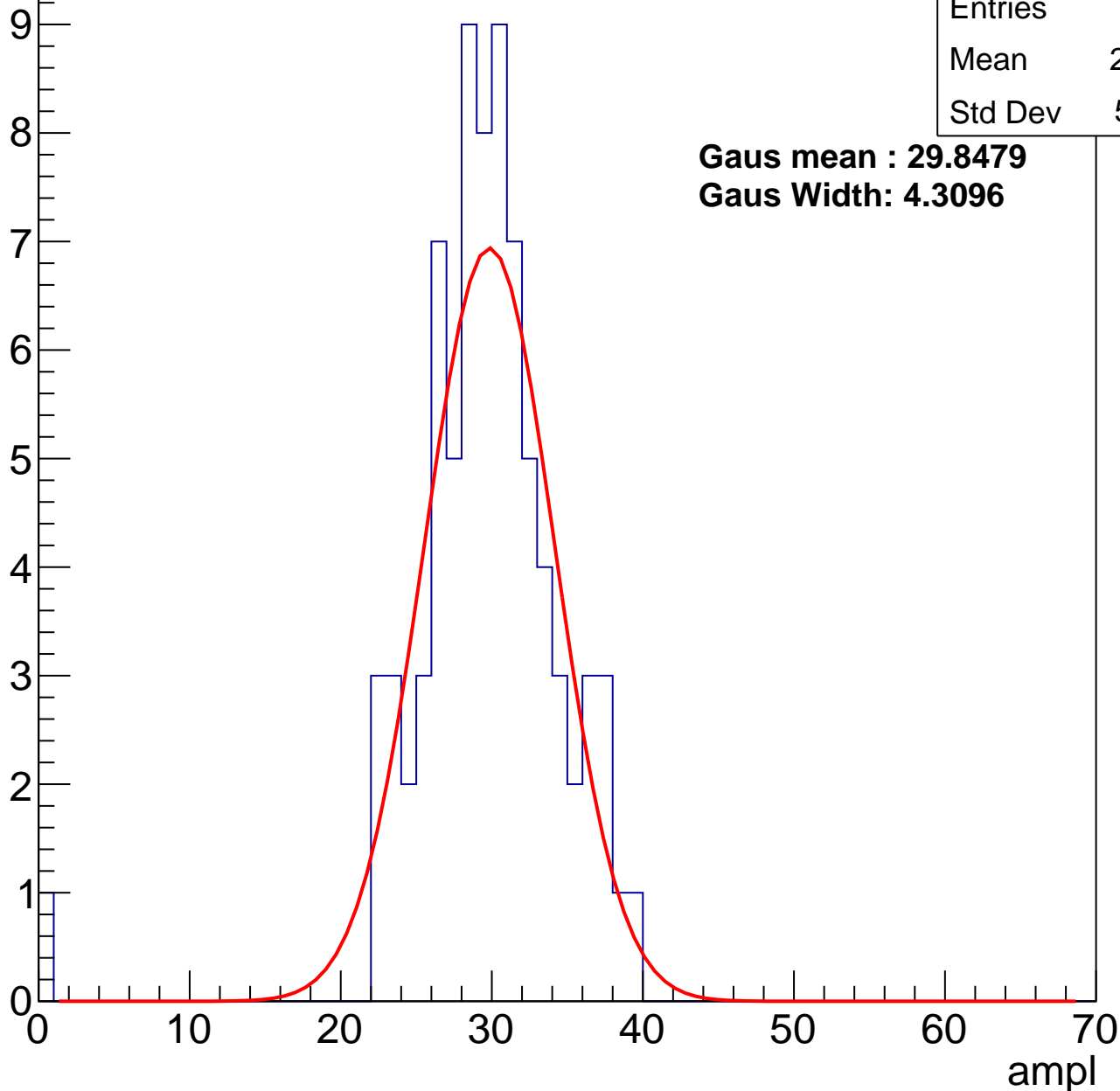
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.19
Std Dev	5.151

Gaus mean : 29.8479

Gaus Width: 4.3096



B1L101S, U12-ch55, adc1

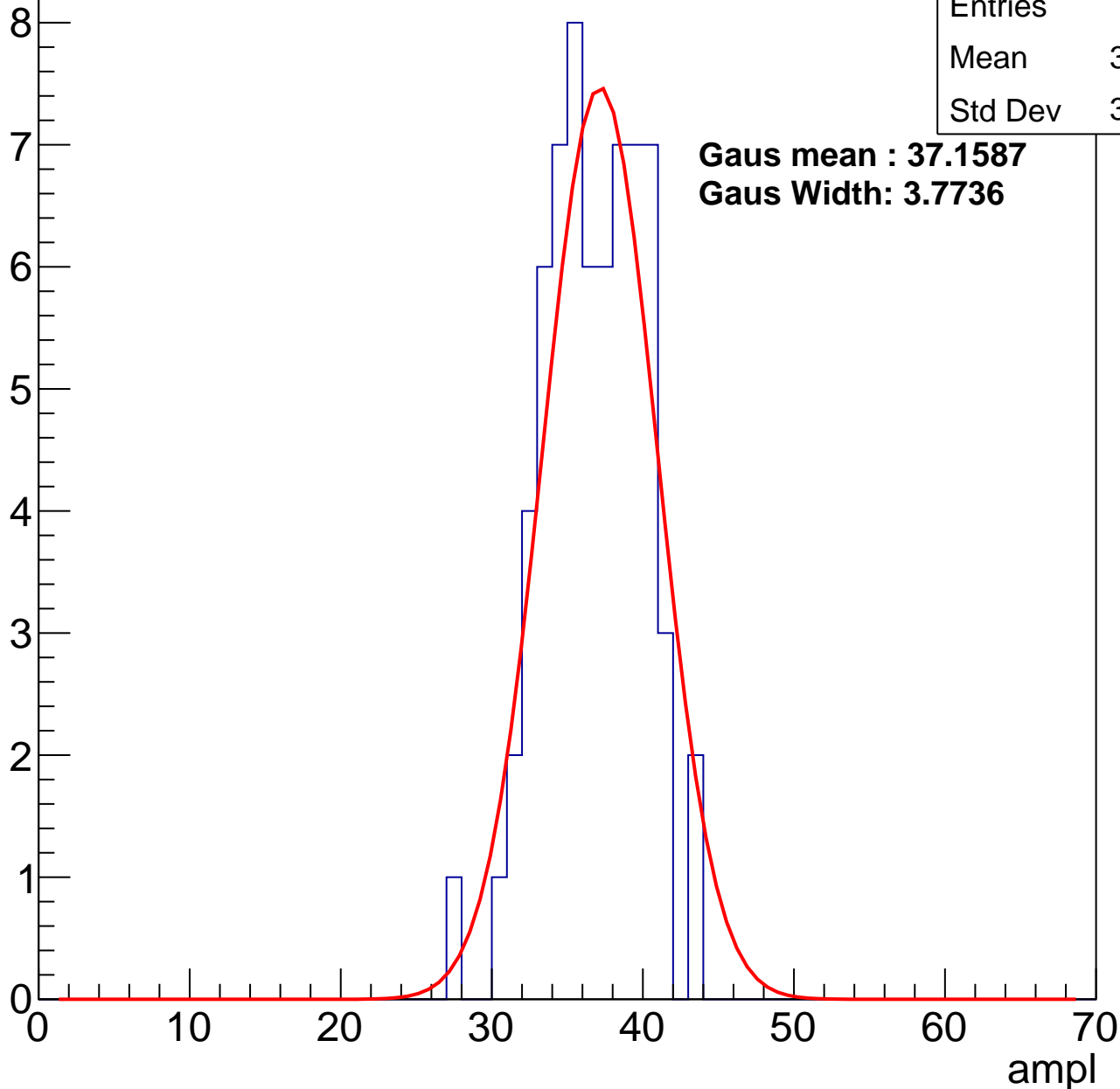
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.25
Std Dev	3.229

Gaus mean : 37.1587

Gaus Width: 3.7736



B1L101S, U12-ch55, adc2

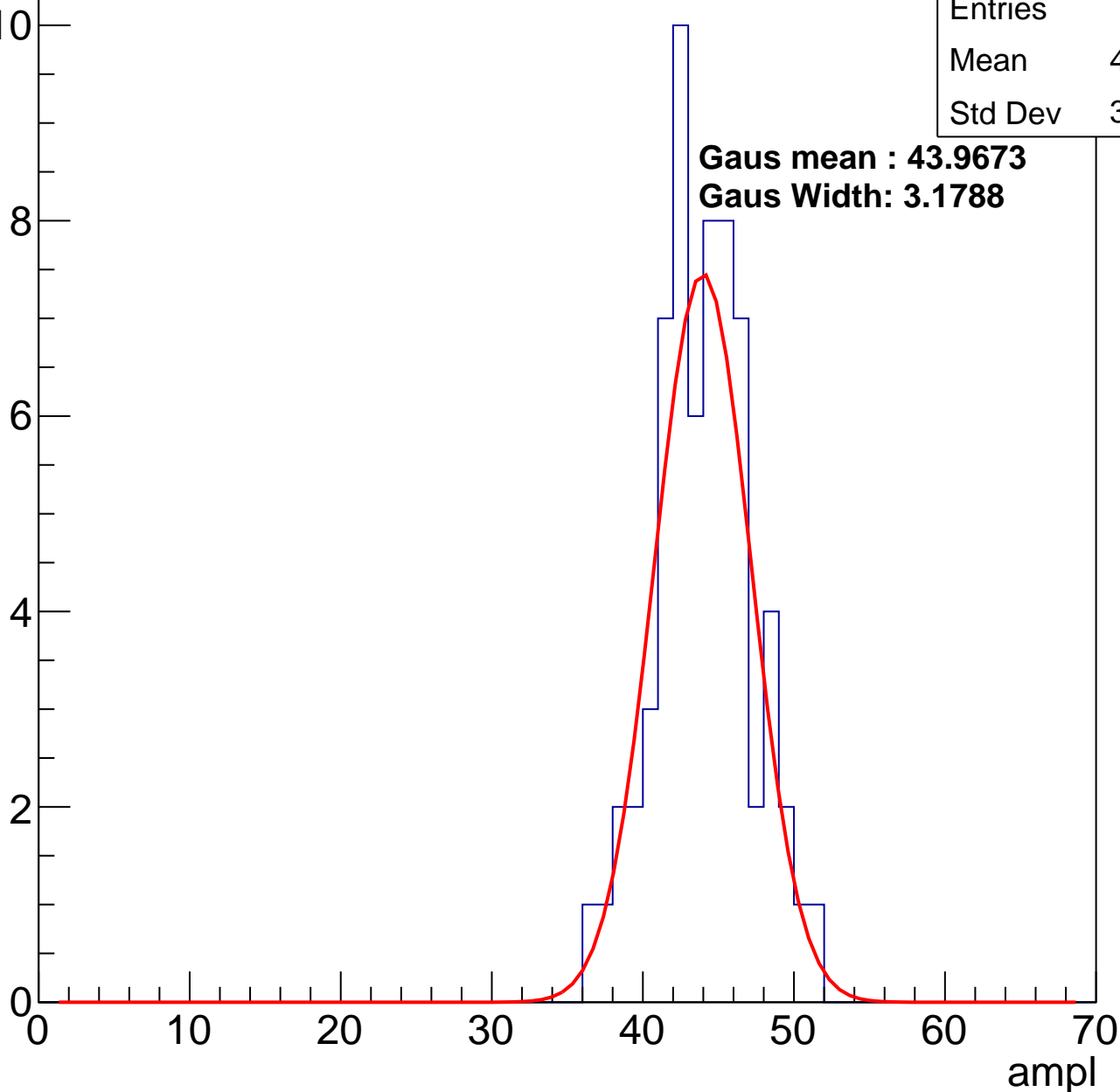
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.55
Std Dev	3.118

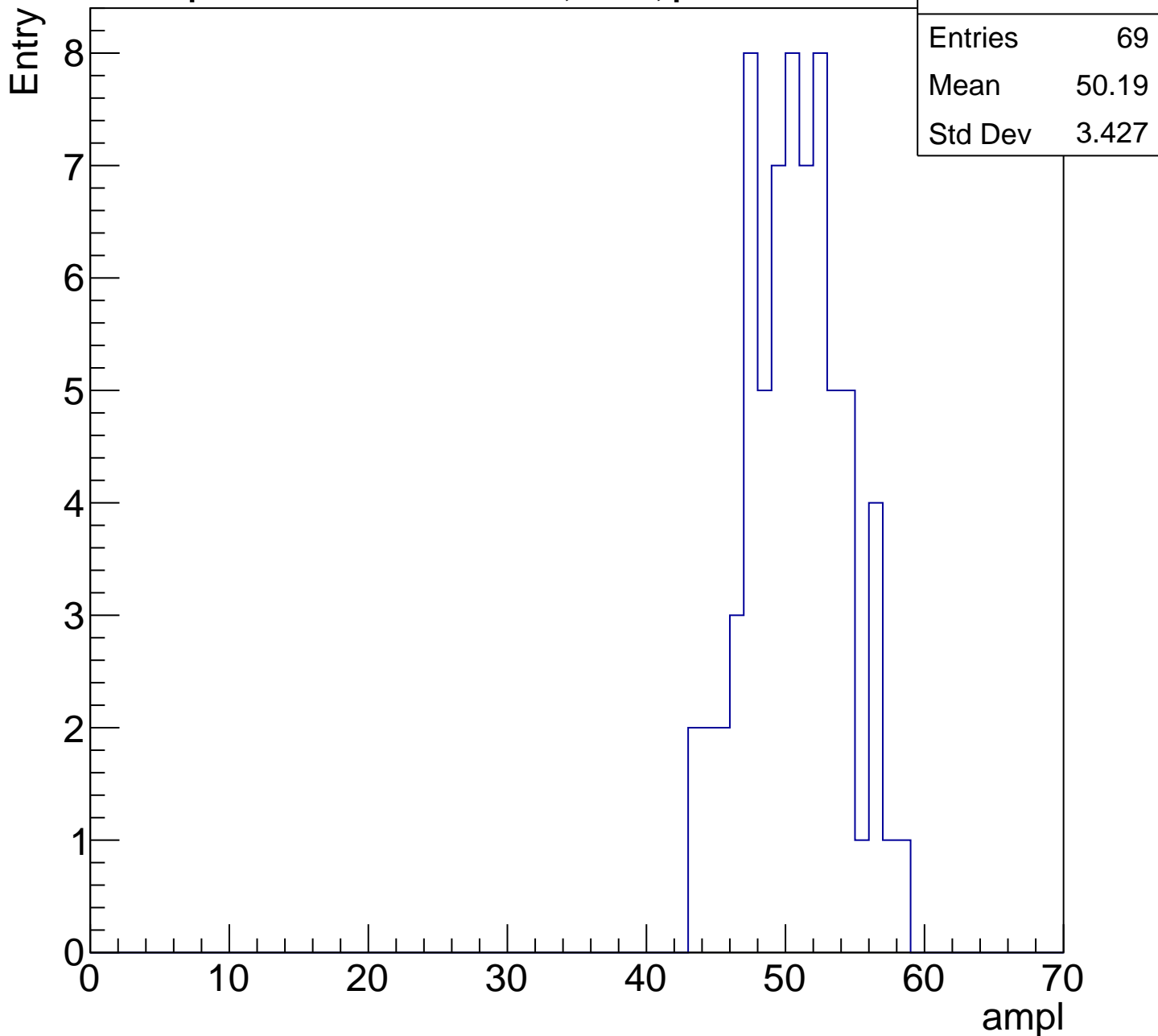
Gaus mean : 43.9673

Gaus Width: 3.1788



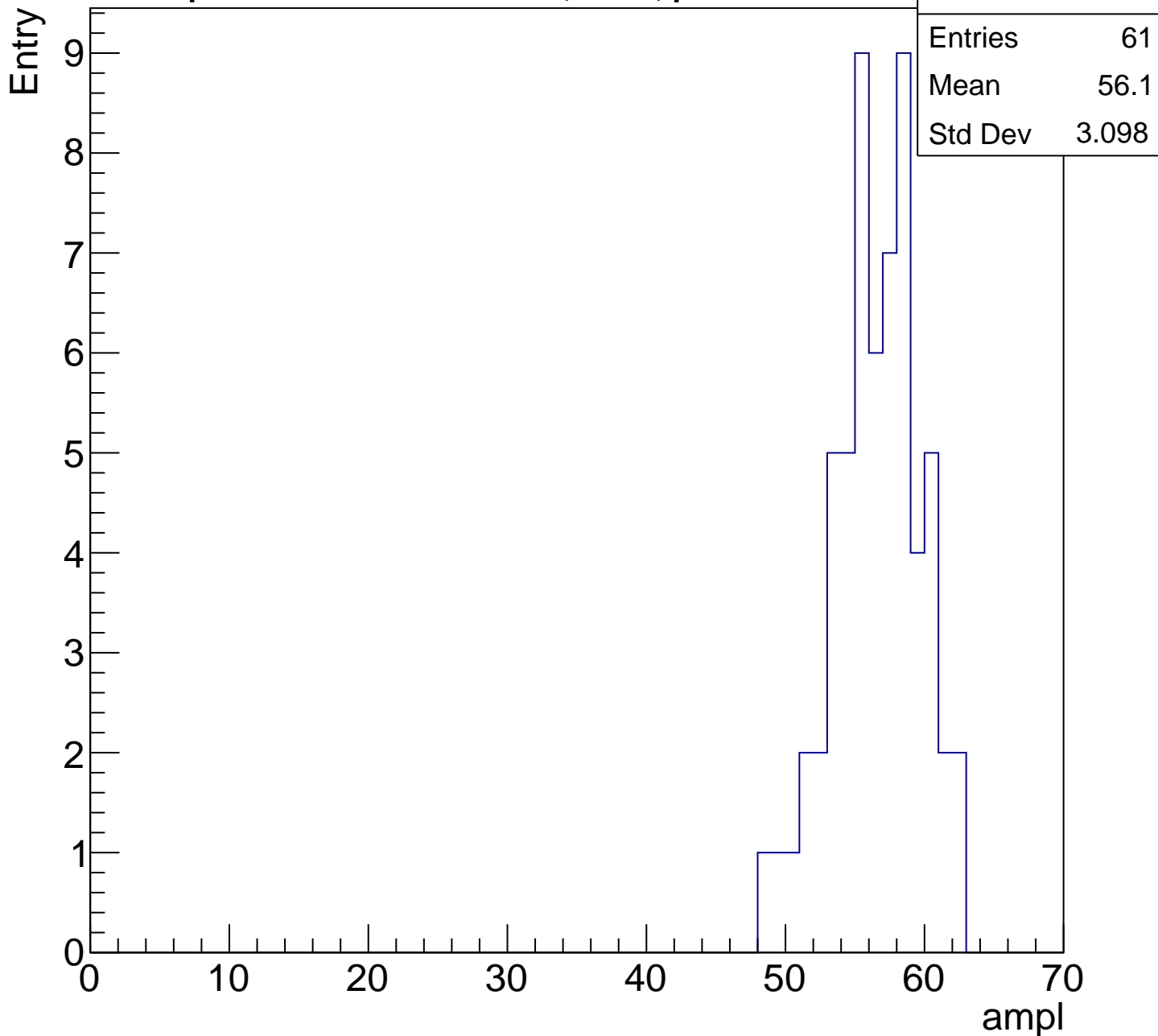
B1L101S, U12-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

59.03

Std Dev

10.4

ampl

0

10

20

30

40

50

60

70

B1L101S, U12-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch56, adc0

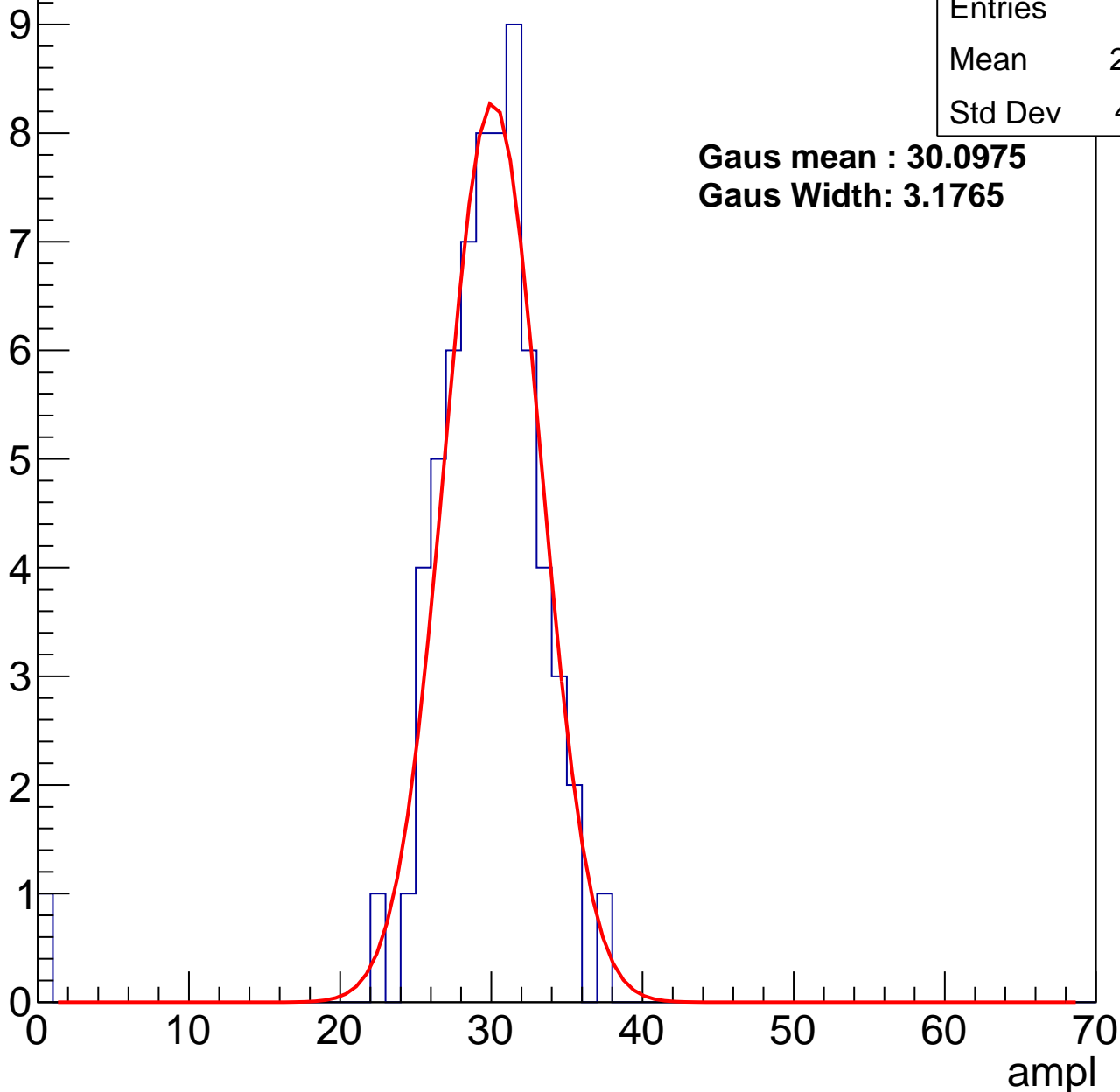
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.06
Std Dev	4.651

Gaus mean : 30.0975

Gaus Width: 3.1765



B1L101S, U12-ch56, adc1

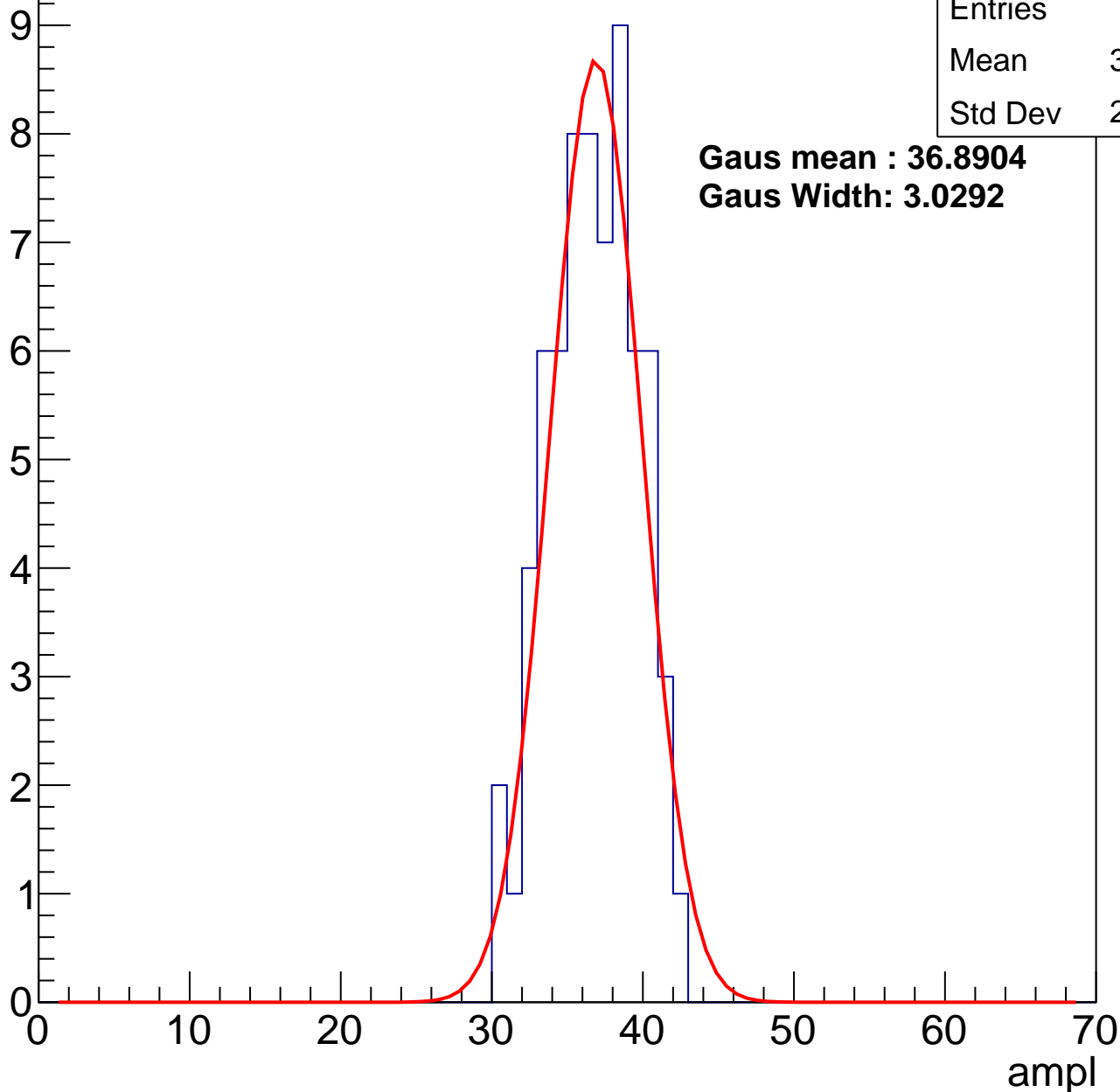
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.25
Std Dev	2.856

Gaus mean : 36.8904

Gaus Width: 3.0292



B1L101S, U12-ch56, adc2

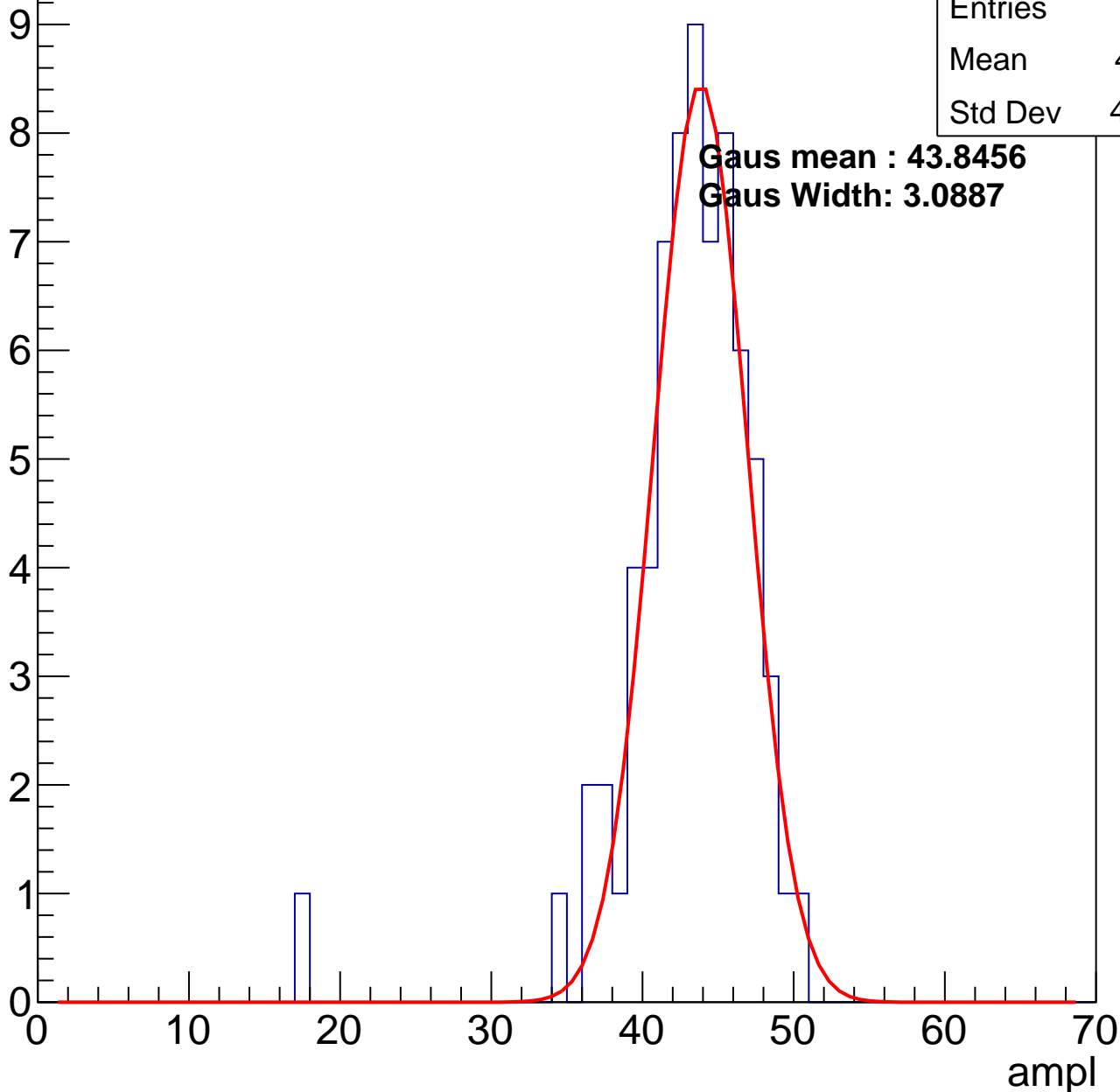
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.61
Std Dev	4.489

Gaus mean : 43.8456

Gaus Width: 3.0887

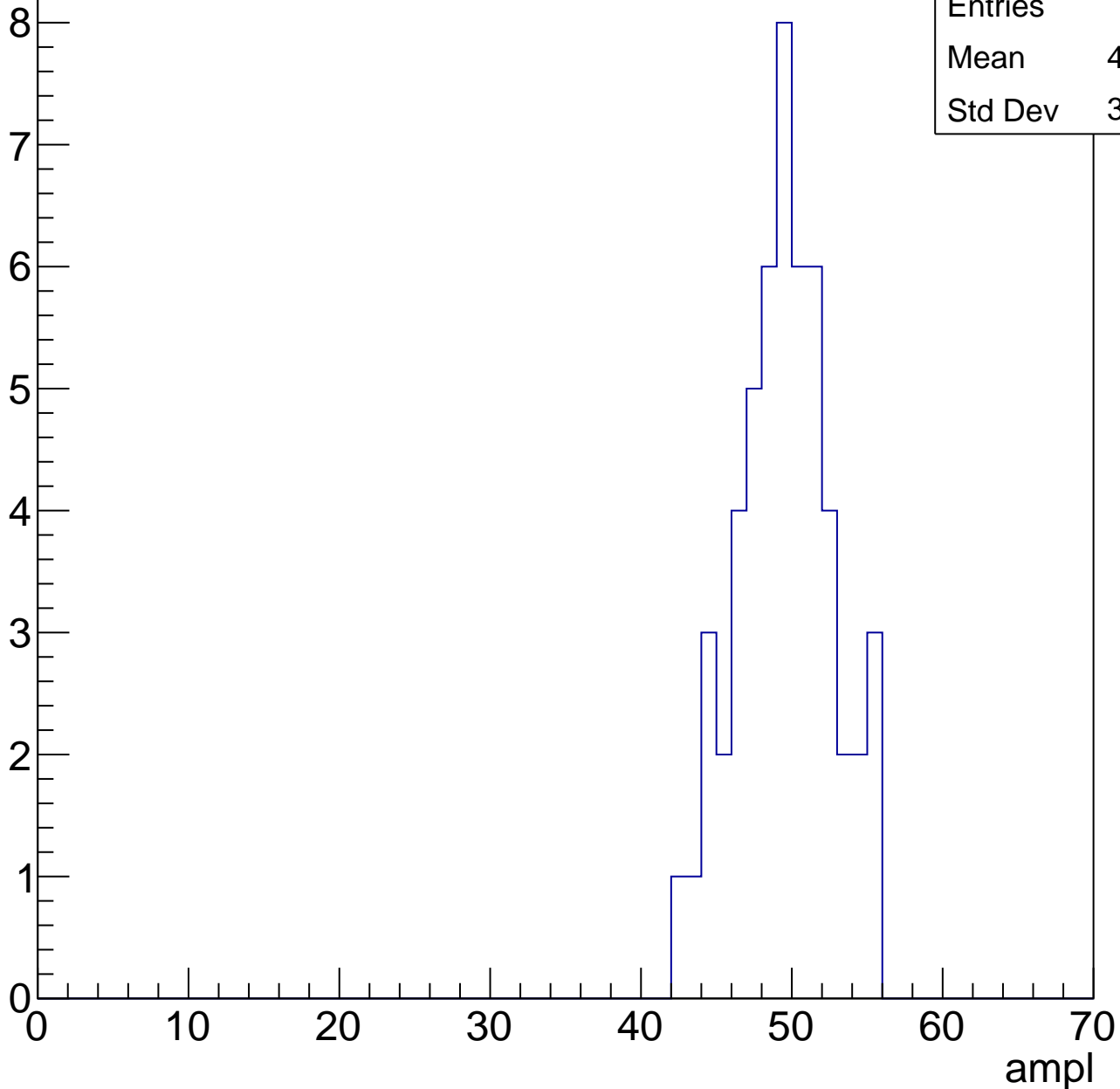


B1L101S, U12-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	49.04
Std Dev	3.102

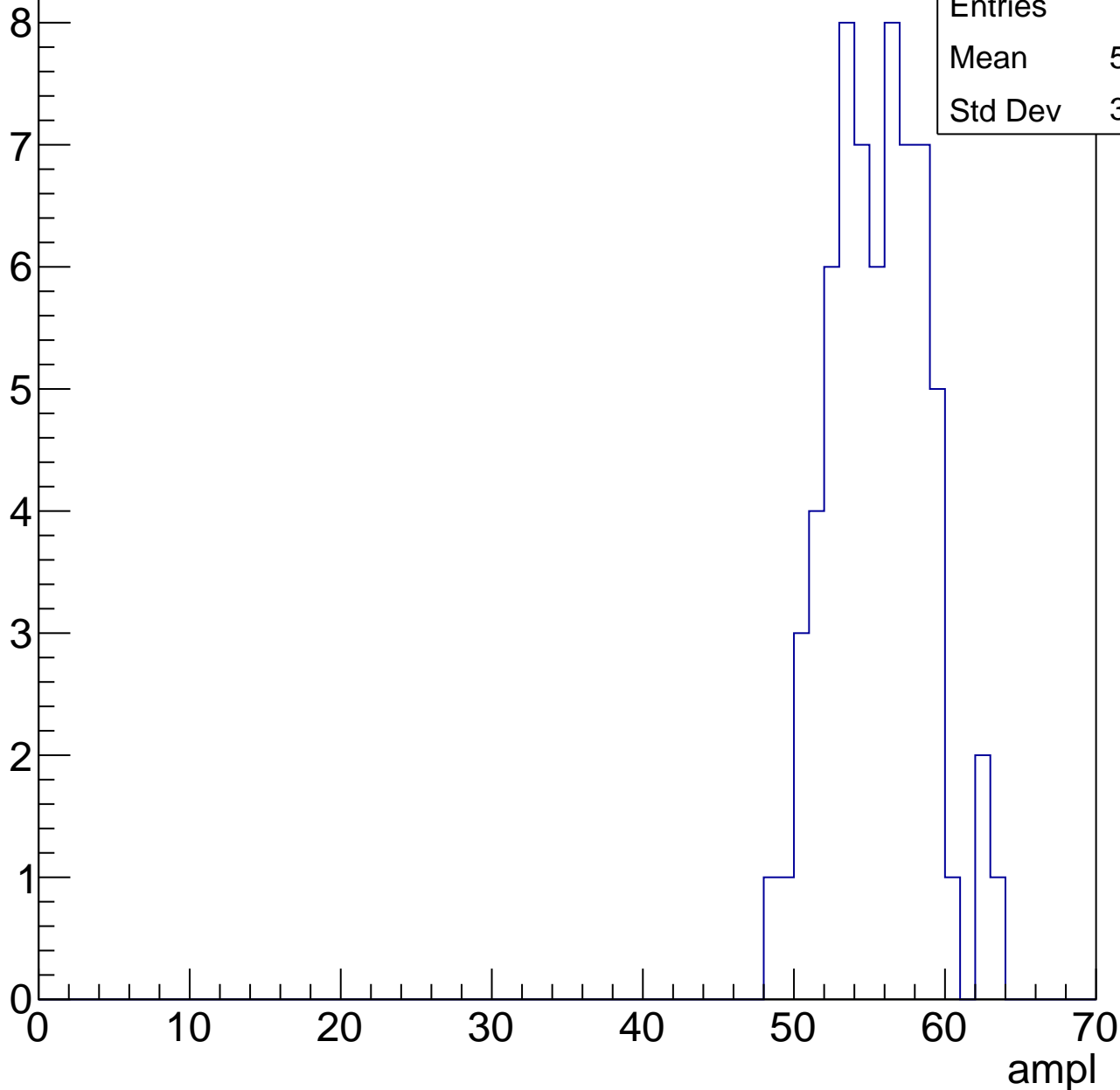


B1L101S, U12-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

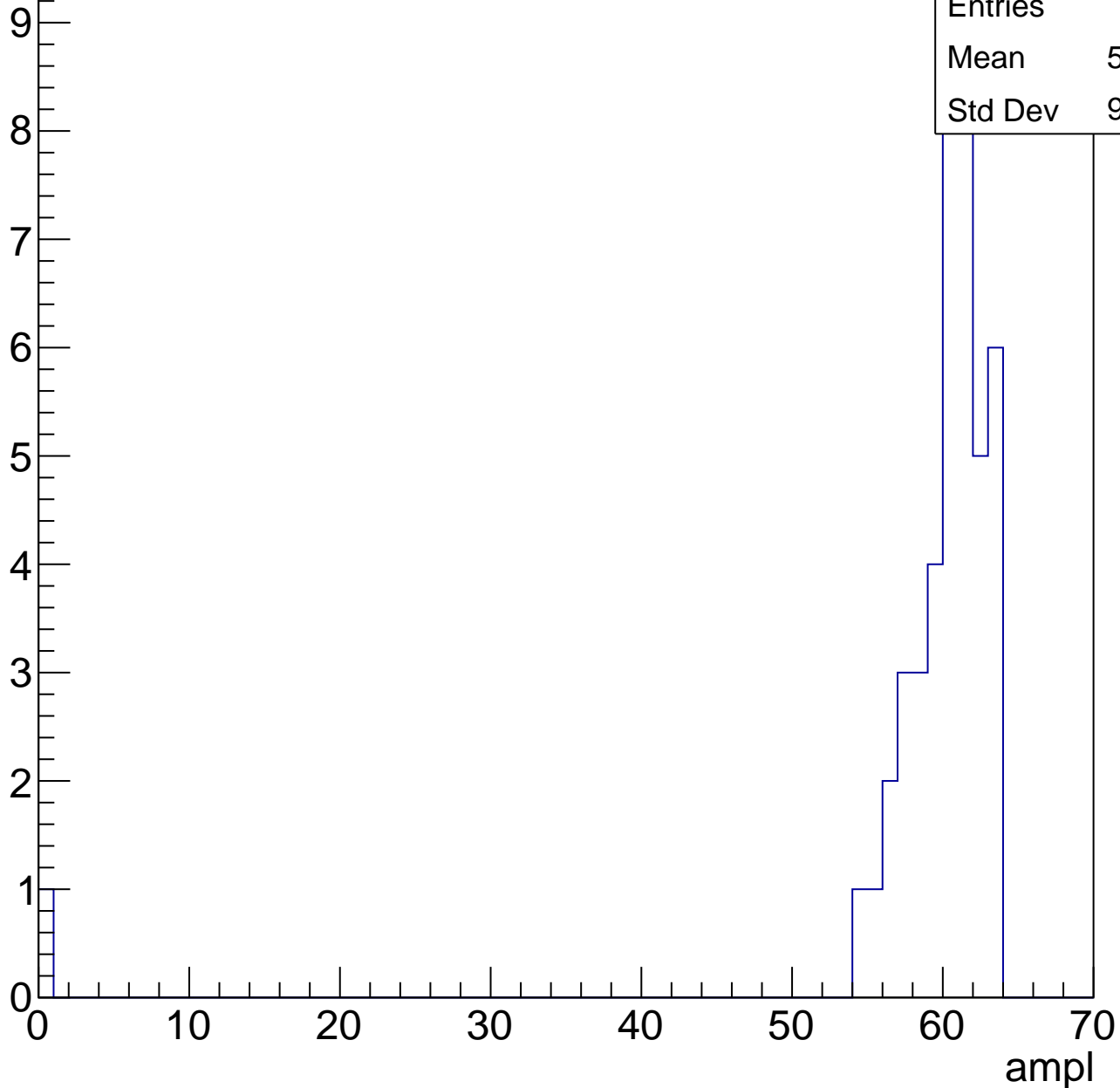
Entries	67
Mean	55.07
Std Dev	3.192



B1L101S, U12-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

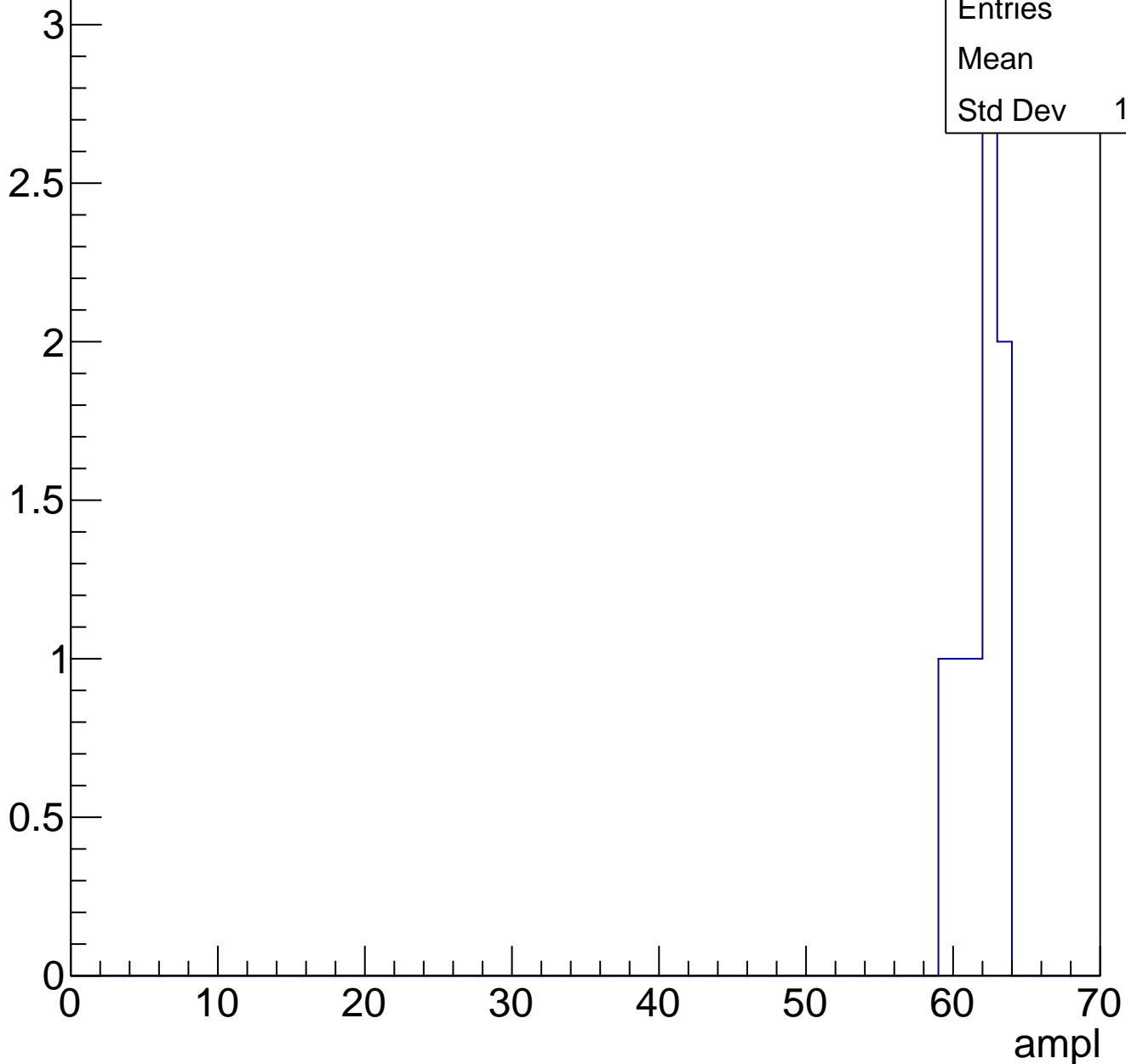
Entry



B1L101S, U12-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch57, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	27.99
Std Dev	3.577

Gaus mean : 28.1199

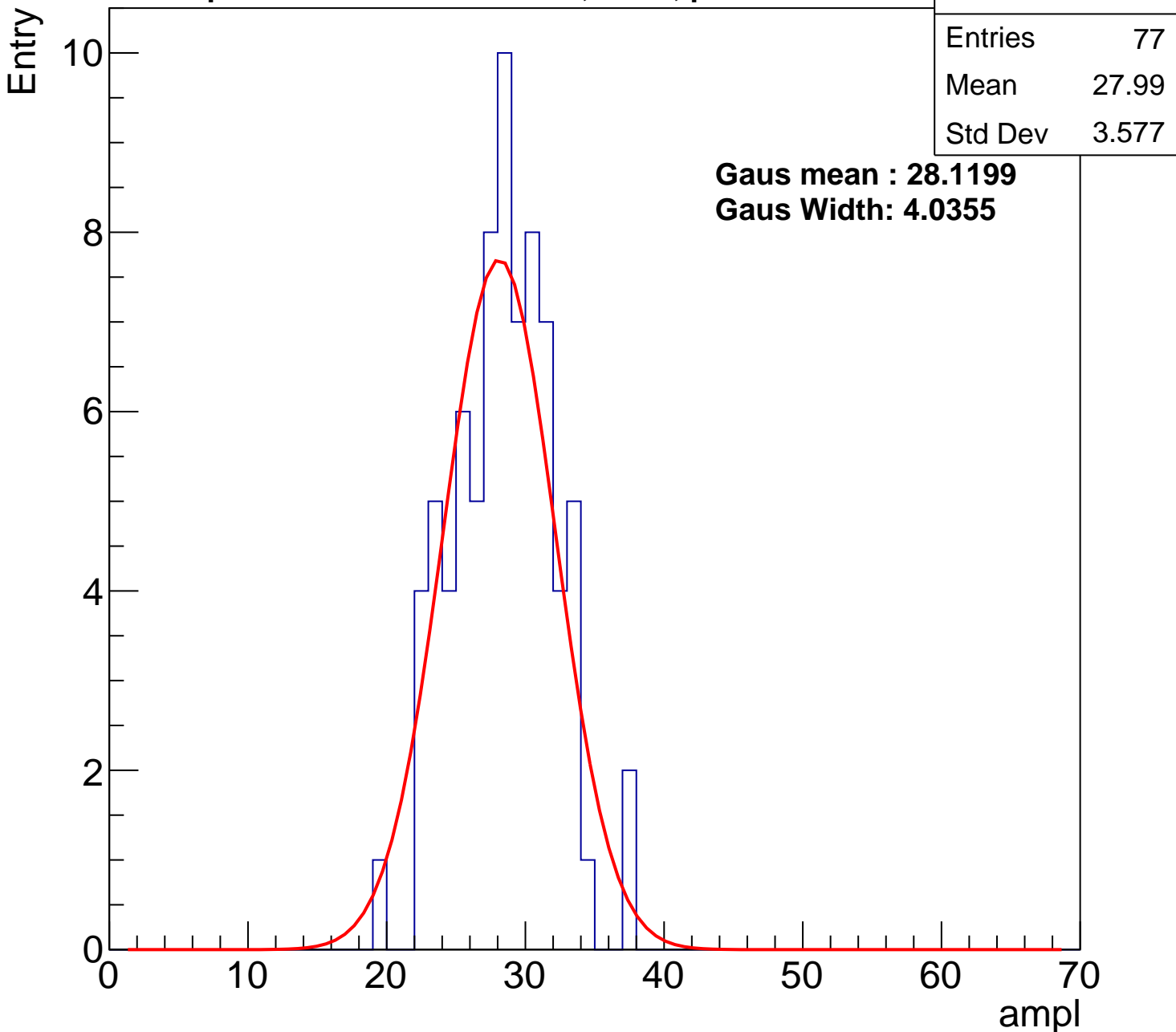
Gaus Width: 4.0355

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch57, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	35.71
Std Dev	3.242

Gaus mean : 36.3633

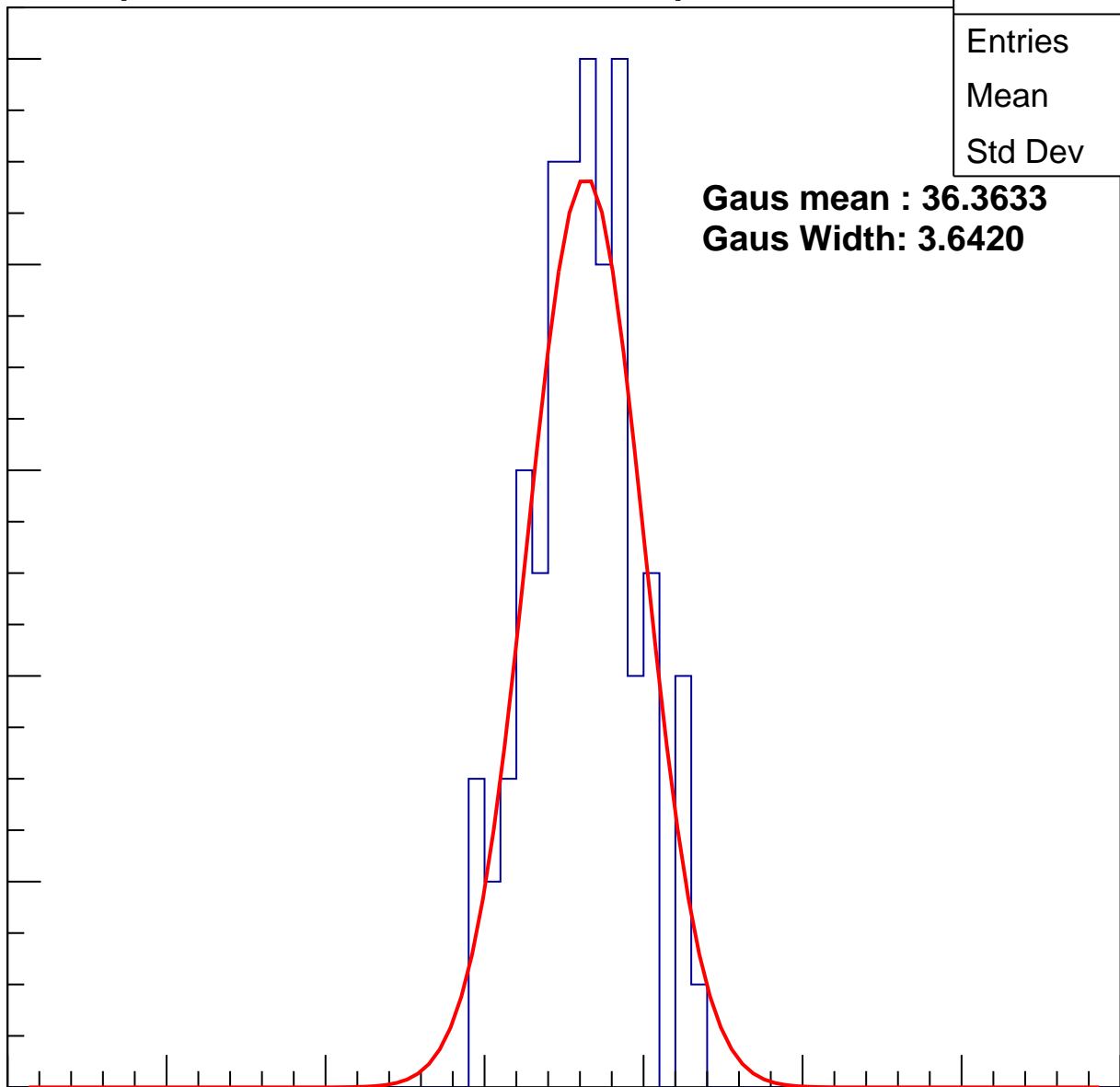
Gaus Width: 3.6420

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch57, adc2

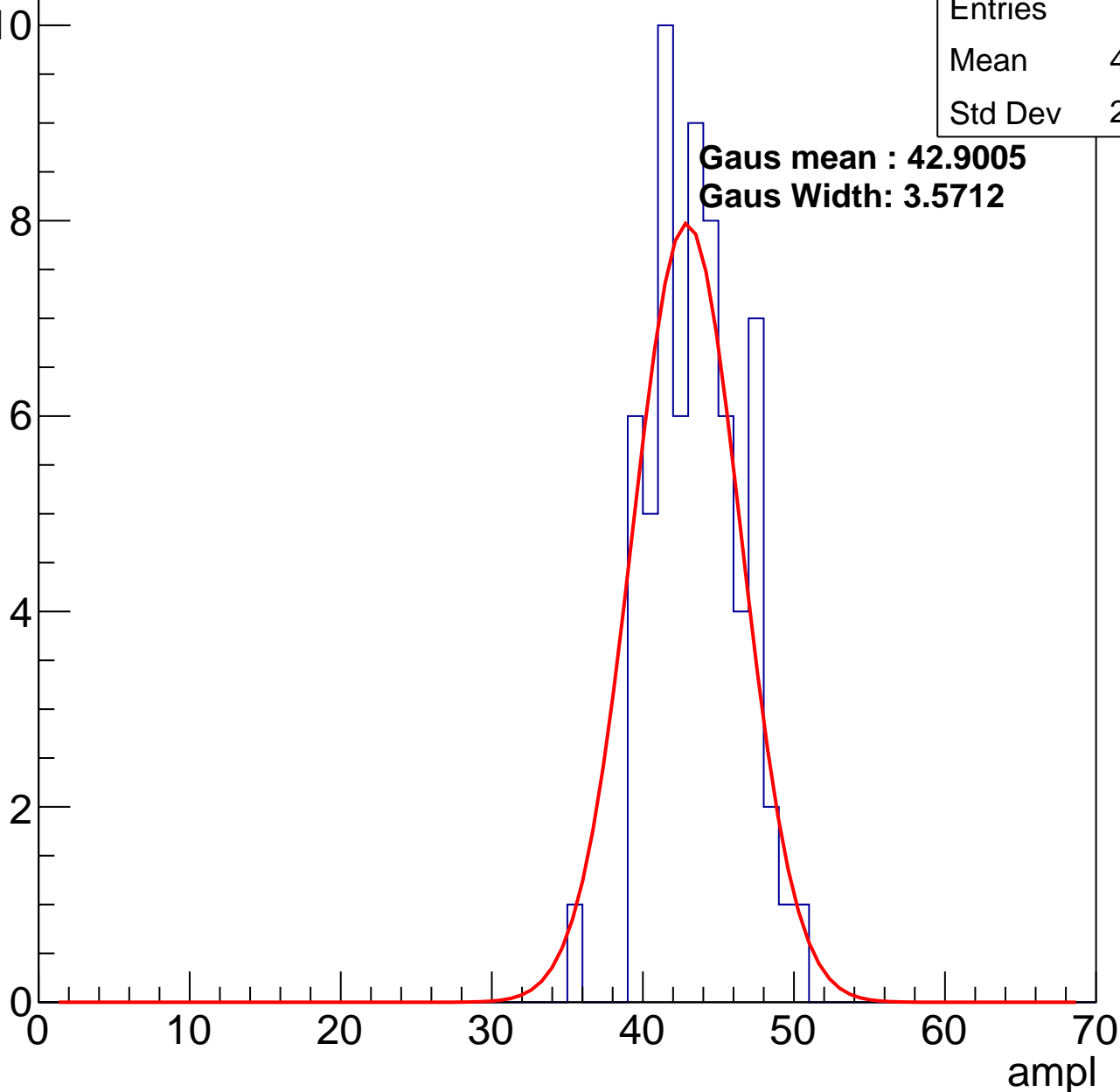
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.15
Std Dev	2.925

Gaus mean : 42.9005

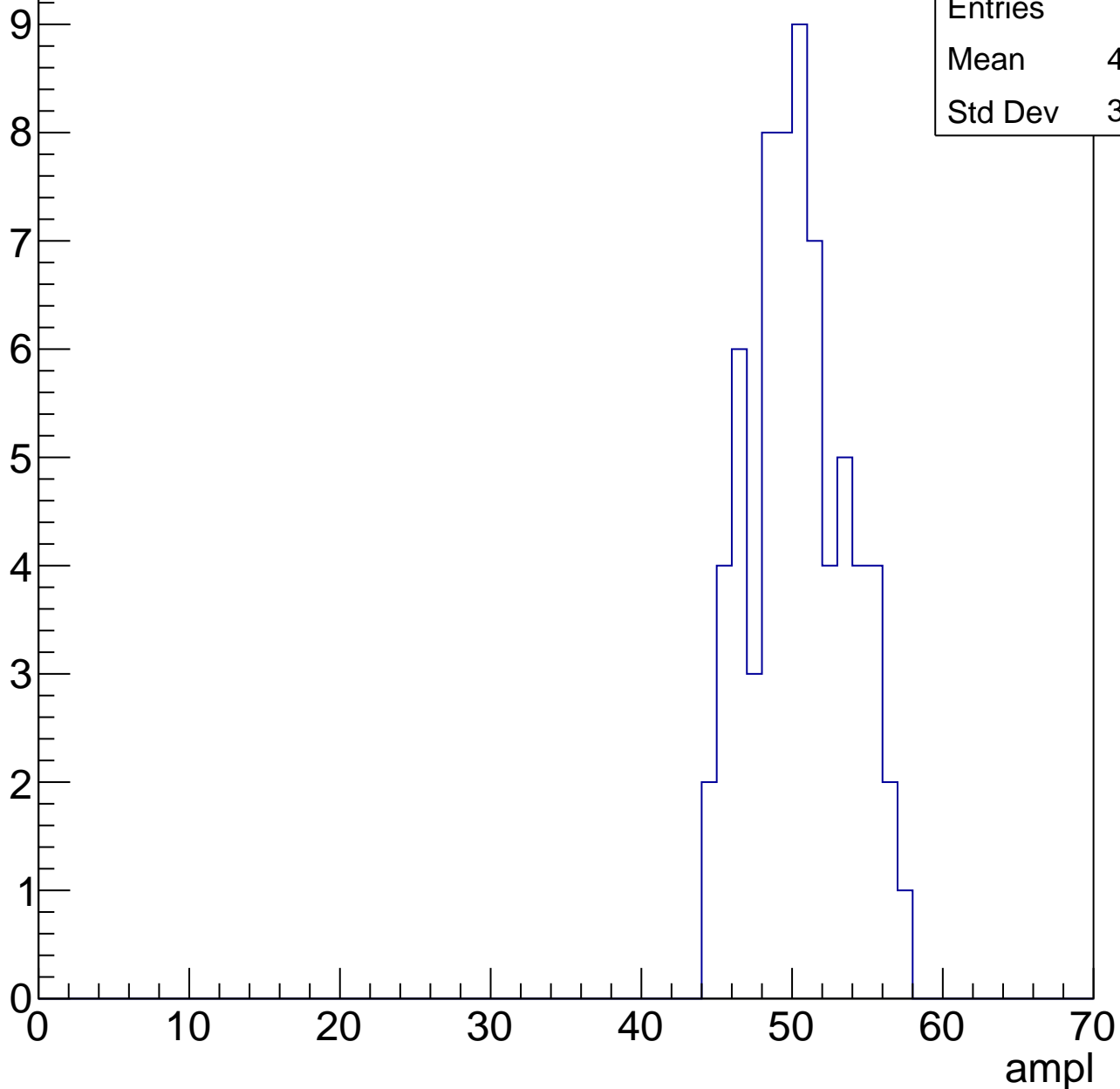
Gaus Width: 3.5712



B1L101S, U12-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

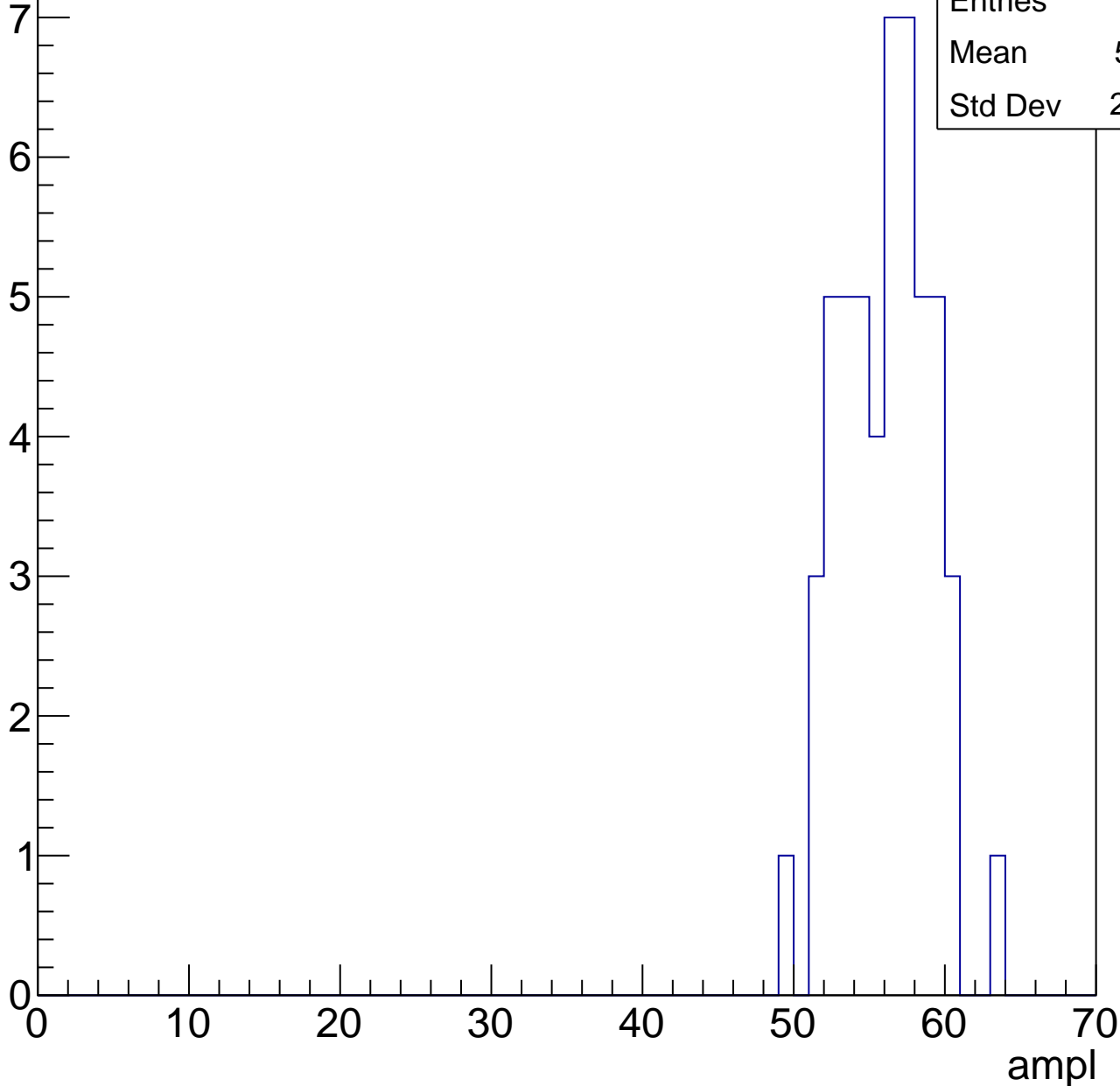


B1L101S, U12-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.61
Std Dev	2.918

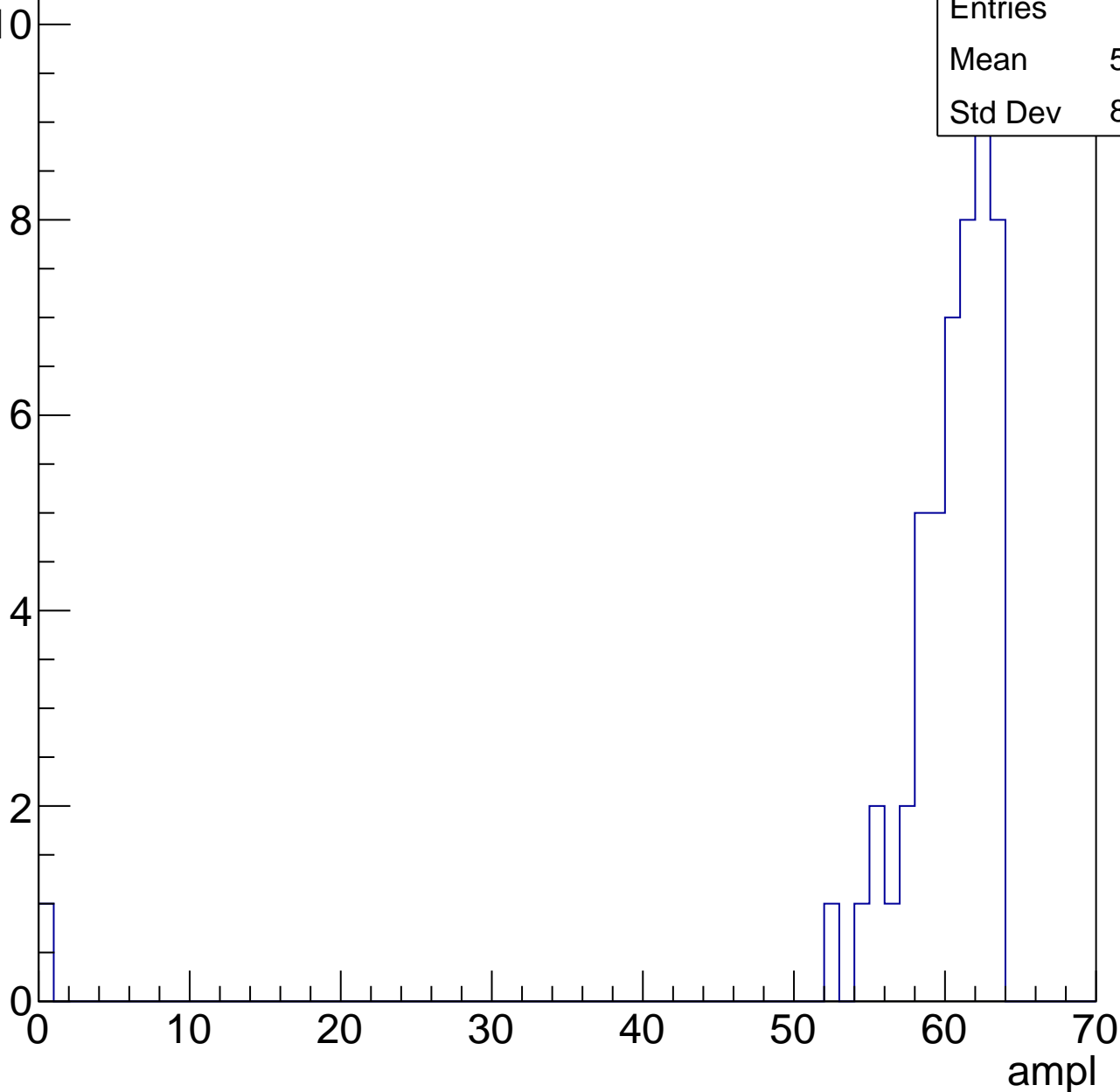


B1L101S, U12-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

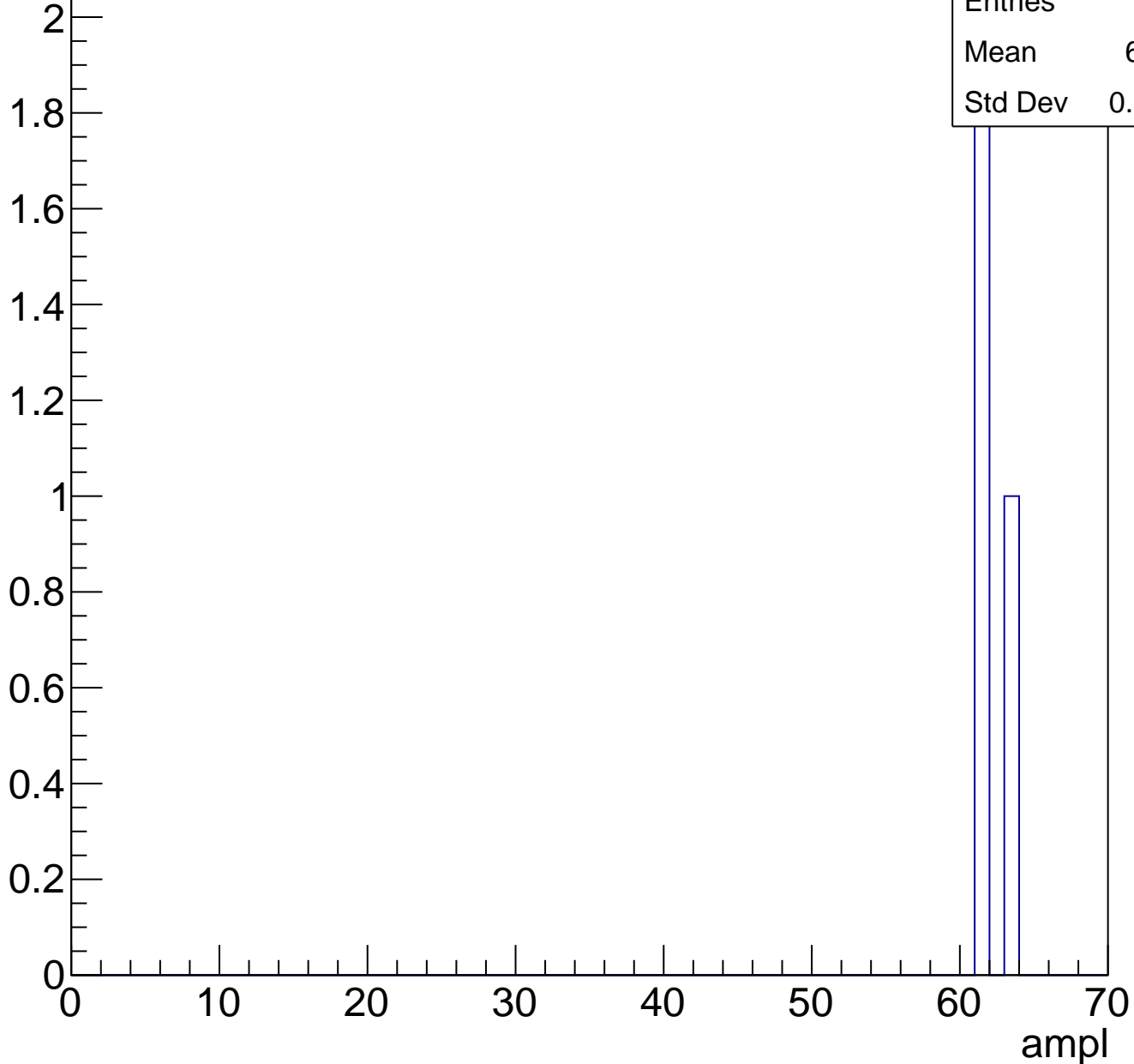
Entries	51
Mean	58.88
Std Dev	8.706



B1L101S, U12-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B1L101S, U12-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch58, adc0

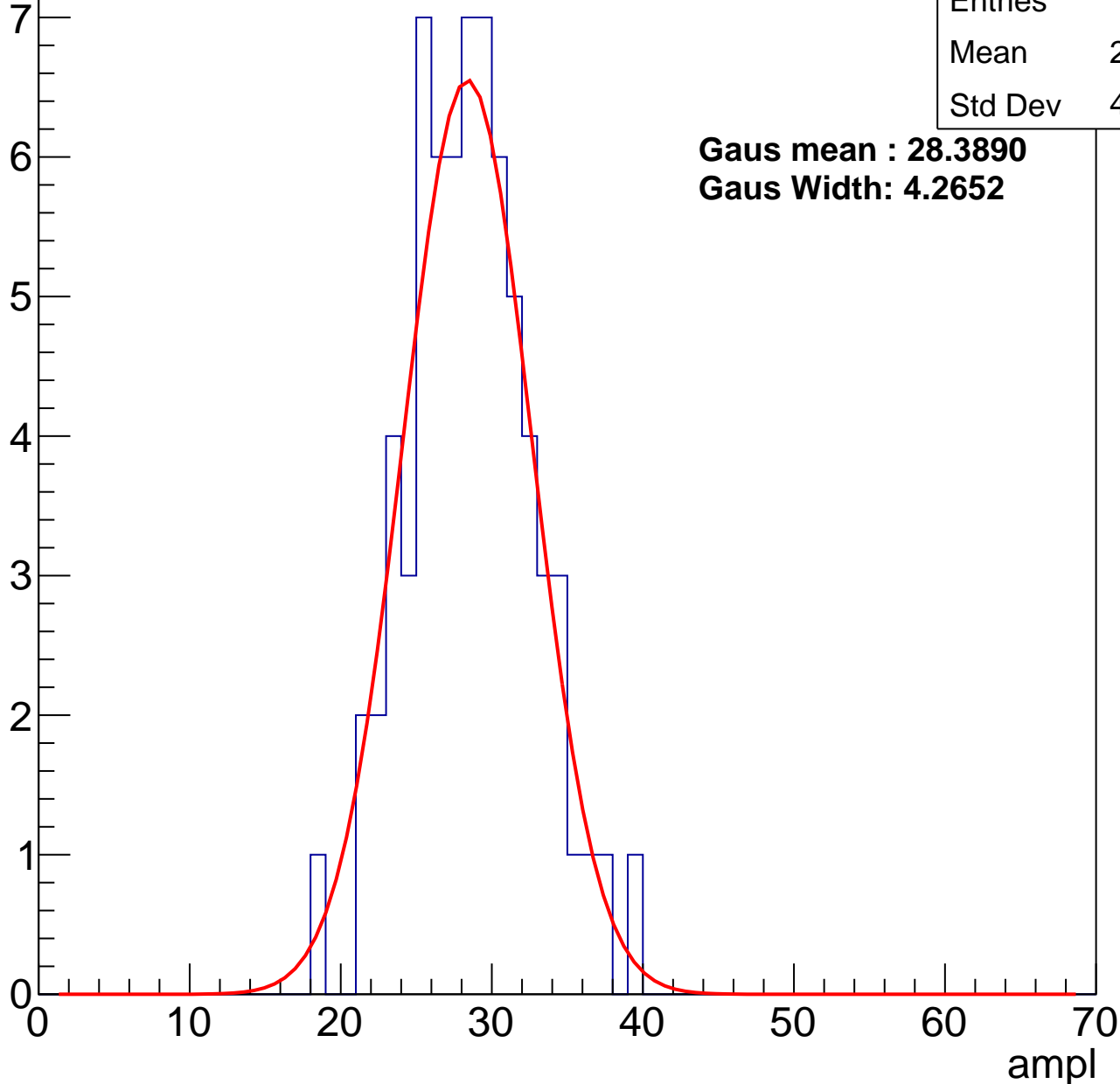
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.16
Std Dev	4.048

Gaus mean : 28.3890

Gaus Width: 4.2652



B1L101S, U12-ch58, adc1

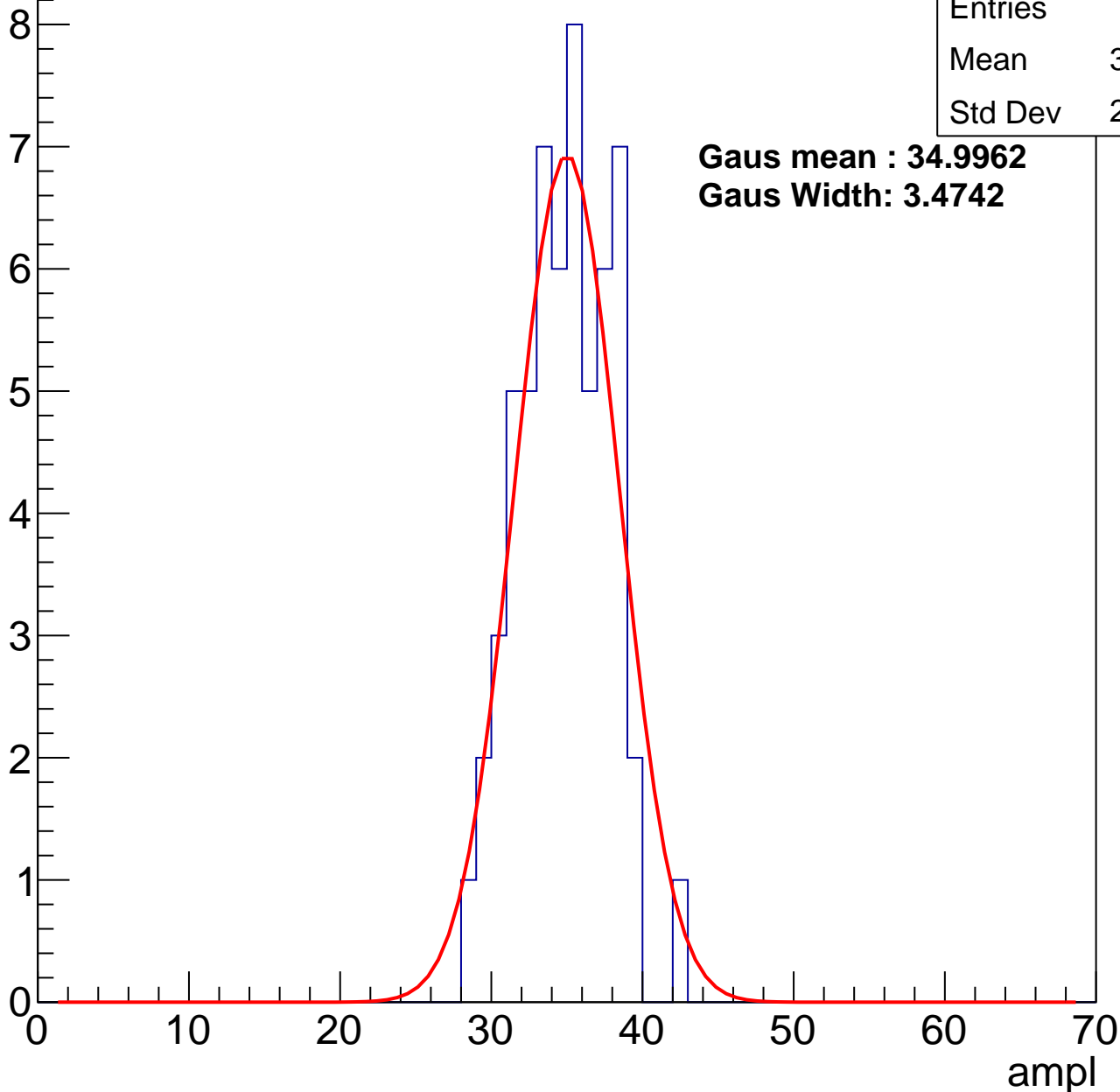
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	34.38
Std Dev	2.953

Gaus mean : 34.9962

Gaus Width: 3.4742



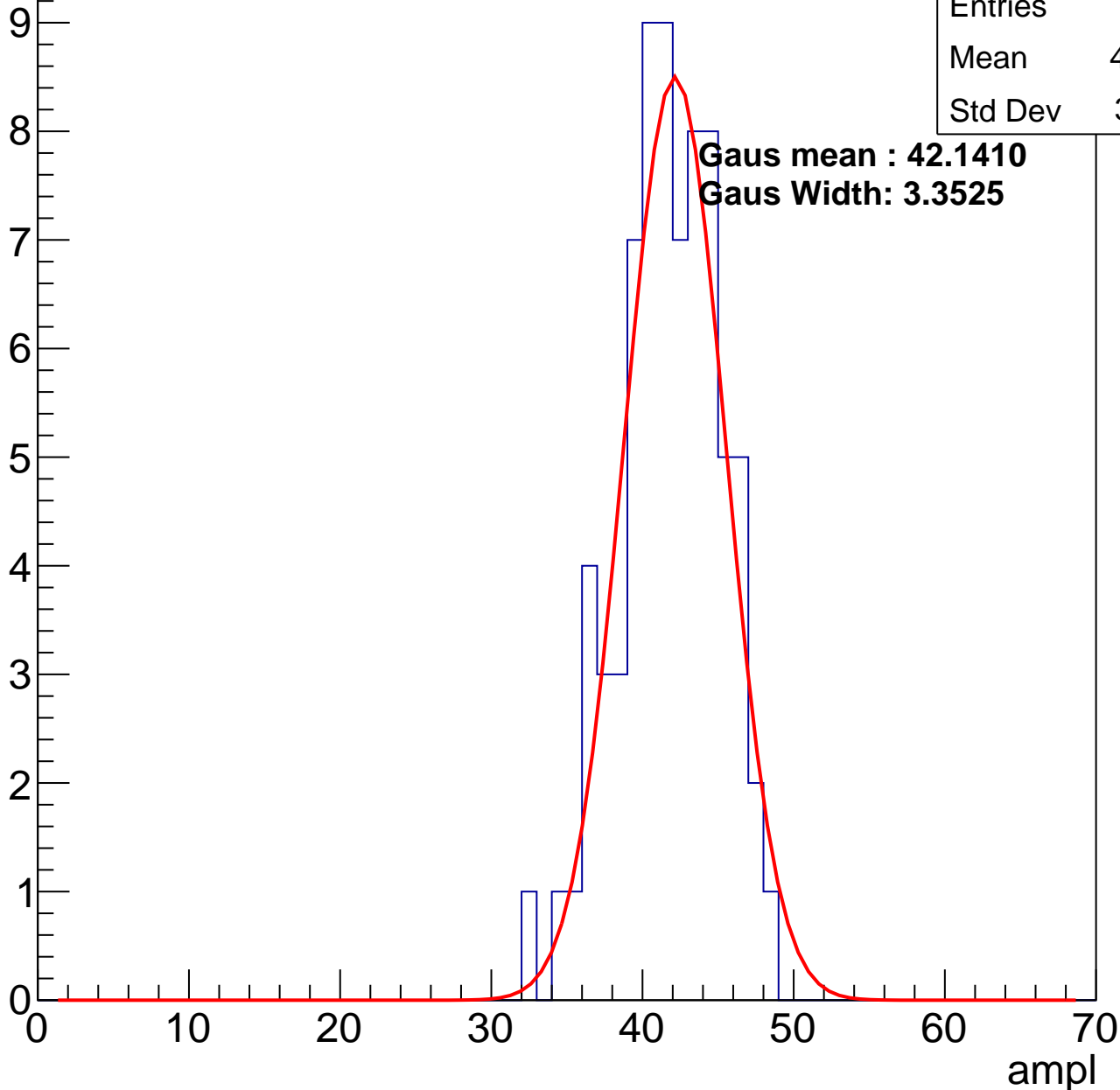
B1L101S, U12-ch58, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	41.34
Std Dev	3.301

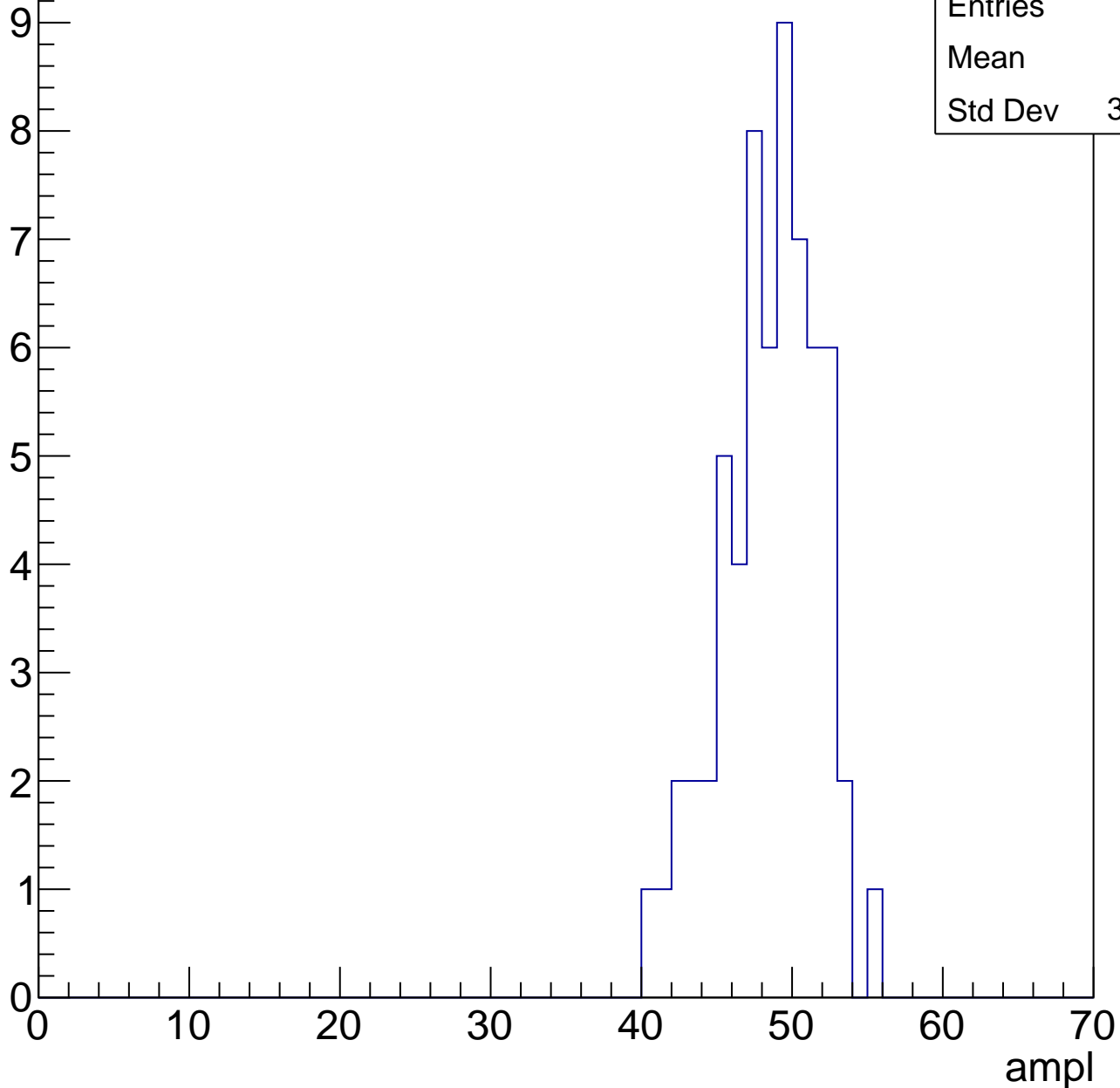
Gaus mean : 42.1410
Gaus Width: 3.3525



B1L101S, U12-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	62
Mean	48.1
Std Dev	3.166

B1L101S, U12-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	54.78
Std Dev	3.515

Entry

10

8

6

4

2

0

0

10

20

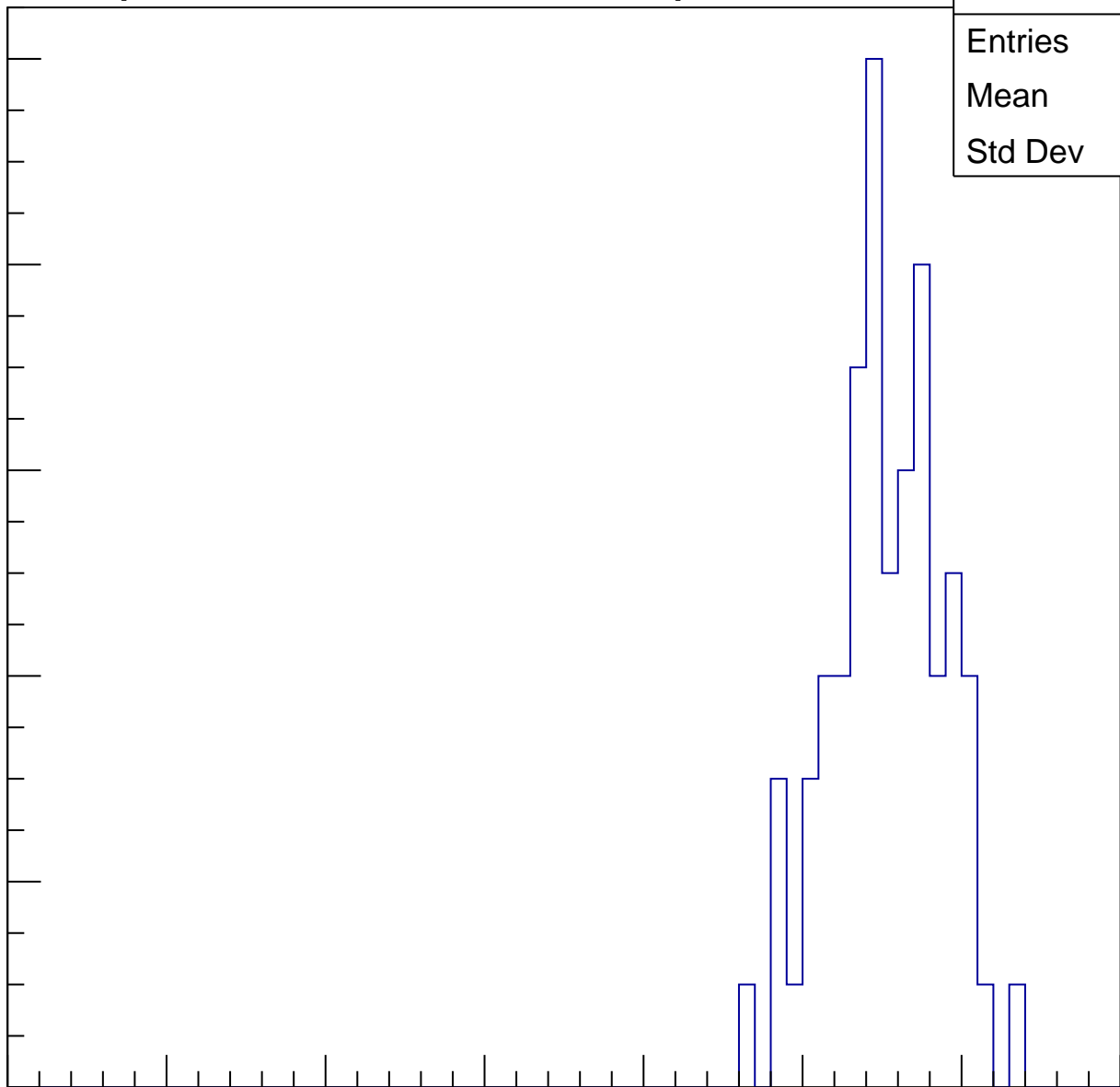
30

40

50

60

ampl

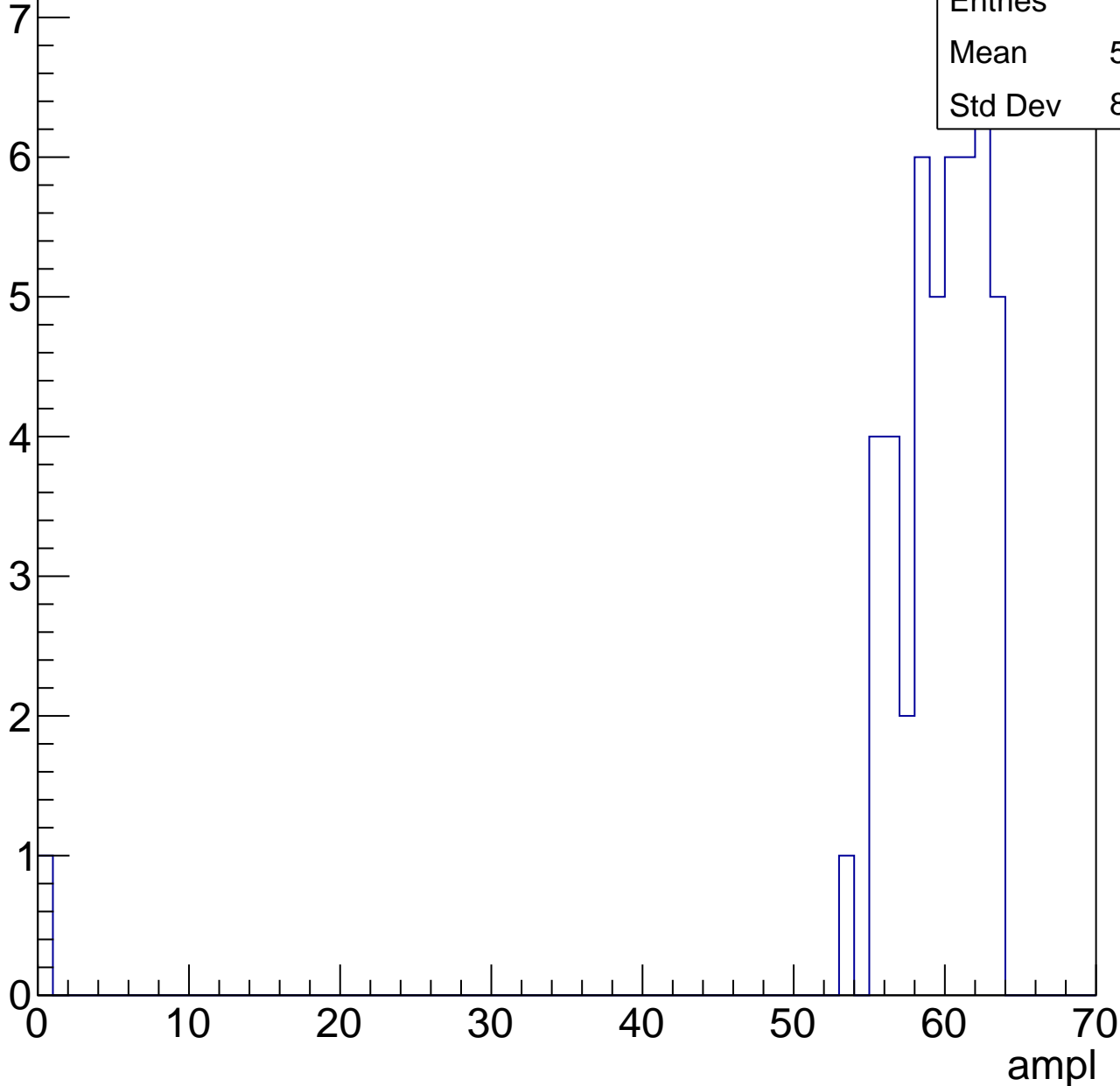


B1L101S, U12-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

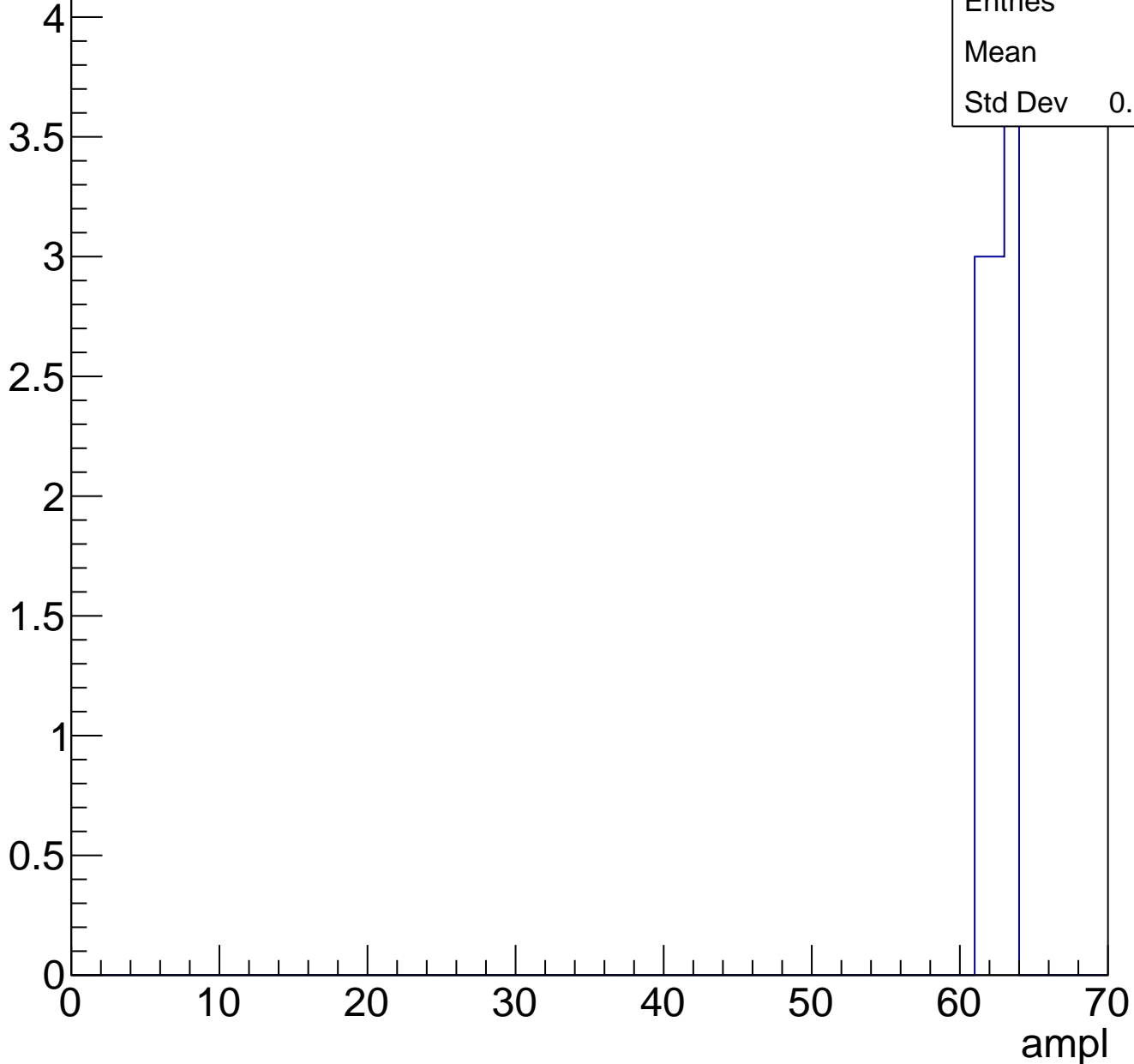
Entries	47
Mean	58.06
Std Dev	8.948



B1L101S, U12-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch59, adc0

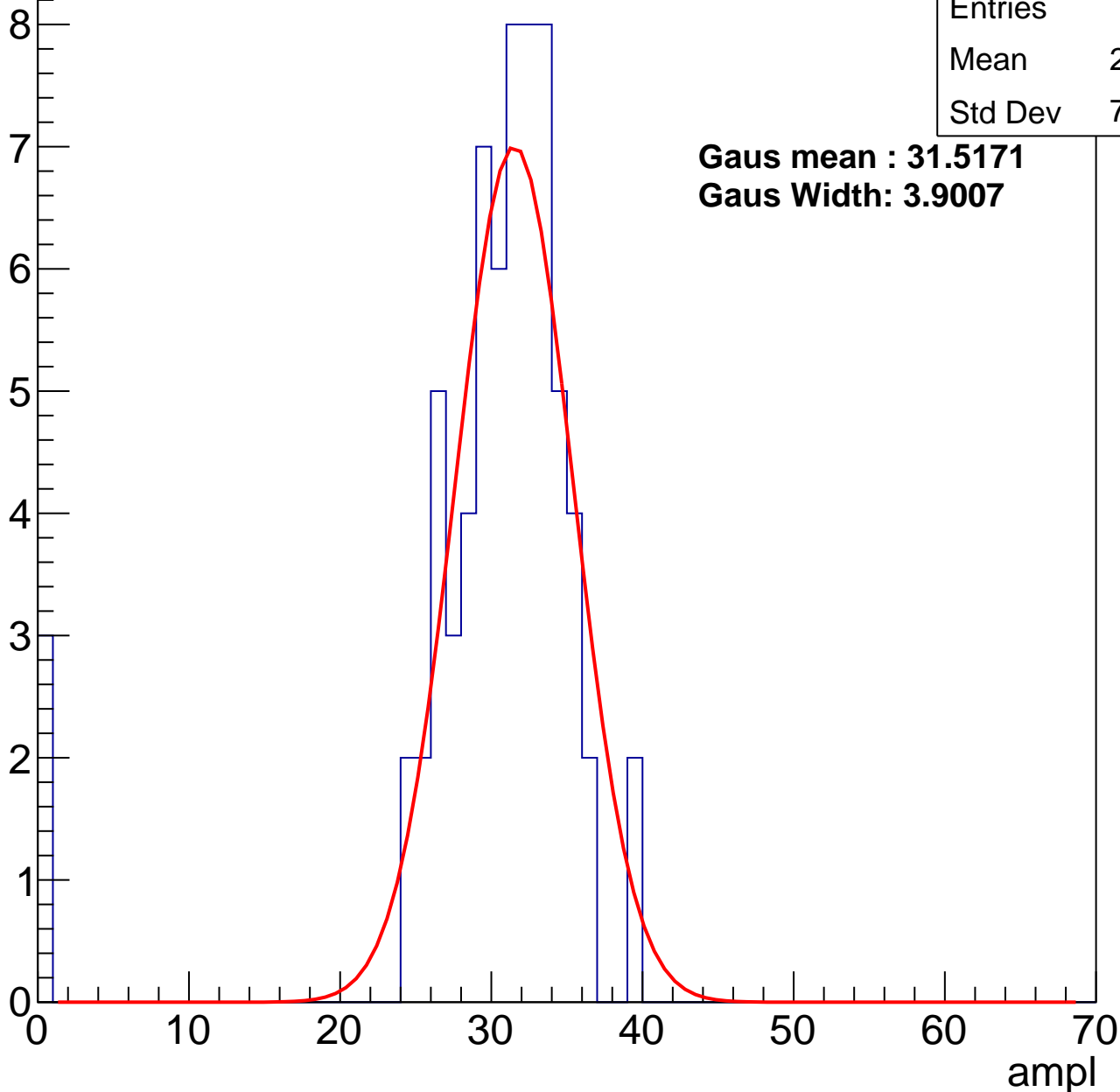
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.45
Std Dev	7.078

Gaus mean : 31.5171

Gaus Width: 3.9007



B1L101S, U12-ch59, adc1

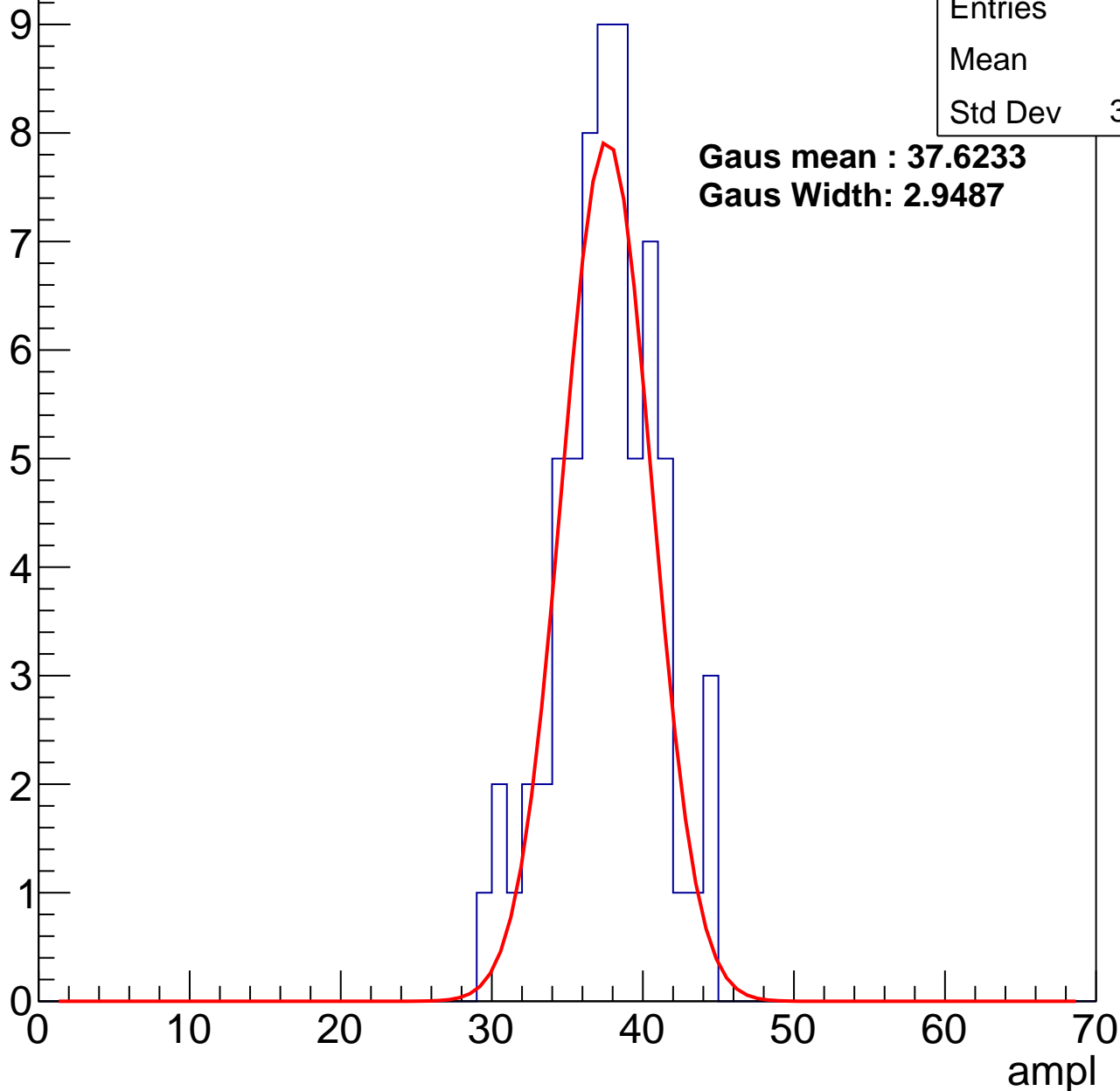
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.2
Std Dev	3.327

Gaus mean : 37.6233

Gaus Width: 2.9487



B1L101S, U12-ch59, adc2

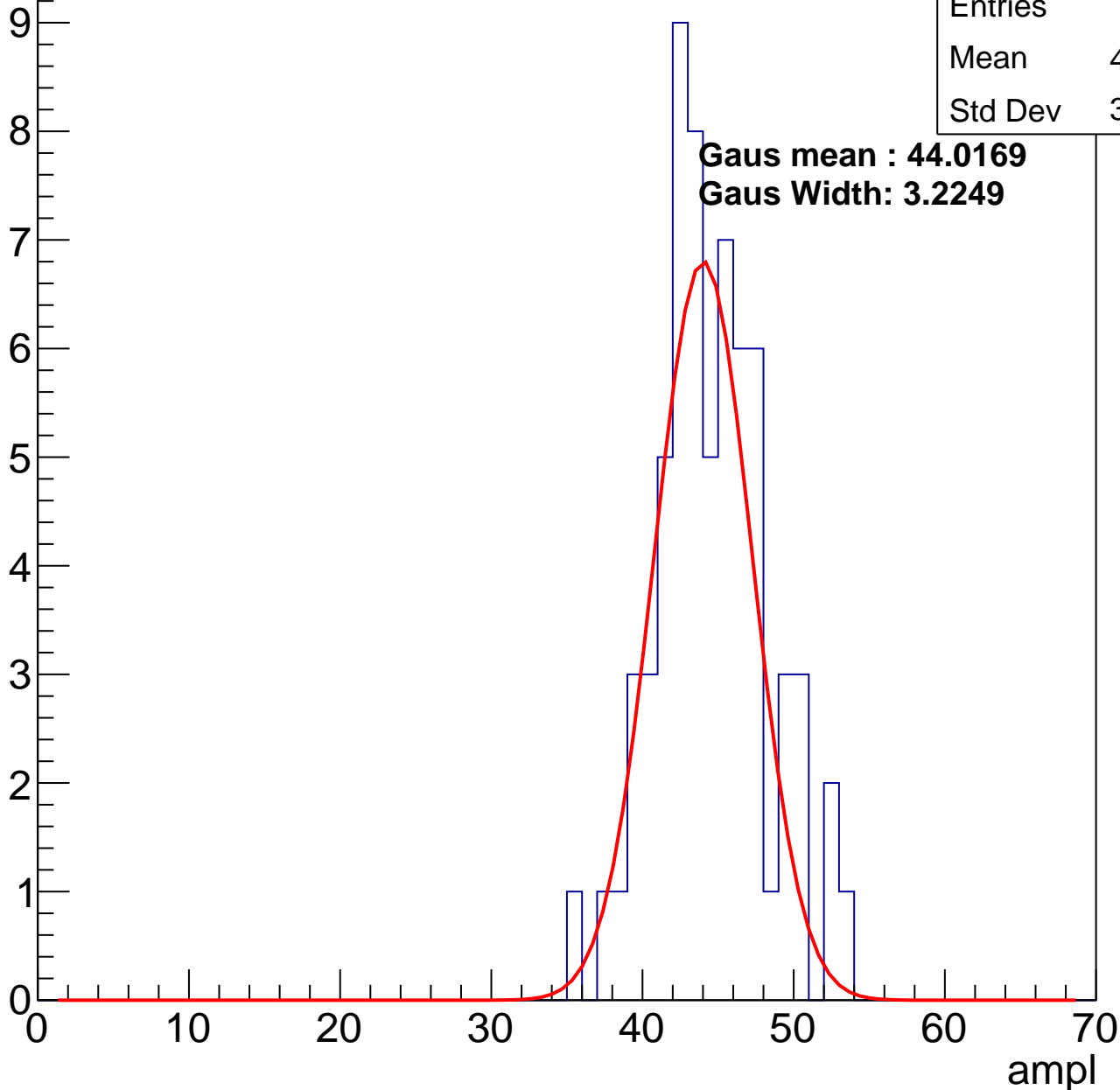
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.14
Std Dev	3.658

Gaus mean : 44.0169

Gaus Width: 3.2249

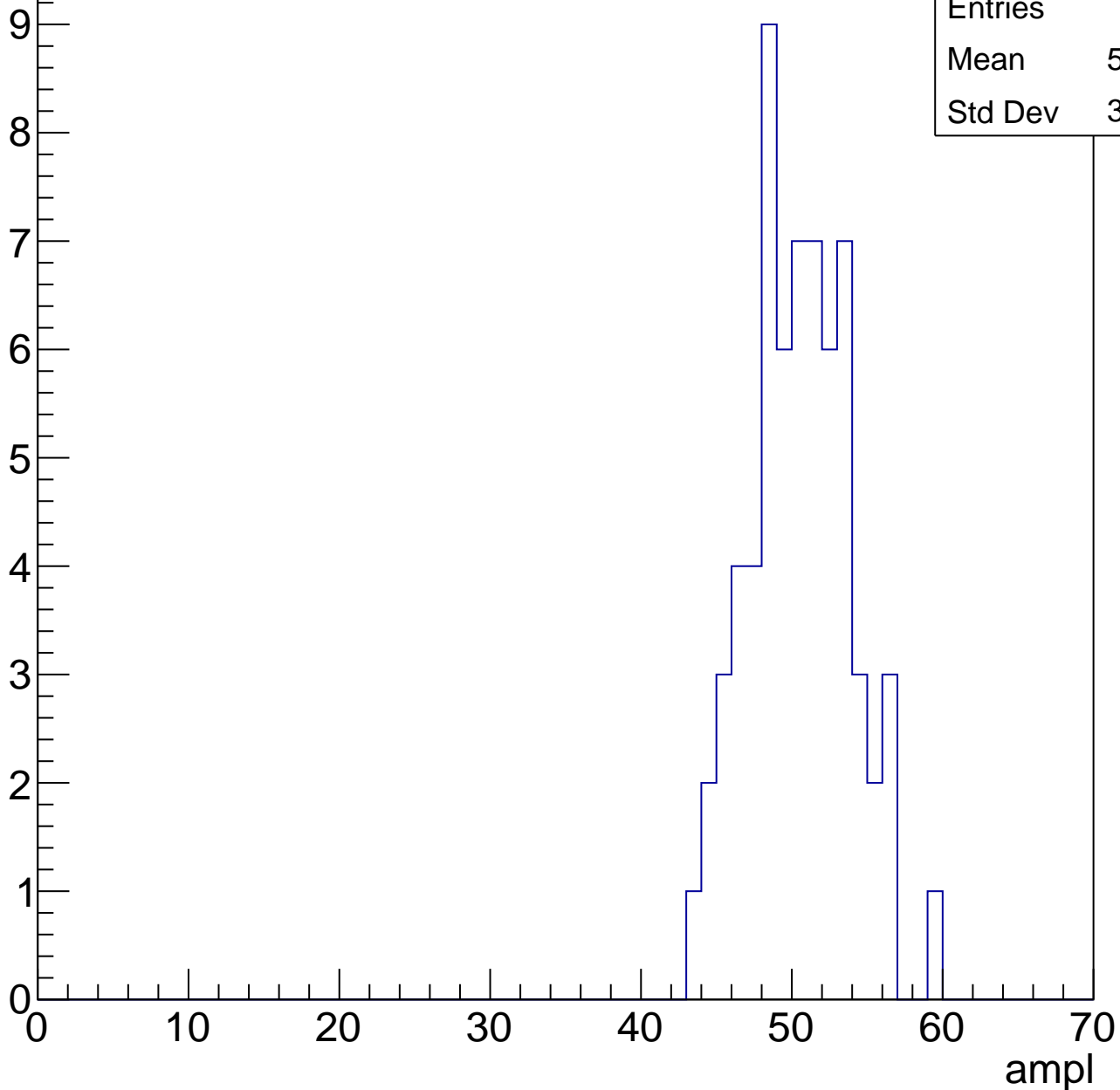


B1L101S, U12-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.05
Std Dev	3.326

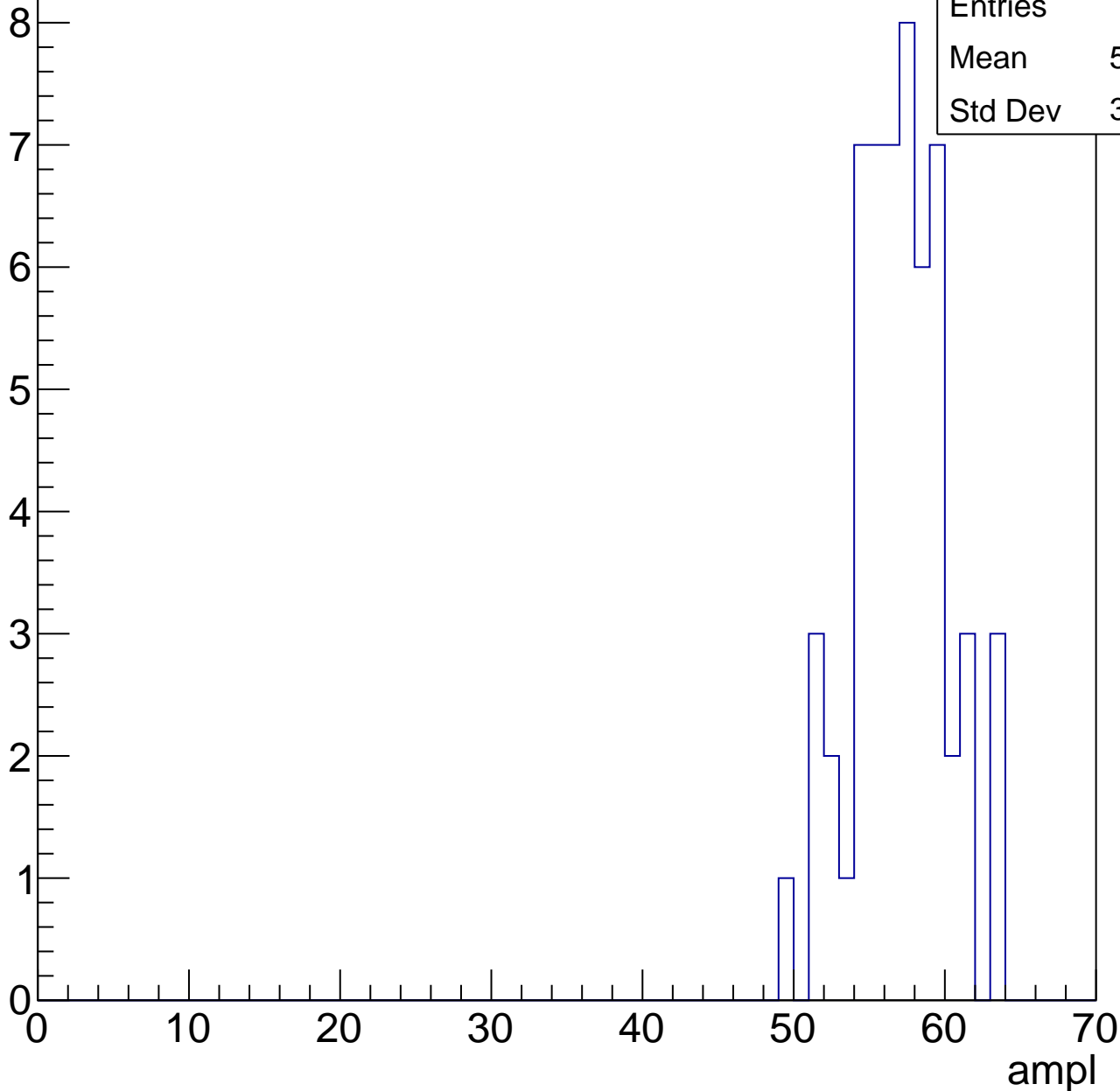


B1L101S, U12-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.54
Std Dev	3.055

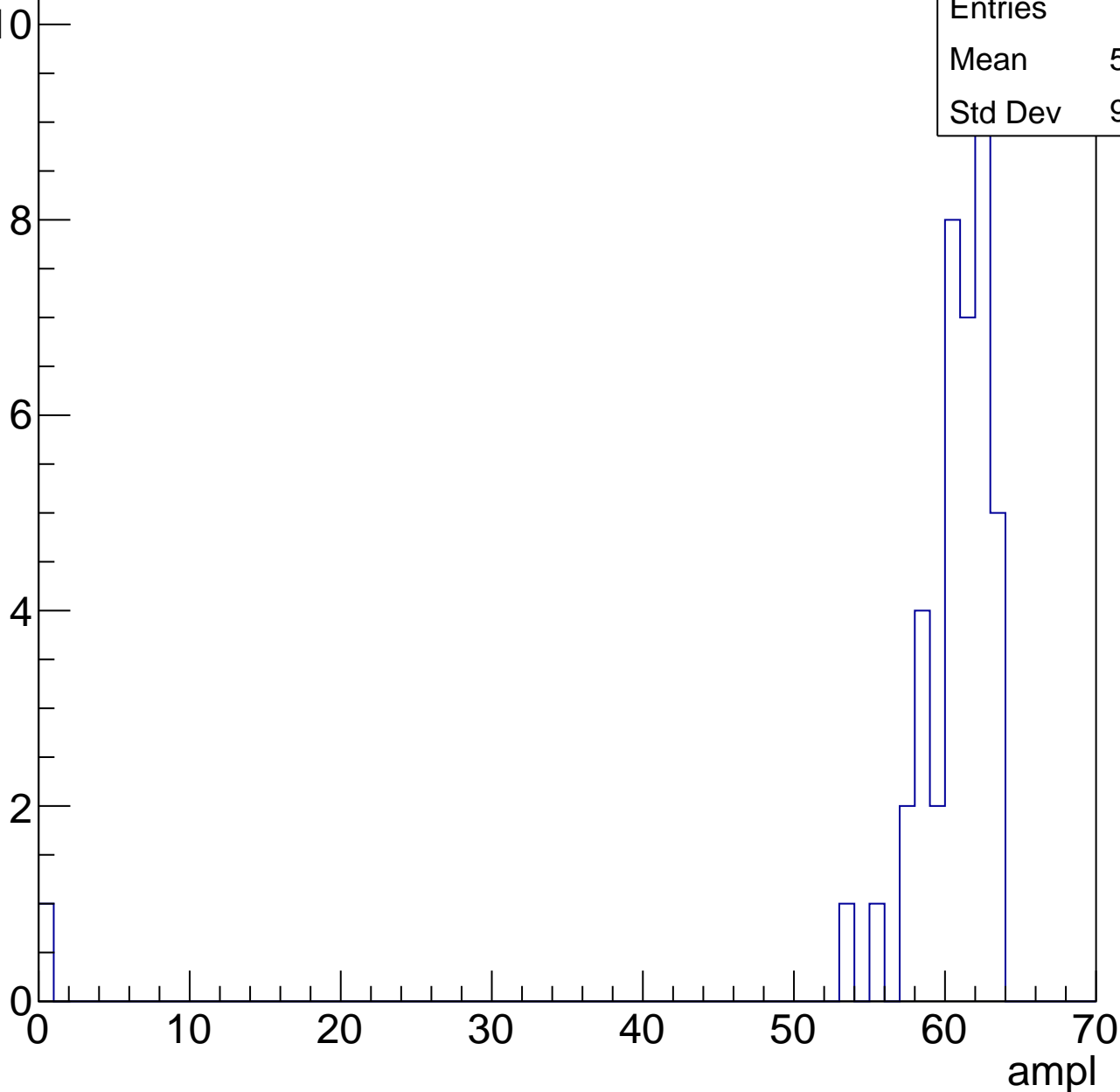


B1L101S, U12-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.88
Std Dev	9.564



B1L101S, U12-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U12-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

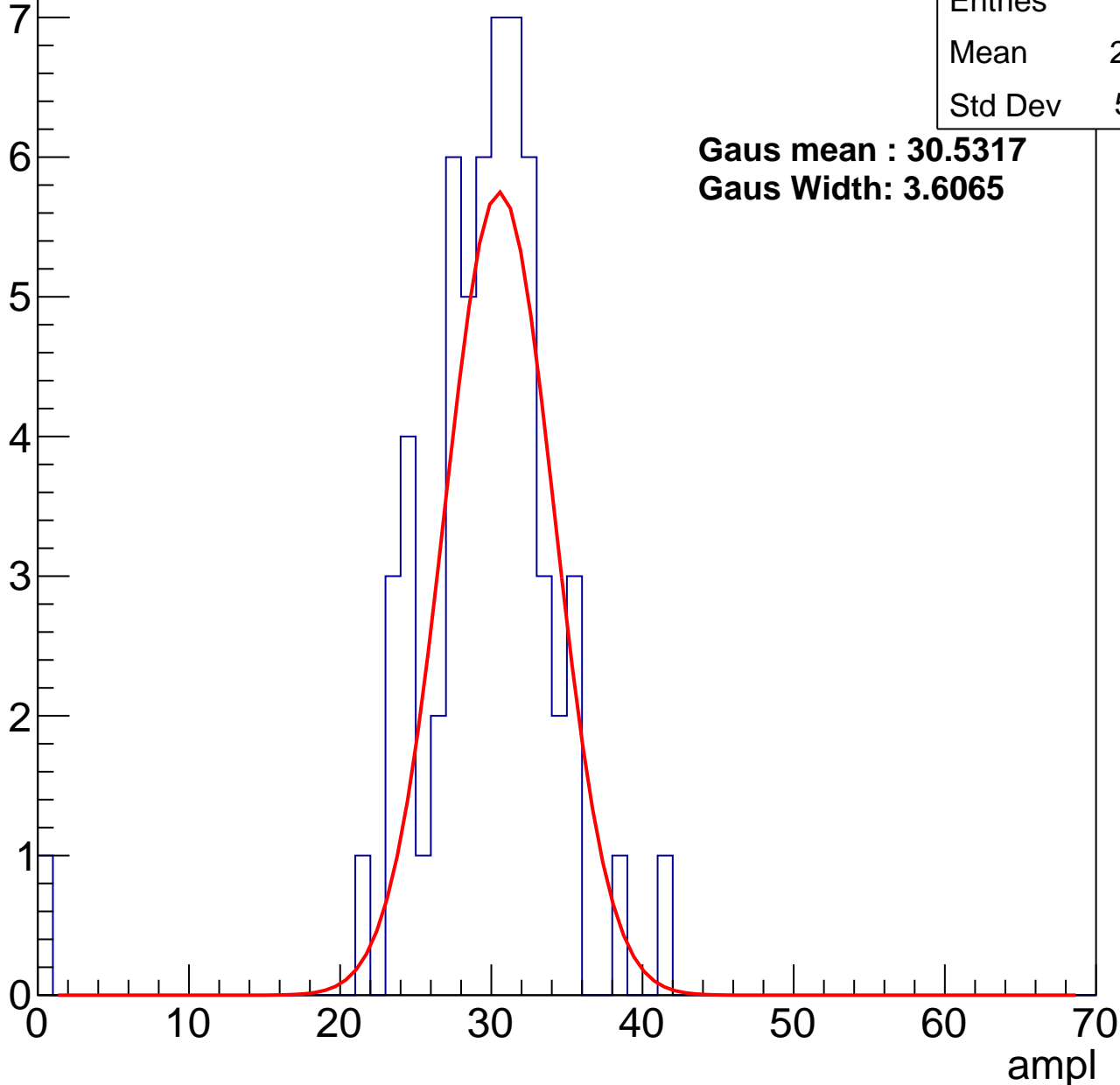
B1L101S, U12-ch60, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.97
Std Dev	5.361

Gaus mean : 30.5317
Gaus Width: 3.6065



B1L101S, U12-ch60, adc1

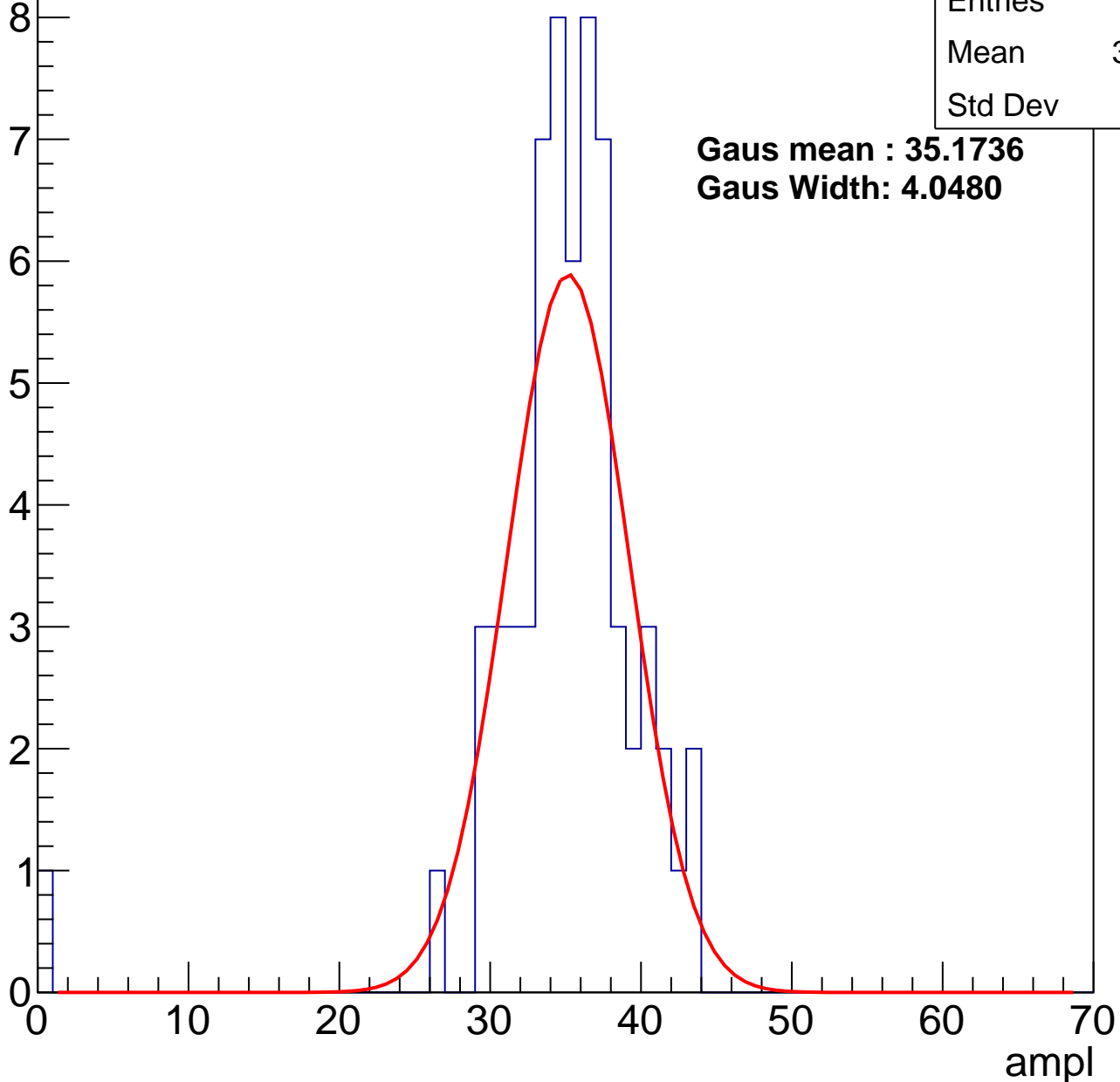
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	34.51
Std Dev	5.64

Gaus mean : 35.1736

Gaus Width: 4.0480



B1L101S, U12-ch60, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8

7

6

5

4

3

2

1

0

Entries

72

Mean

41.81

Std Dev

3.252

Gaus mean : 41.9526

Gaus Width: 3.2768

0

10

20

30

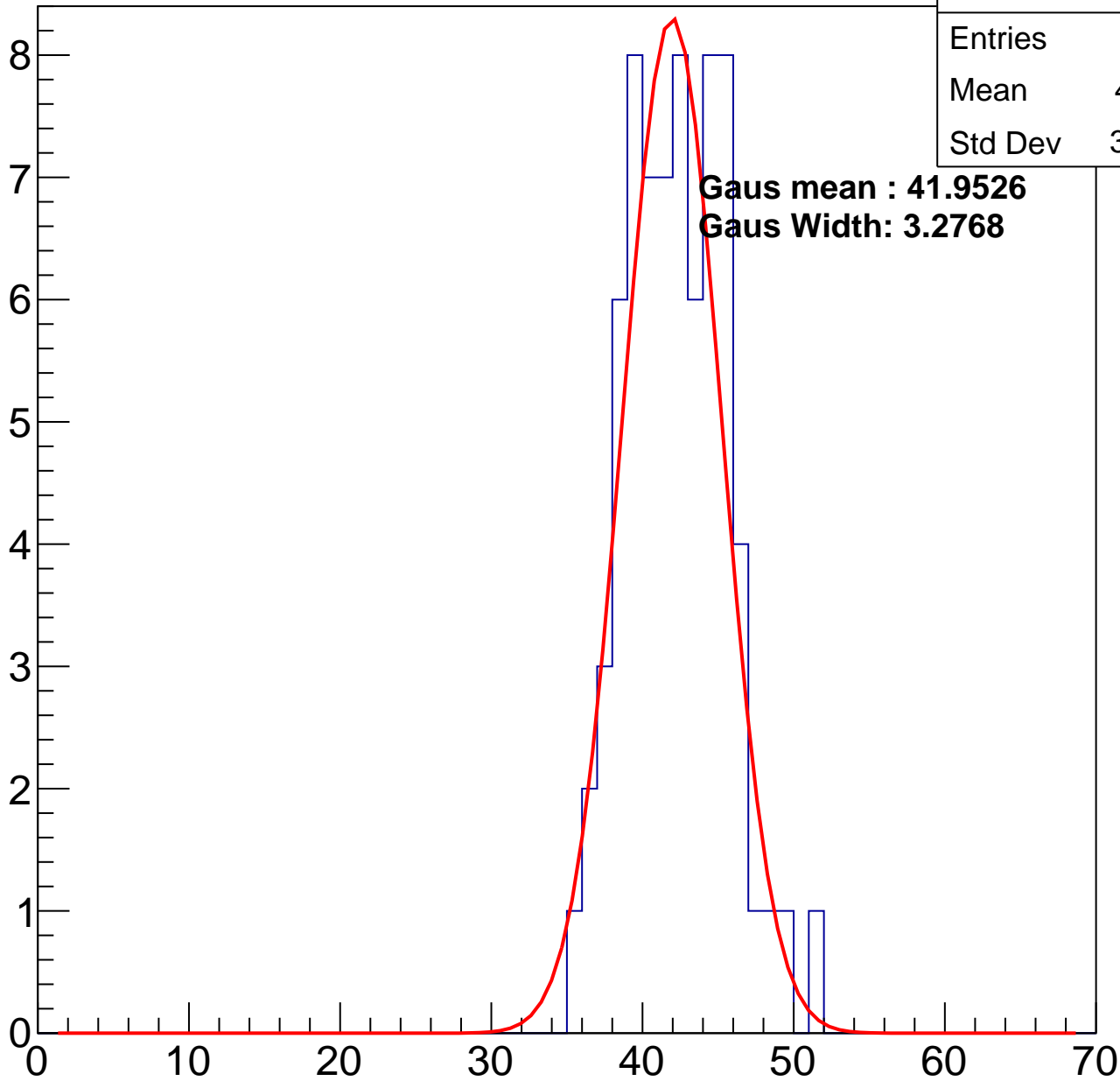
40

50

60

70

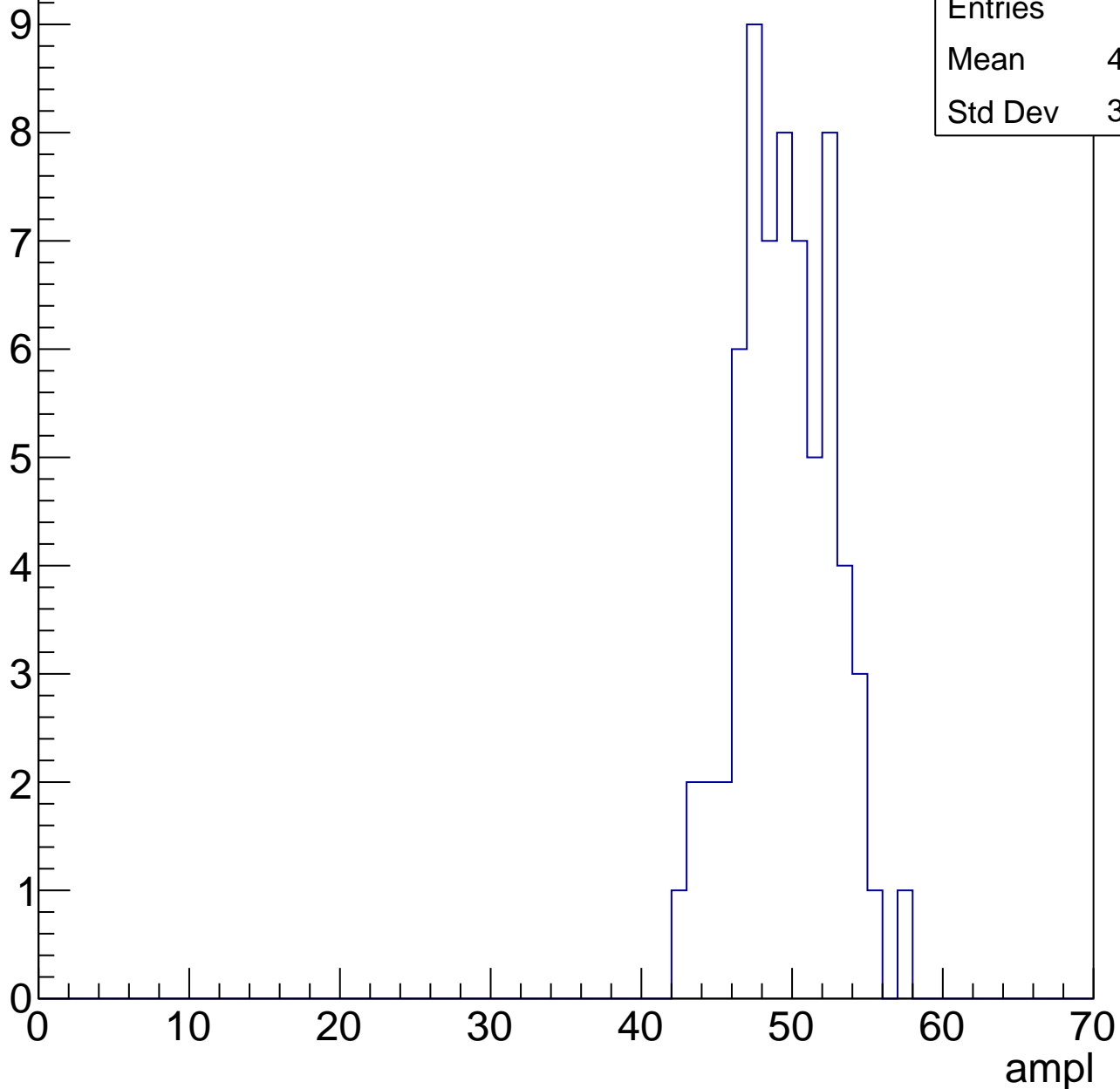
ampl



B1L101S, U12-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



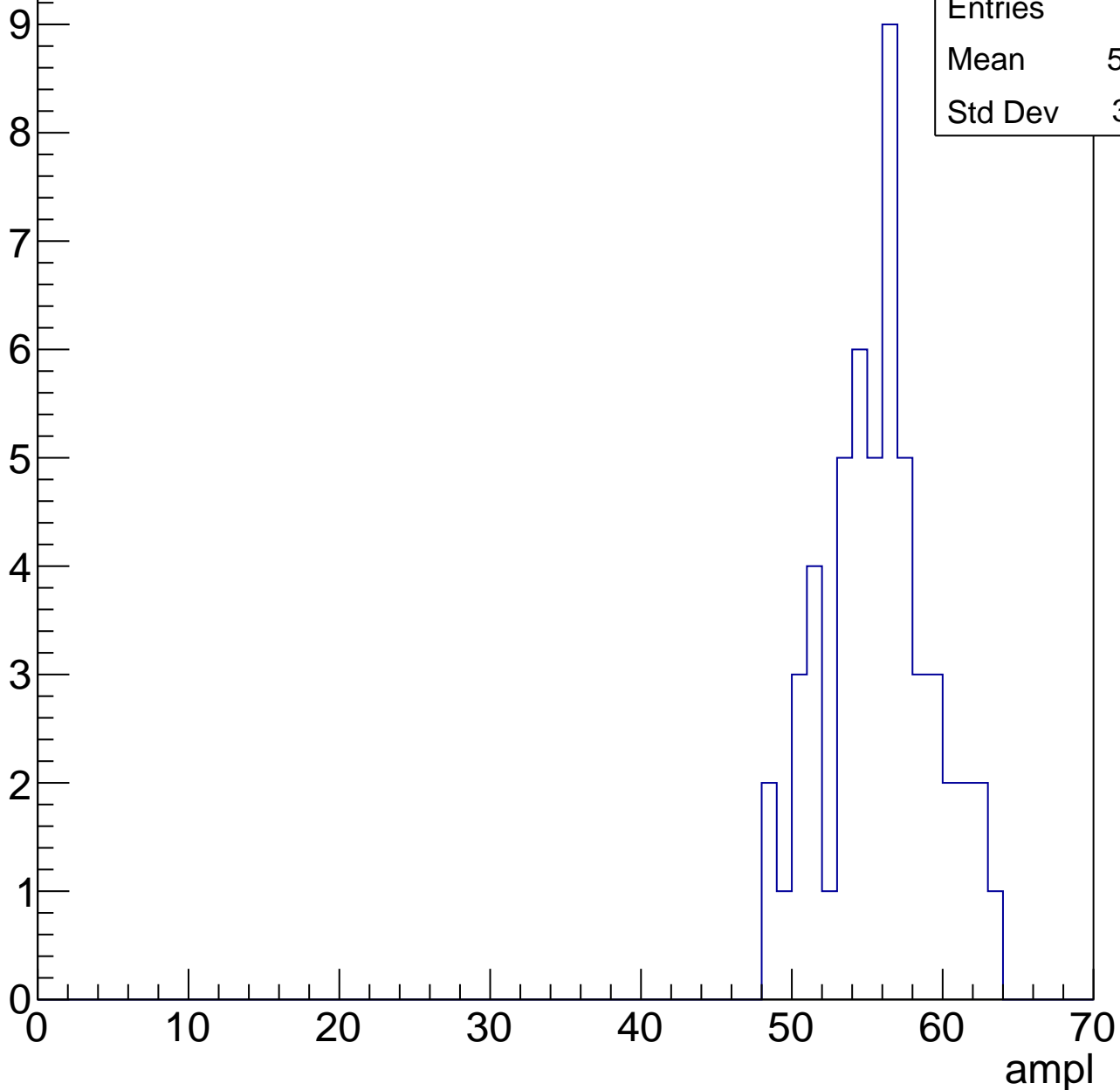
Entries	66
Mean	49.09
Std Dev	3.108

B1L101S, U12-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

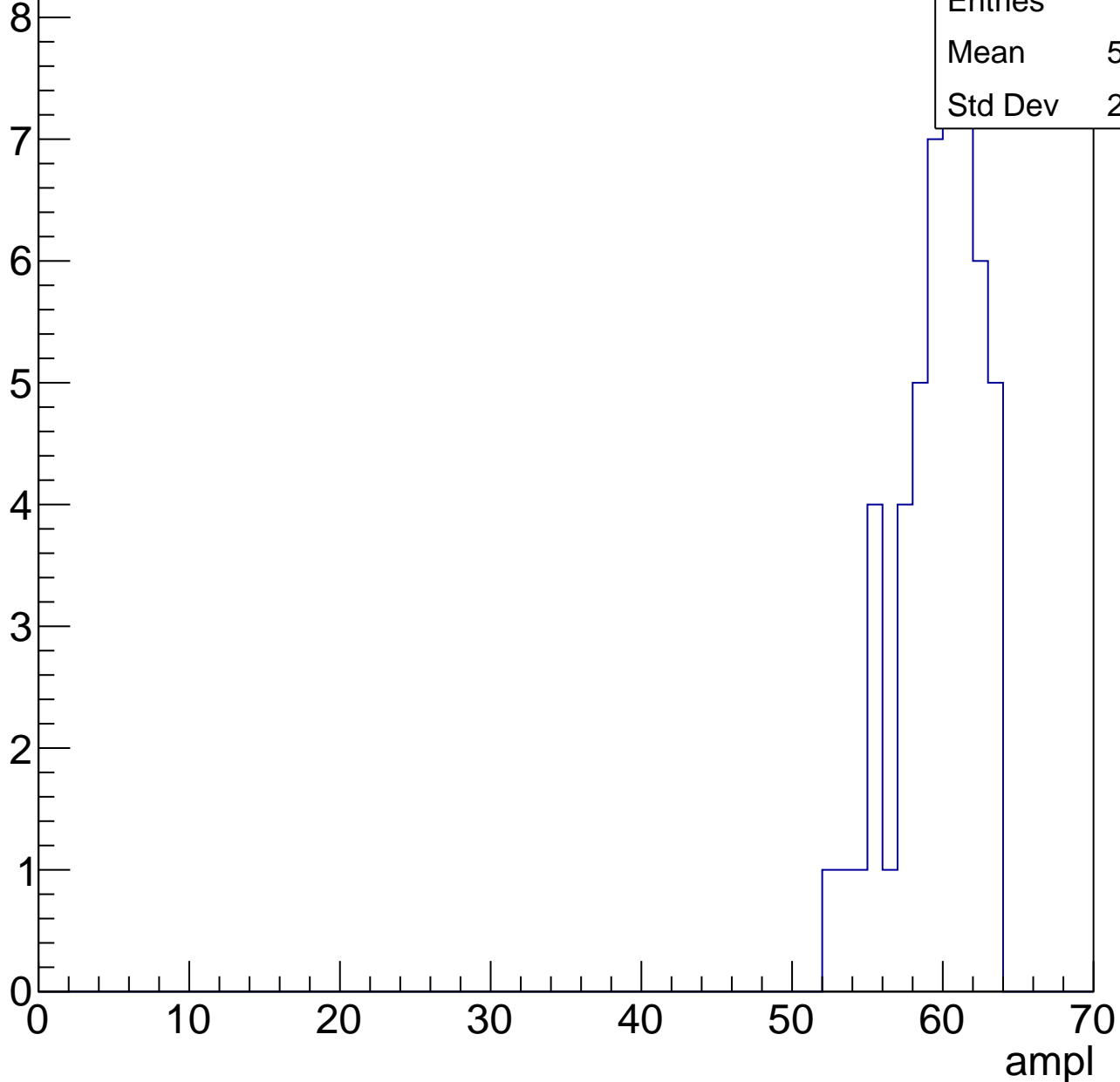
Entries	54
Mean	55.26
Std Dev	3.581



B1L101S, U12-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

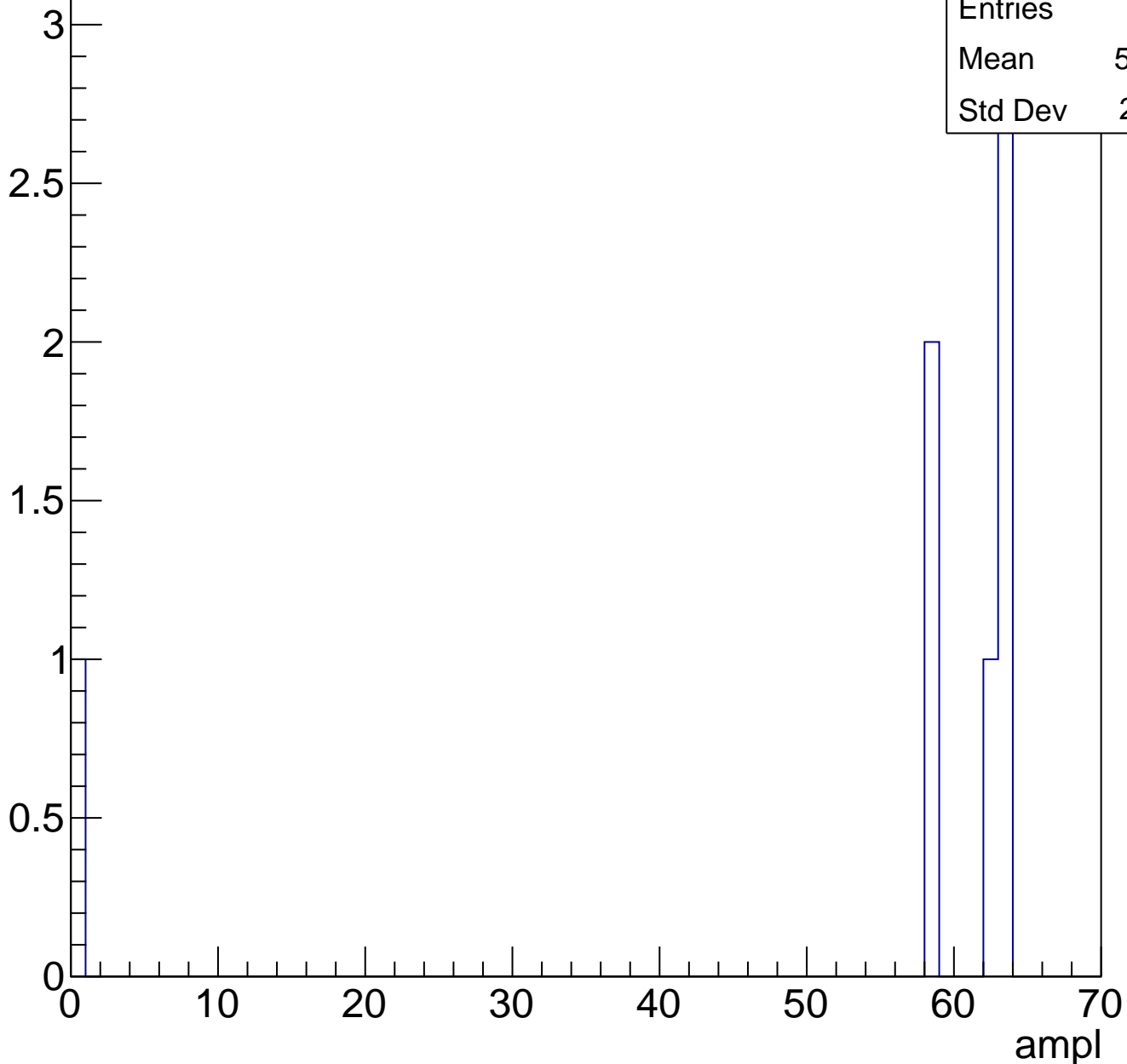
Entry



B1L101S, U12-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch61, adc0

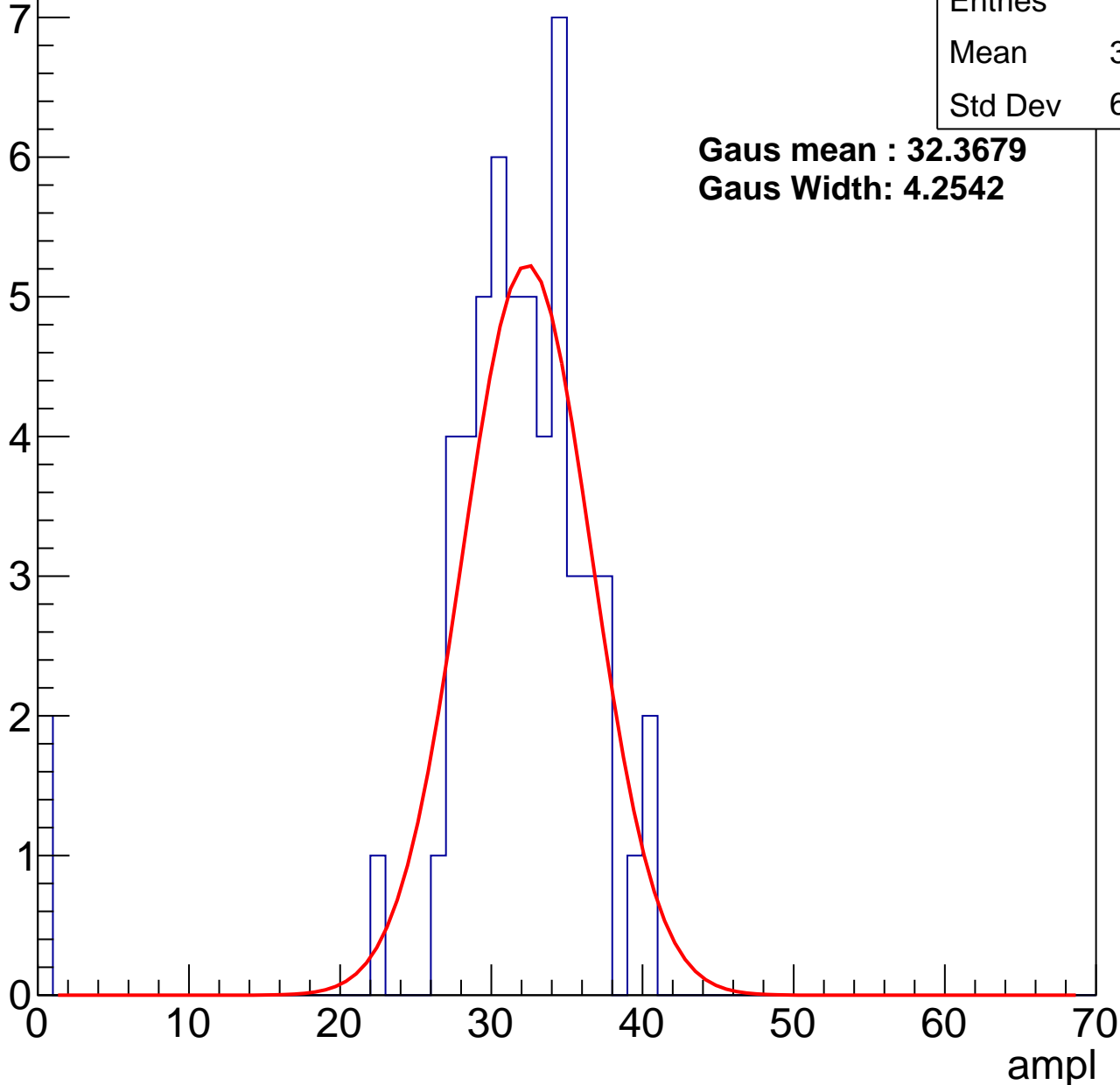
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	30.73
Std Dev	6.932

Gaus mean : 32.3679

Gaus Width: 4.2542



B1L101S, U12-ch61, adc1

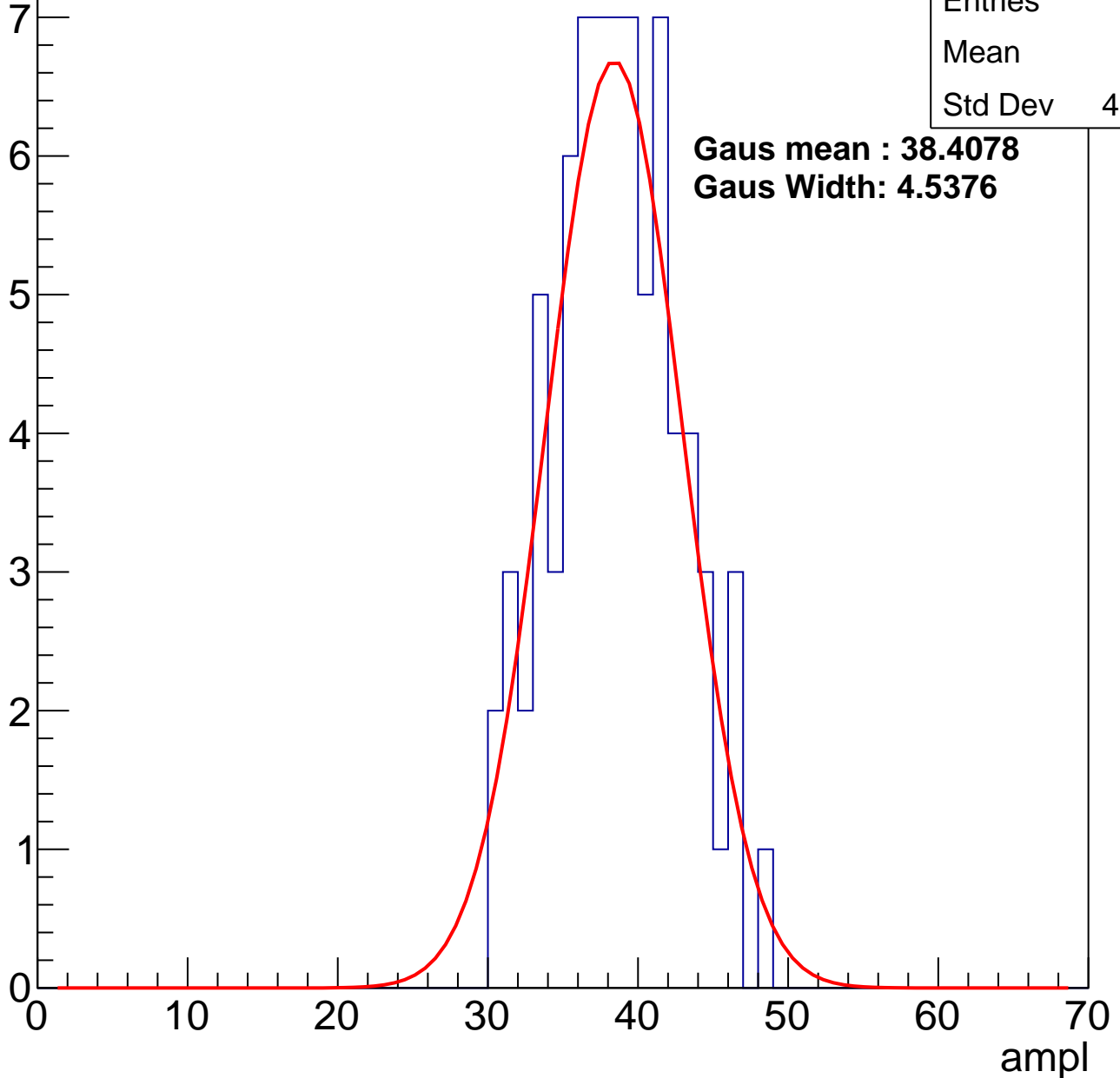
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	38.1
Std Dev	4.127

Gaus mean : 38.4078

Gaus Width: 4.5376



B1L101S, U12-ch61, adc2

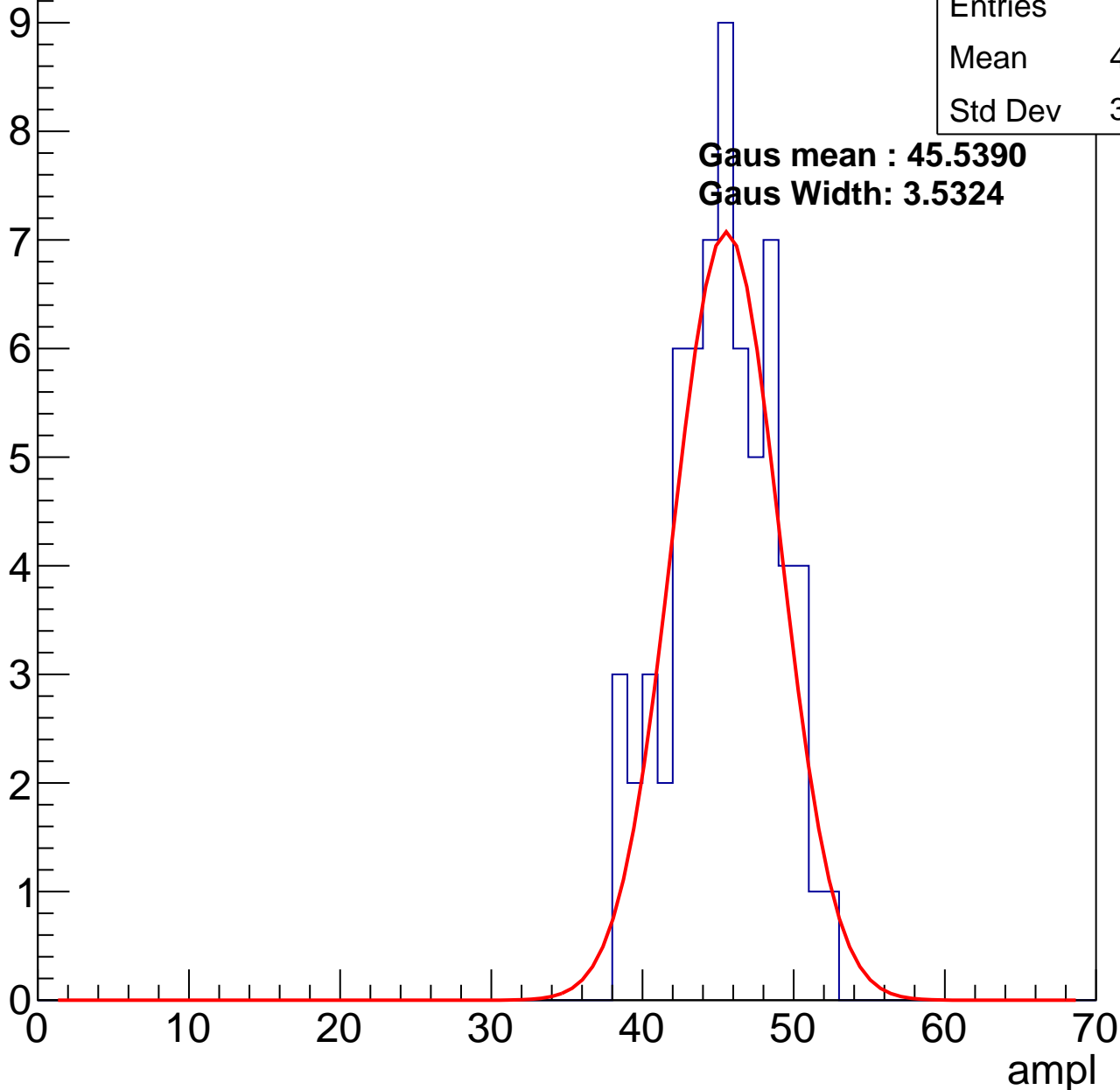
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.89
Std Dev	3.367

Gaus mean : 45.5390

Gaus Width: 3.5324

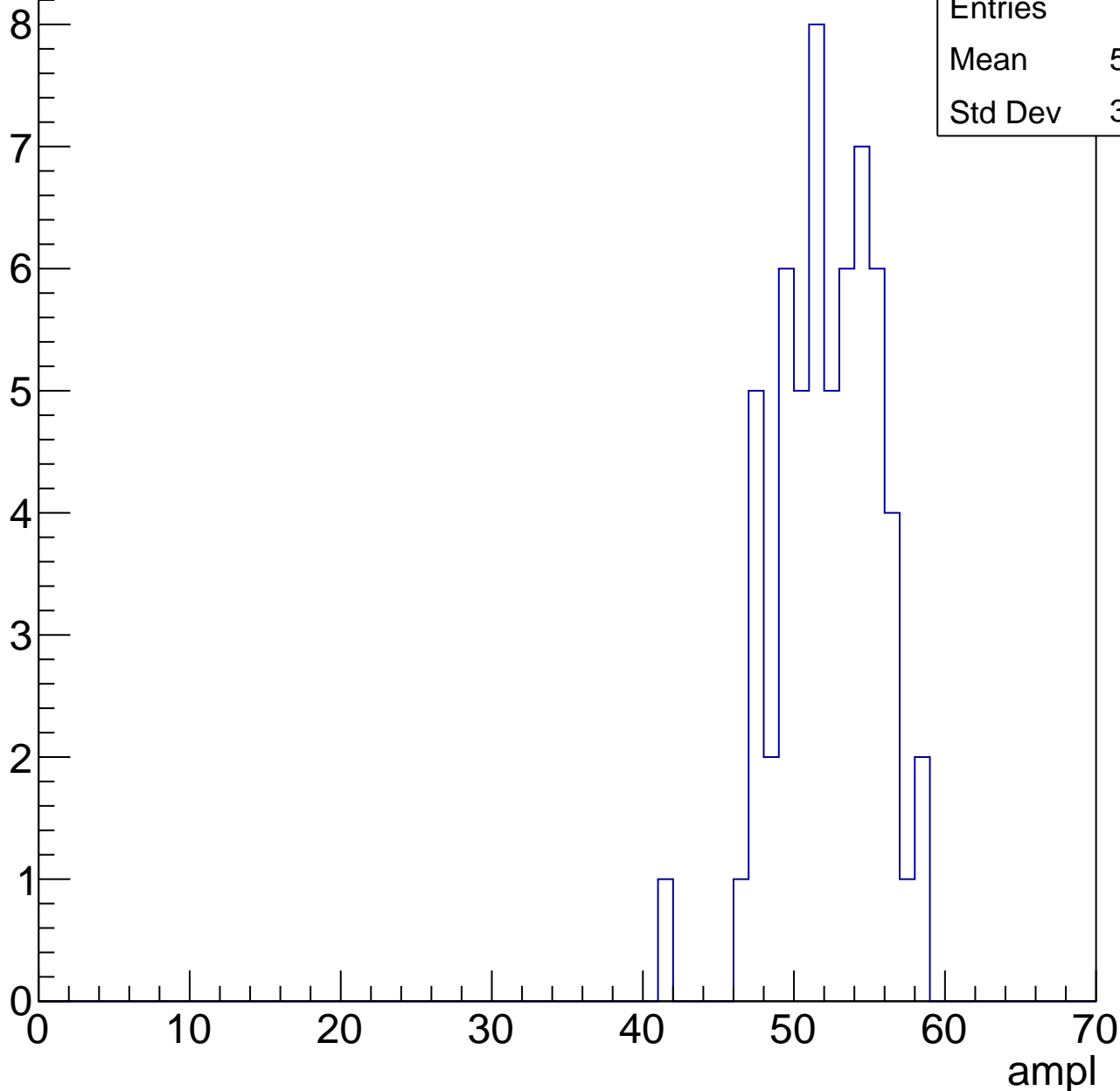


B1L101S, U12-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

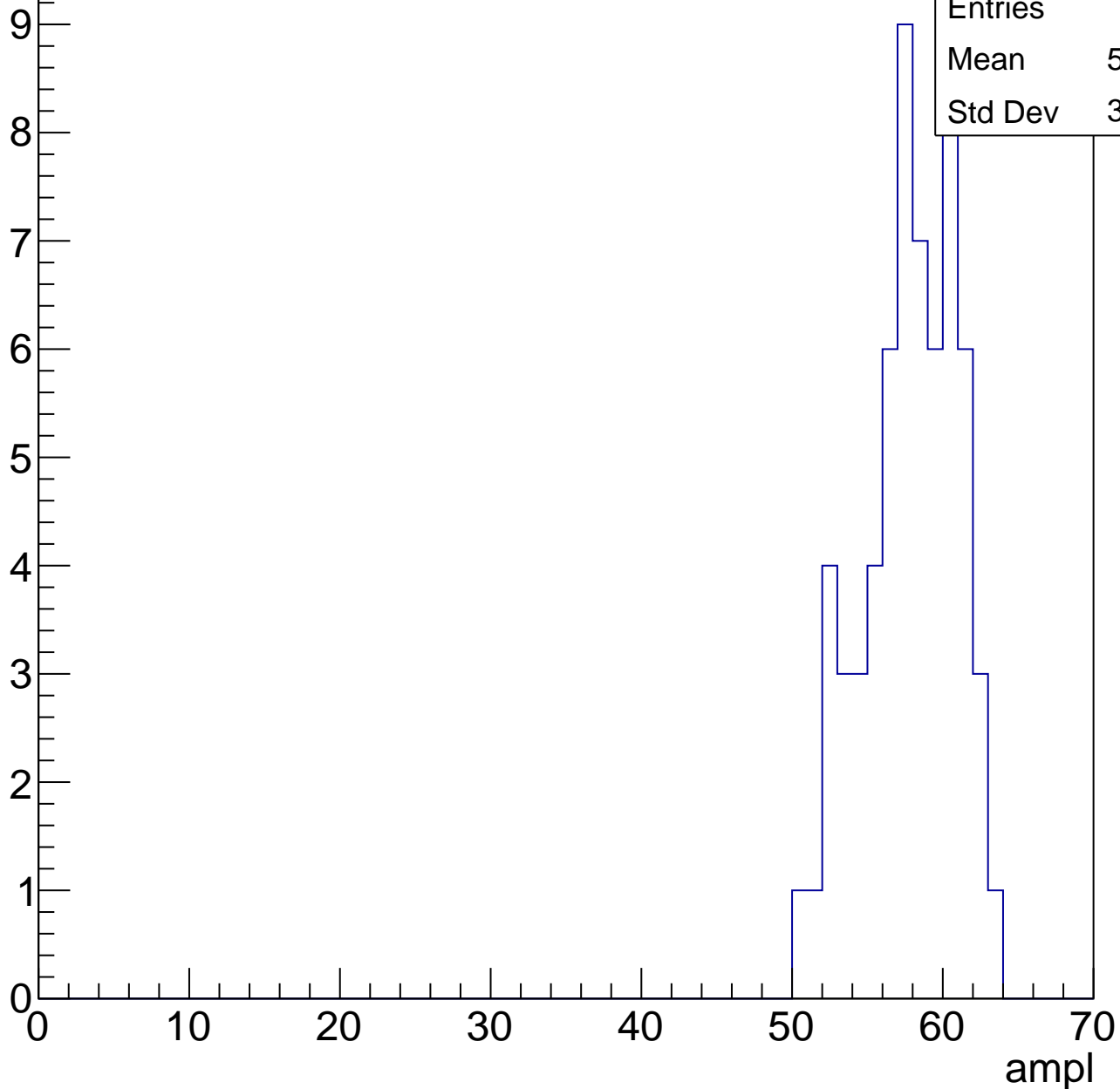
Entries	59
Mean	51.75
Std Dev	3.297



B1L101S, U12-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

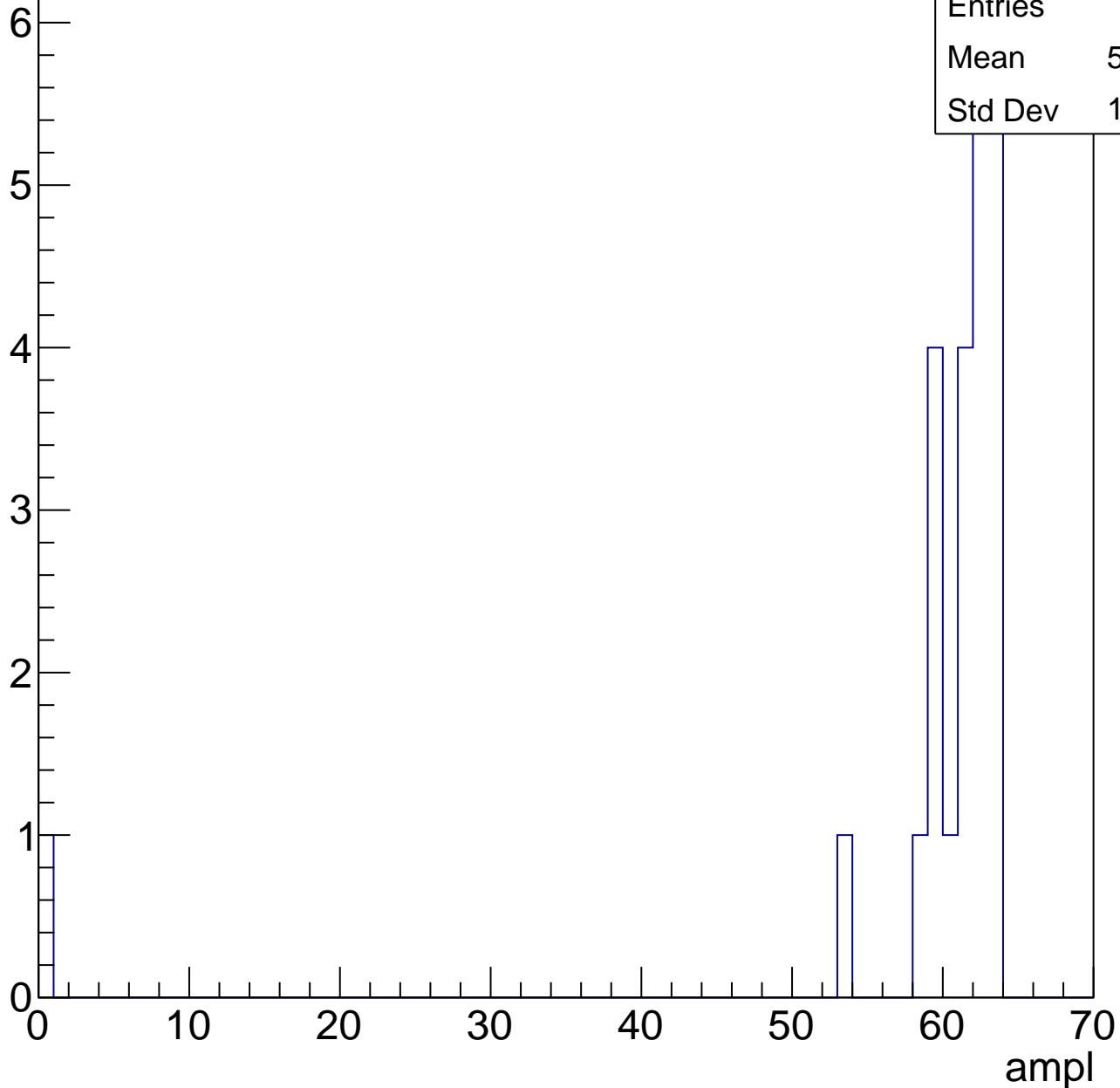


Entries	63
Mean	57.37
Std Dev	3.067

B1L101S, U12-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch62, adc0

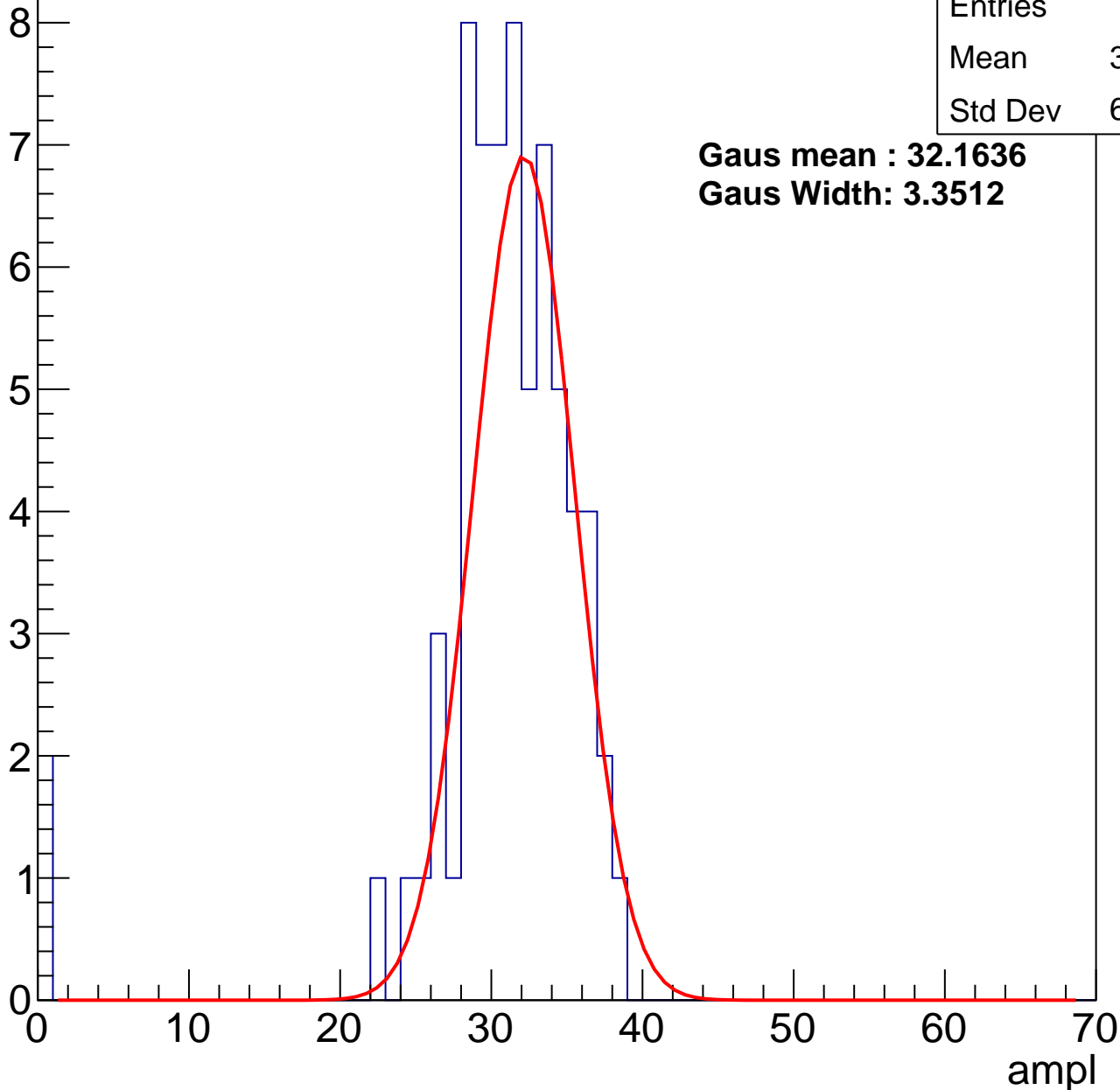
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.12
Std Dev	6.226

Gaus mean : 32.1636

Gaus Width: 3.3512



B1L101S, U12-ch62, adc1

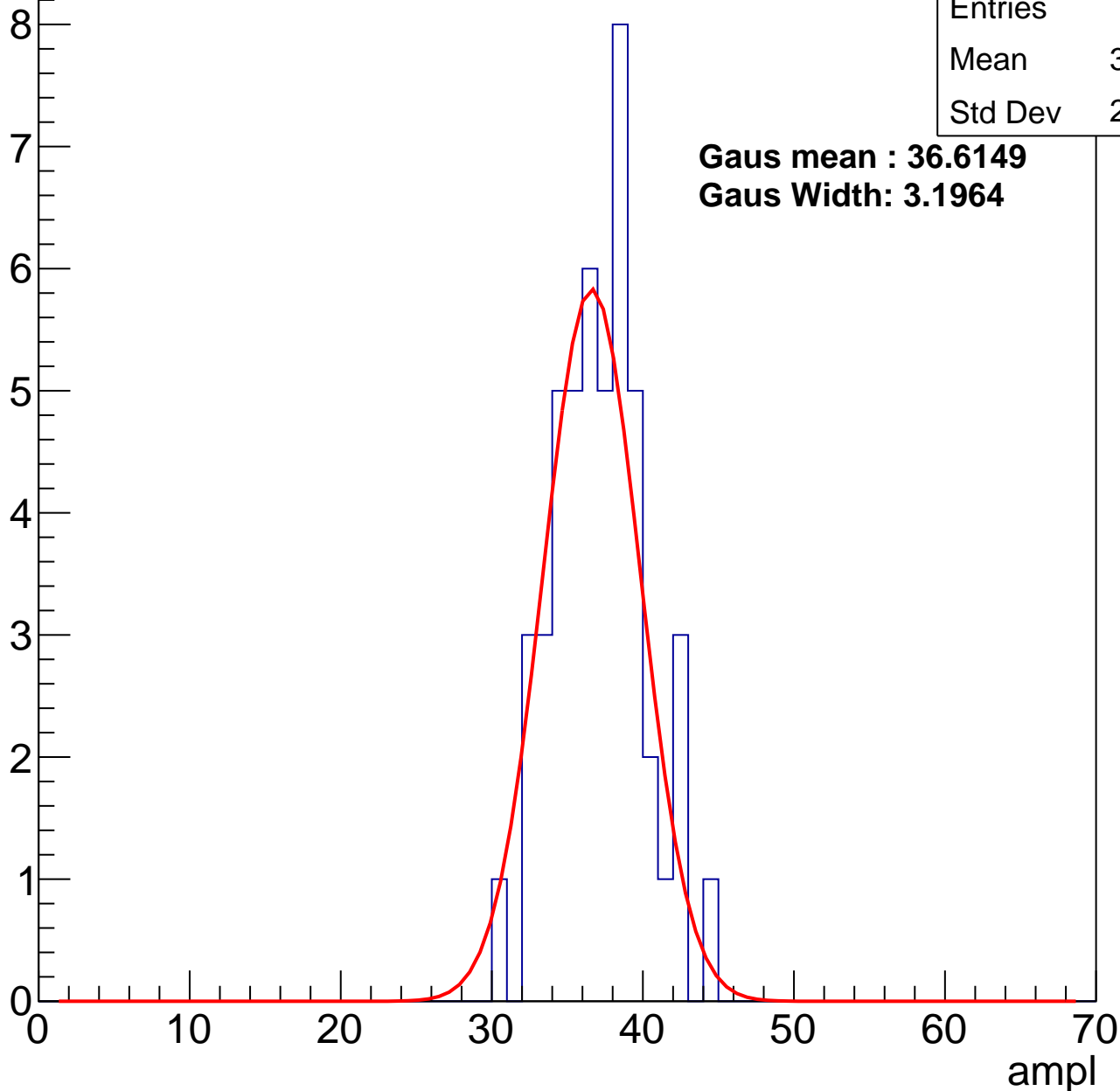
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	36.69
Std Dev	2.973

Gaus mean : 36.6149

Gaus Width: 3.1964



B1L101S, U12-ch62, adc2

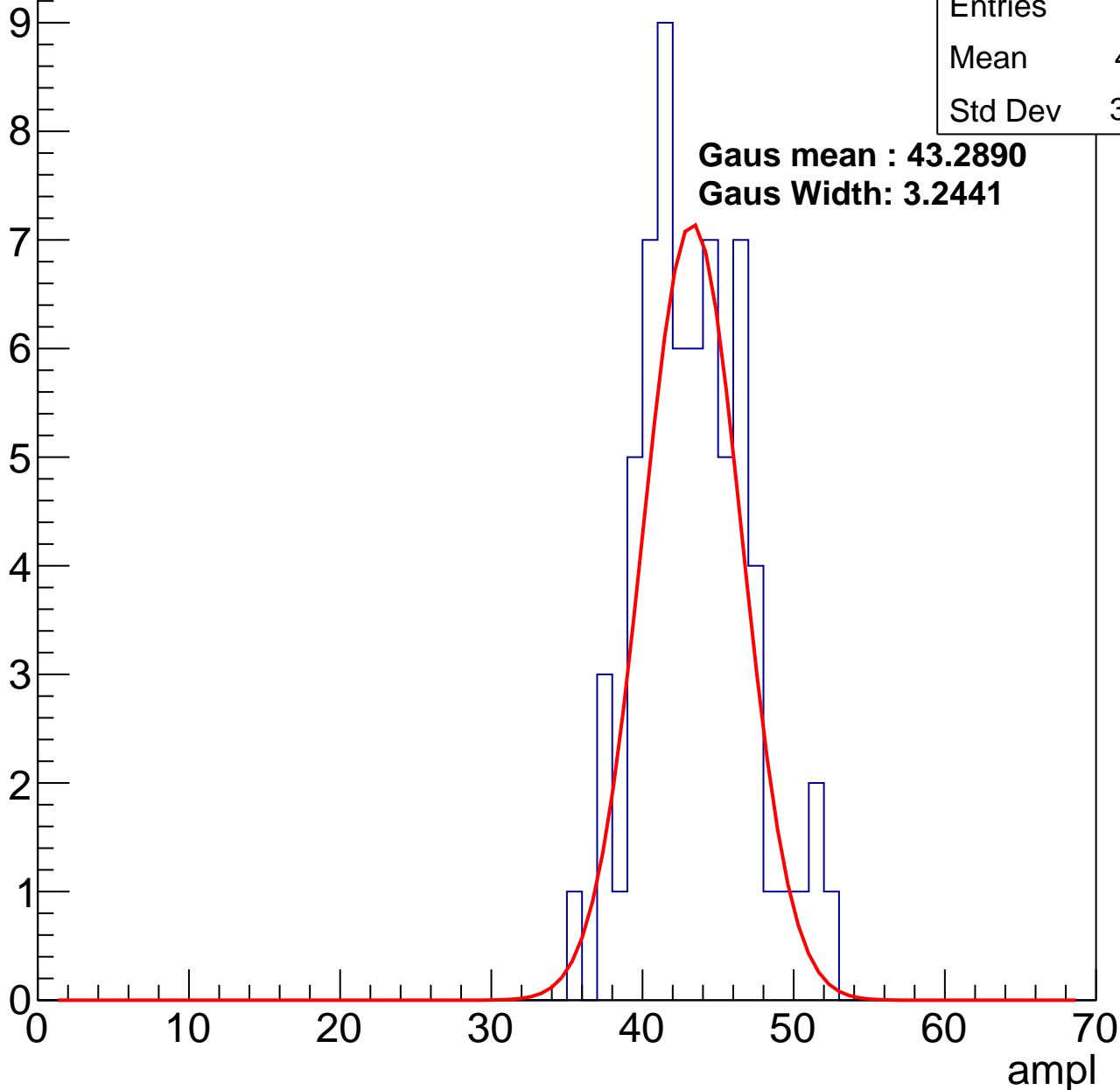
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.01
Std Dev	3.572

Gaus mean : 43.2890

Gaus Width: 3.2441

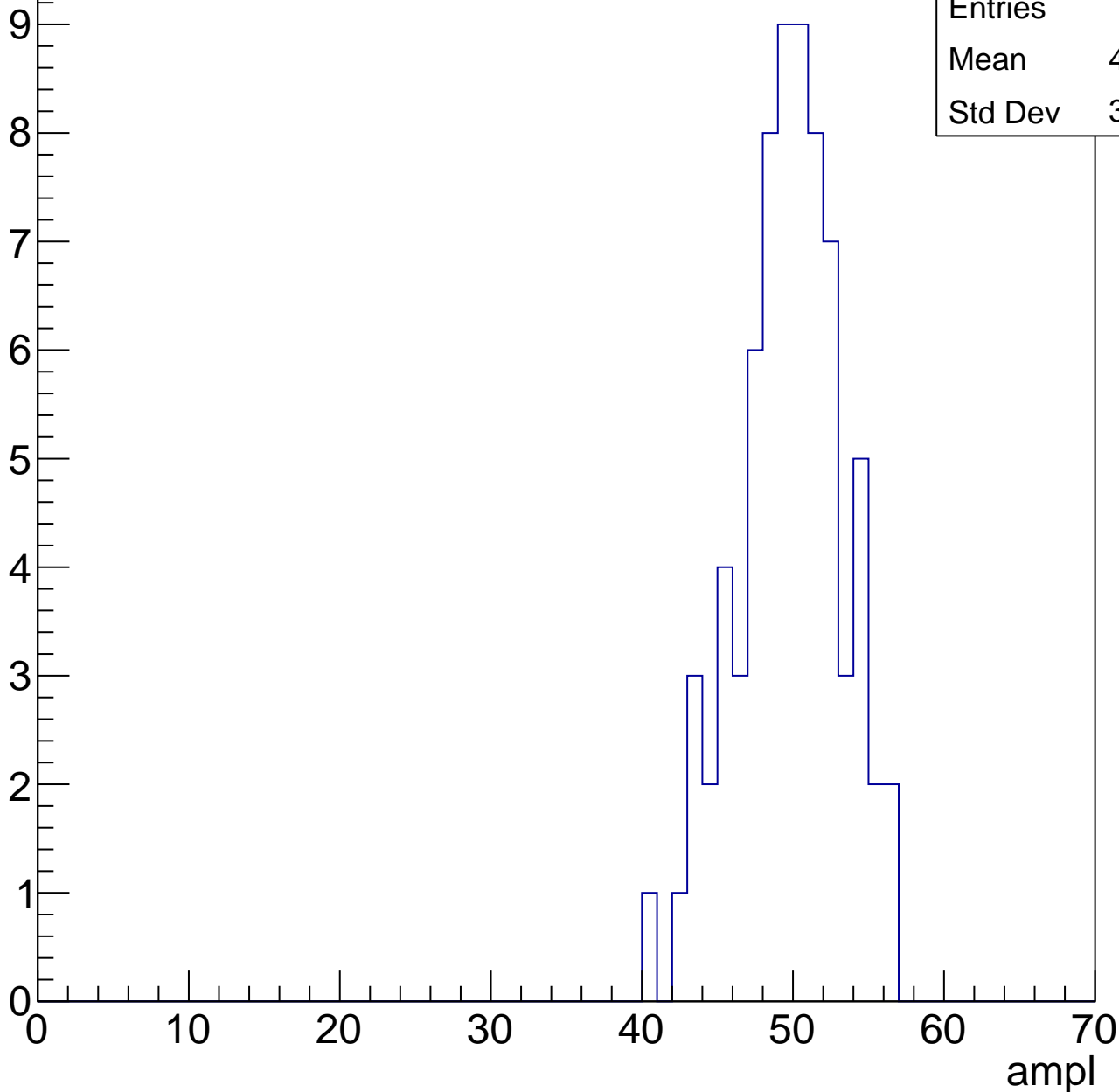


B1L101S, U12-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

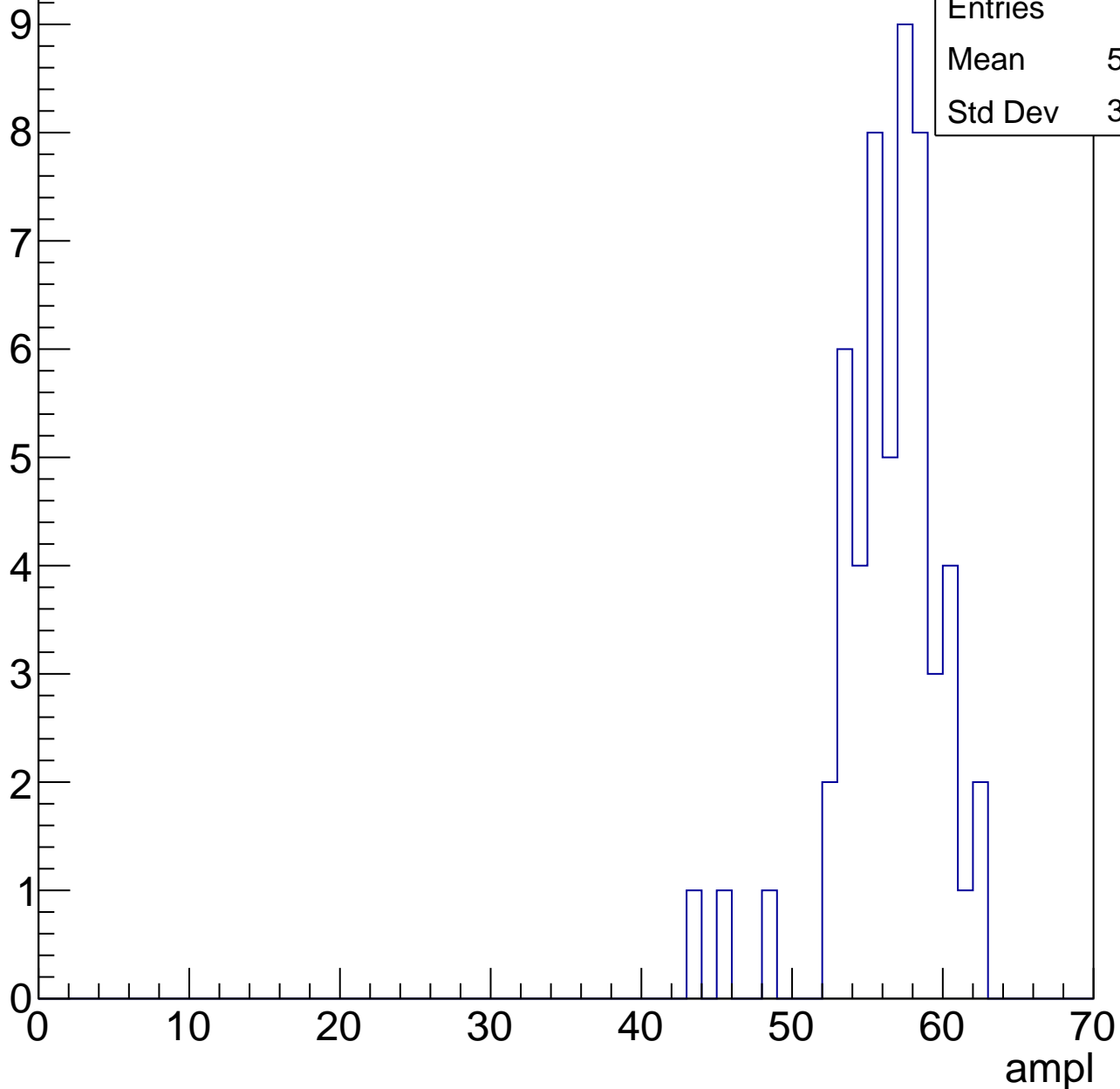
Entries	73
Mean	49.27
Std Dev	3.417



B1L101S, U12-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

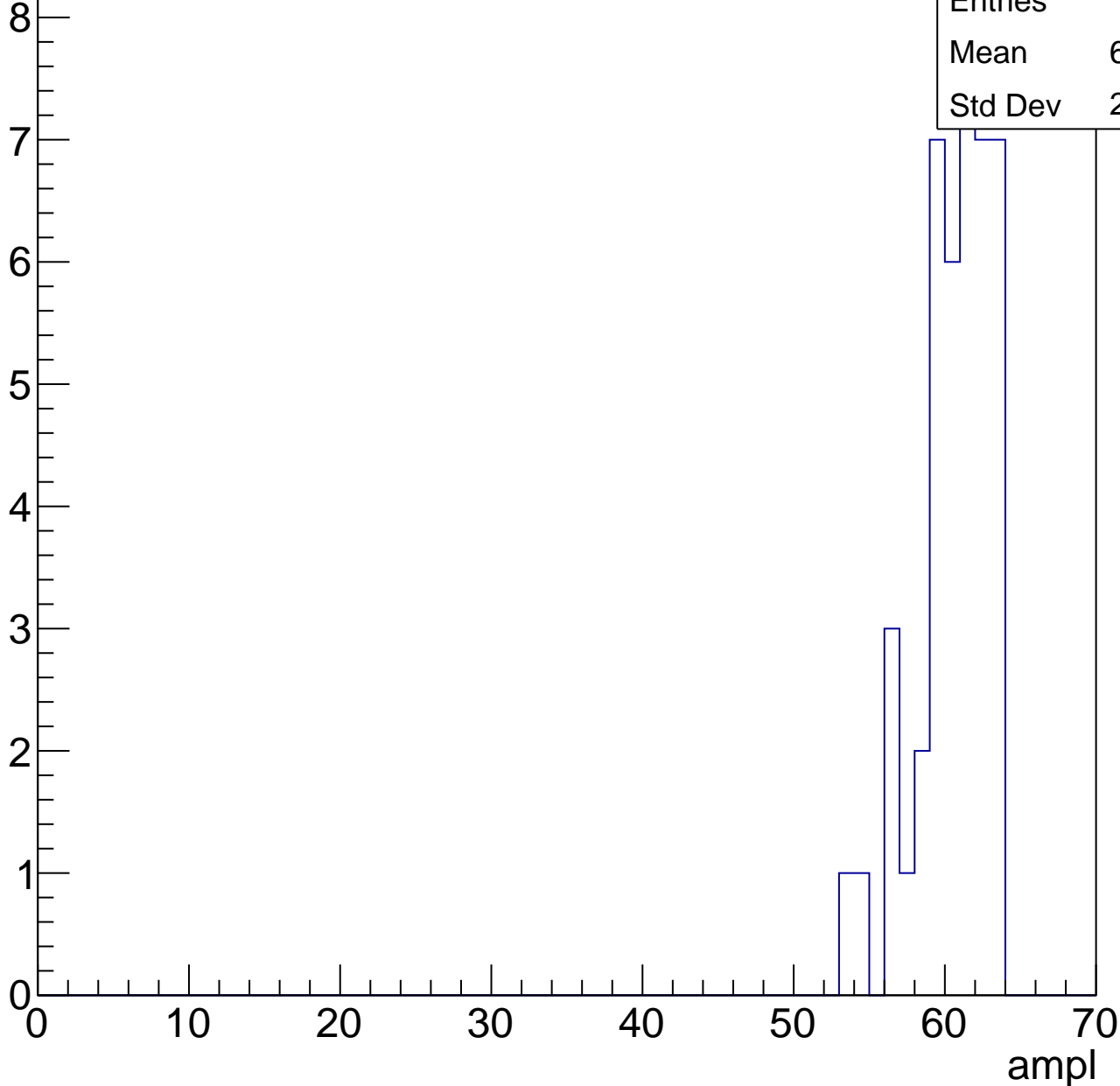


B1L101S, U12-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

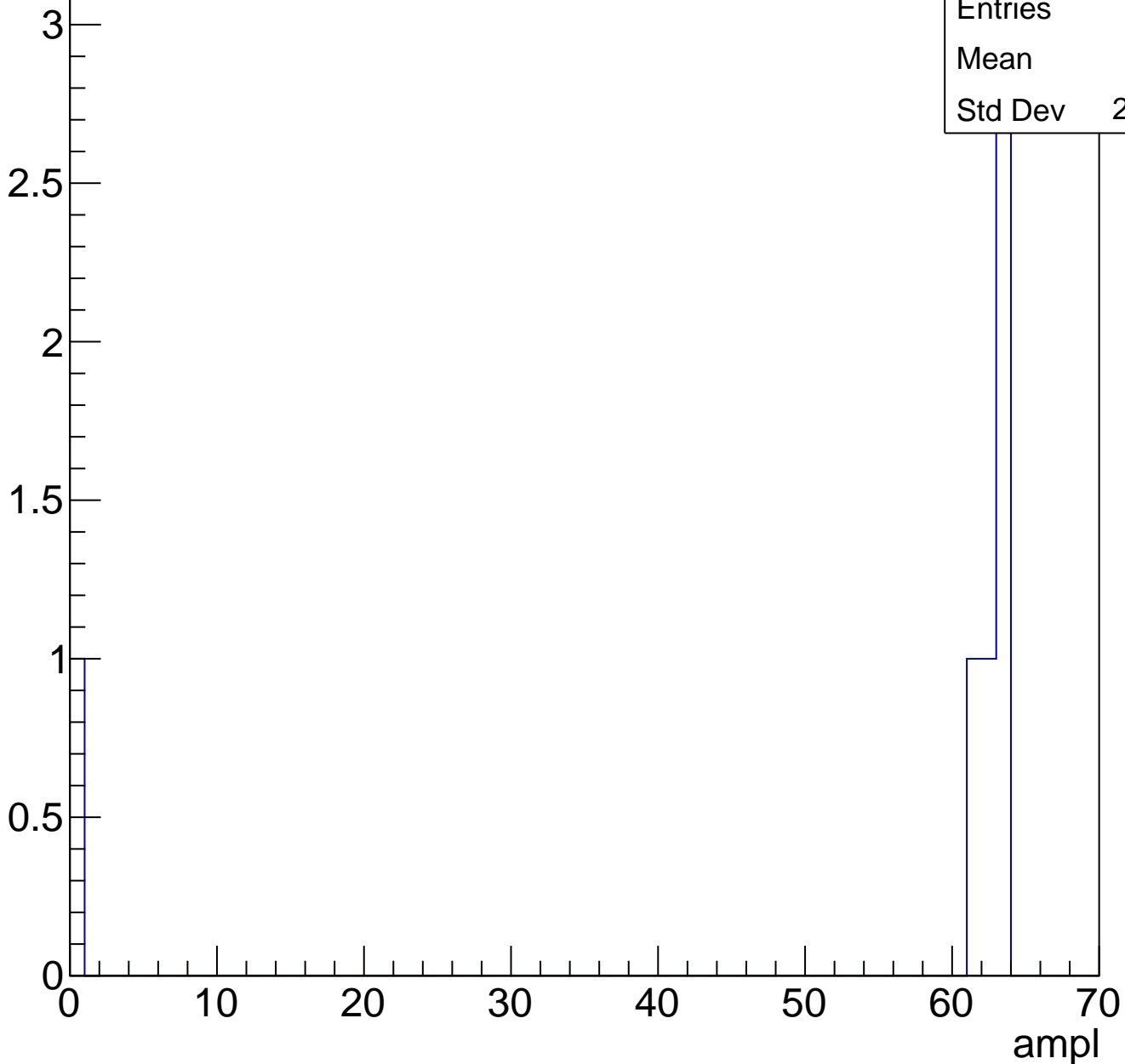
Entries	43
Mean	60.09
Std Dev	2.438



B1L101S, U12-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch63, adc0

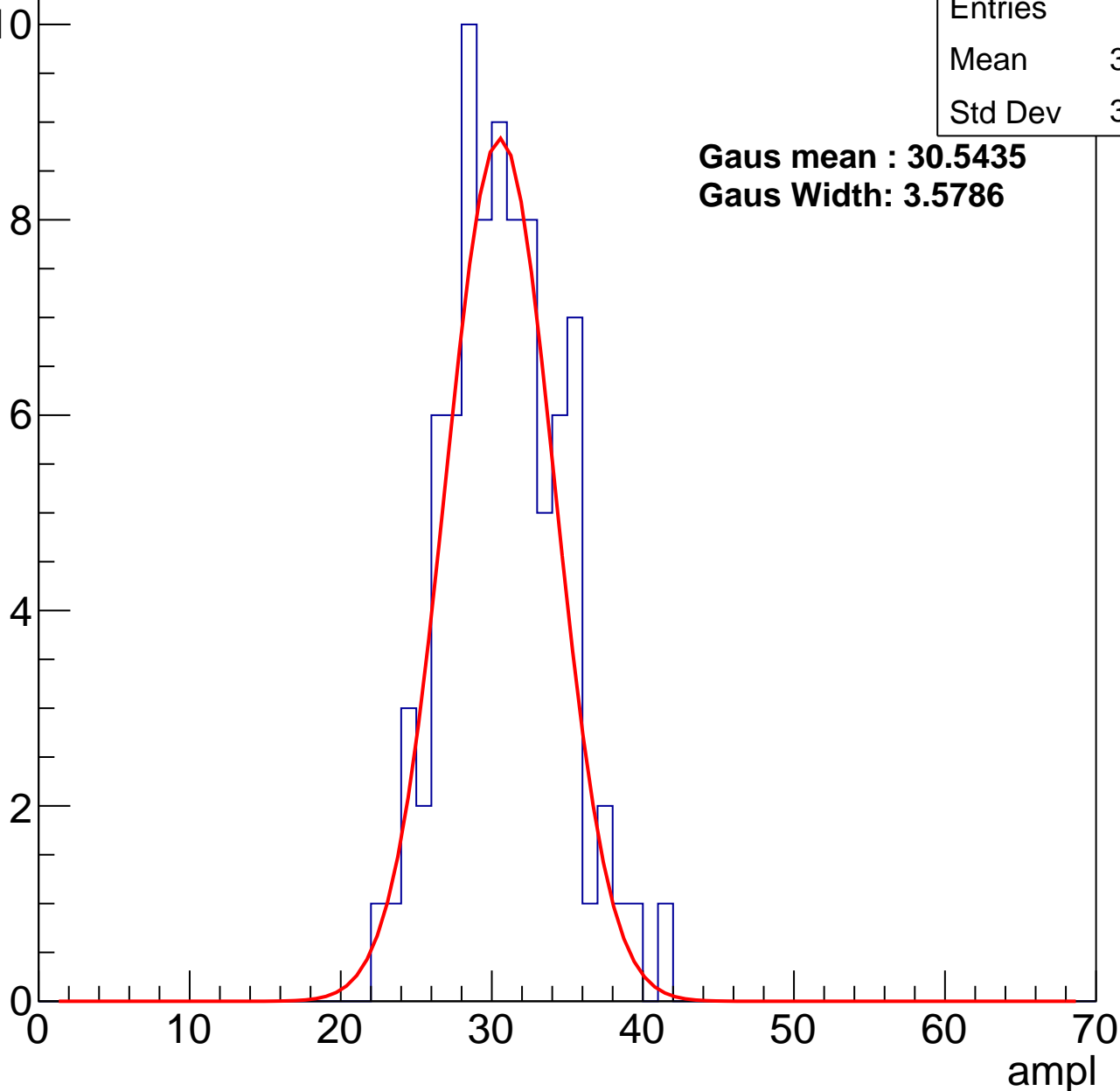
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	30.38
Std Dev	3.755

Gaus mean : 30.5435

Gaus Width: 3.5786



B1L101S, U12-ch63, adc1

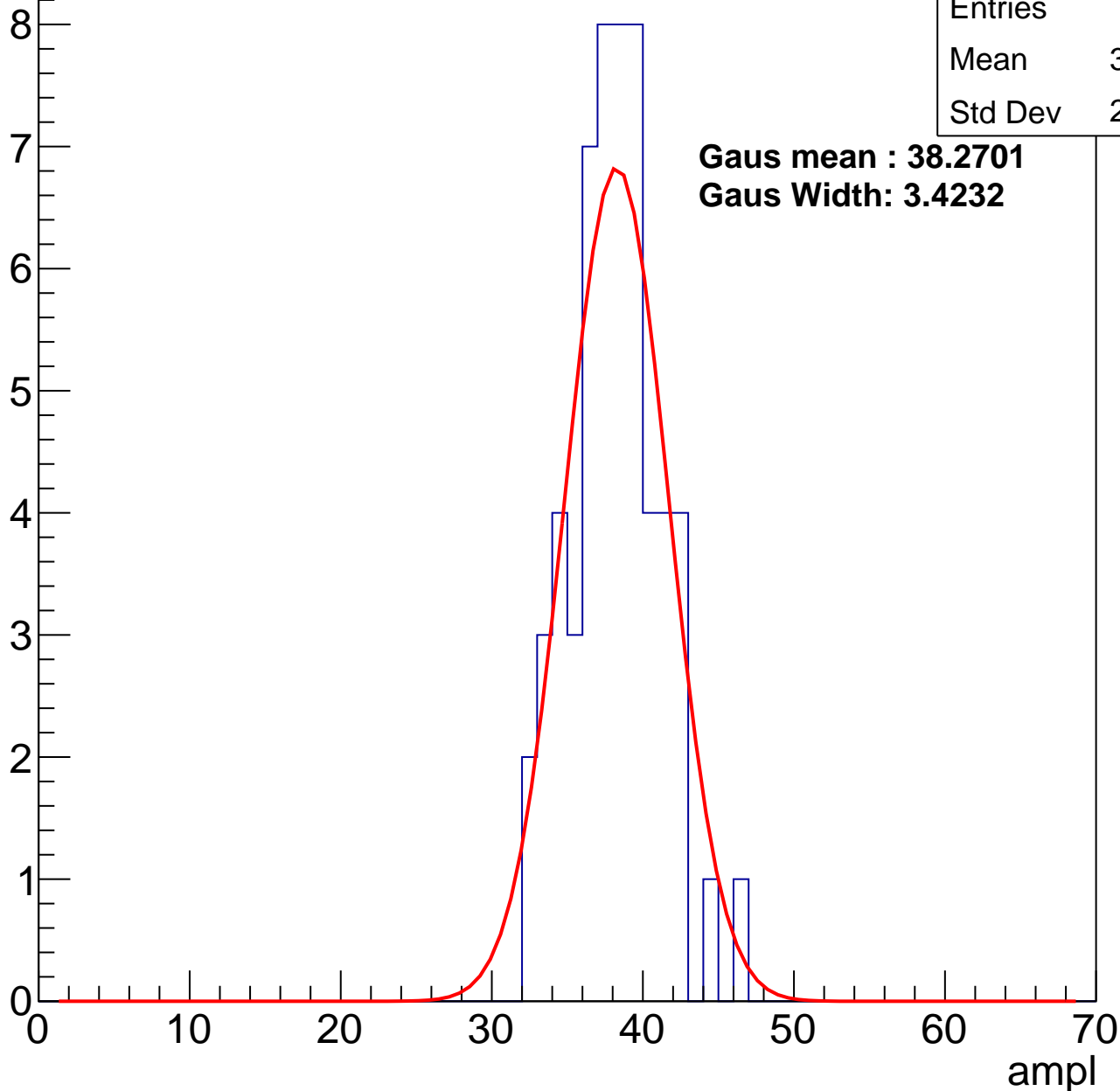
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.72
Std Dev	2.942

Gaus mean : 38.2701

Gaus Width: 3.4232



B1L101S, U12-ch63, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	43.45
Std Dev	3.368

Gaus mean : 44.2589

Gaus Width: 2.8907

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

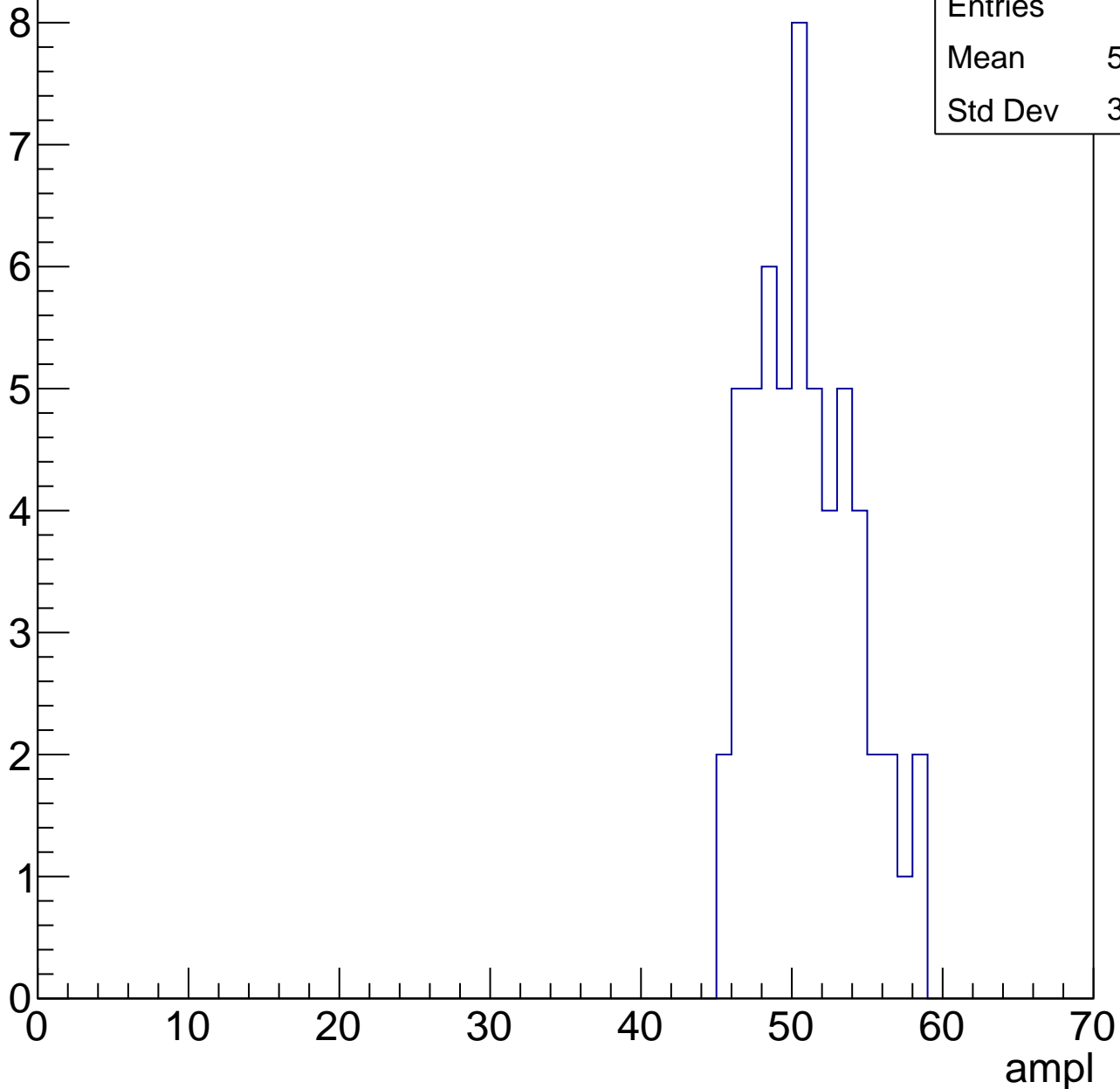
70

B1L101S, U12-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

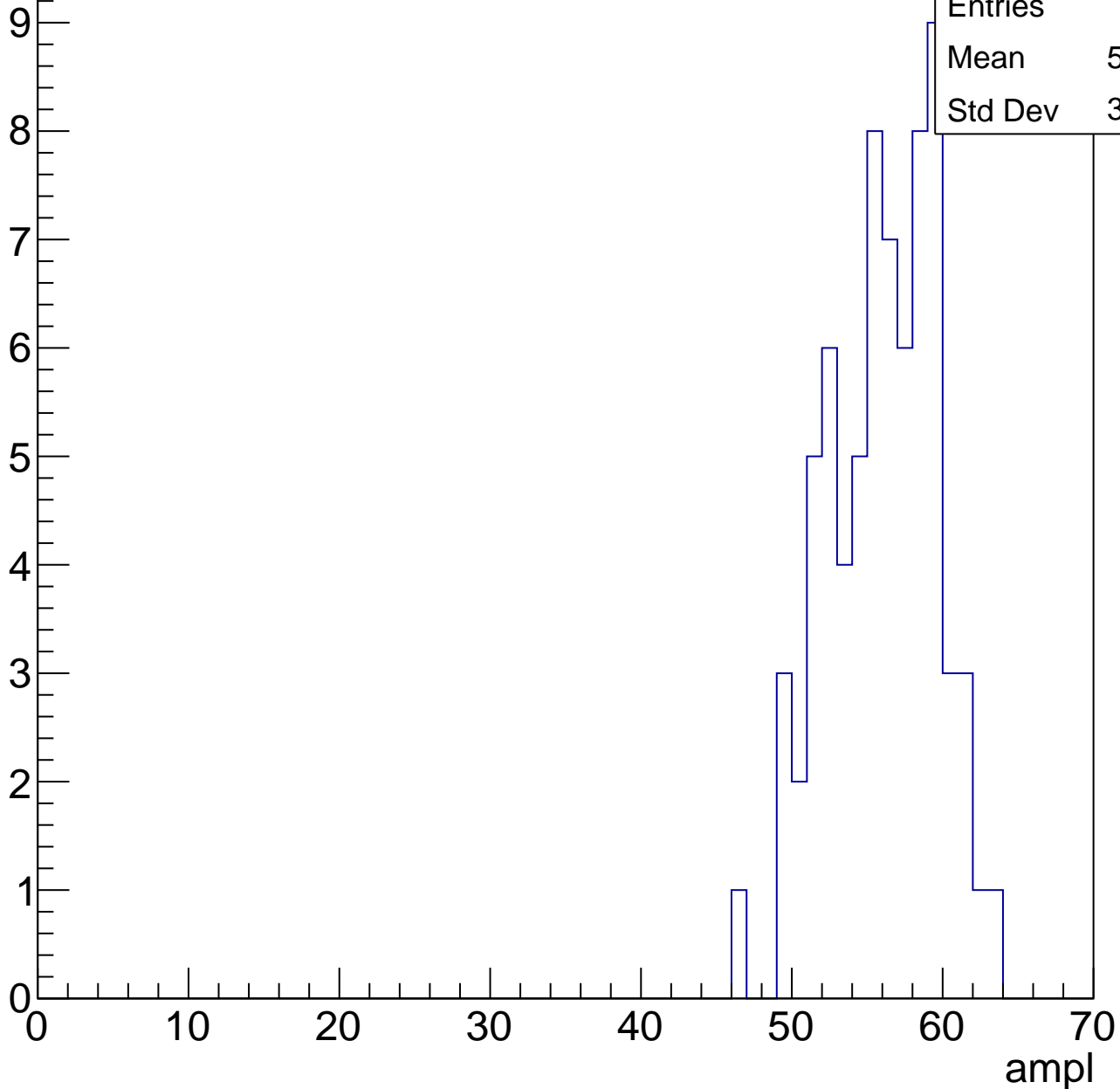
Entries	56
Mean	50.48
Std Dev	3.327



B1L101S, U12-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



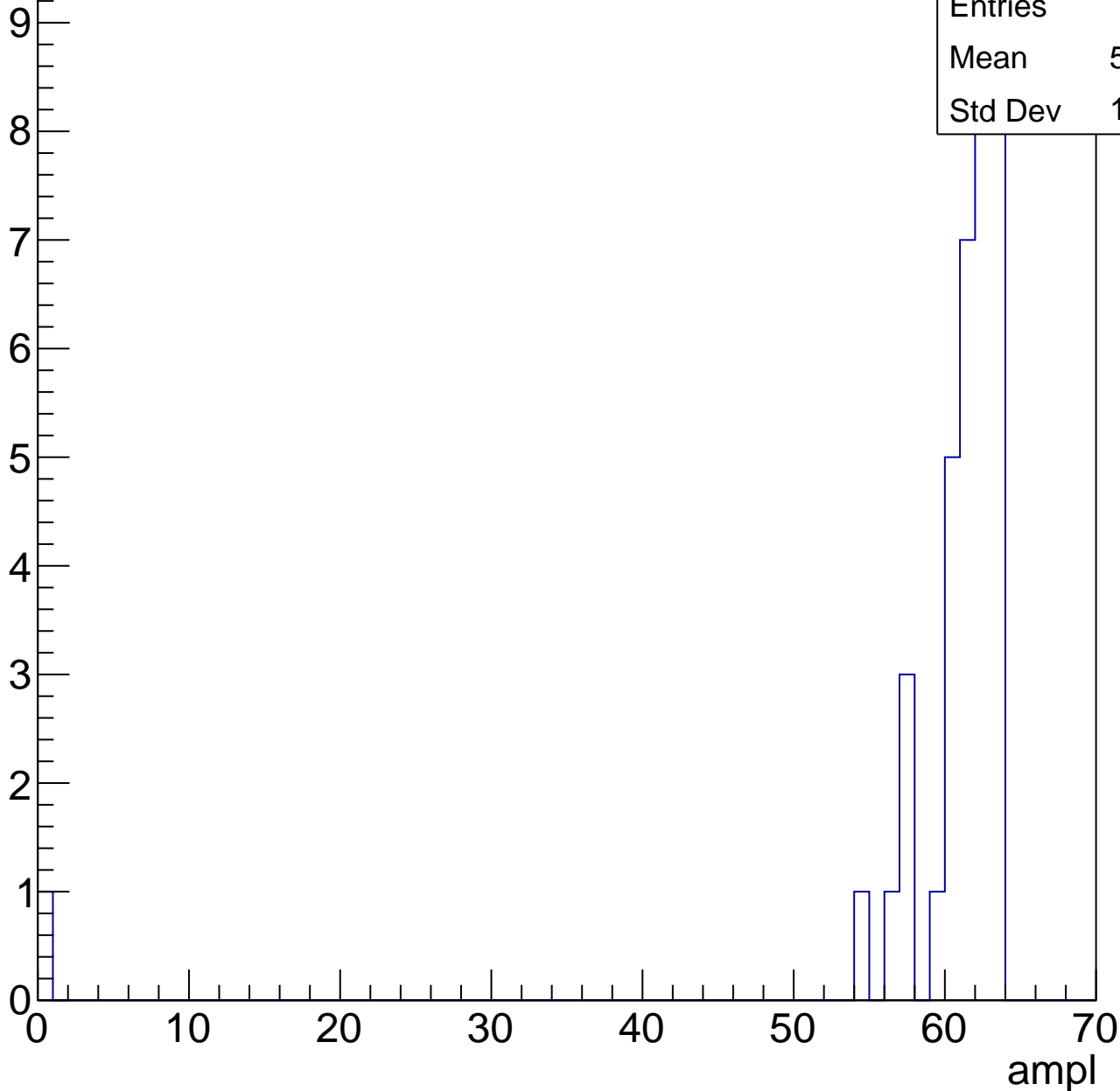
Entries	72
Mean	55.54
Std Dev	3.567

B1L101S, U12-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	59.14
Std Dev	10.23



B1L101S, U12-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

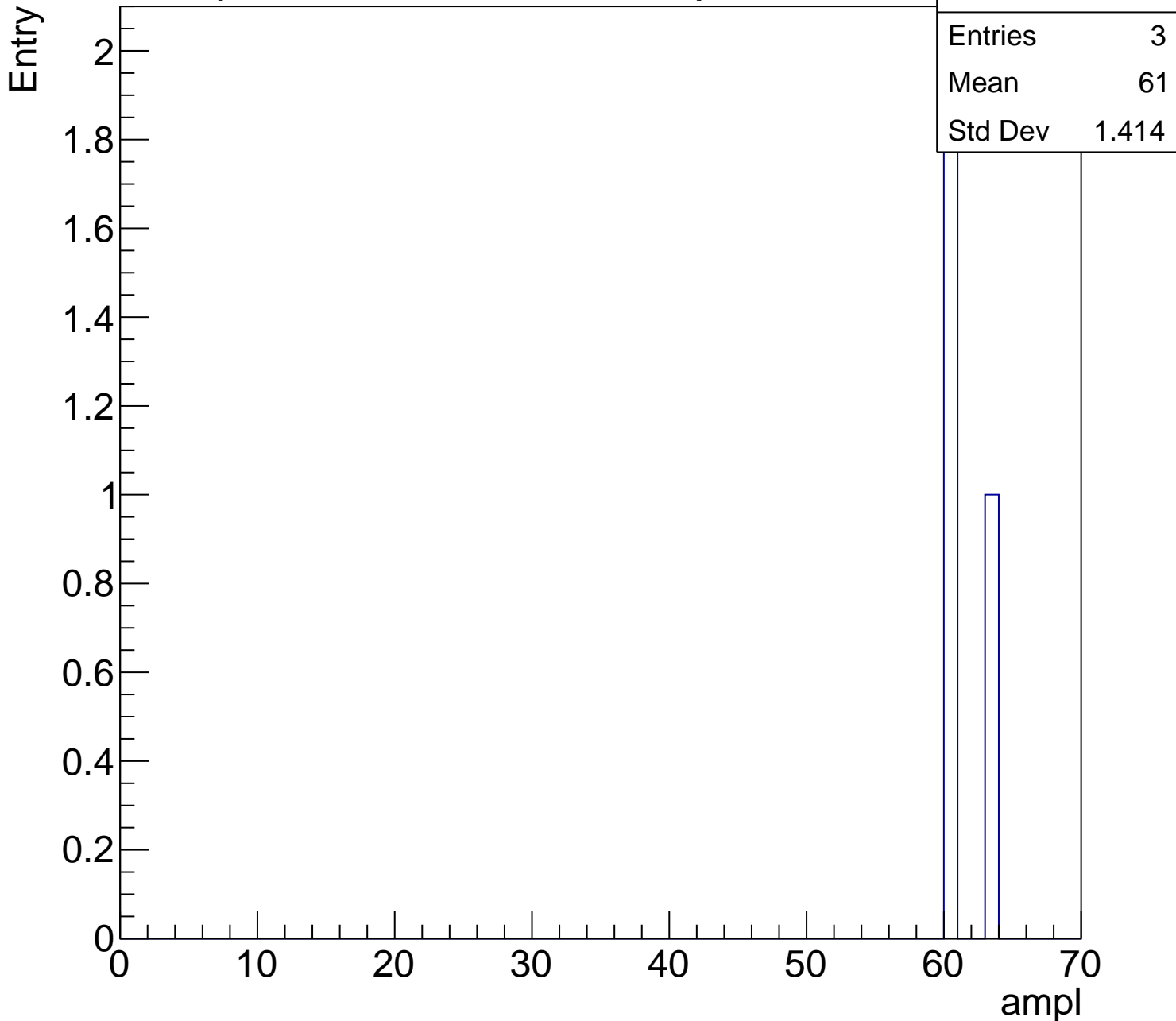
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61
Std Dev	1.414

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch64, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	29.25
Std Dev	3.34

Gaus mean : 29.3042

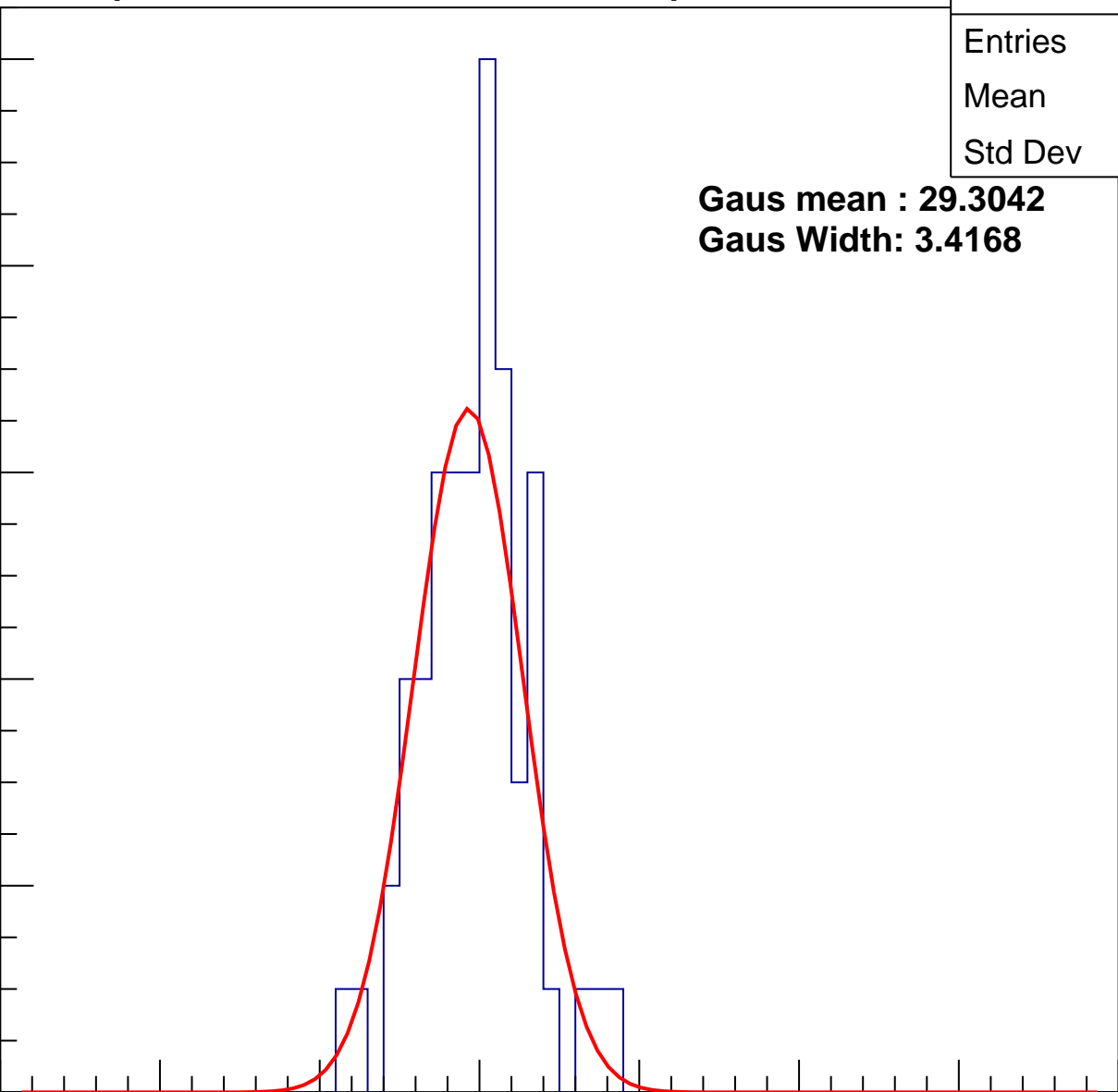
Gaus Width: 3.4168

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch64, adc1

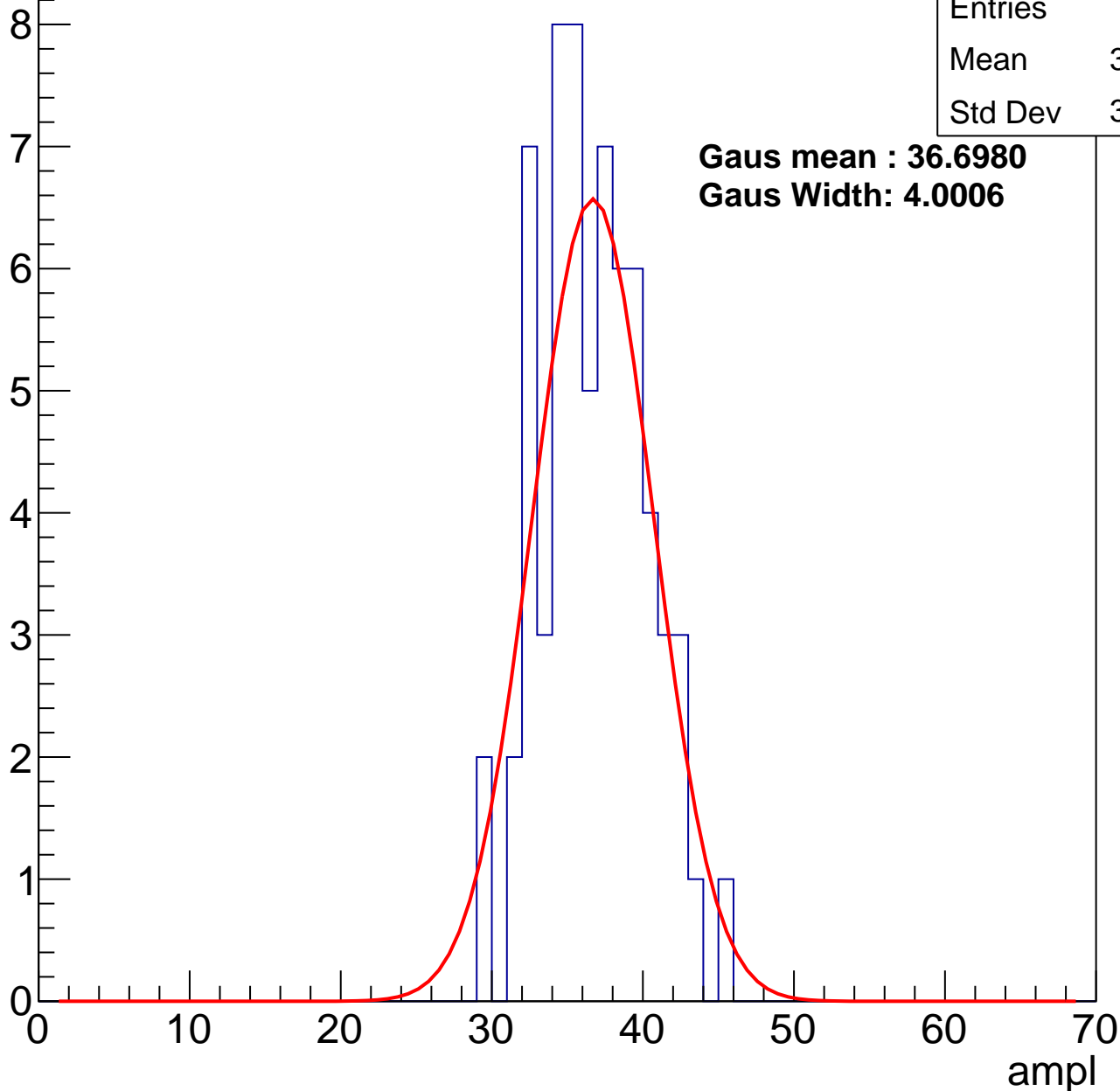
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.26
Std Dev	3.448

Gaus mean : 36.6980

Gaus Width: 4.0006

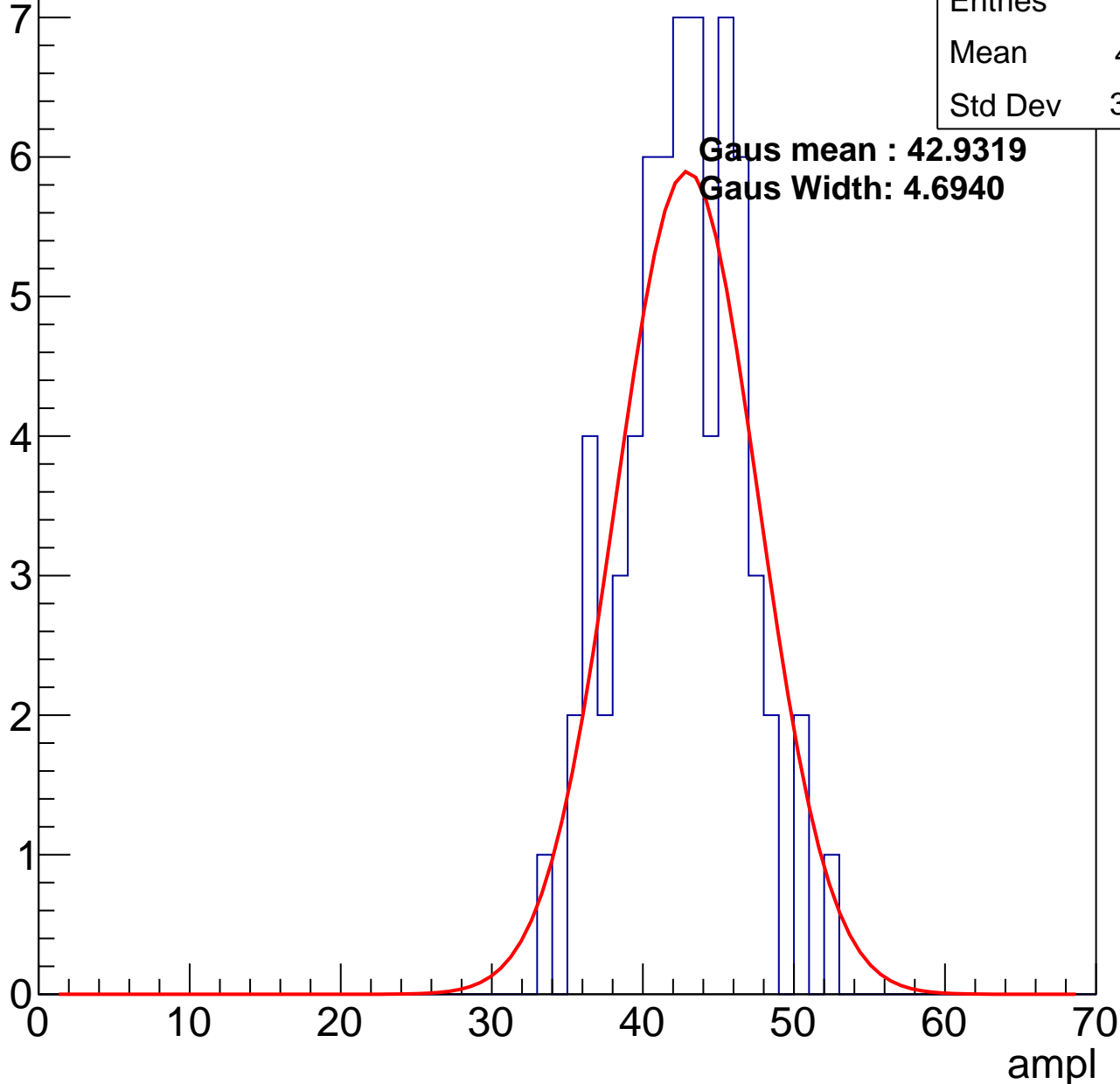


B1L101S, U12-ch64, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.21
Std Dev	3.942

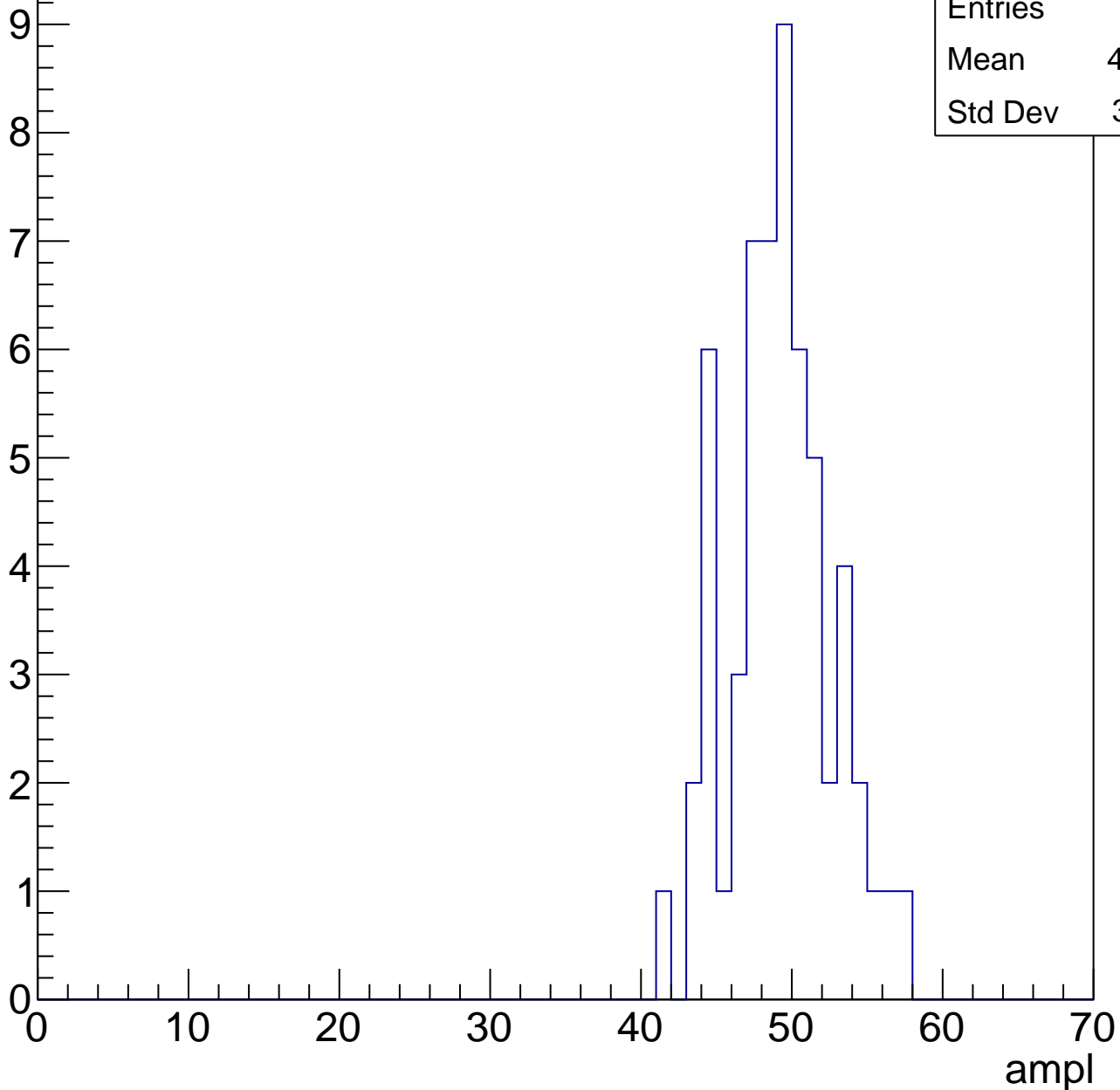


B1L101S, U12-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	48.74
Std Dev	3.391

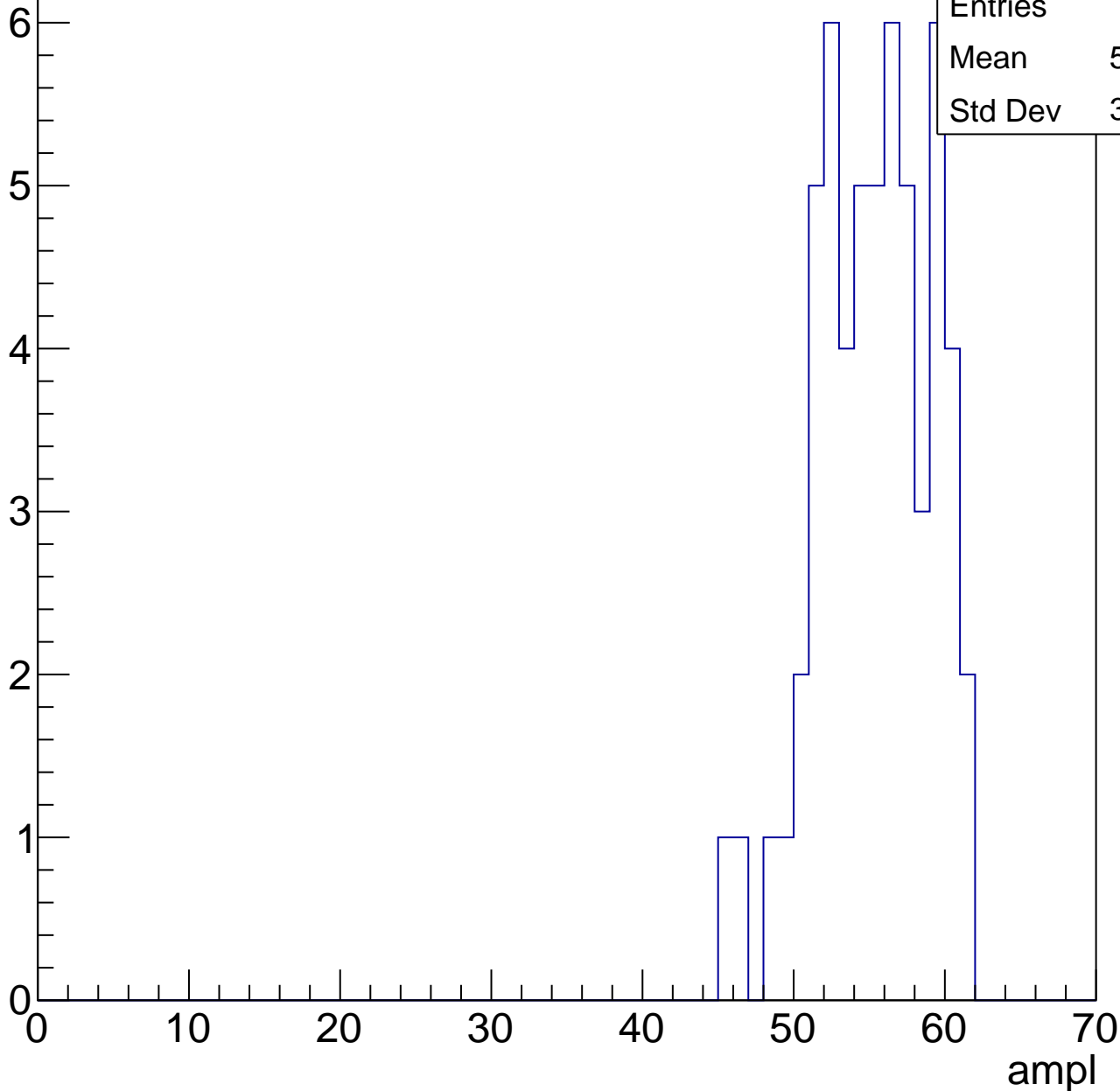


B1L101S, U12-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	54.79
Std Dev	3.722

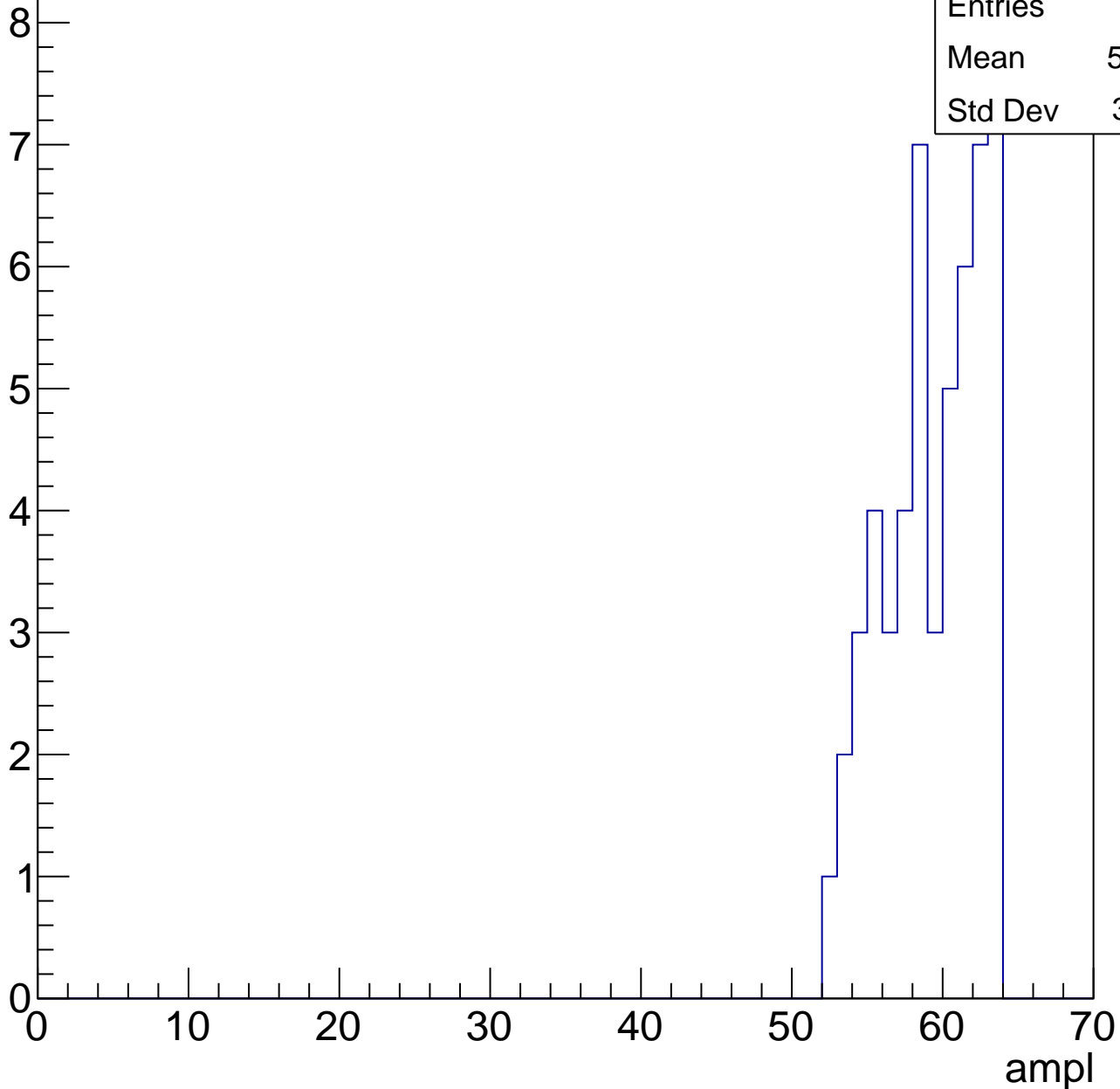


B1L101S, U12-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

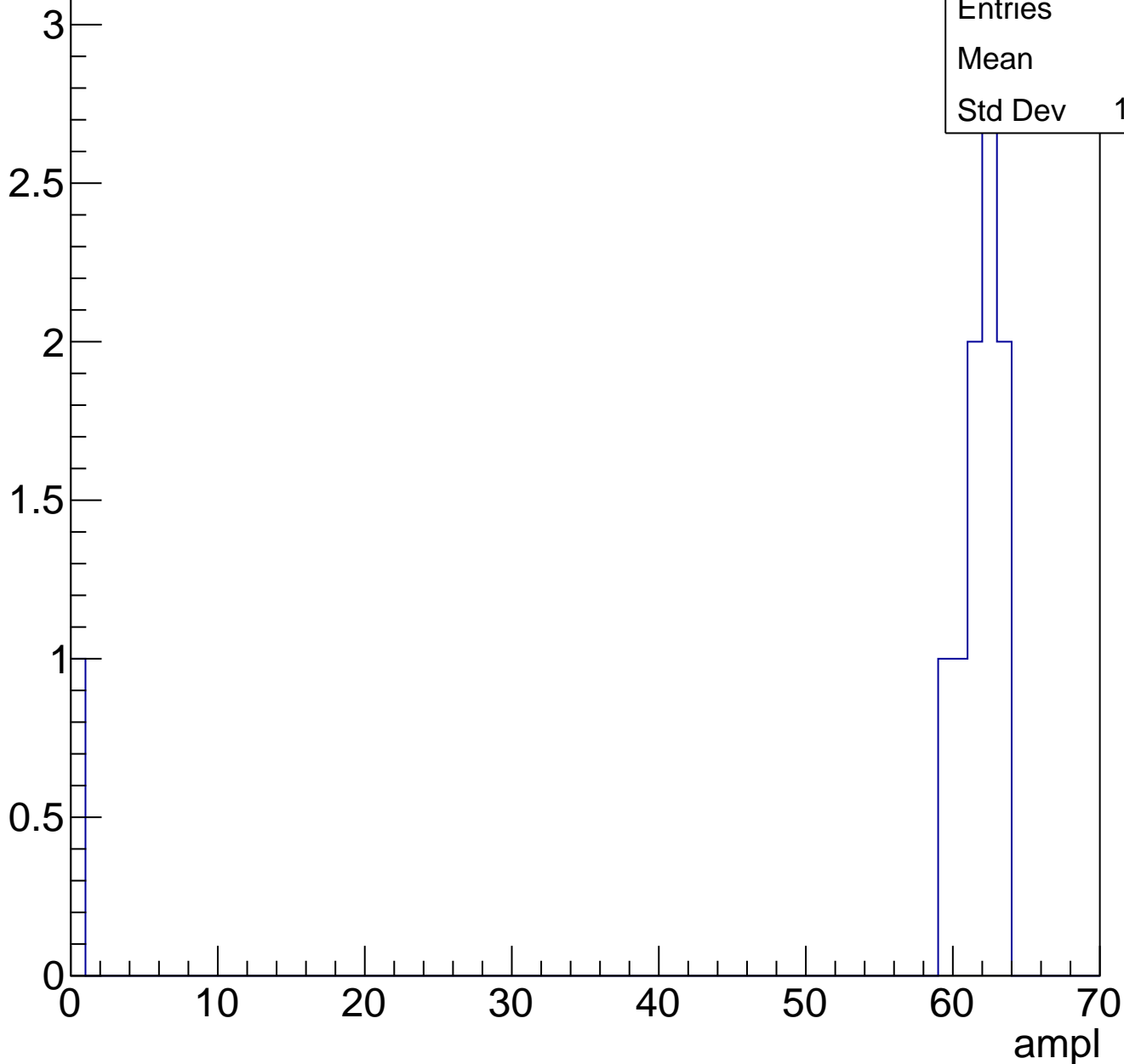
Entries	53
Mean	58.92
Std Dev	3.161



B1L101S, U12-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch65, adc0

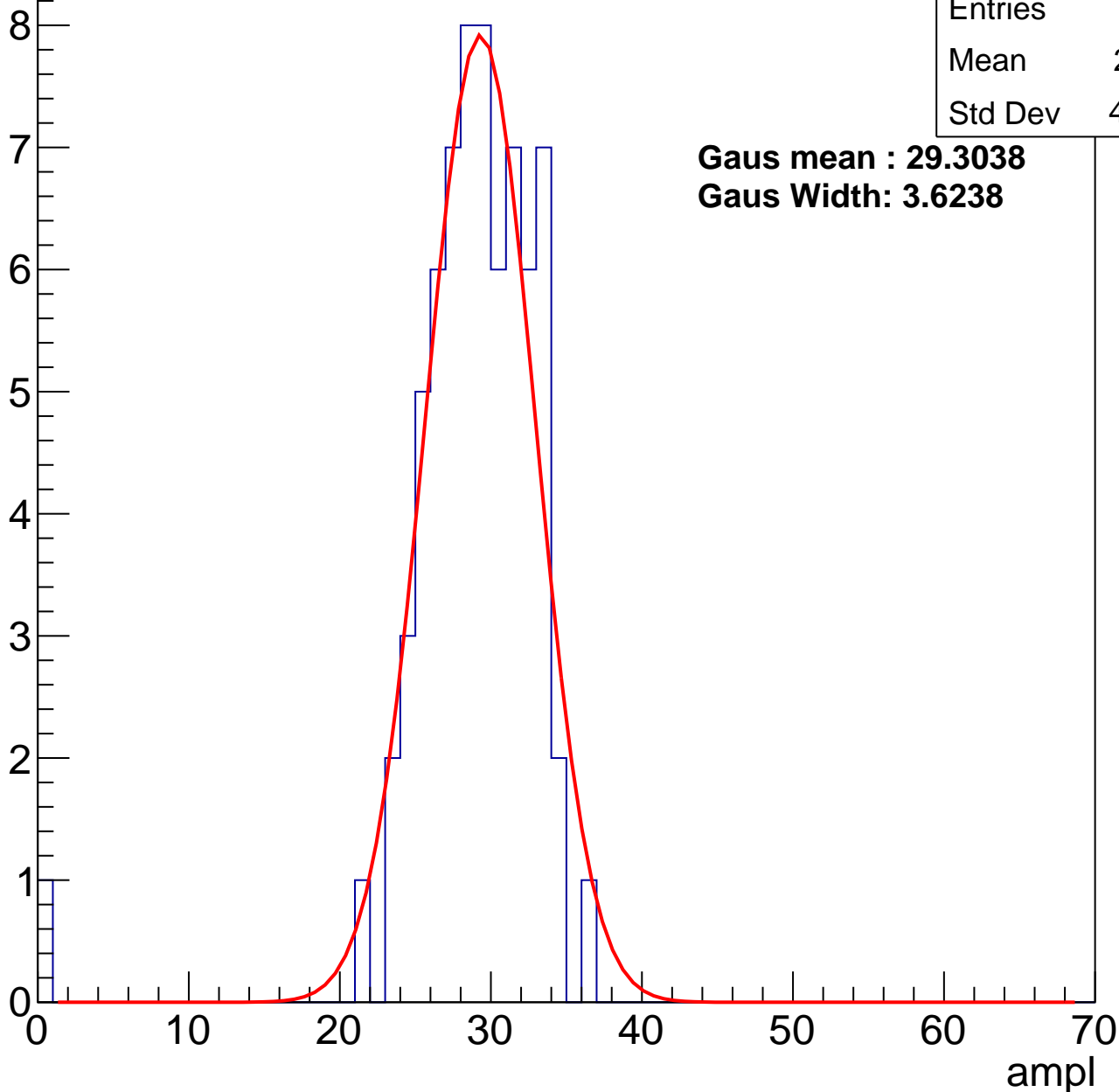
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.41
Std Dev	4.624

Gaus mean : 29.3038

Gaus Width: 3.6238



B1L101S, U12-ch65, adc1

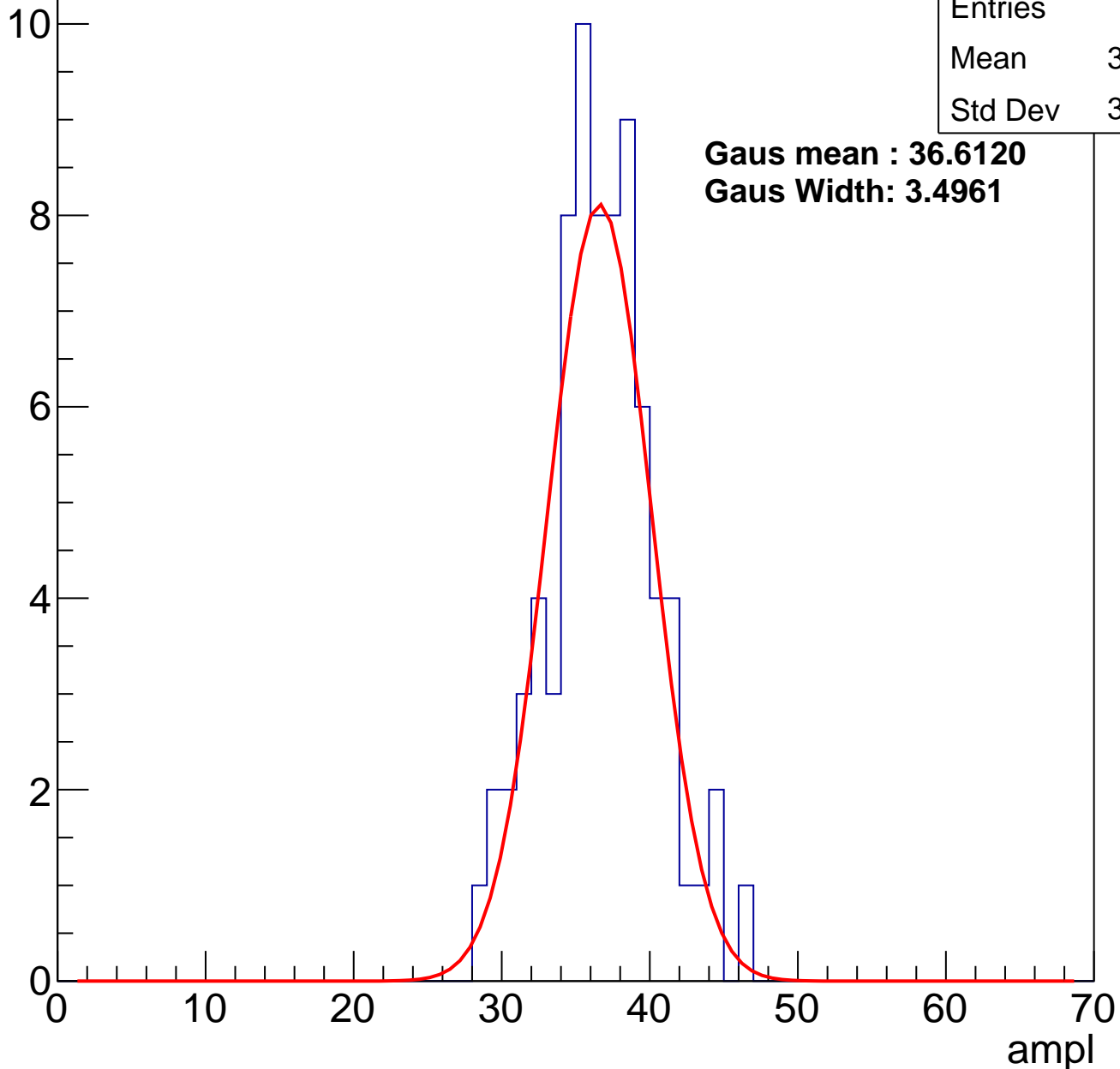
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	36.25
Std Dev	3.626

Gaus mean : 36.6120

Gaus Width: 3.4961

Entry



B1L101S, U12-ch65, adc2

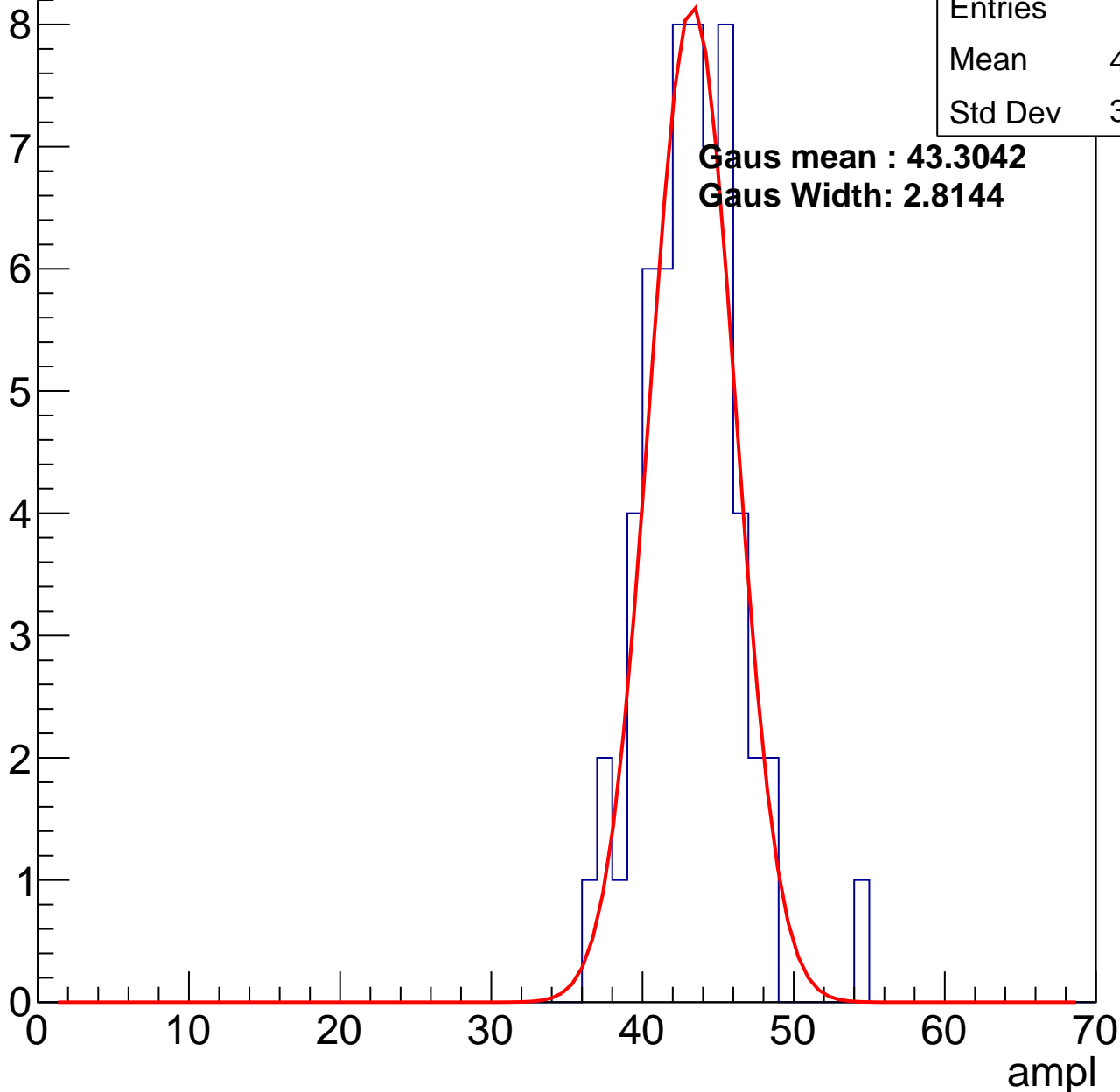
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.77
Std Dev	3.084

Gaus mean : 43.3042

Gaus Width: 2.8144

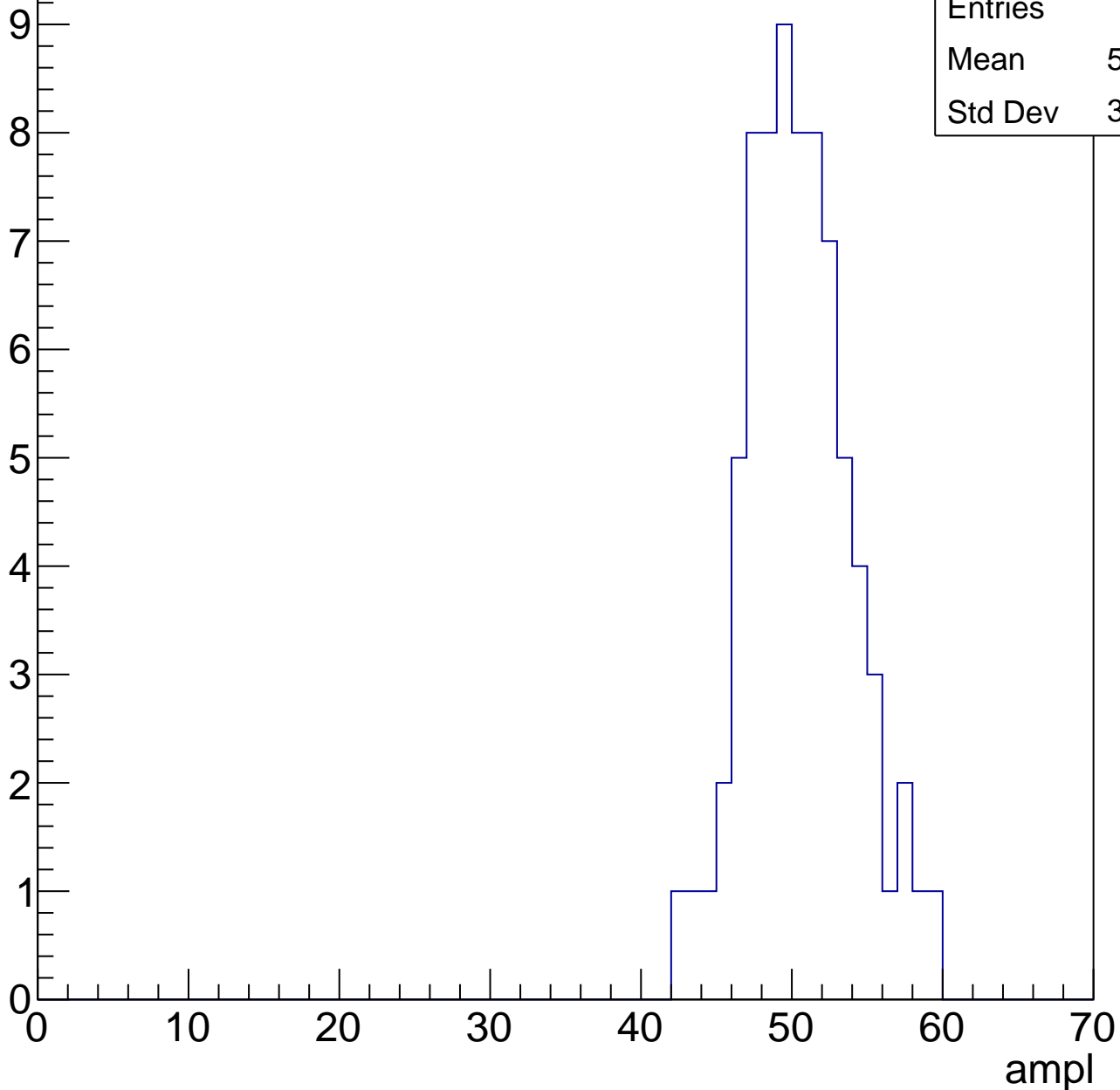


B1L101S, U12-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

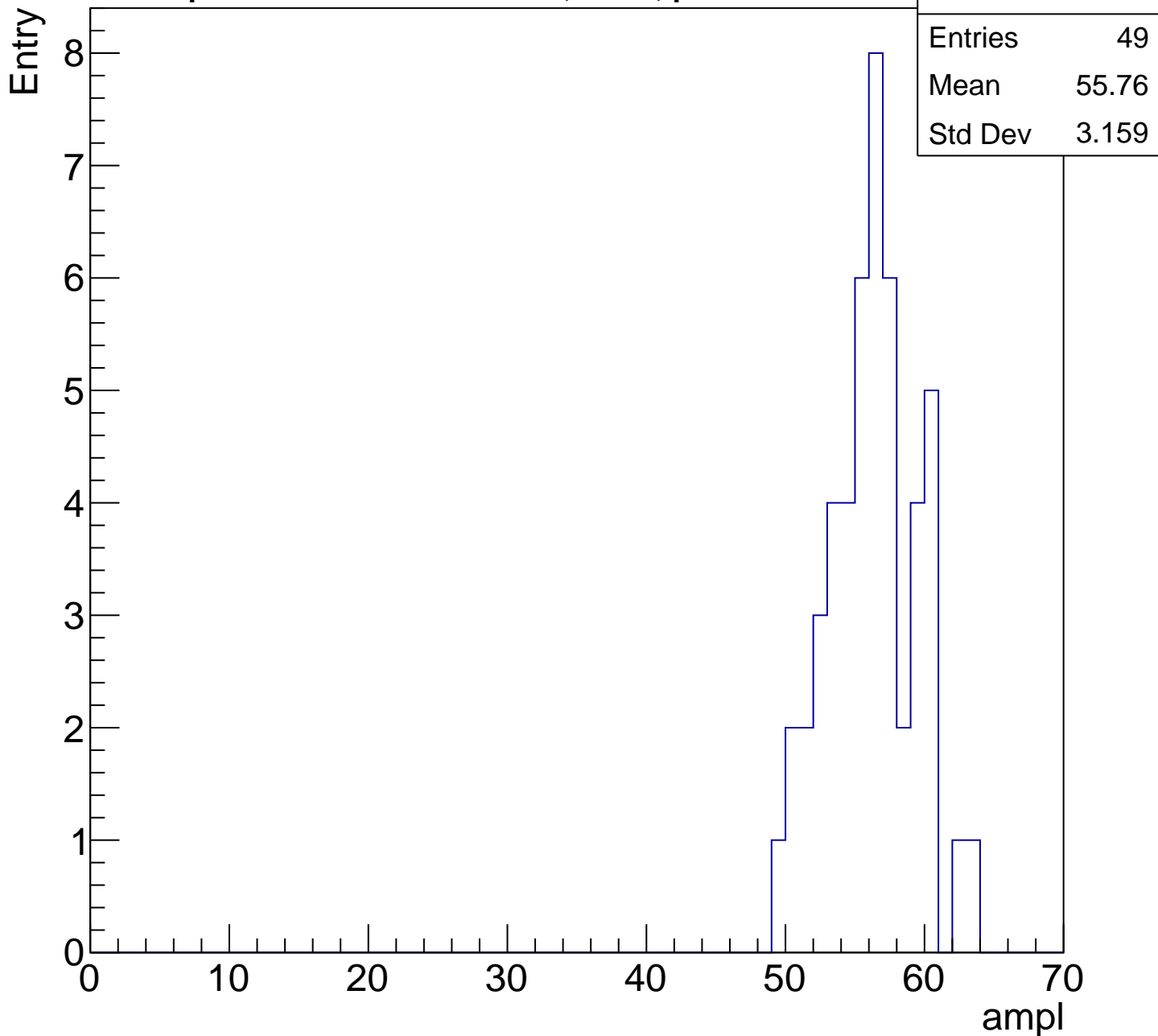
Entry

Entries	75
Mean	50.07
Std Dev	3.446



B1L101S, U12-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

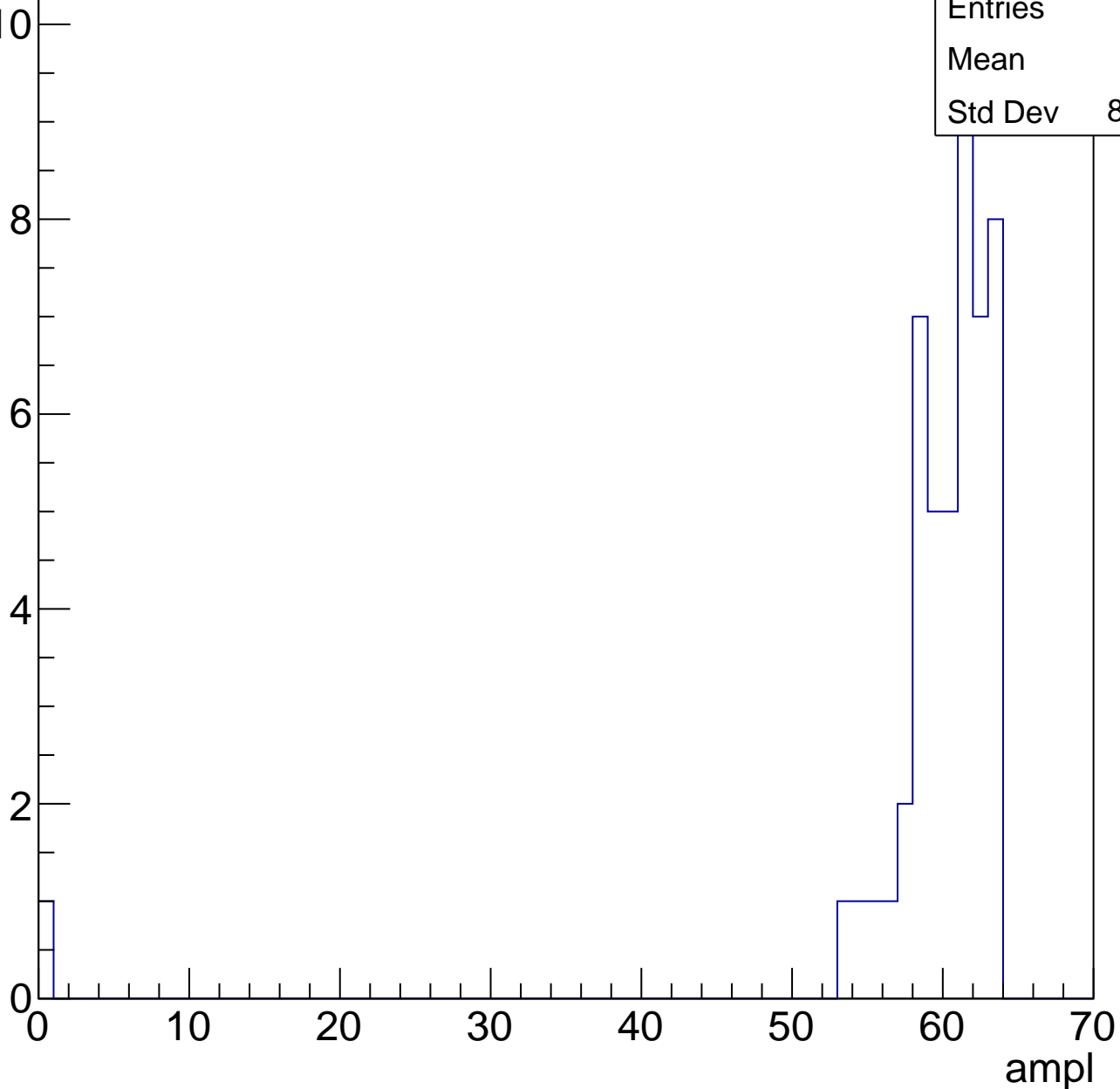


B1L101S, U12-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.8
Std Dev	8.825



B1L101S, U12-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L101S, U12-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch66, adc0

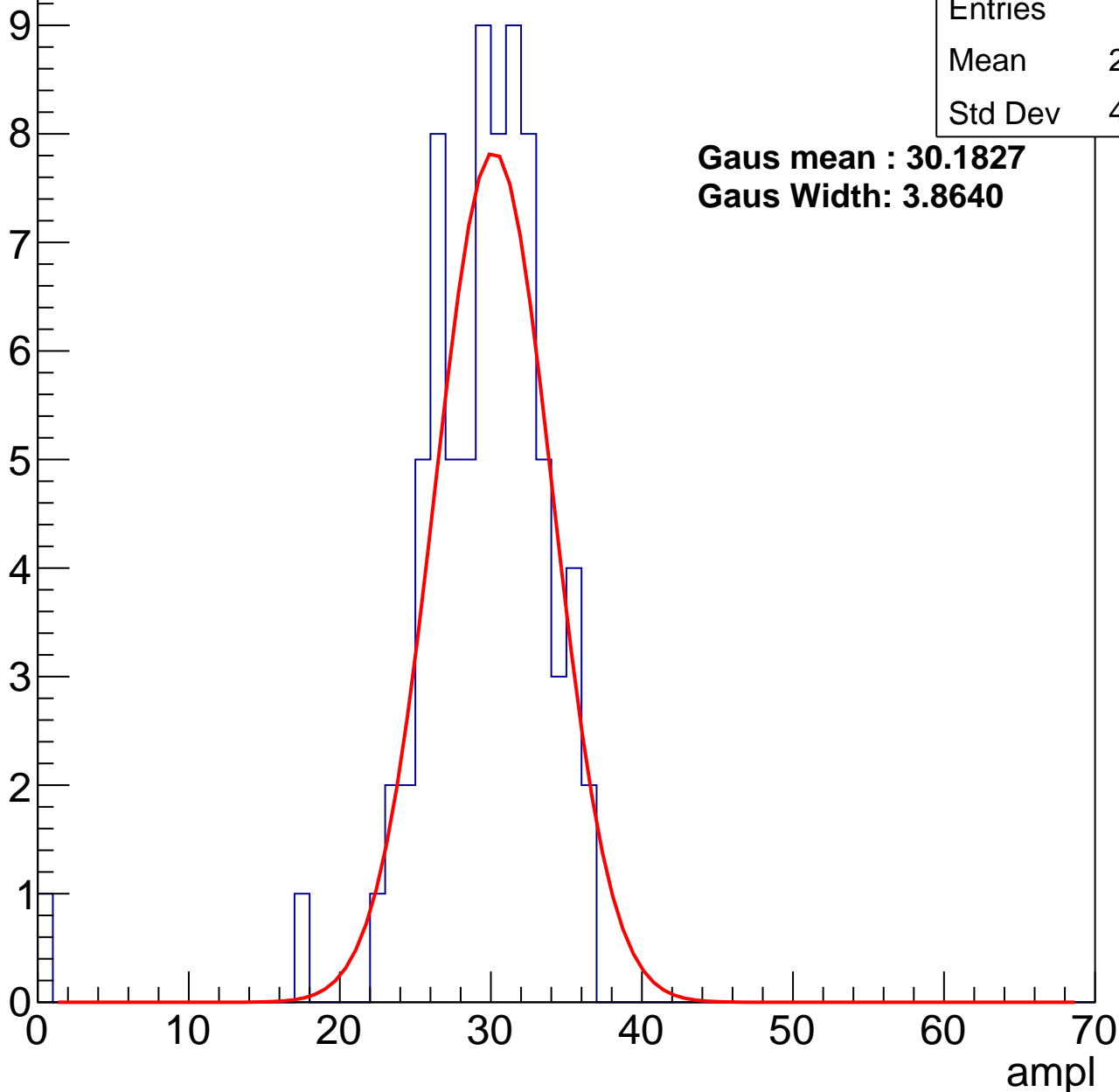
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.92
Std Dev	4.864

Gaus mean : 30.1827

Gaus Width: 3.8640



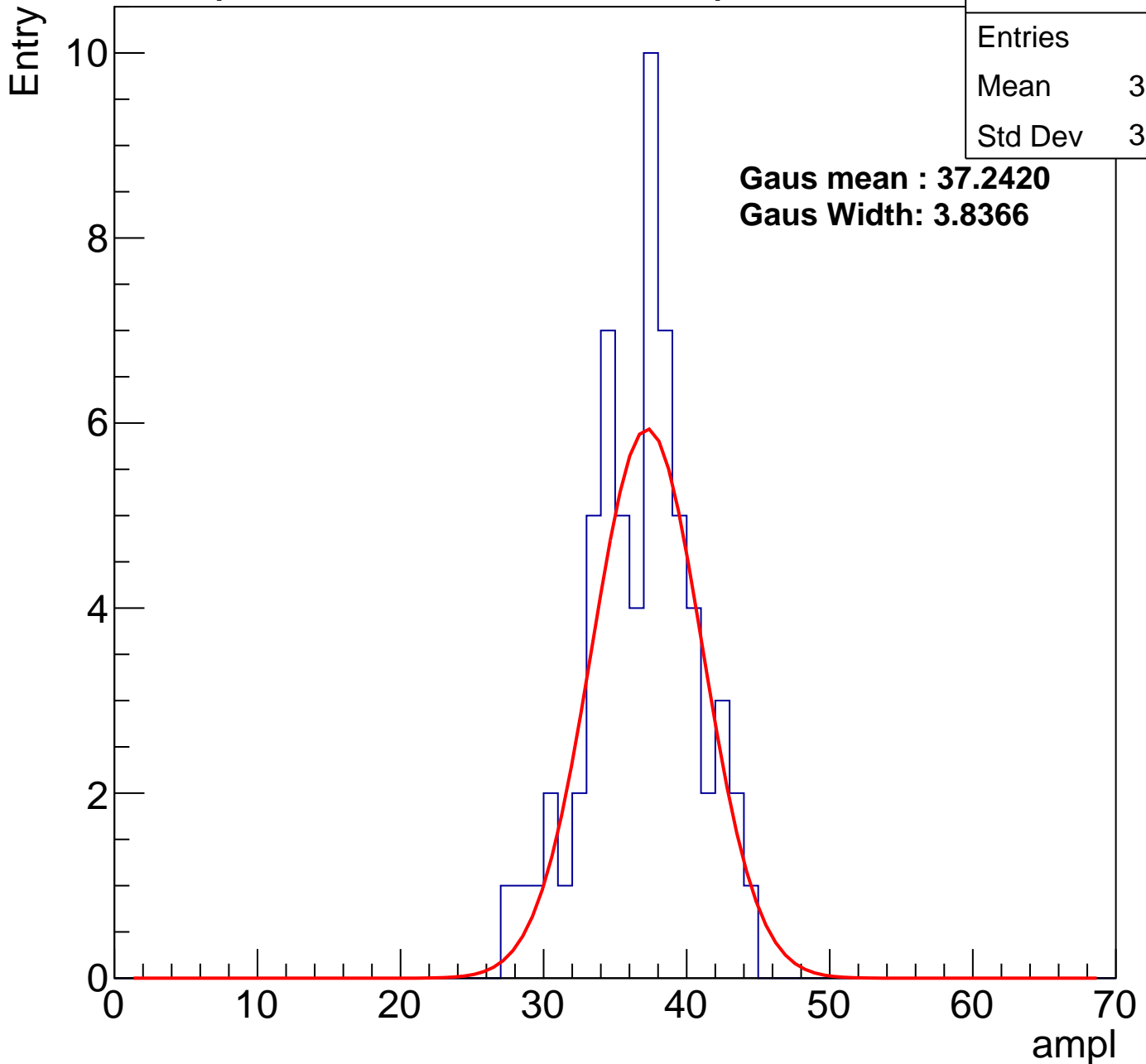
B1L101S, U12-ch66, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	36.35
Std Dev	3.695

Gaus mean : 37.2420

Gaus Width: 3.8366



B1L101S, U12-ch66, adc2

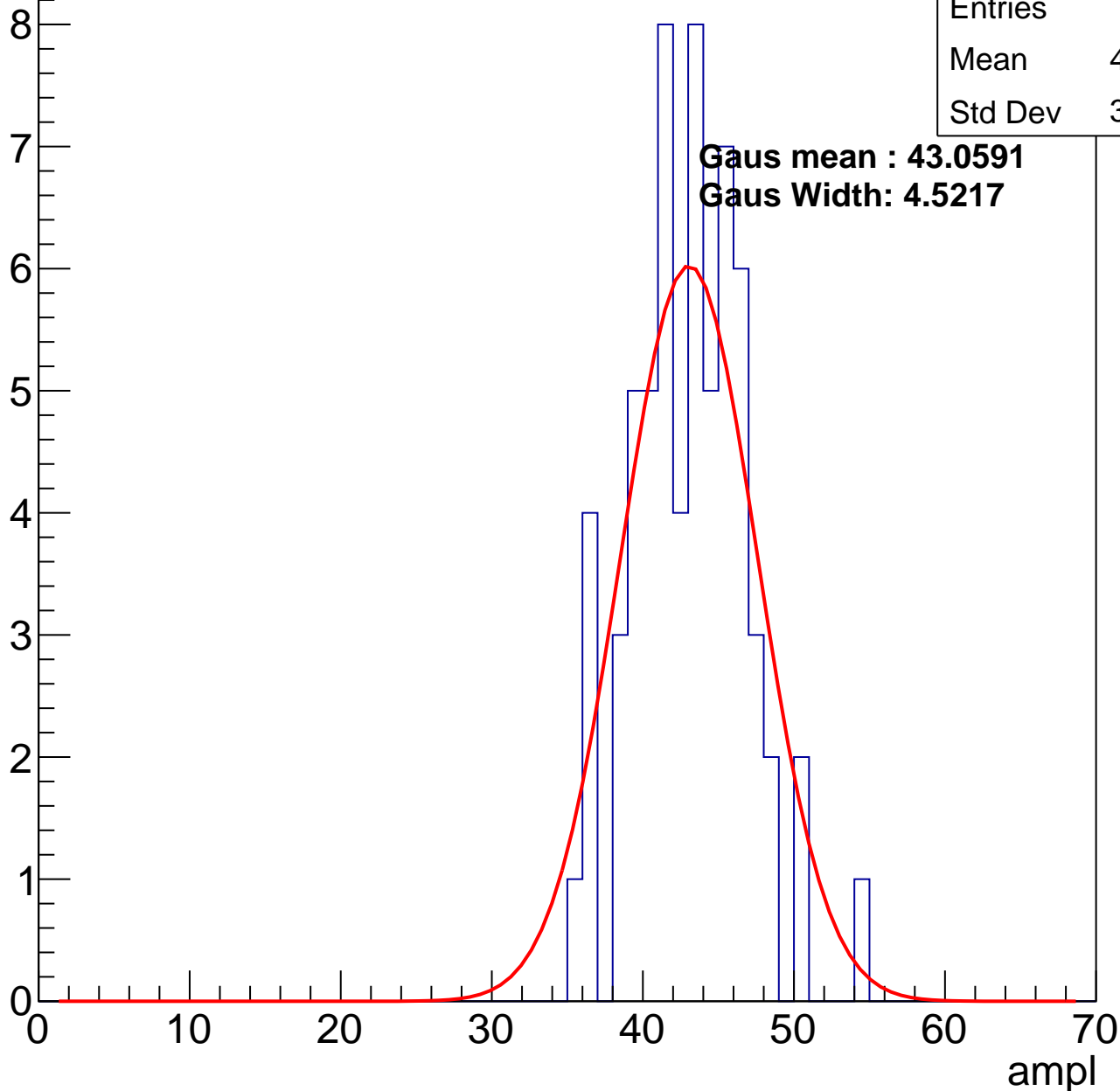
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.66
Std Dev	3.734

Gaus mean : 43.0591

Gaus Width: 4.5217

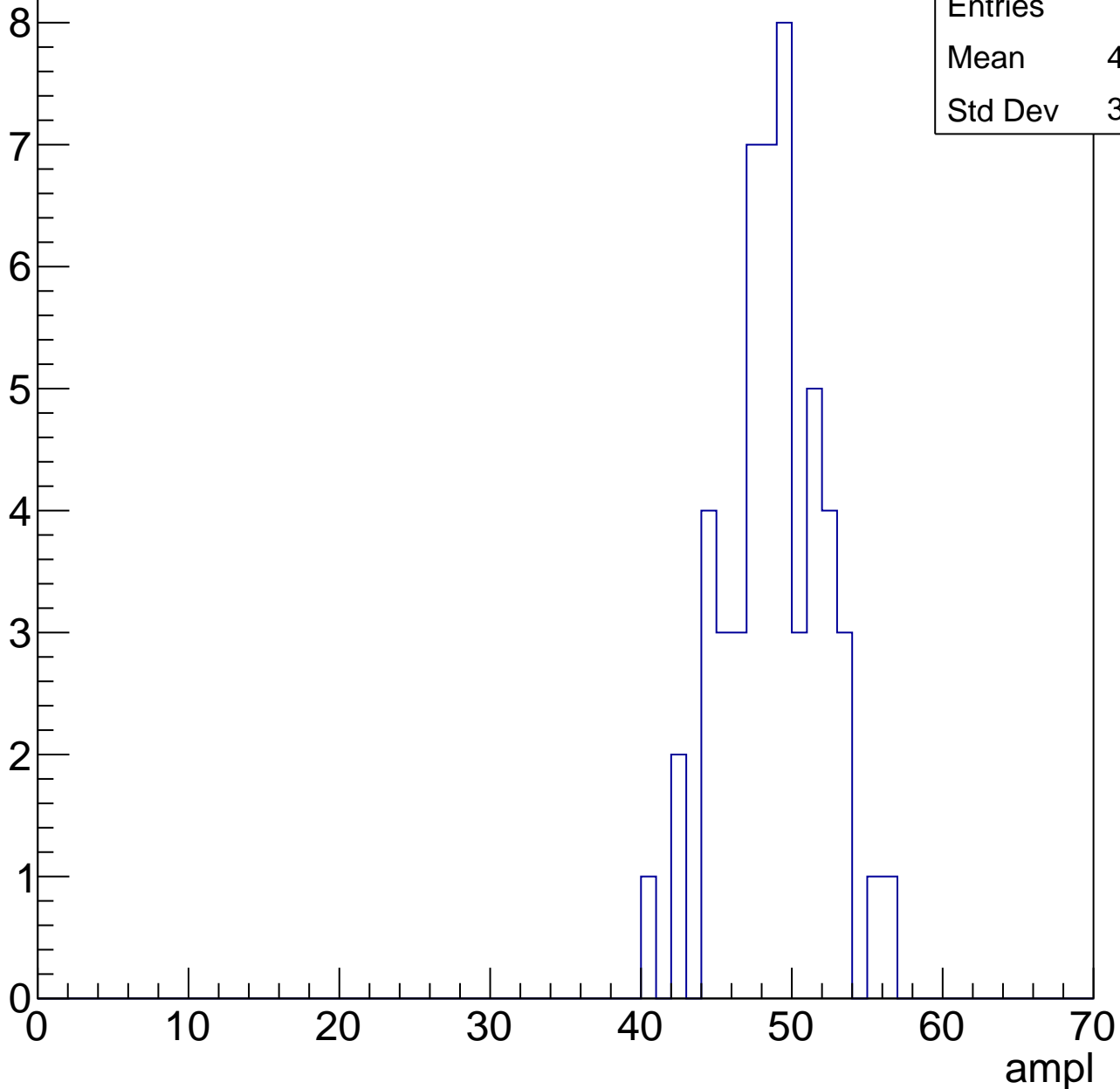


B1L101S, U12-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	48.33
Std Dev	3.274

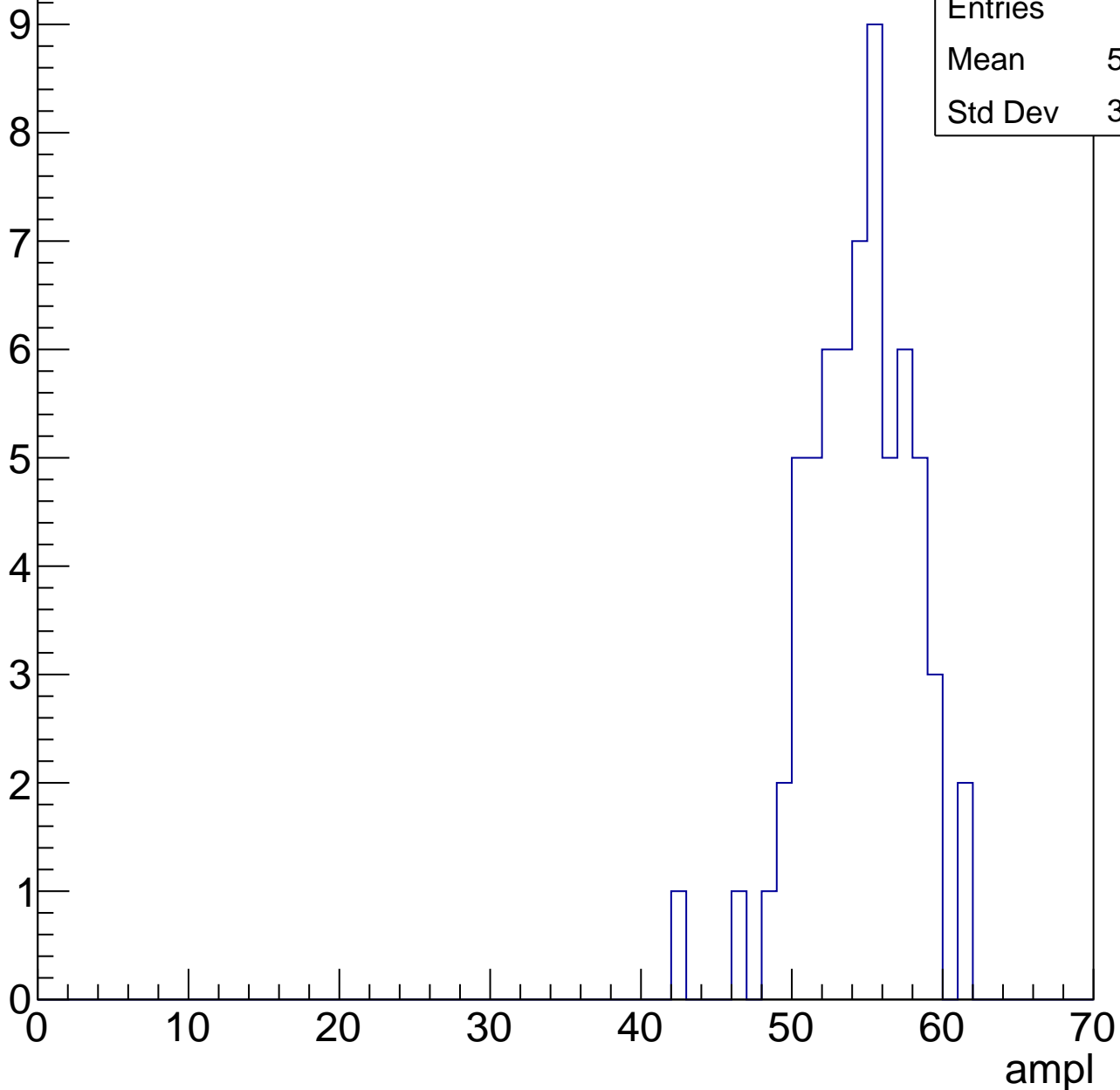


B1L101S, U12-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	53.95
Std Dev	3.502



B1L101S, U12-ch66, adc5

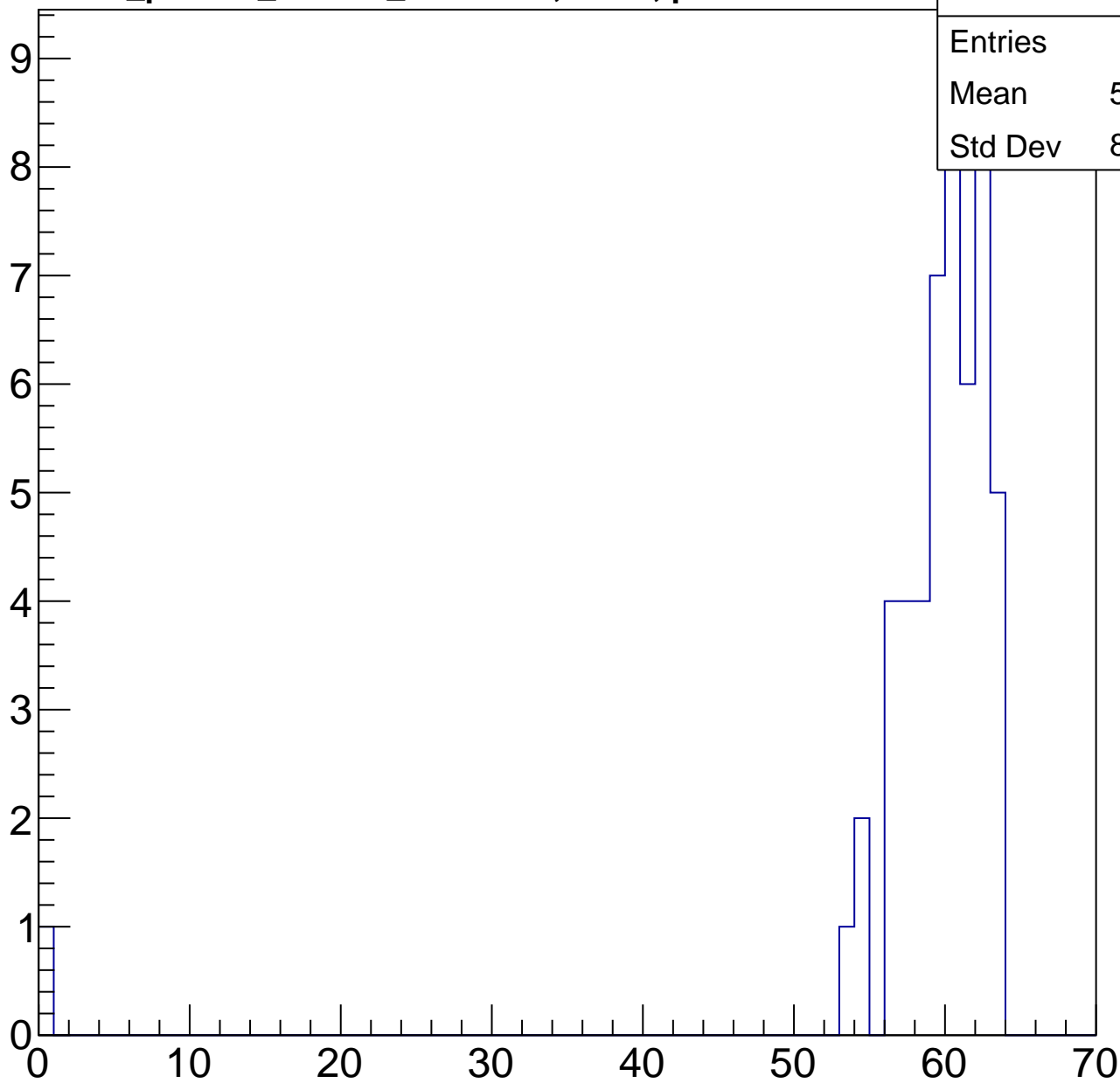
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.33
Std Dev	8.615

ampl

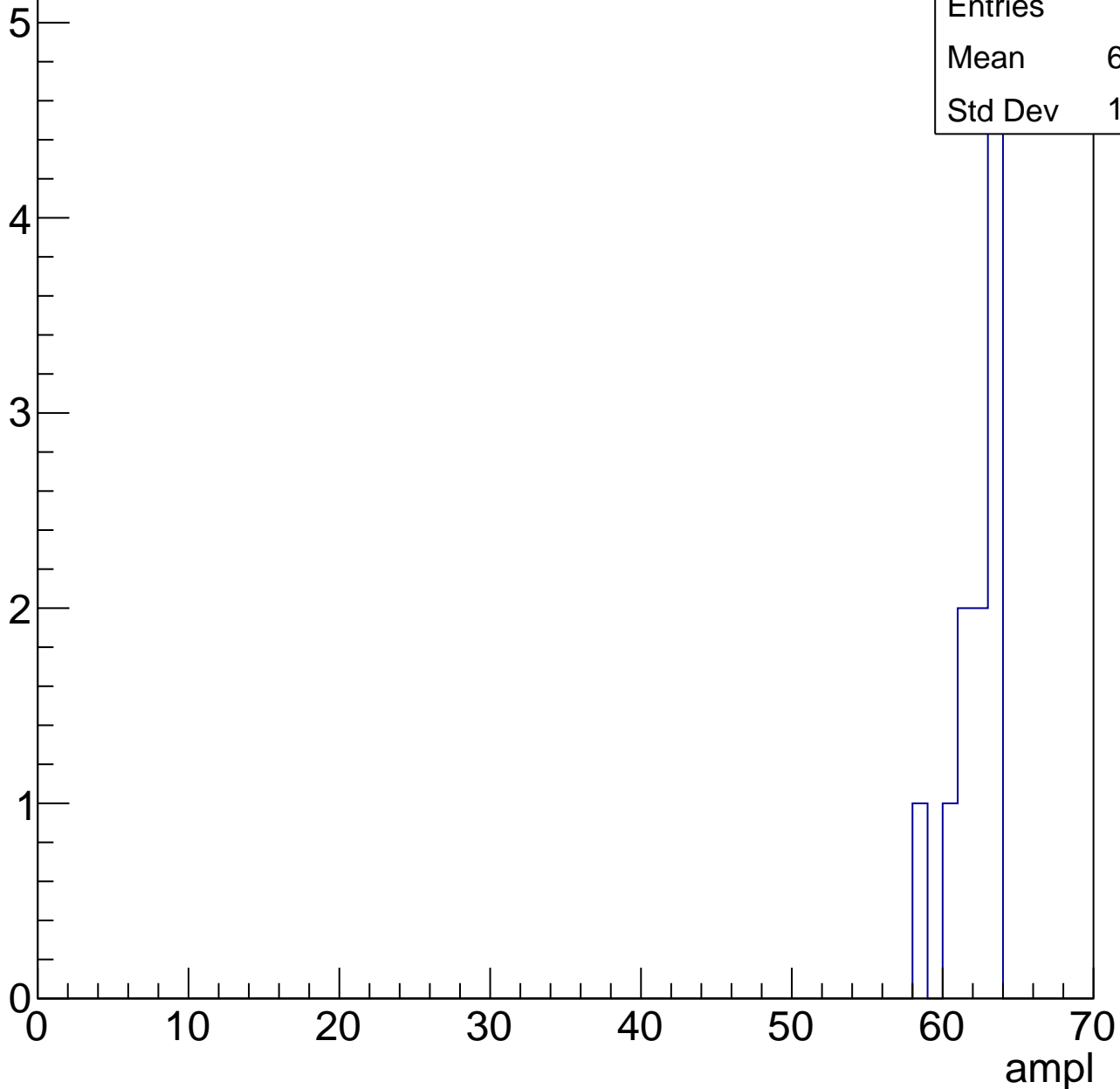


B1L101S, U12-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	61.73
Std Dev	1.543



B1L101S, U12-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch67, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	28.18
Std Dev	3.566

Gaus mean : 28.7531

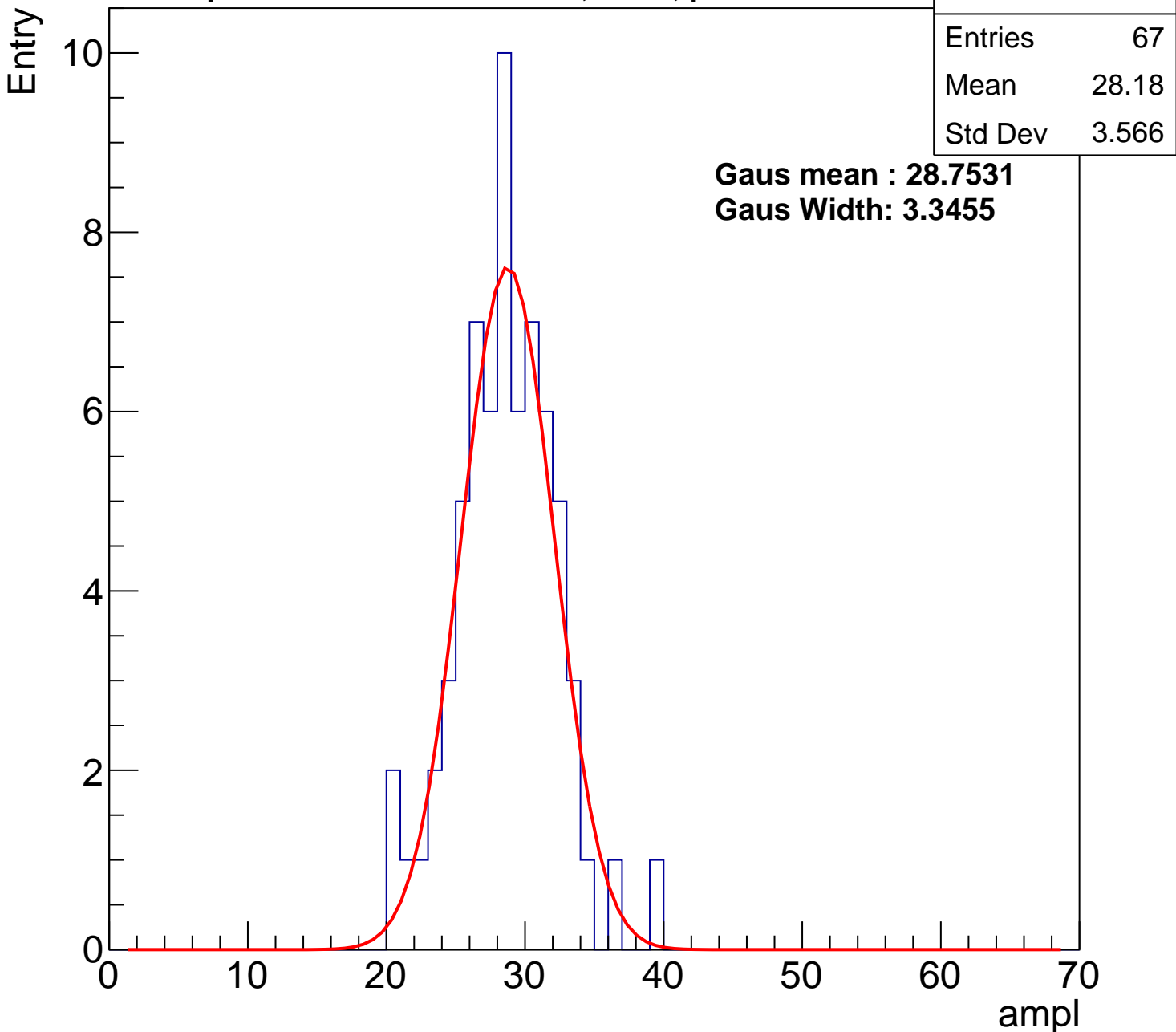
Gaus Width: 3.3455

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch67, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	34.32
Std Dev	3.274

Gaus mean : 34.9052

Gaus Width: 3.6925

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L101S, U12-ch67, adc2

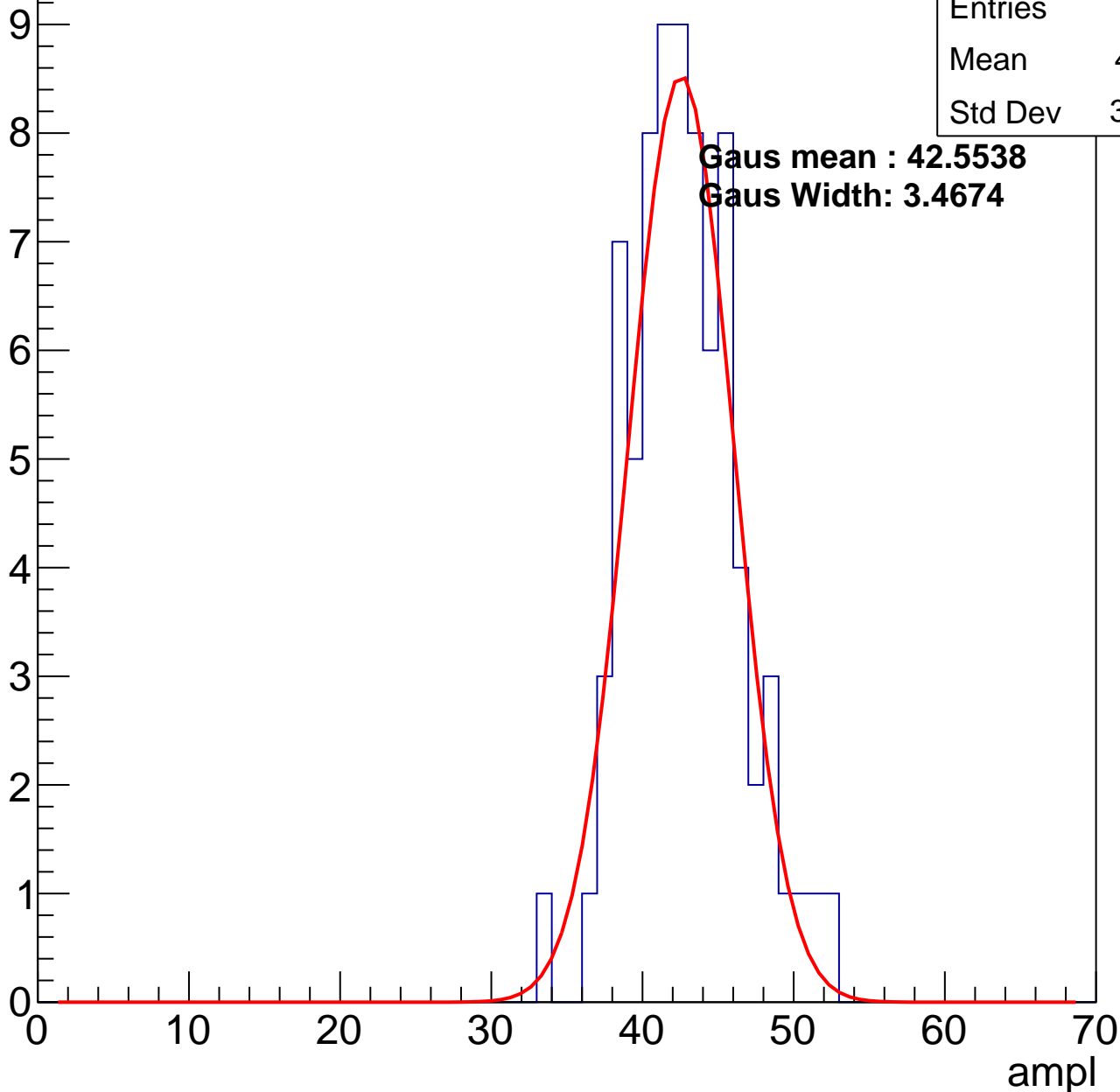
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.31
Std Dev	3.585

Gaus mean : 42.5538

Gaus Width: 3.4674

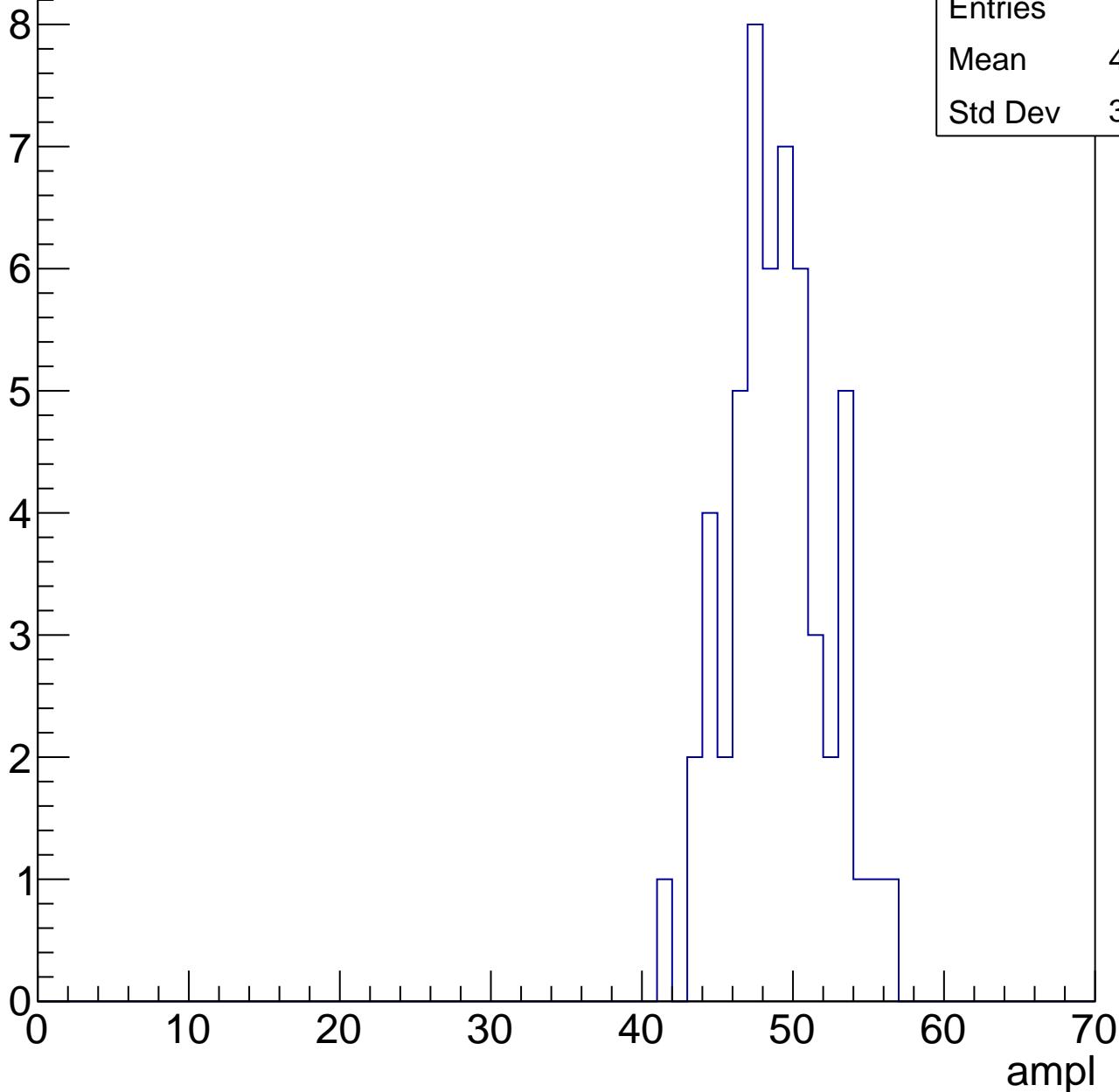


B1L101S, U12-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

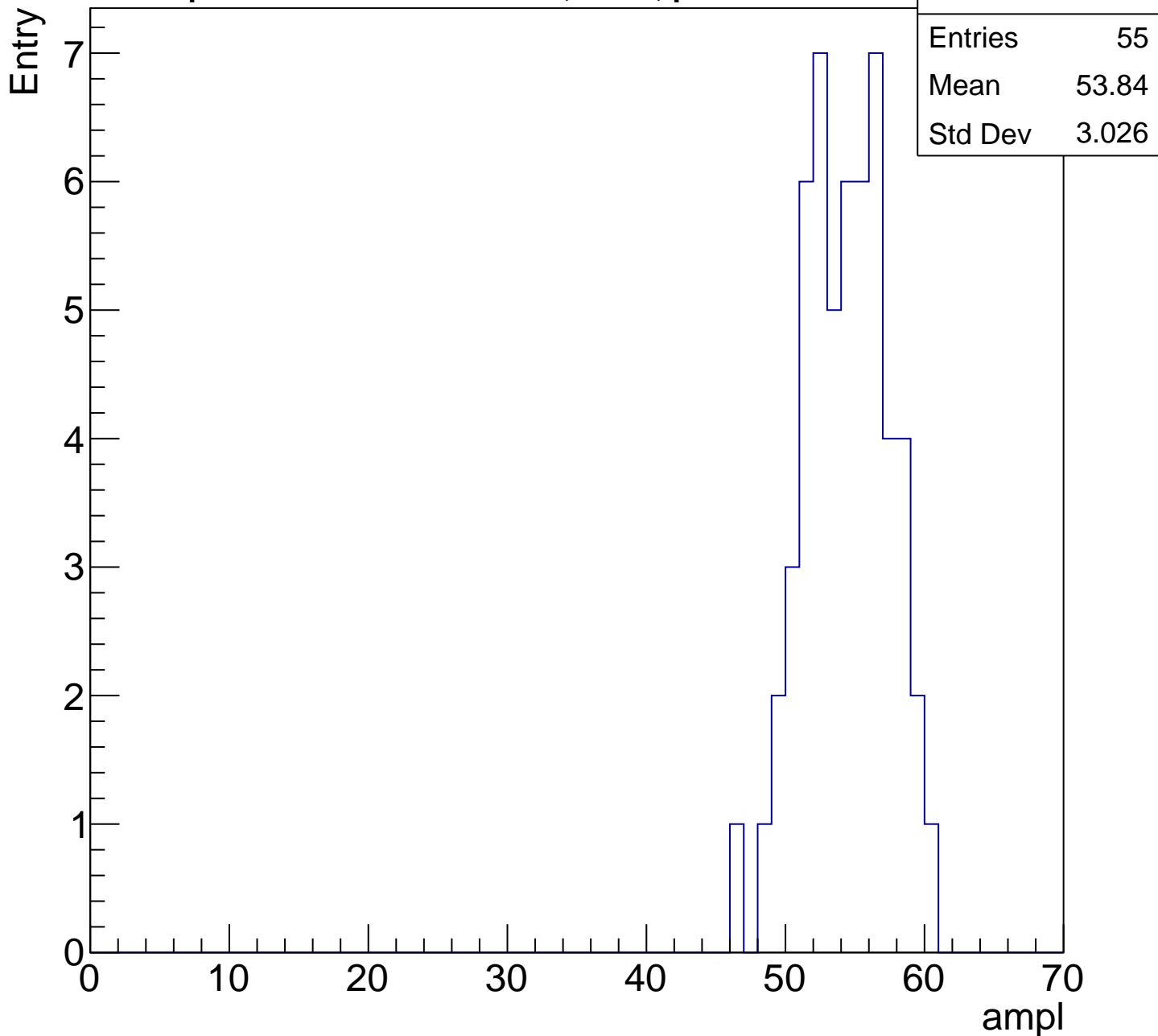
Entry

Entries	54
Mean	48.46
Std Dev	3.224



B1L101S, U12-ch67, adc4

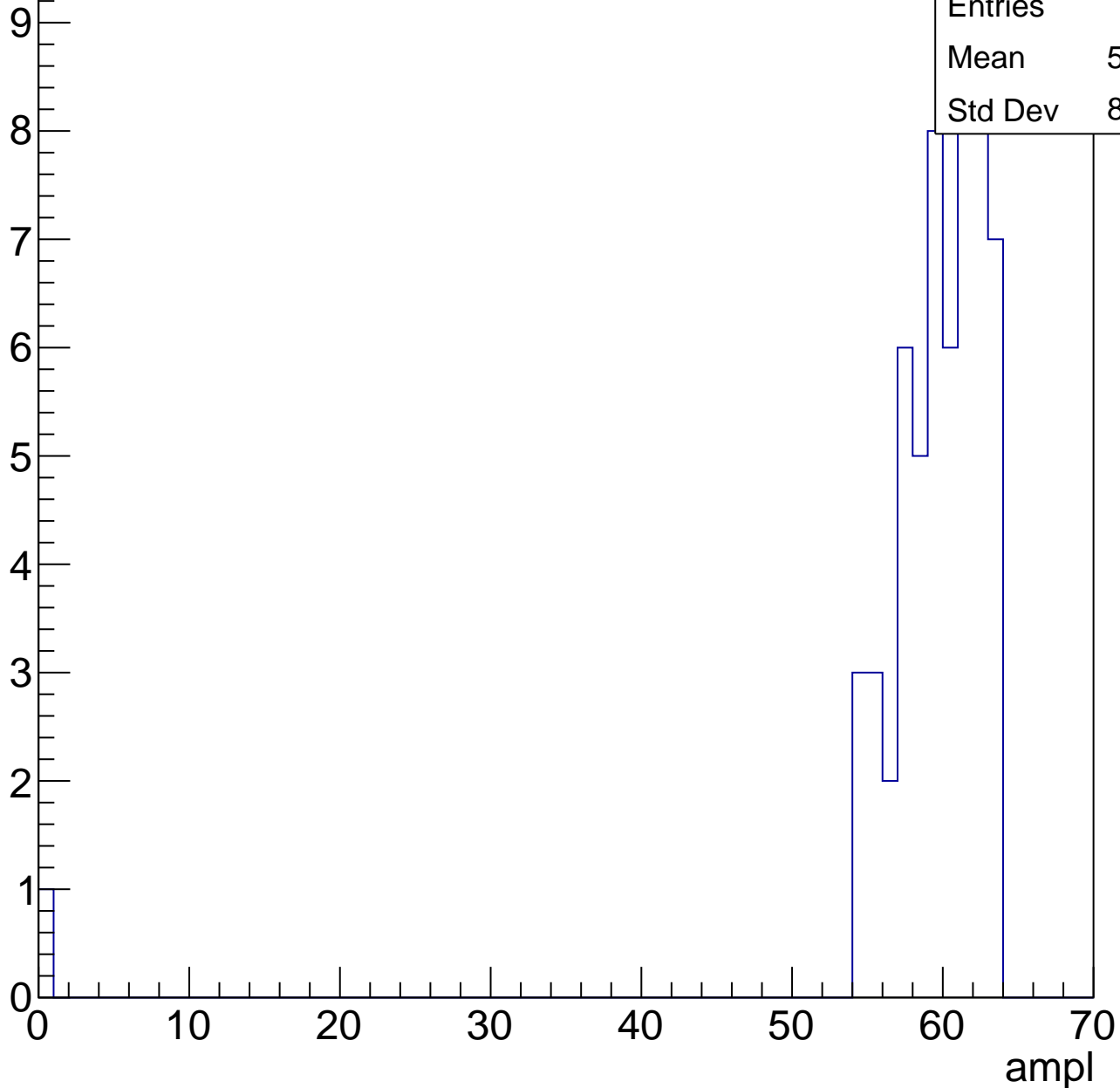
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.1
Std Dev	1.578

B1L101S, U12-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch68, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	30.66
Std Dev	3.449

Gaus mean : 31.0435

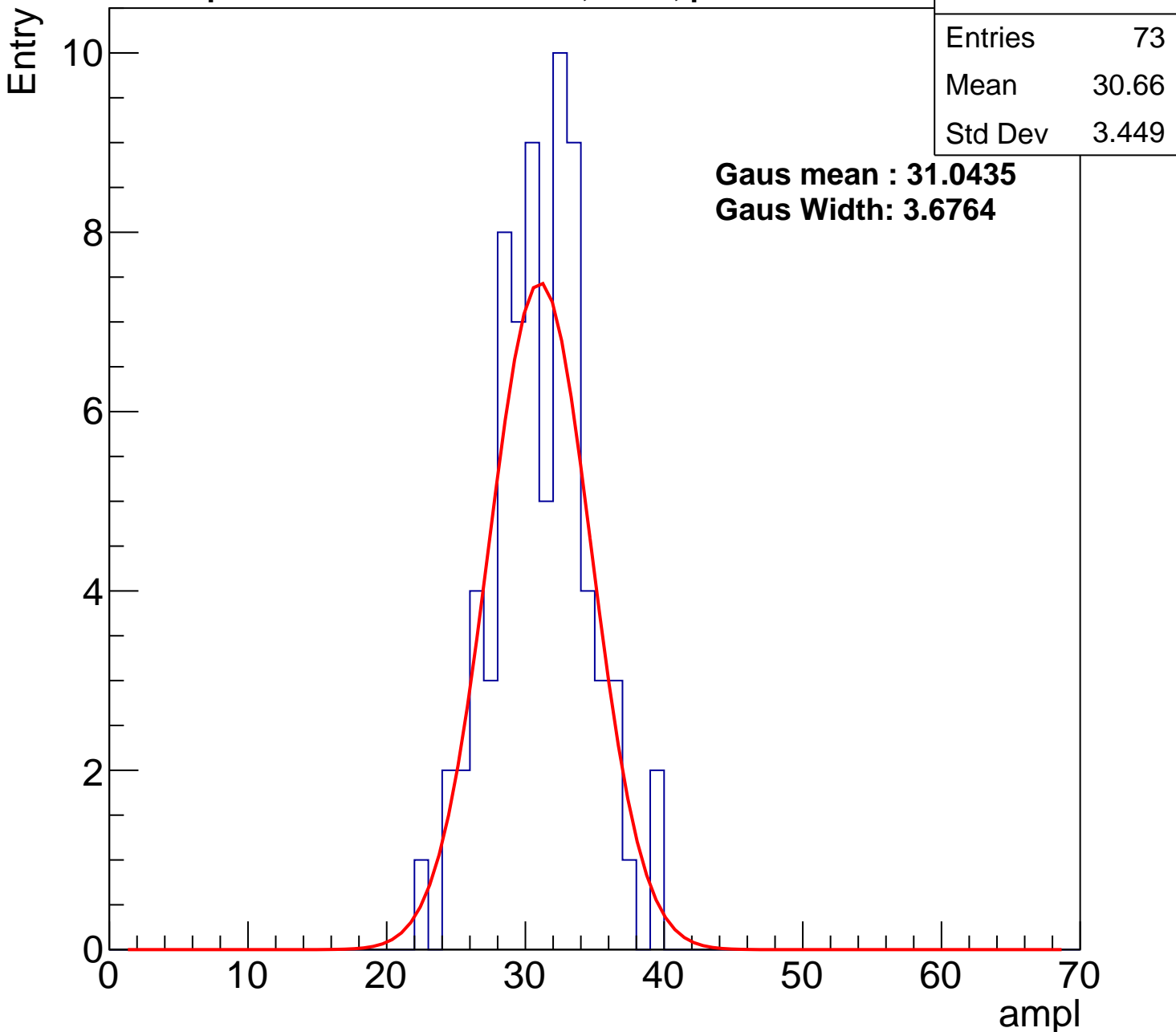
Gaus Width: 3.6764

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch68, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	37.99
Std Dev	3.682

Gaus mean : 38.7101

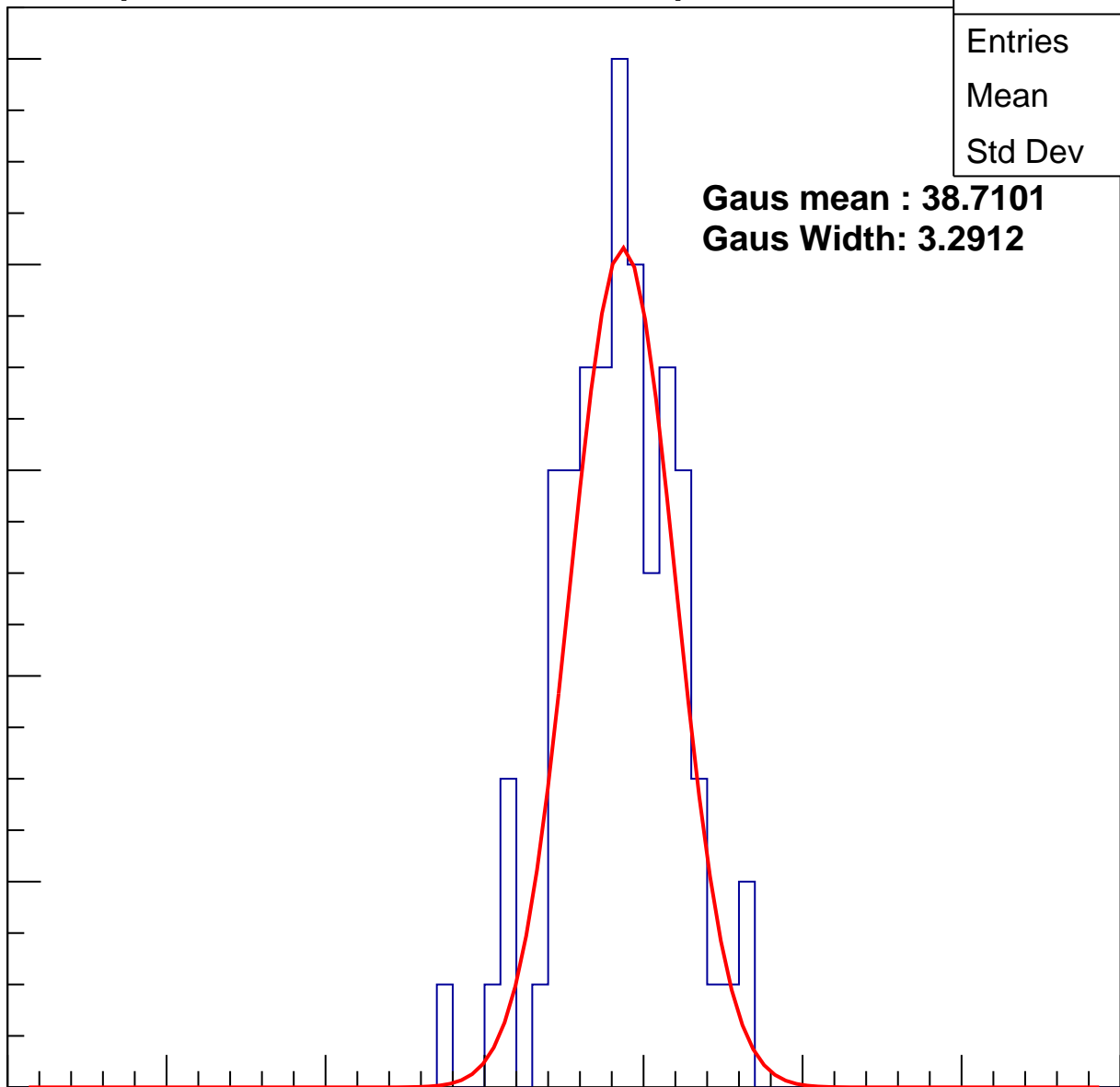
Gaus Width: 3.2912

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch68, adc2

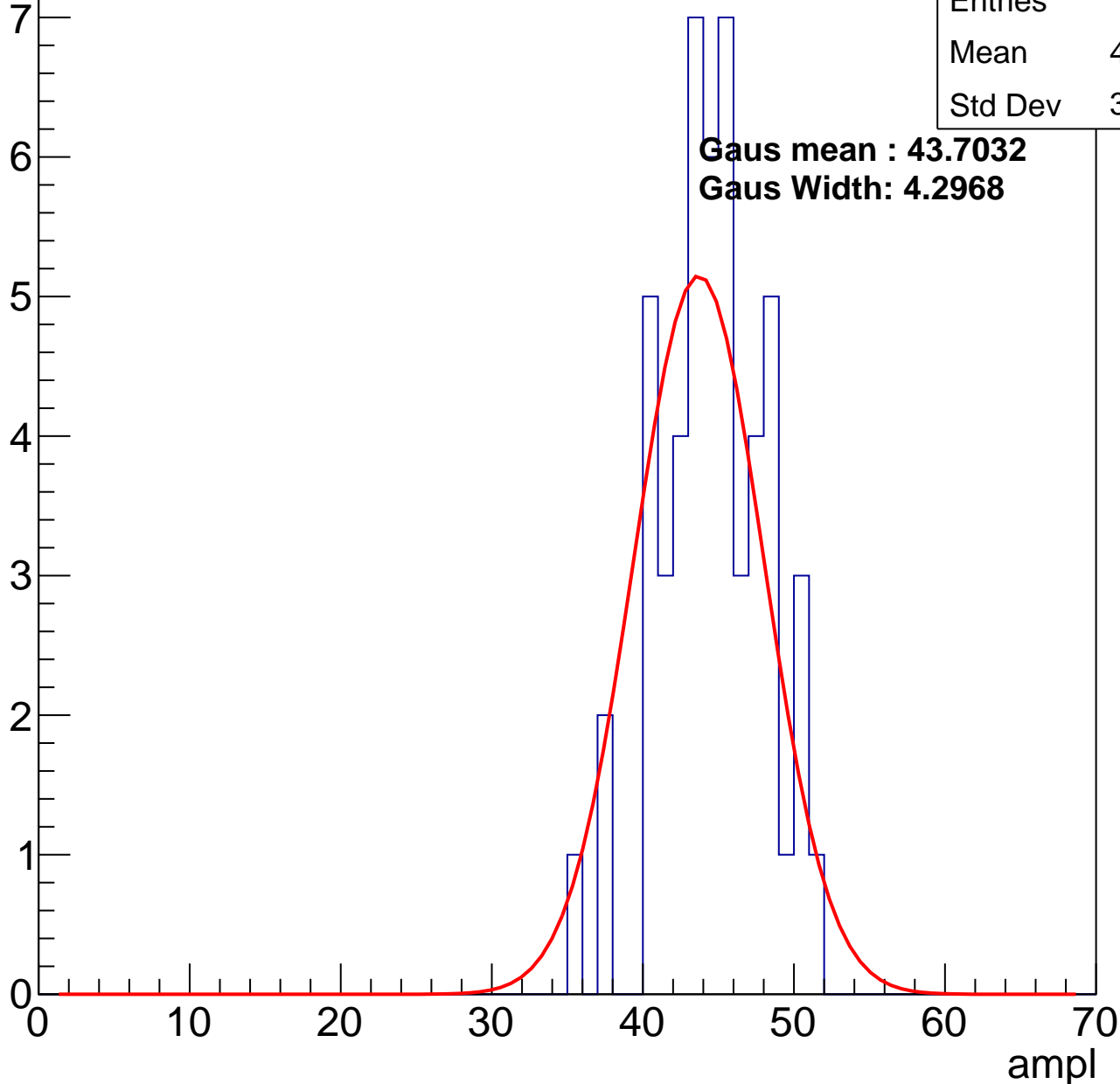
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.15
Std Dev	3.466

Gaus mean : 43.7032

Gaus Width: 4.2968

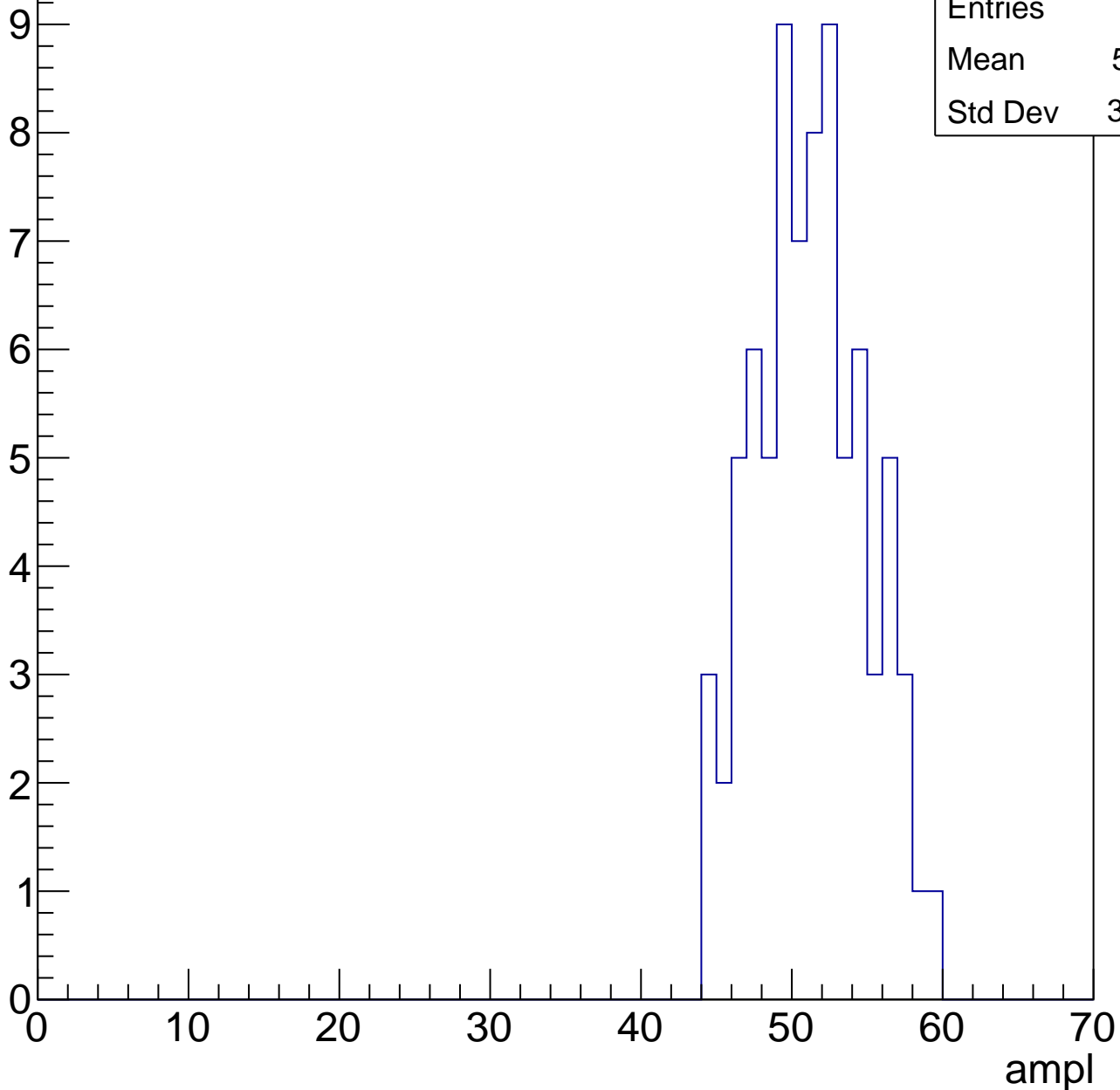


B1L101S, U12-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

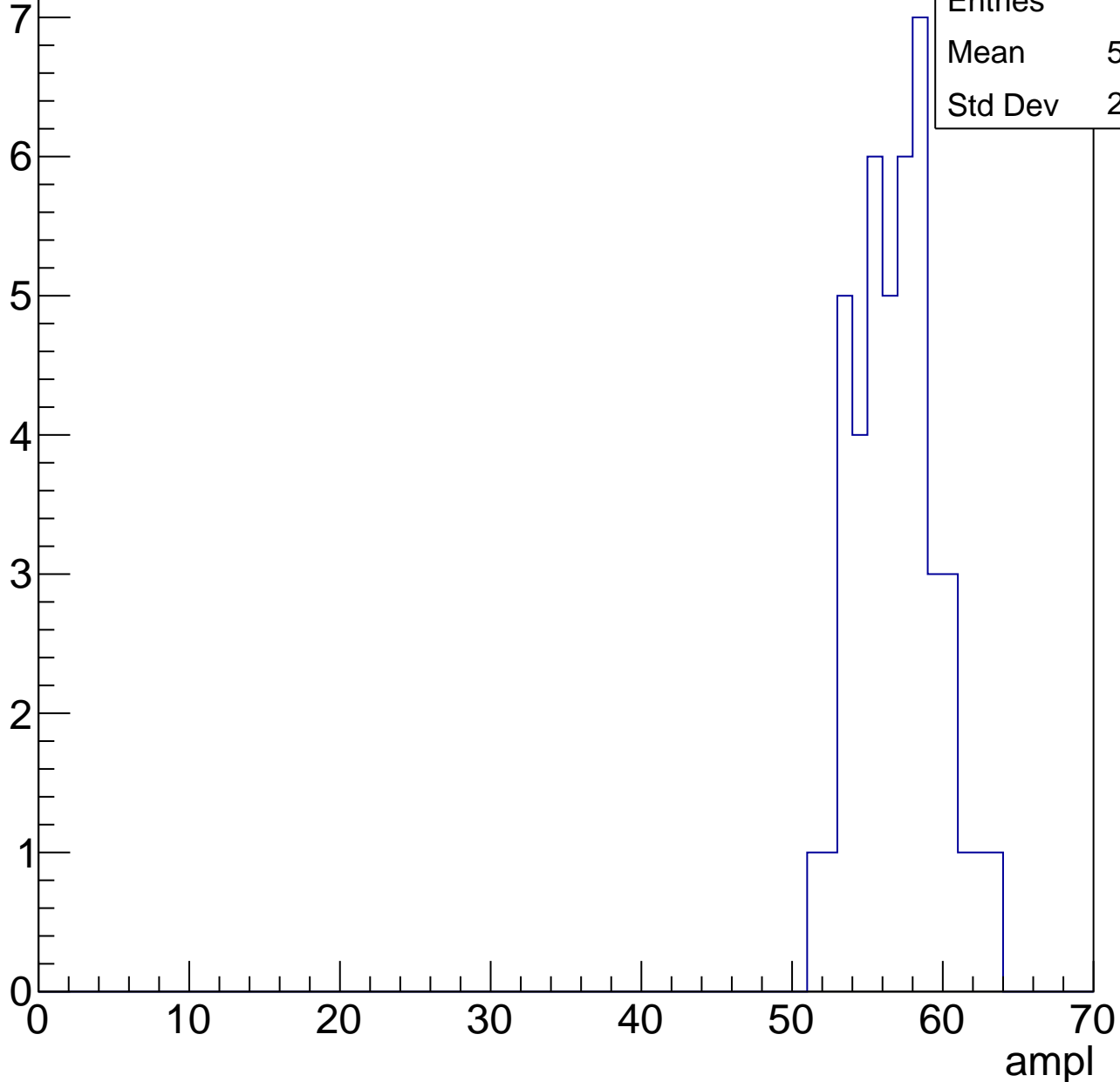
Entries	78
Mean	50.81
Std Dev	3.584



B1L101S, U12-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	44
Mean	56.48
Std Dev	2.684

B1L101S, U12-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.37
Std Dev	9.336

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch69, adc0

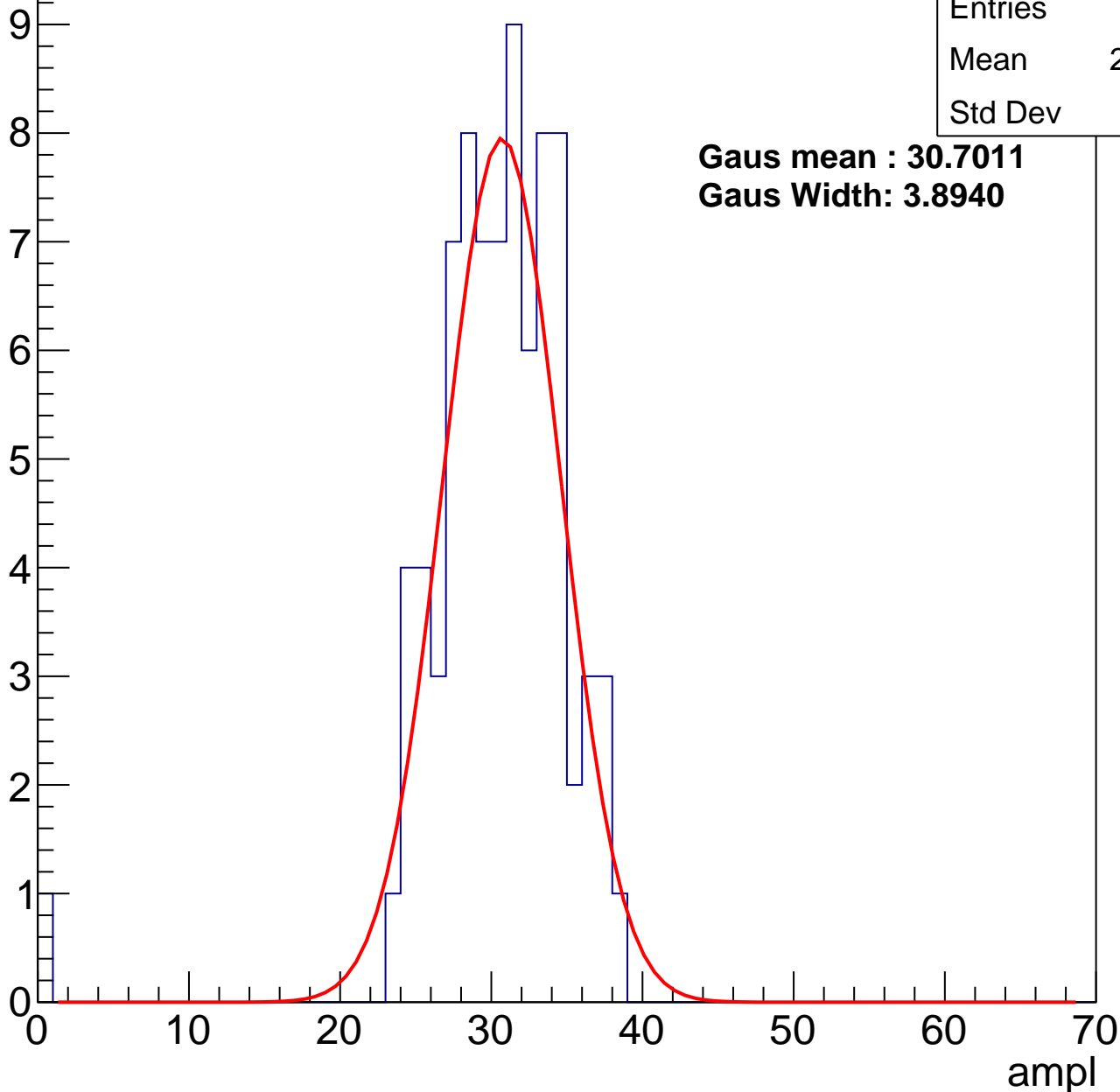
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	29.96
Std Dev	4.87

Gaus mean : 30.7011

Gaus Width: 3.8940



B1L101S, U12-ch69, adc1

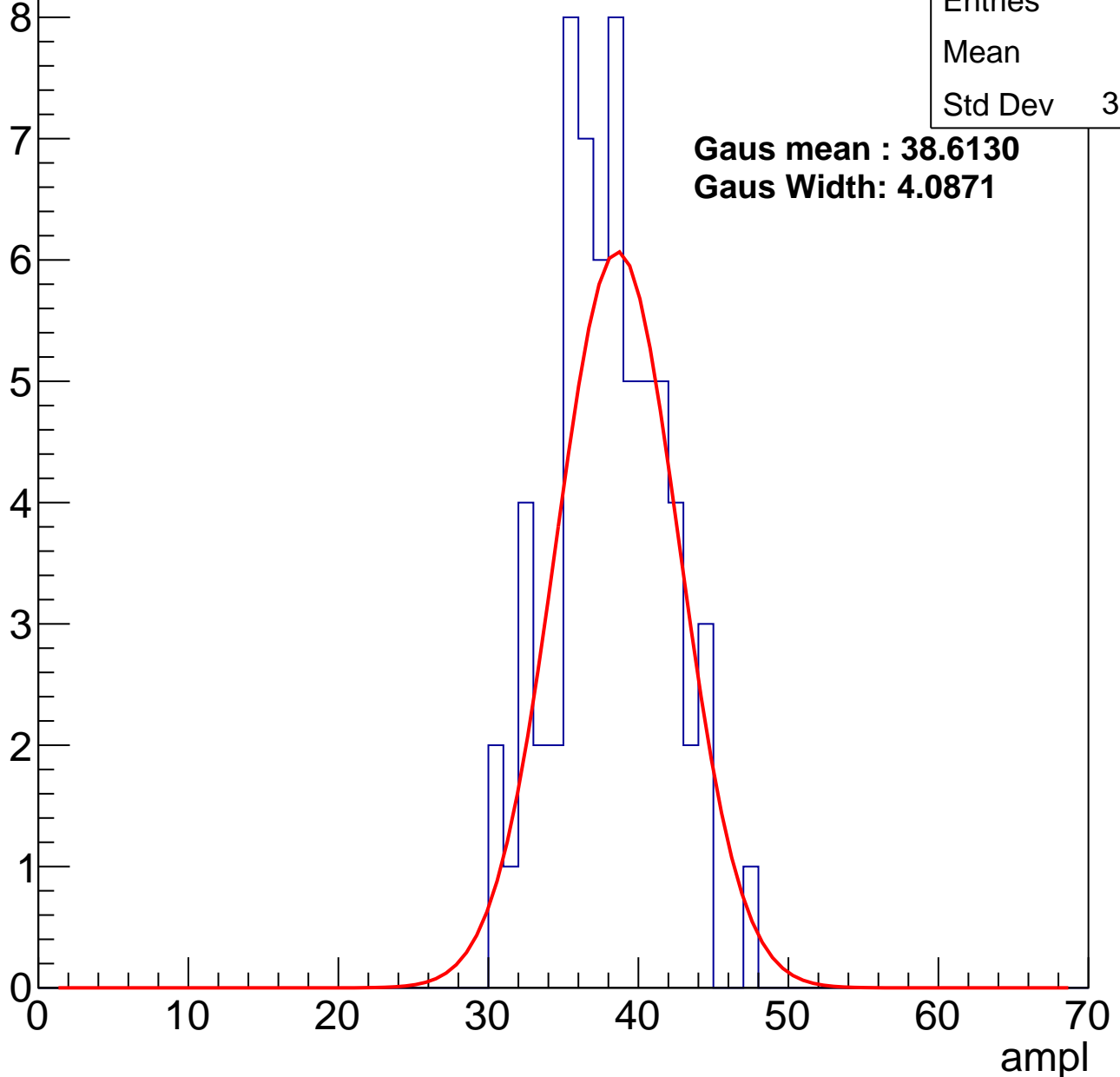
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.6
Std Dev	3.662

Gaus mean : 38.6130

Gaus Width: 4.0871



B1L101S, U12-ch69, adc2

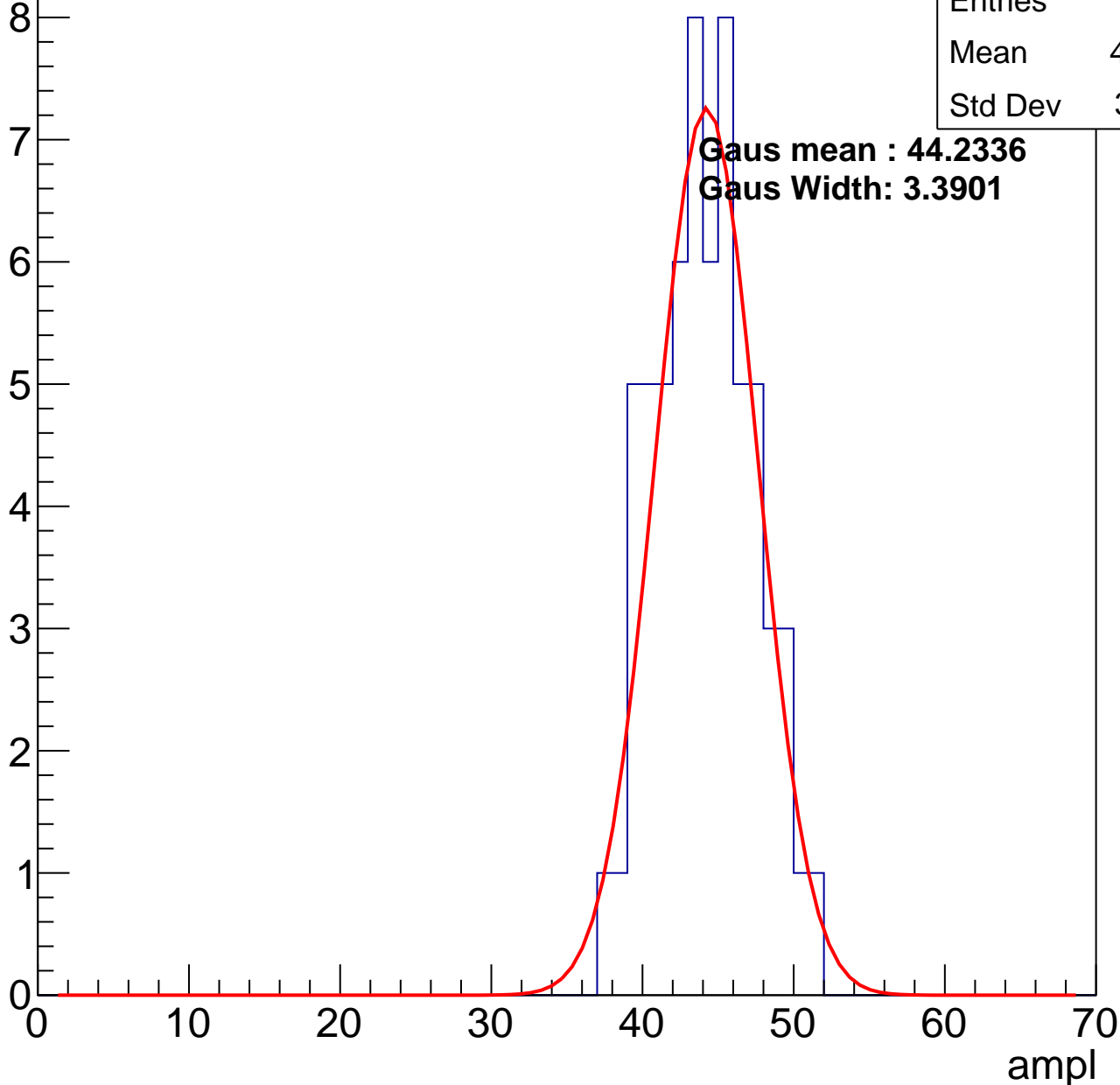
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.68
Std Dev	3.191

Gaus mean : 44.2336

Gaus Width: 3.3901

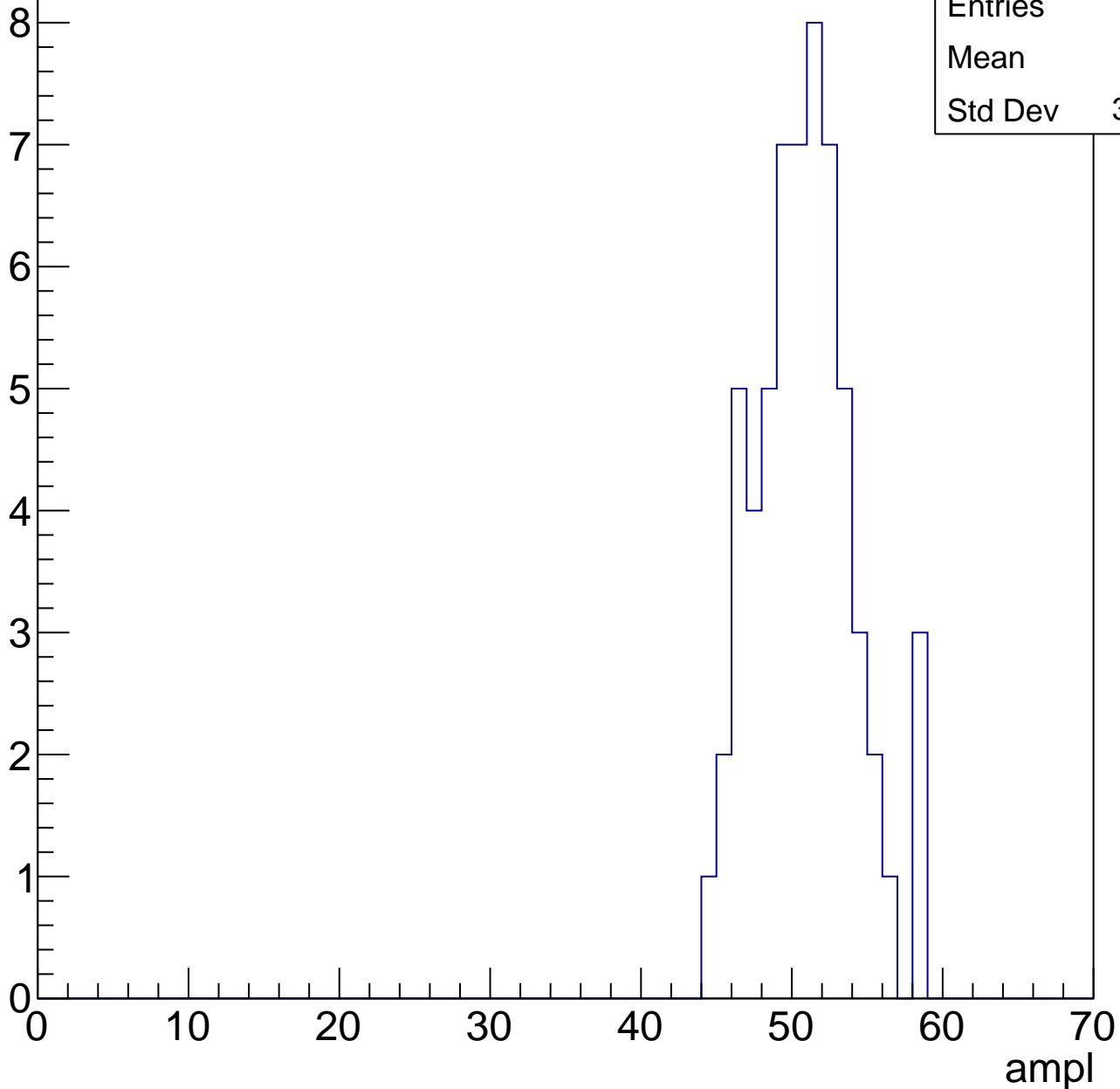


B1L101S, U12-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.4
Std Dev	3.231

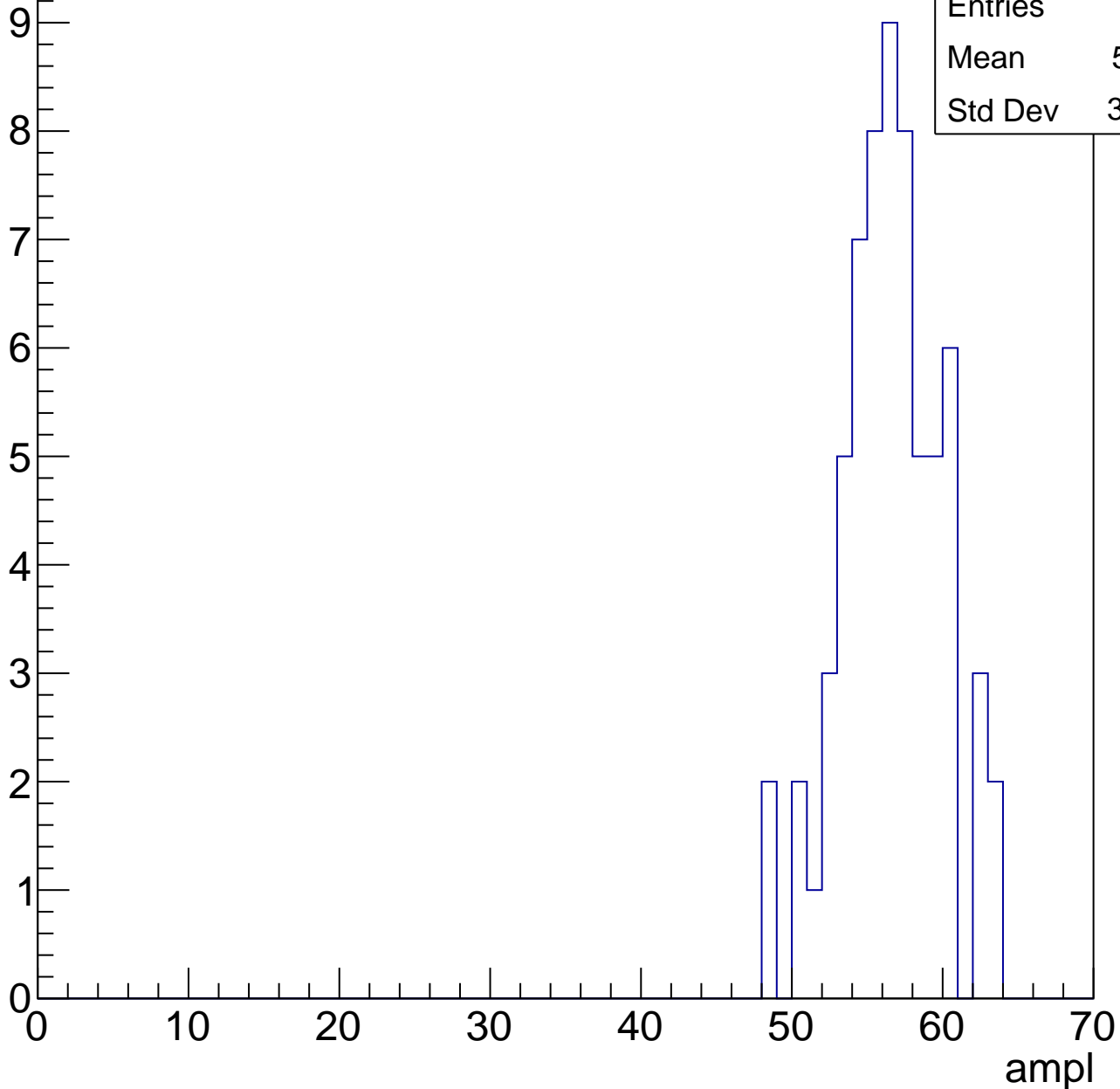


B1L101S, U12-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	56.11
Std Dev	3.322

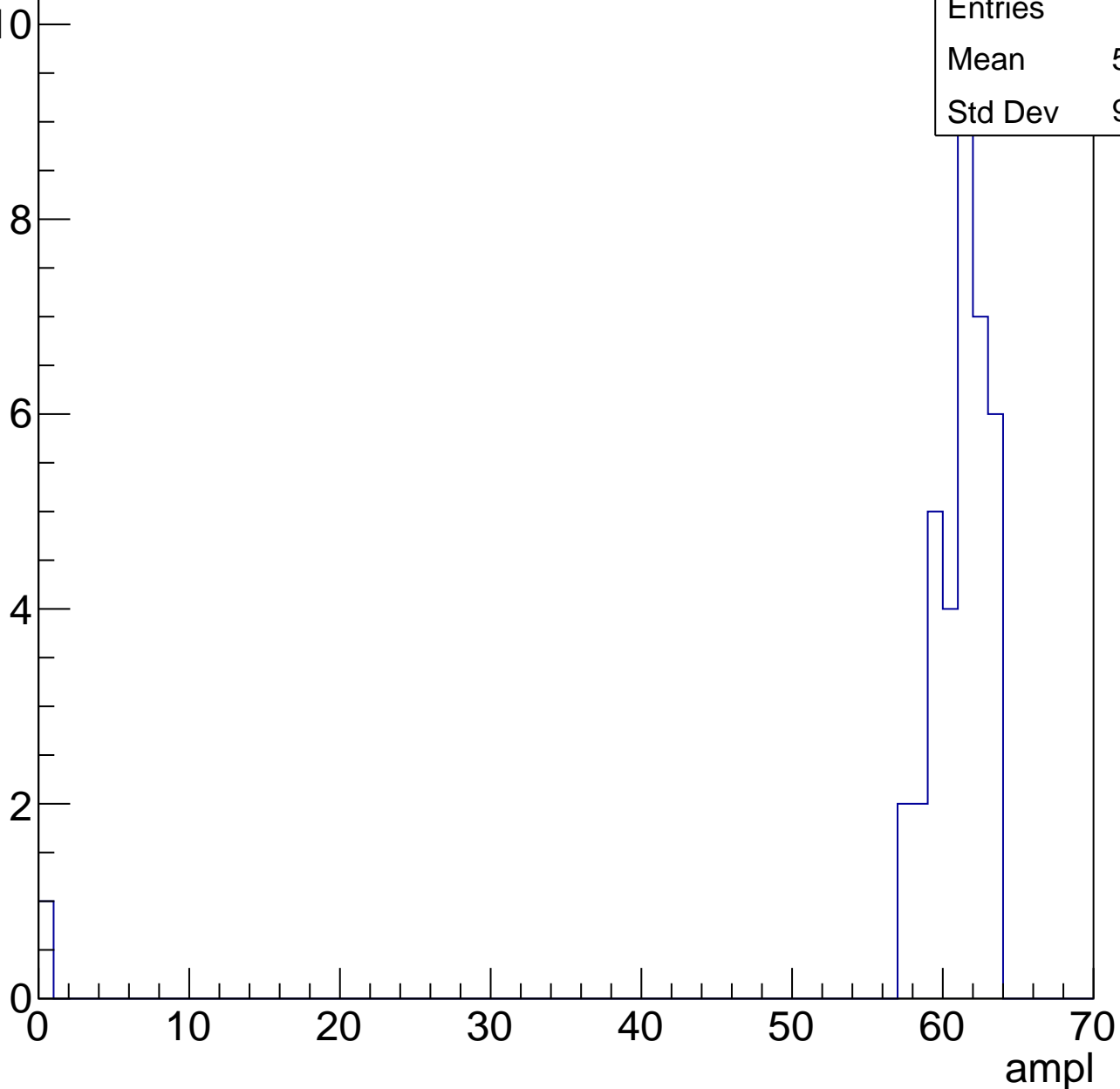


B1L101S, U12-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	59.11
Std Dev	9.991



B1L101S, U12-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L101S, U12-ch69, adc7

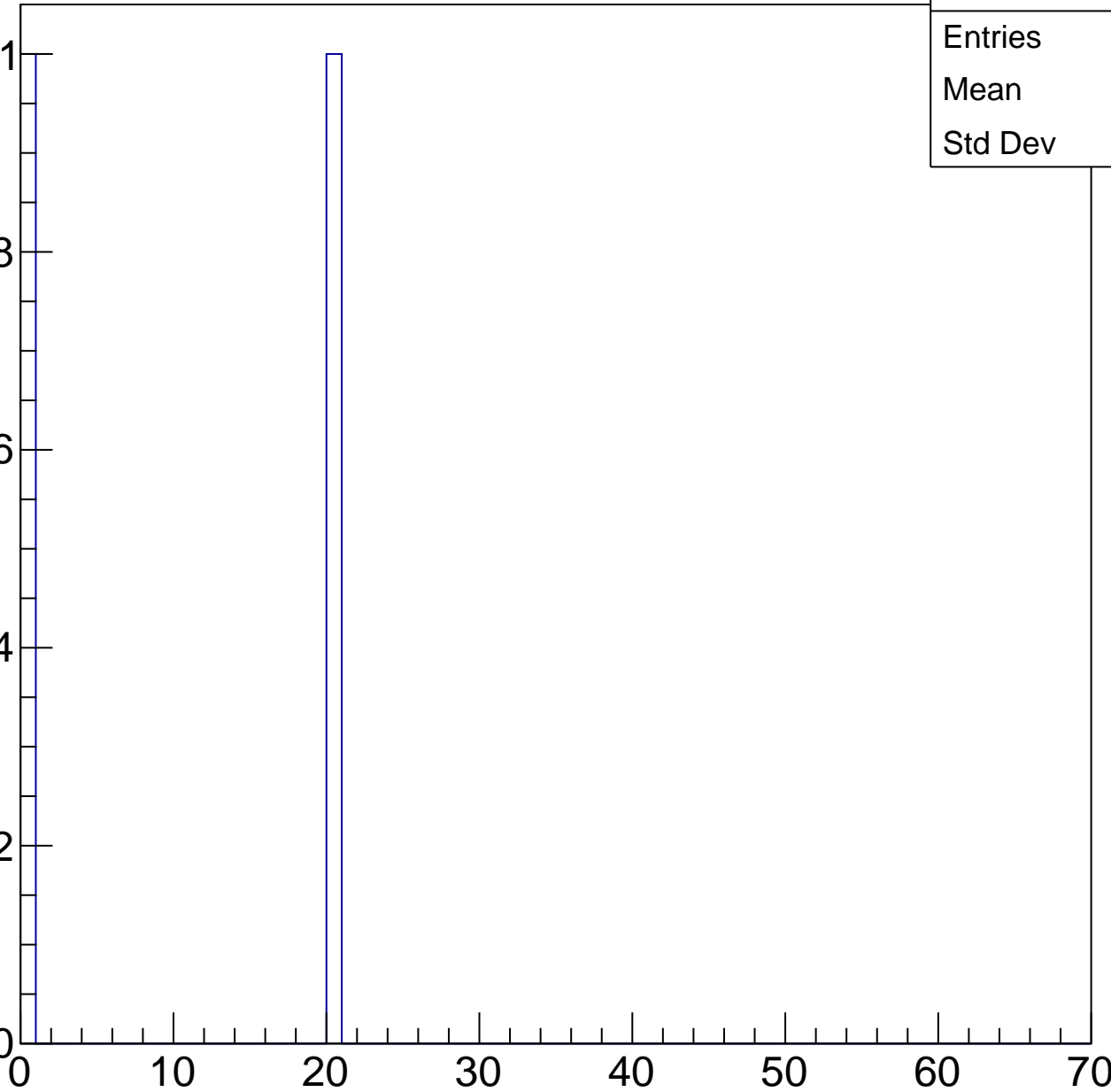
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B1L101S, U12-ch70, adc0

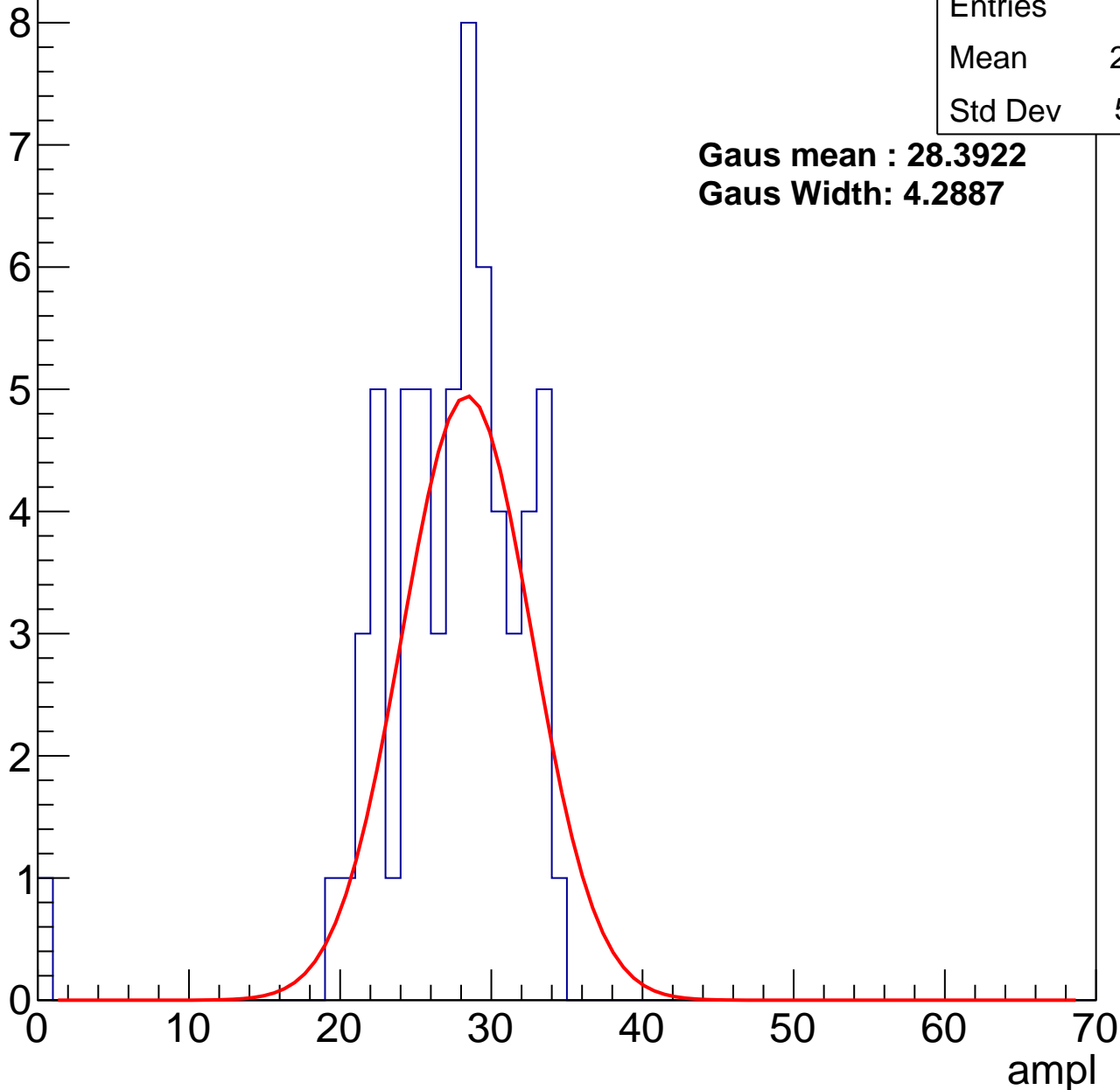
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	26.74
Std Dev	5.121

Gaus mean : 28.3922

Gaus Width: 4.2887



B1L101S, U12-ch70, adc1

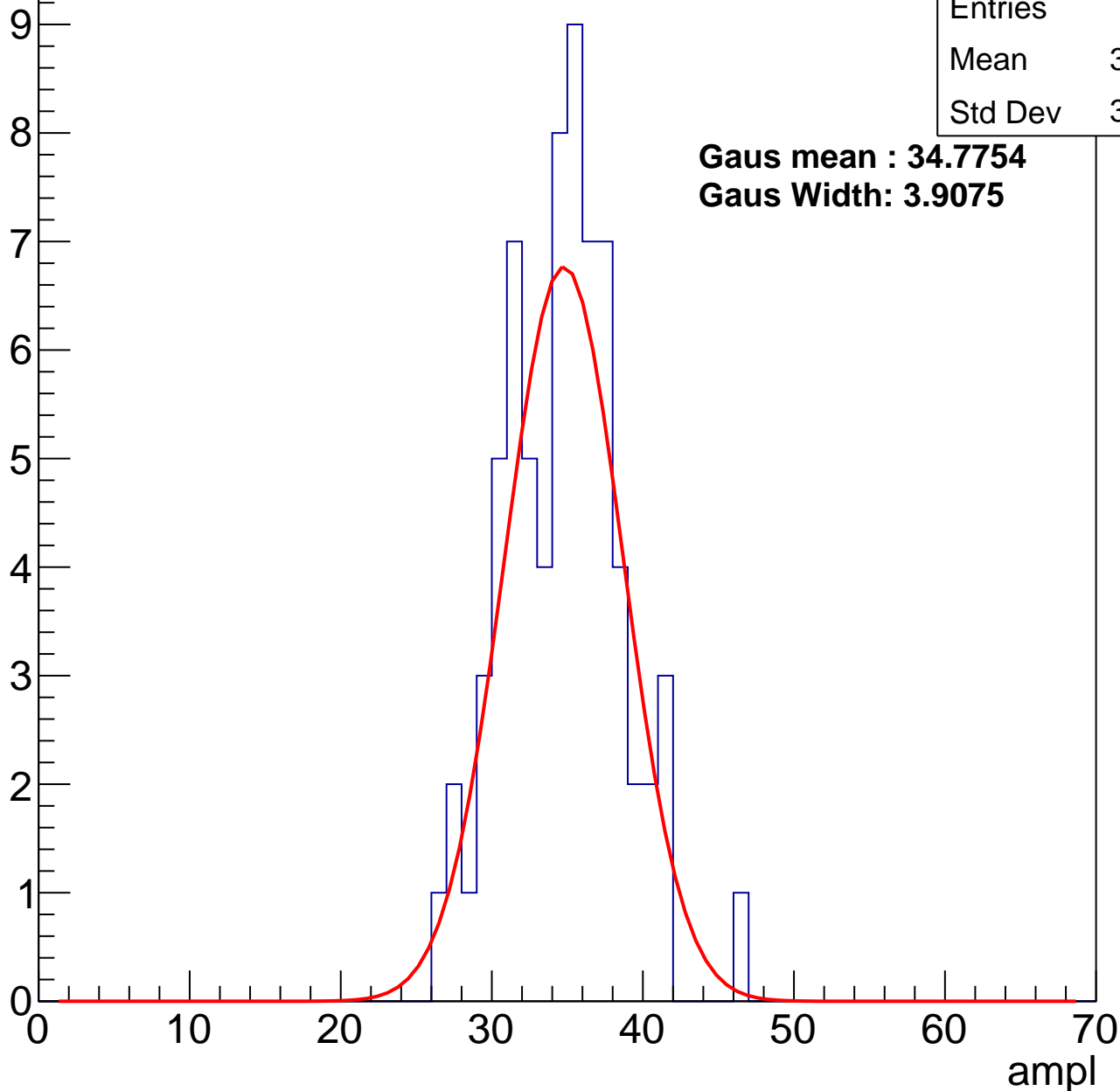
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	34.24
Std Dev	3.777

Gaus mean : 34.7754

Gaus Width: 3.9075



B1L101S, U12-ch70, adc2

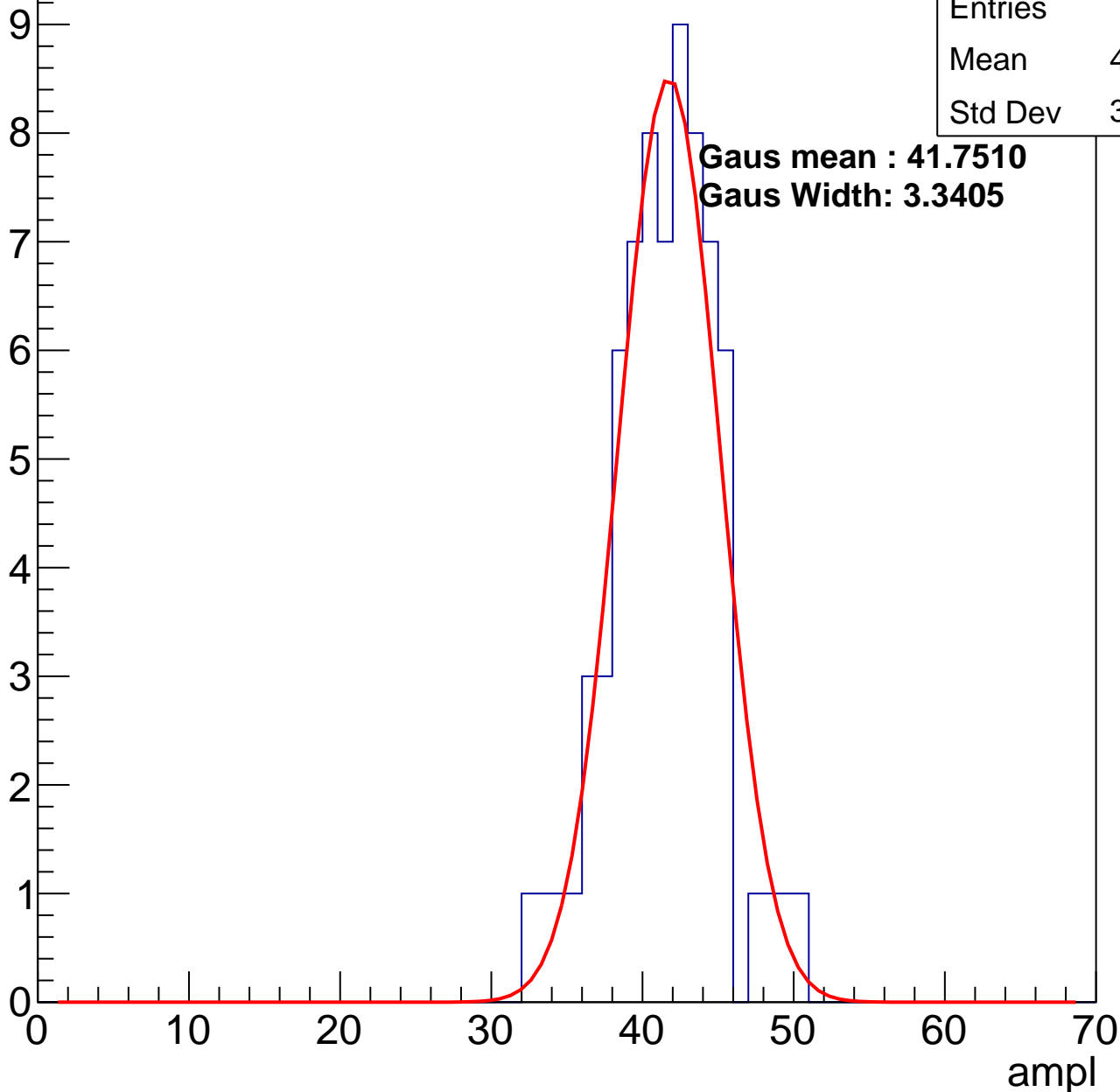
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	41.04
Std Dev	3.482

Gaus mean : 41.7510

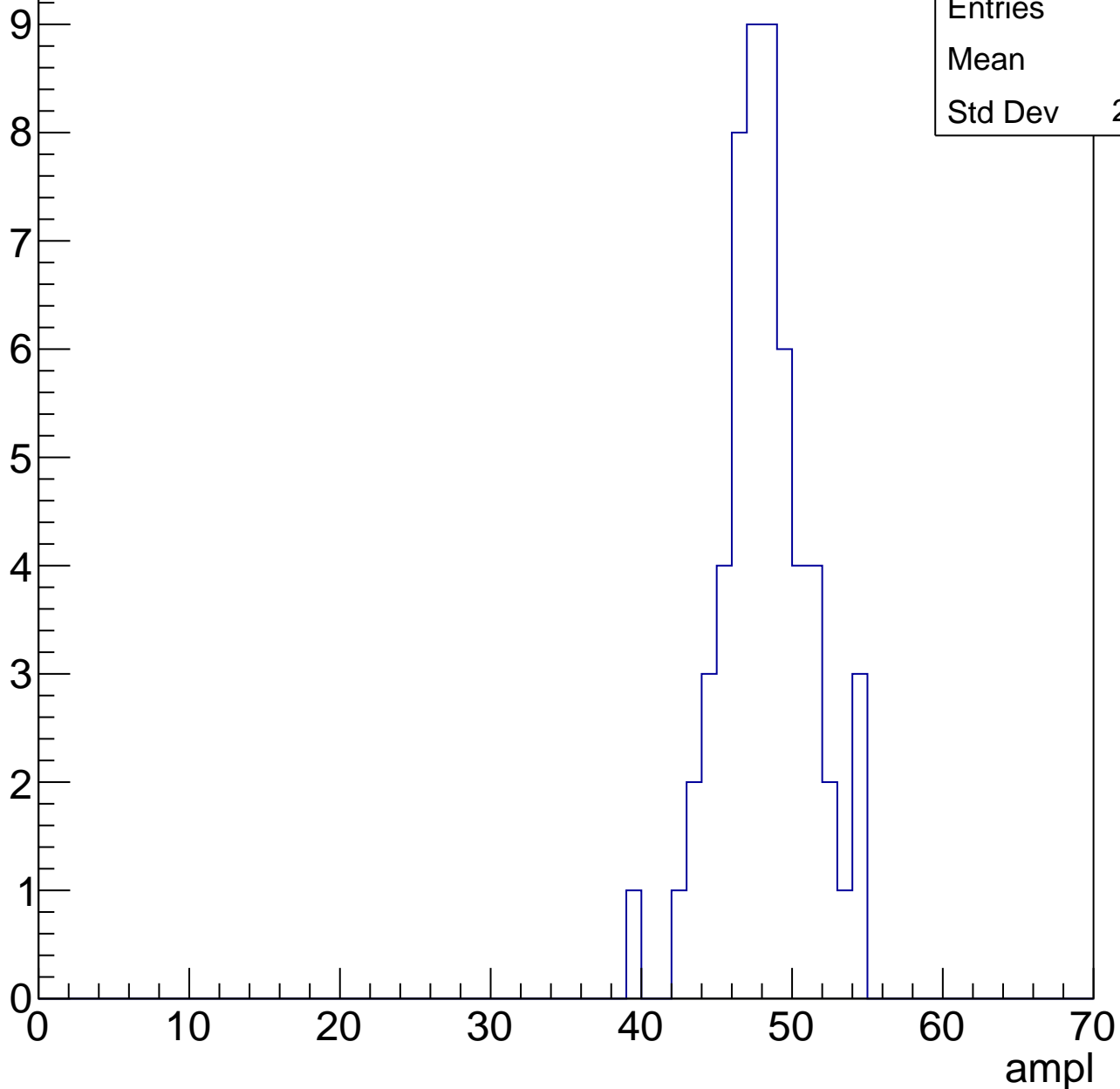
Gaus Width: 3.3405



B1L101S, U12-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



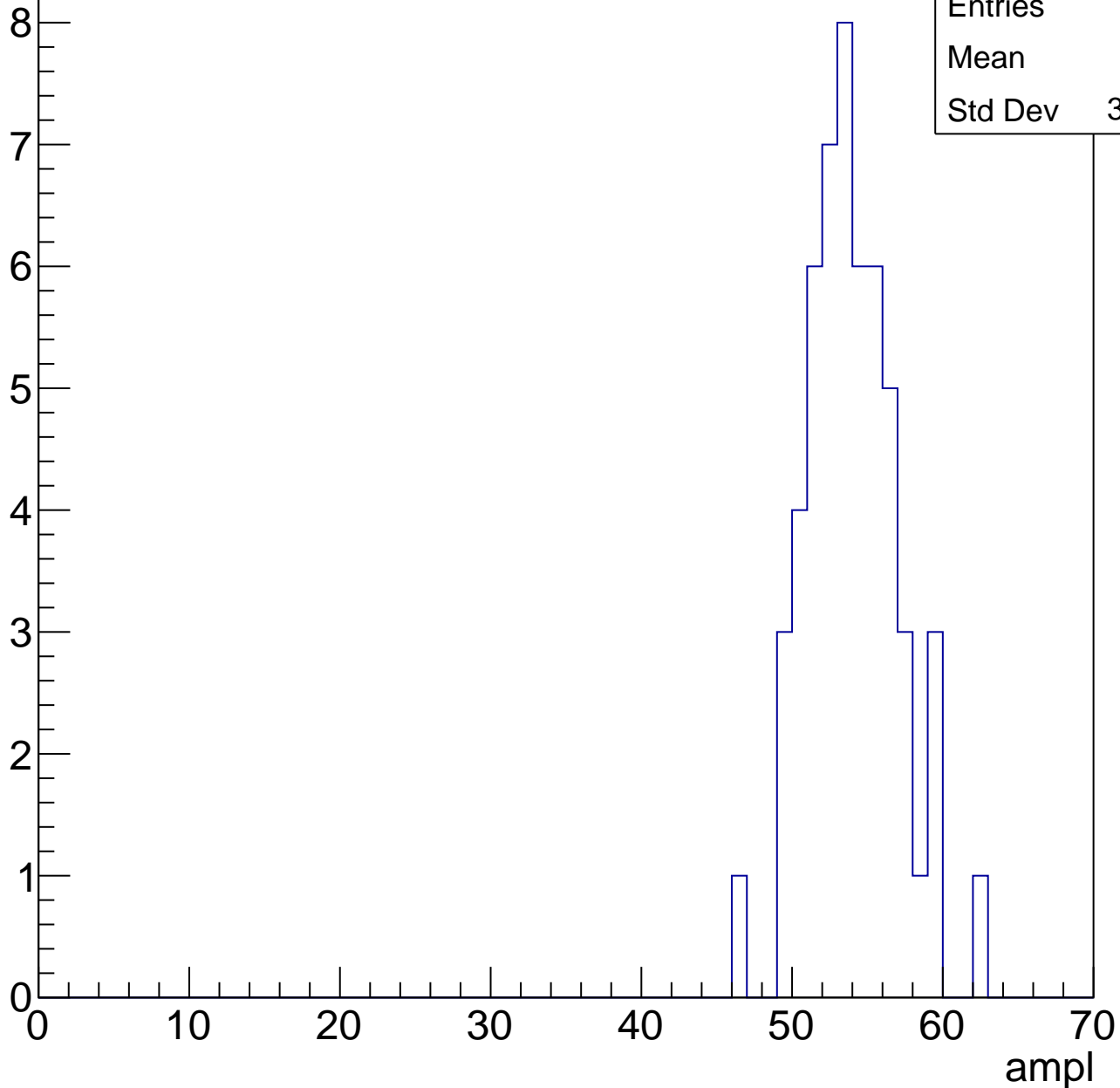
Entries	57
Mean	47.7
Std Dev	2.991

B1L101S, U12-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

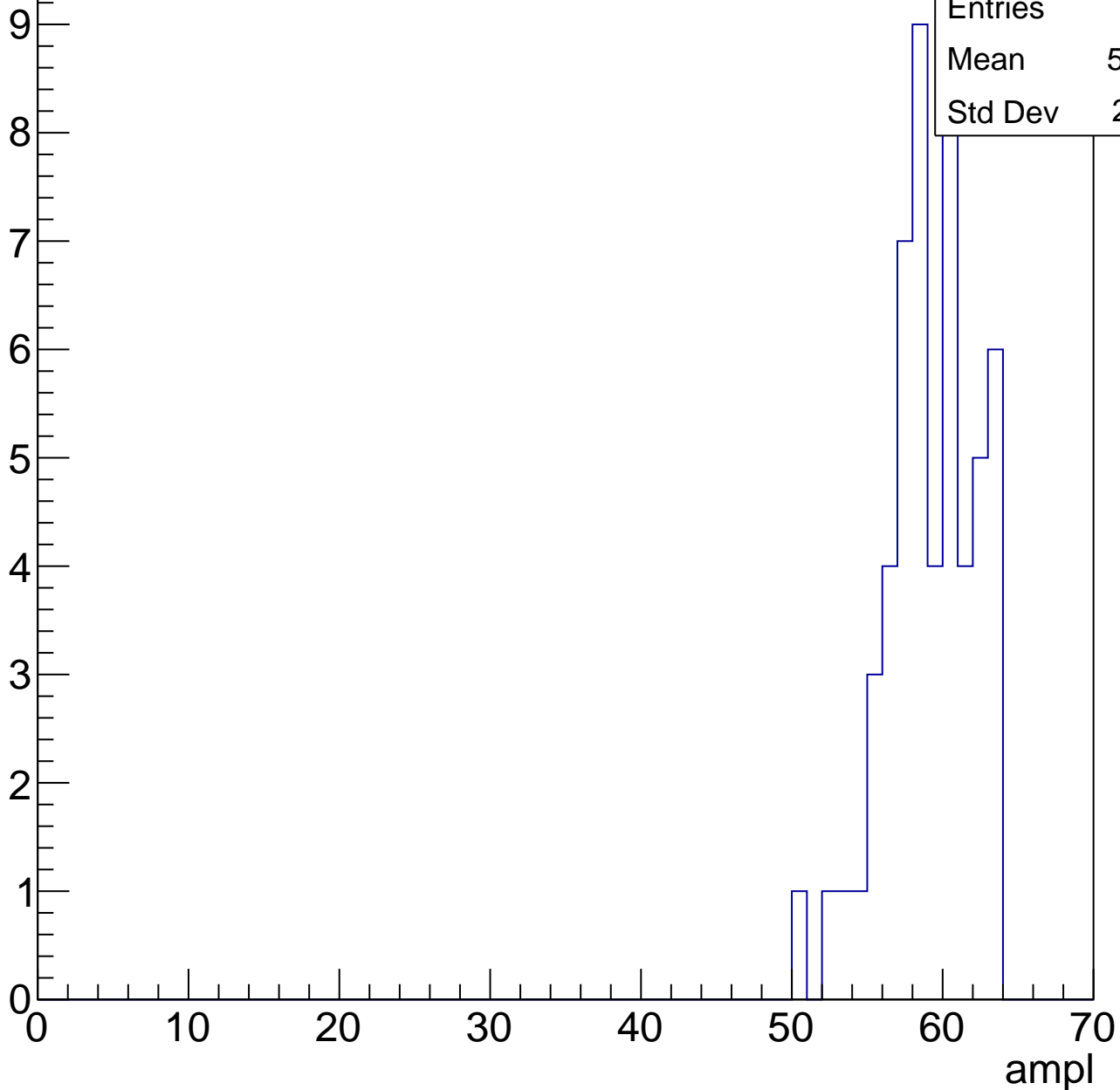
Entries	54
Mean	53.5
Std Dev	3.005



B1L101S, U12-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

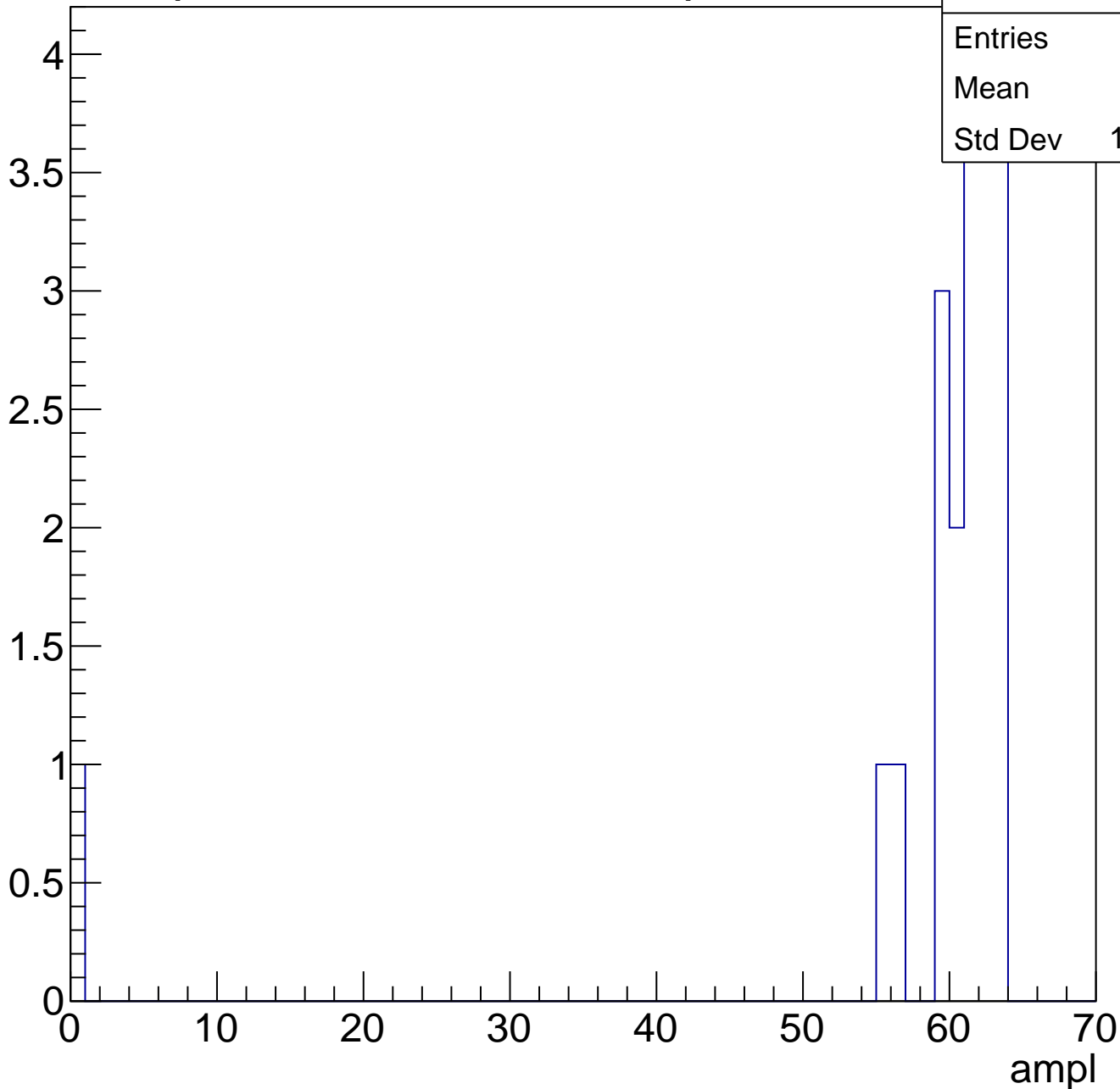
Entry



B1L101S, U12-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	61
Std Dev	0

ampl

B1L101S, U12-ch71, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	31.73
Std Dev	3.772

Gaus mean : 32.0286

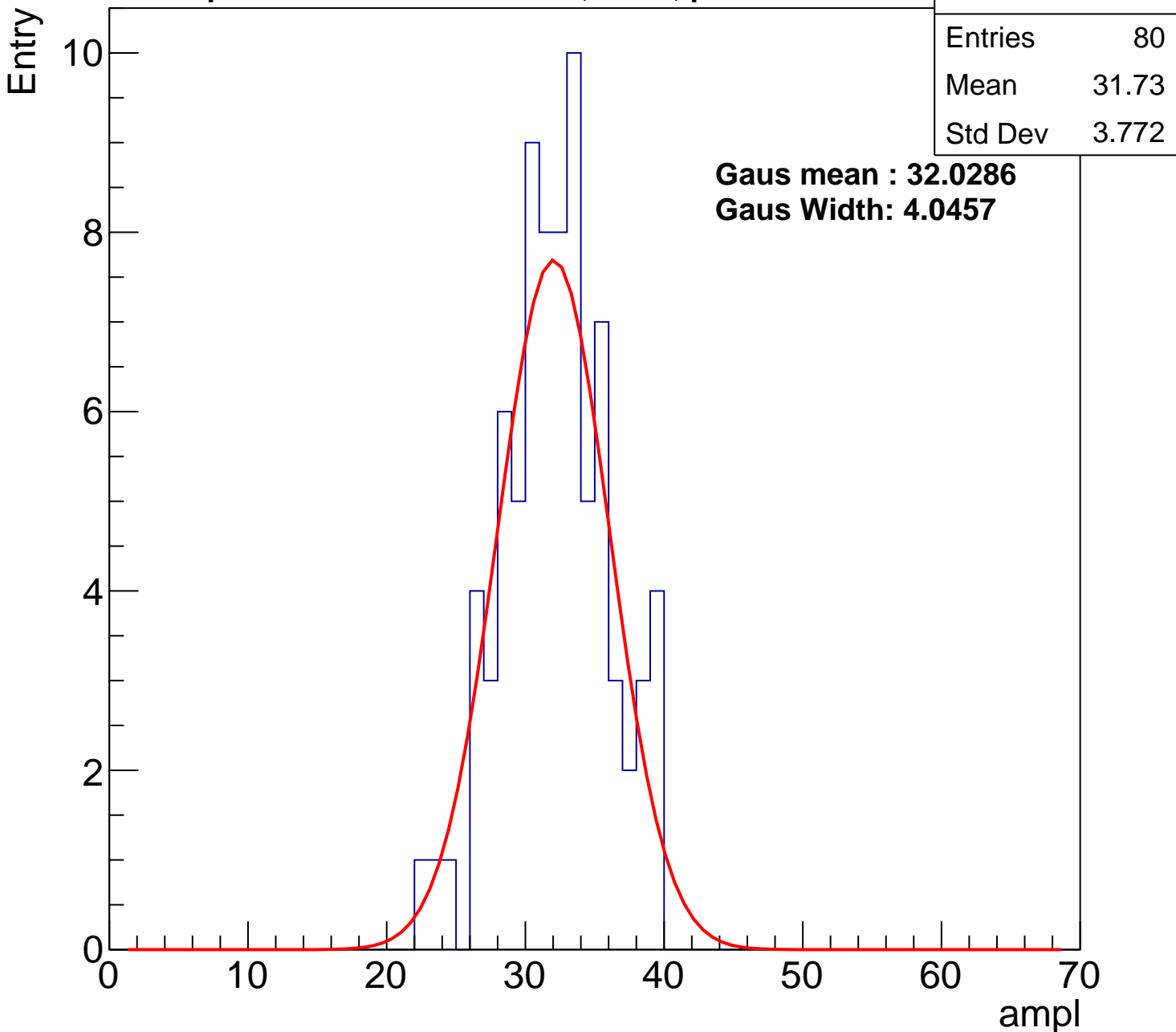
Gaus Width: 4.0457

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch71, adc1

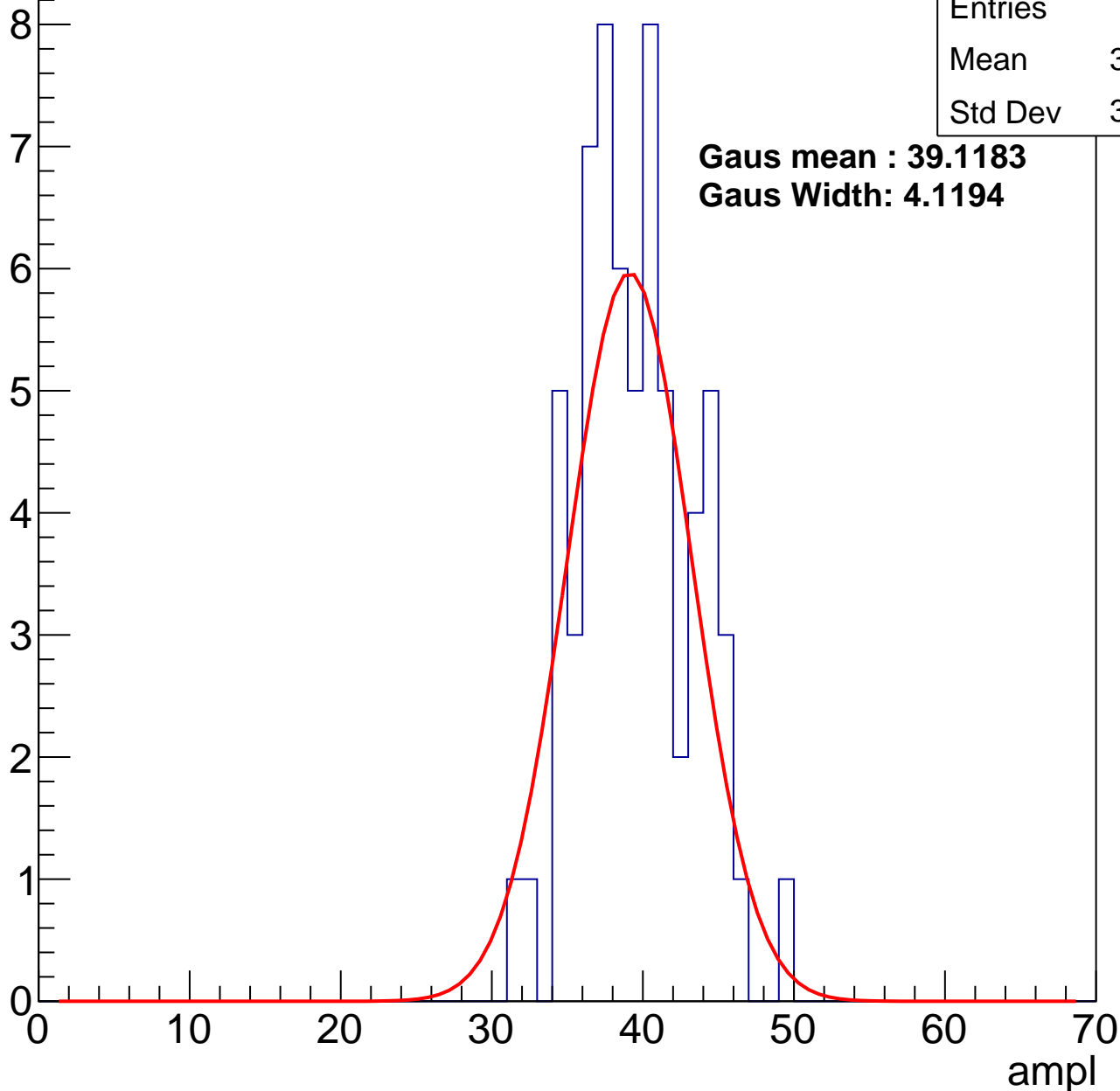
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	39.08
Std Dev	3.677

Gaus mean : 39.1183

Gaus Width: 4.1194



B1L101S, U12-ch71, adc2

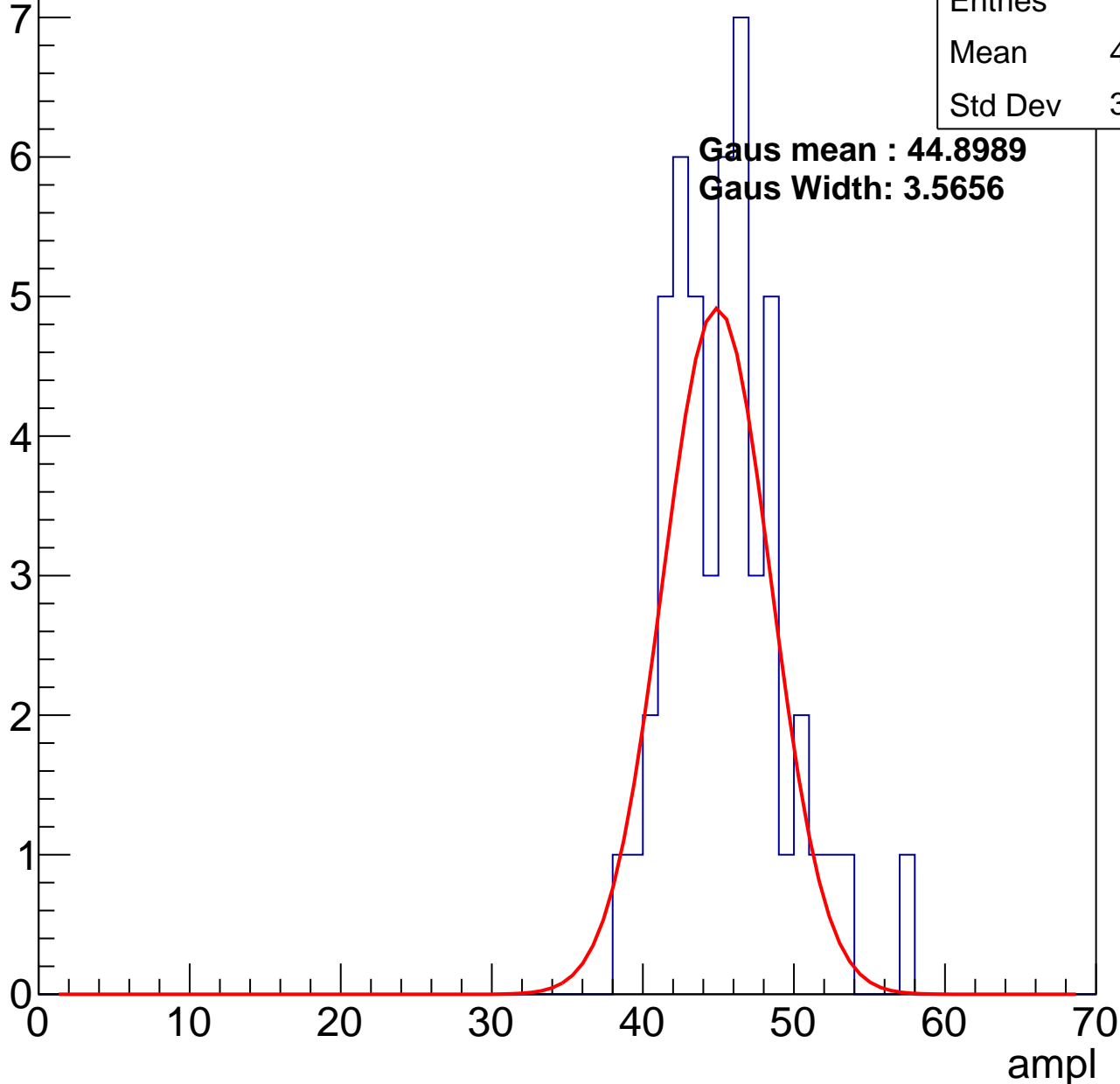
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	45.02
Std Dev	3.744

Gaus mean : 44.8989

Gaus Width: 3.5656

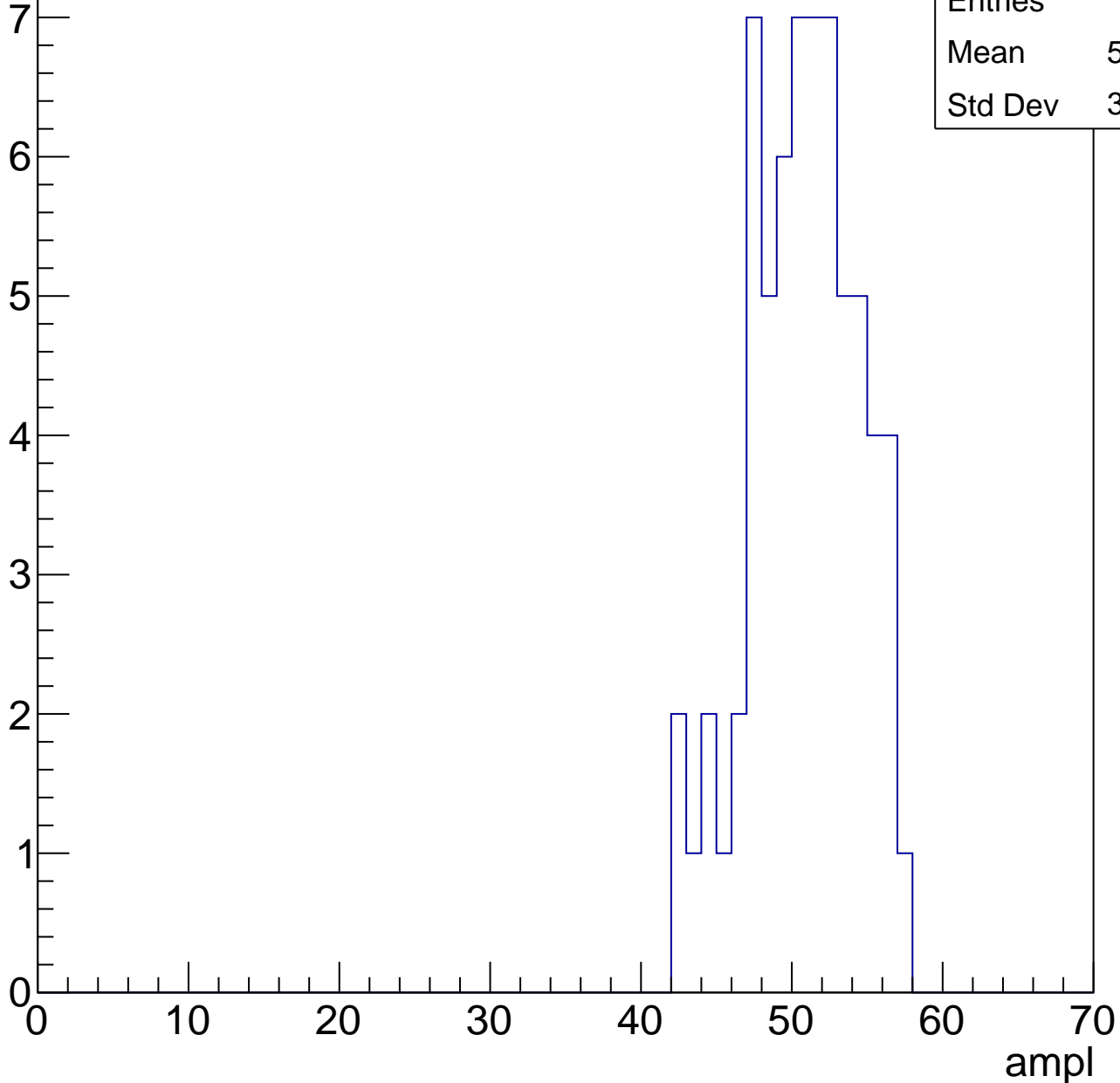


B1L101S, U12-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

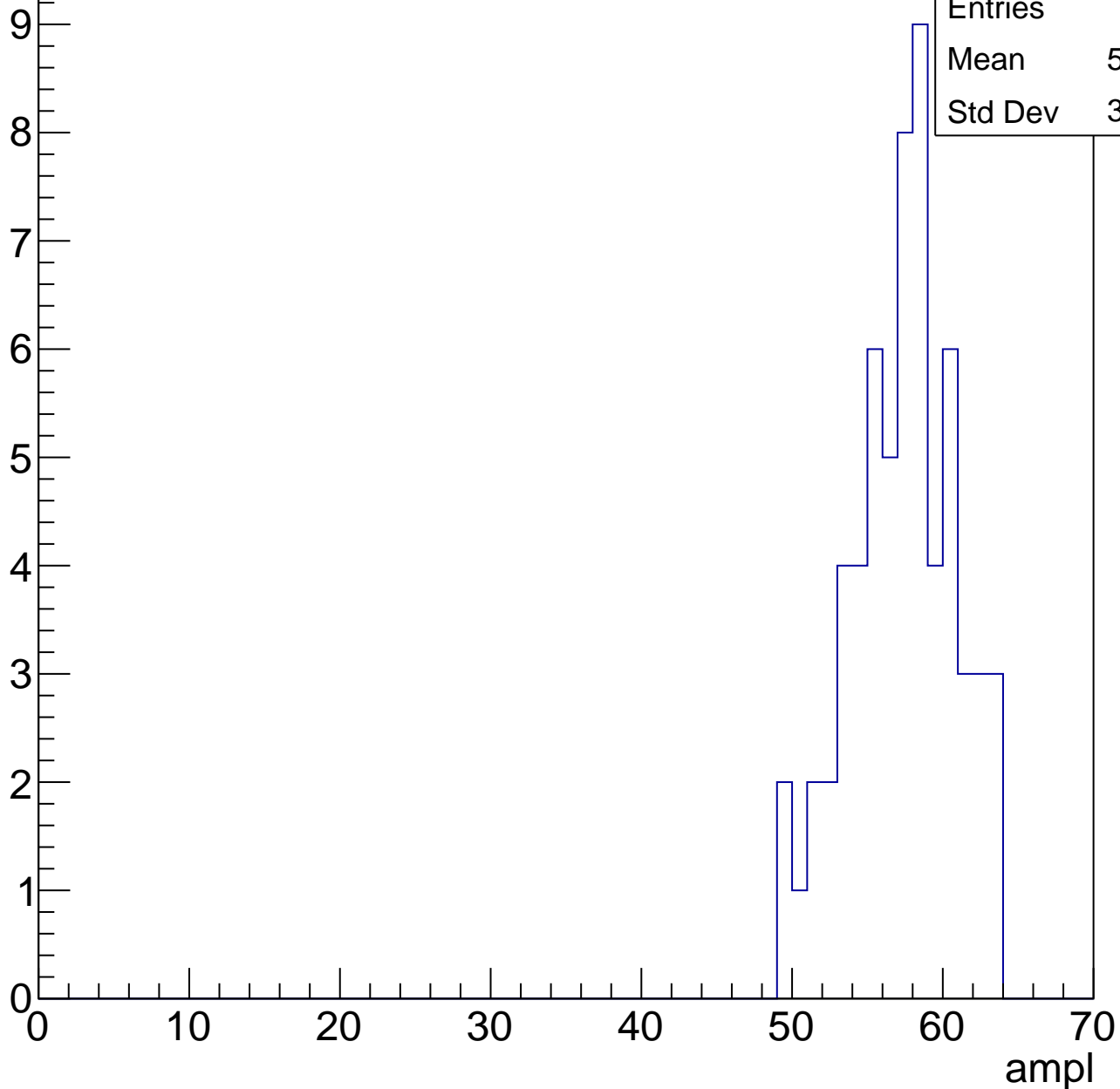
Entries	66
Mean	50.33
Std Dev	3.569



B1L101S, U12-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

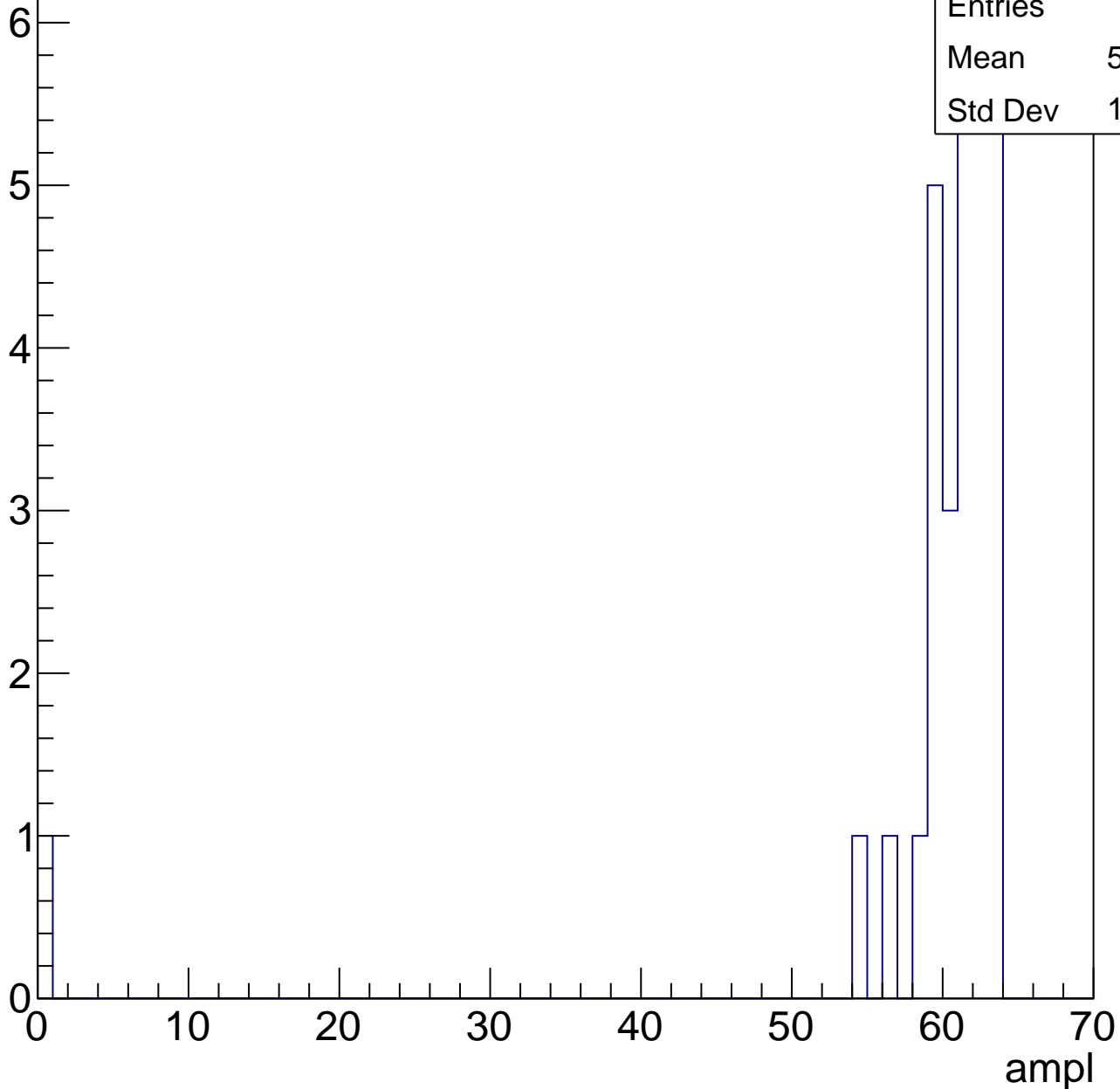


B1L101S, U12-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

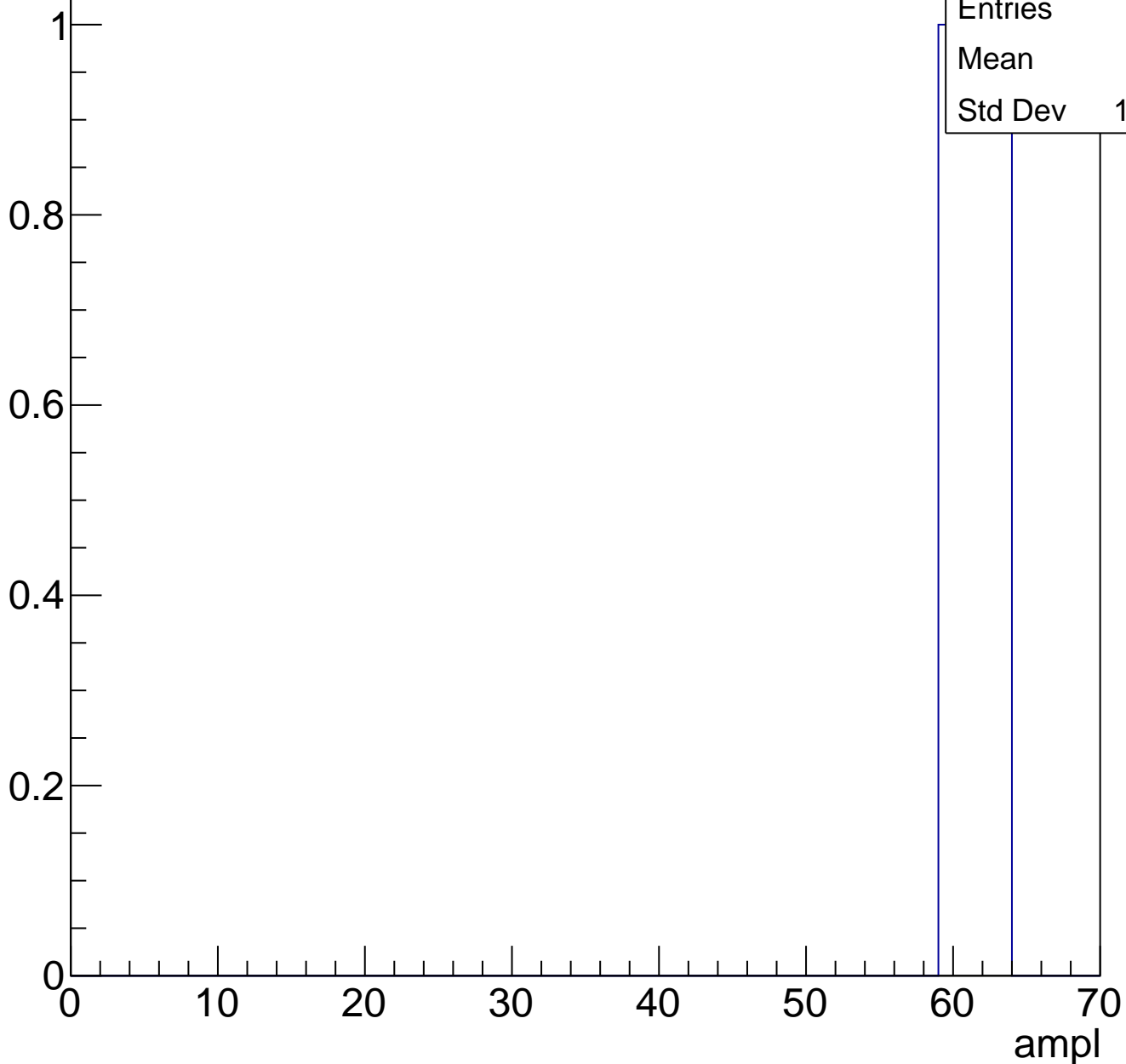
Entries	30
Mean	58.63
Std Dev	11.09



B1L101S, U12-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch72, adc0

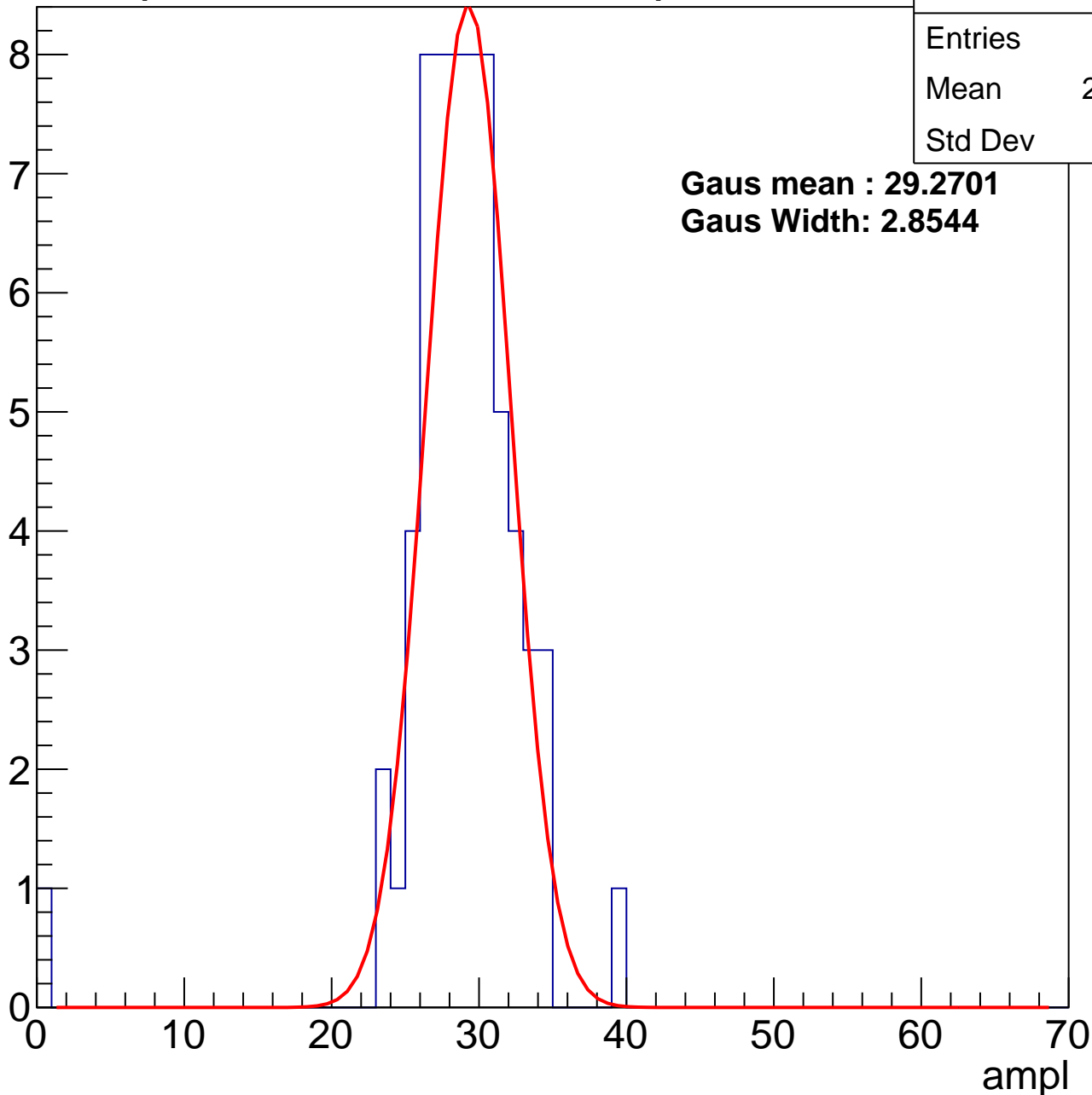
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	28.33
Std Dev	4.63

Gaus mean : 29.2701

Gaus Width: 2.8544



B1L101S, U12-ch72, adc1

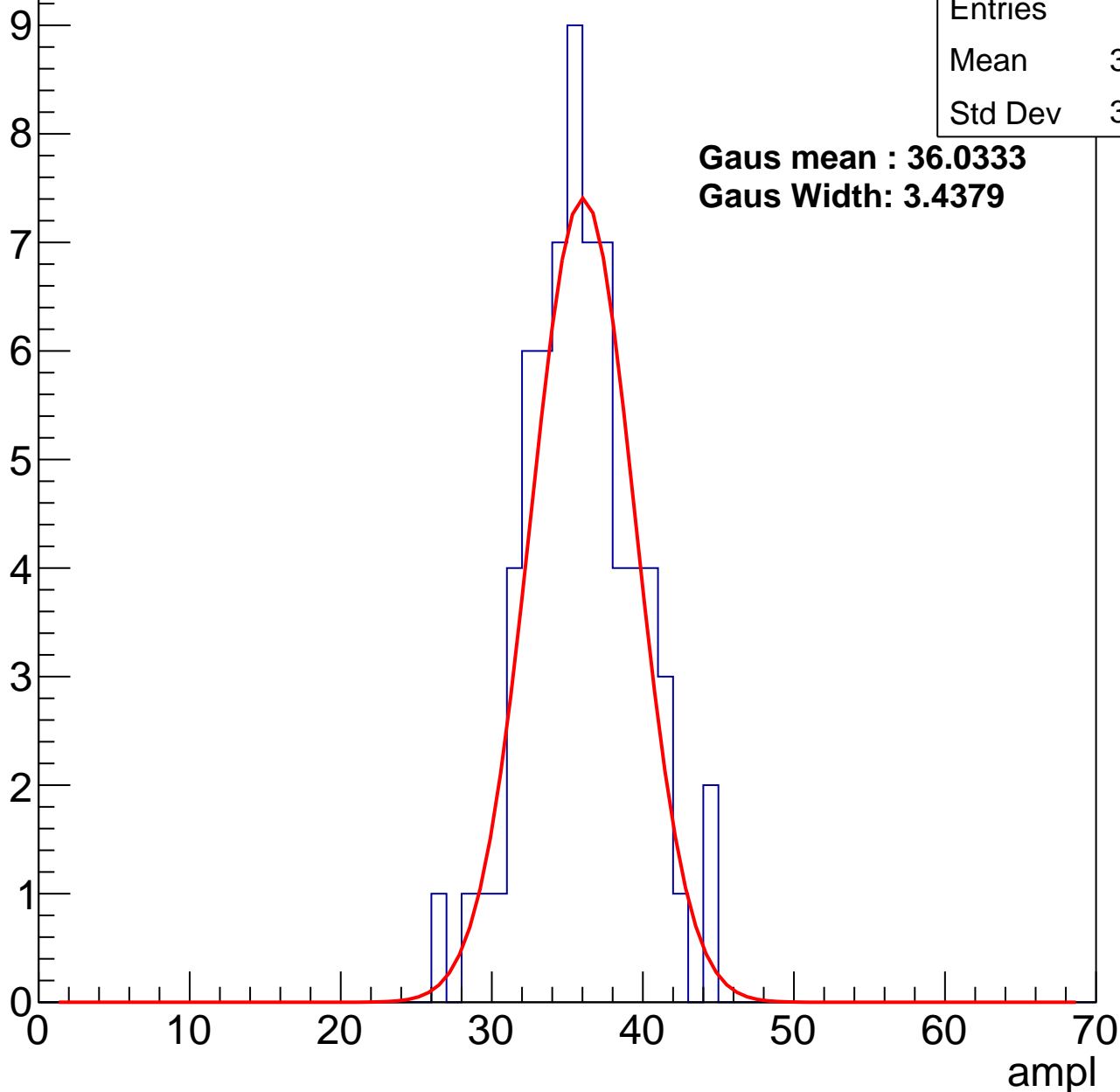
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.47
Std Dev	3.595

Gaus mean : 36.0333

Gaus Width: 3.4379



B1L101S, U12-ch72, adc2

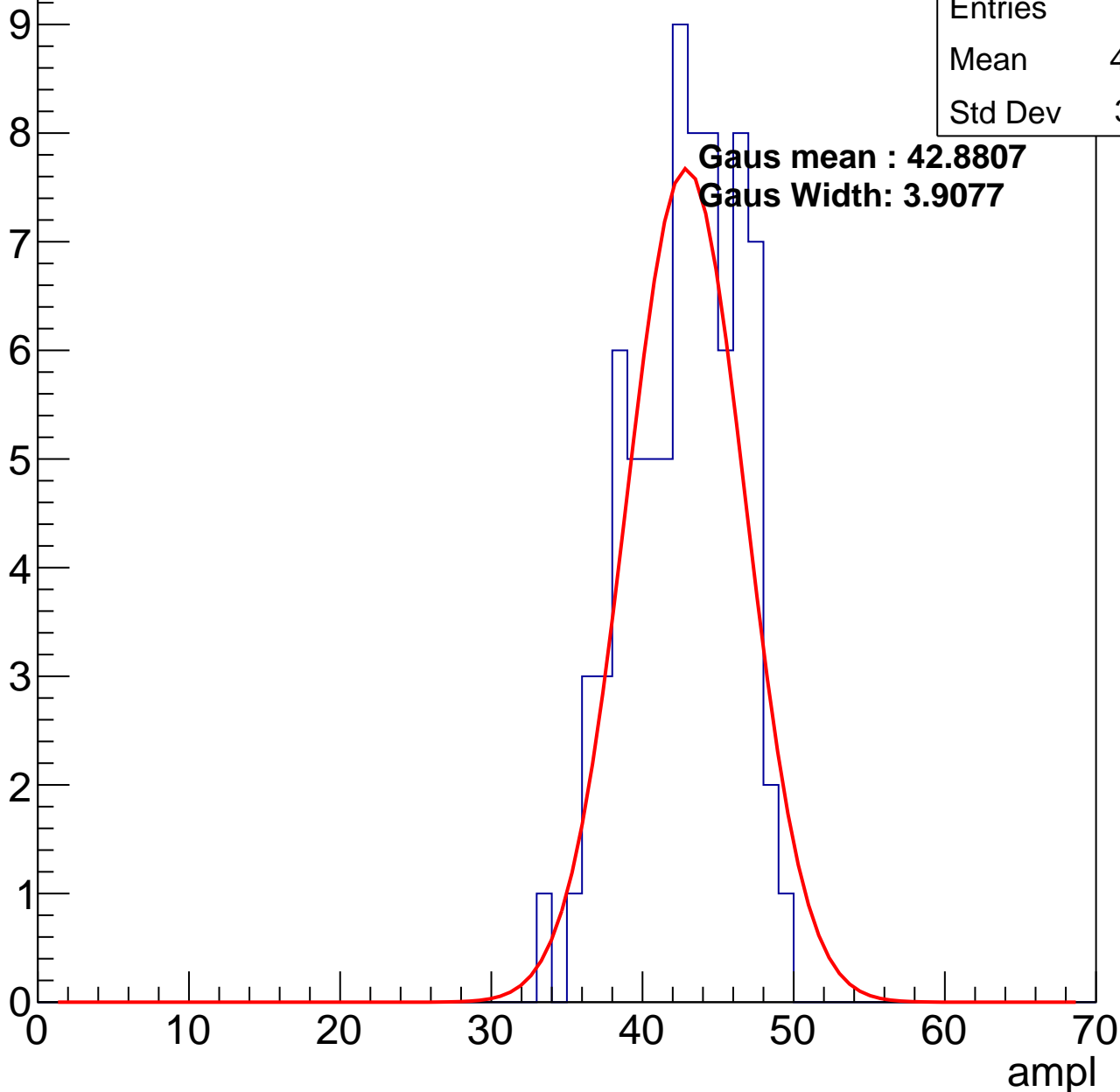
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.32
Std Dev	3.561

Gaus mean : 42.8807

Gaus Width: 3.9077

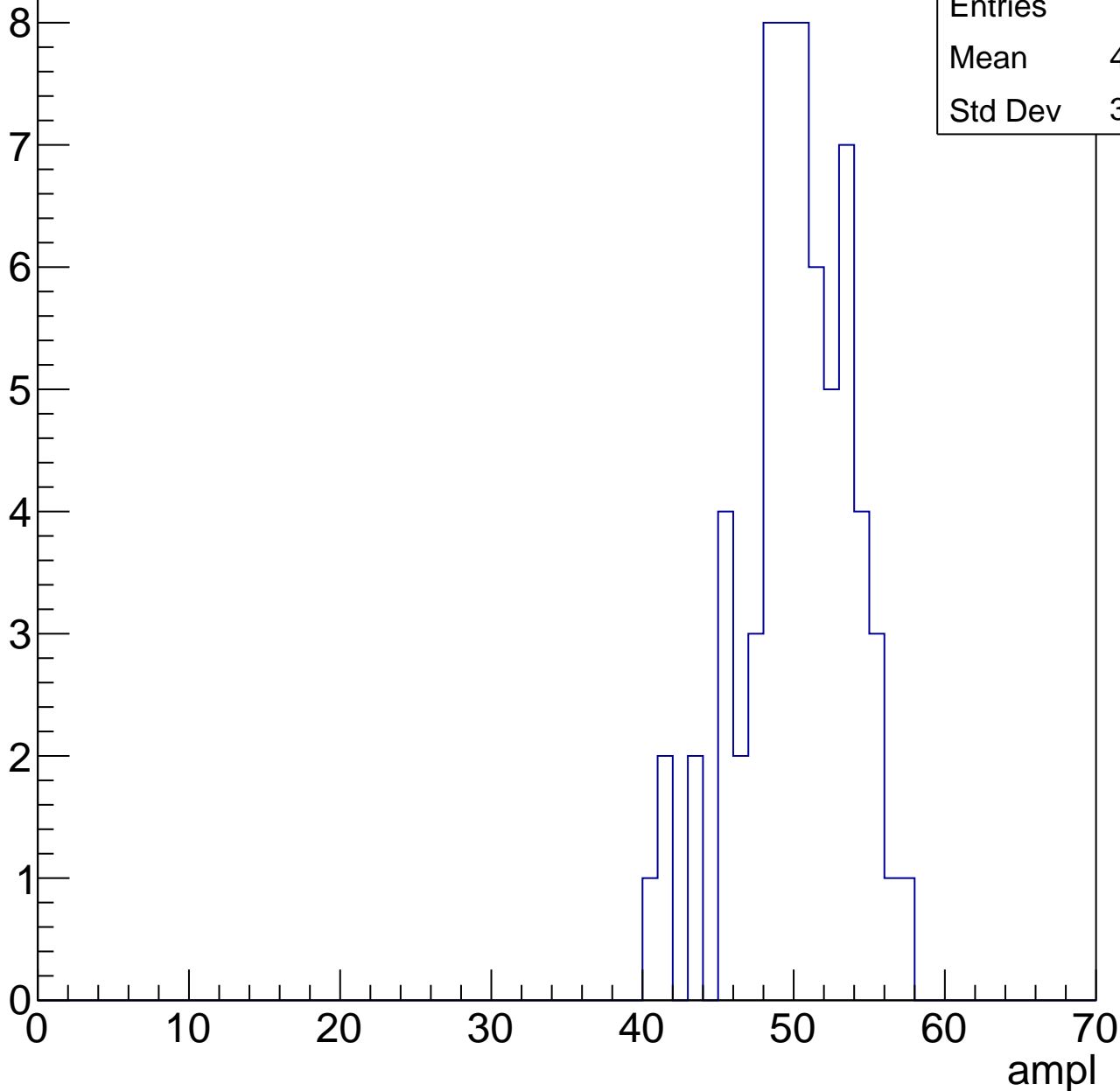


B1L101S, U12-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.66
Std Dev	3.647

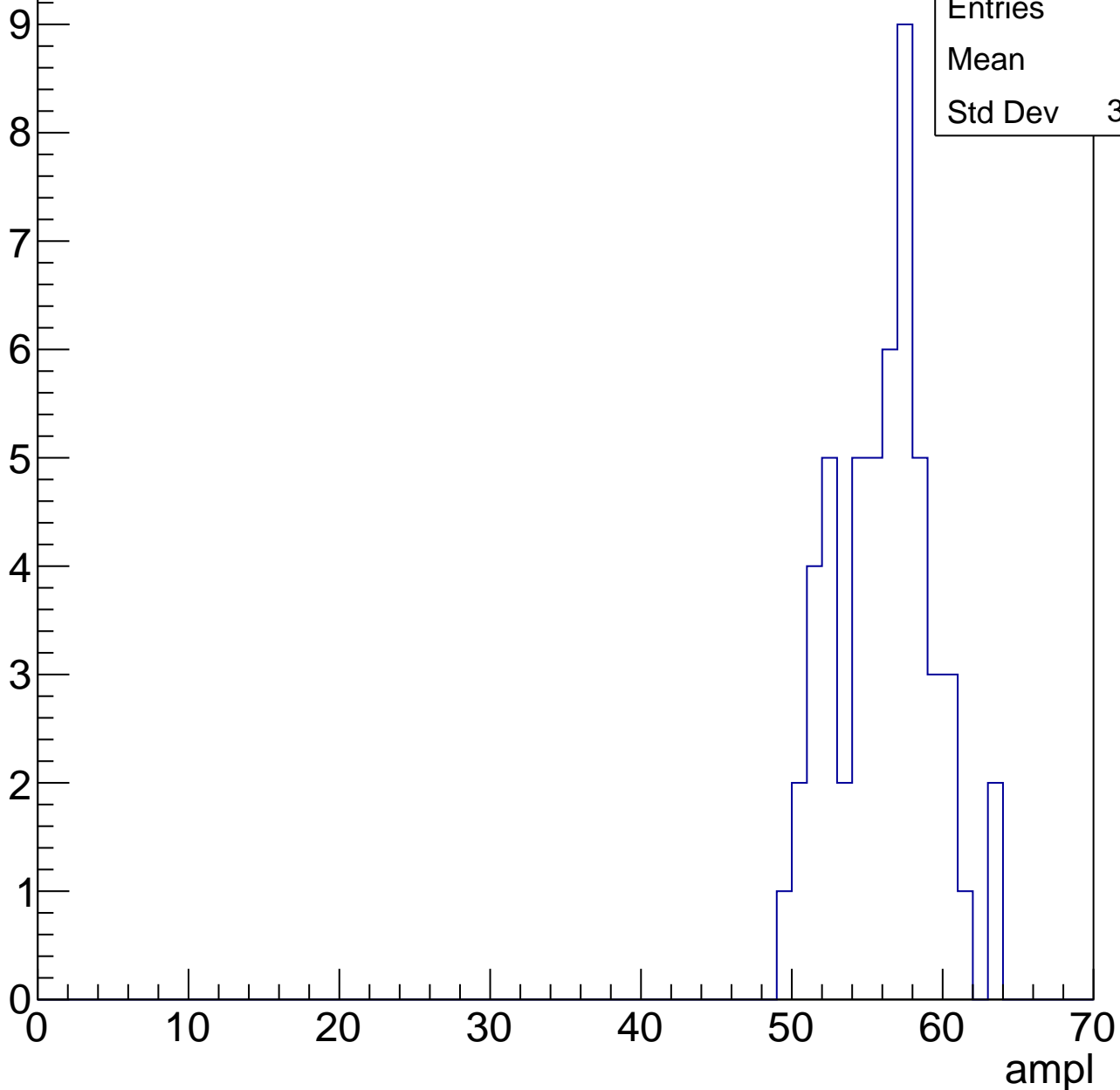


B1L101S, U12-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.6
Std Dev	3.258

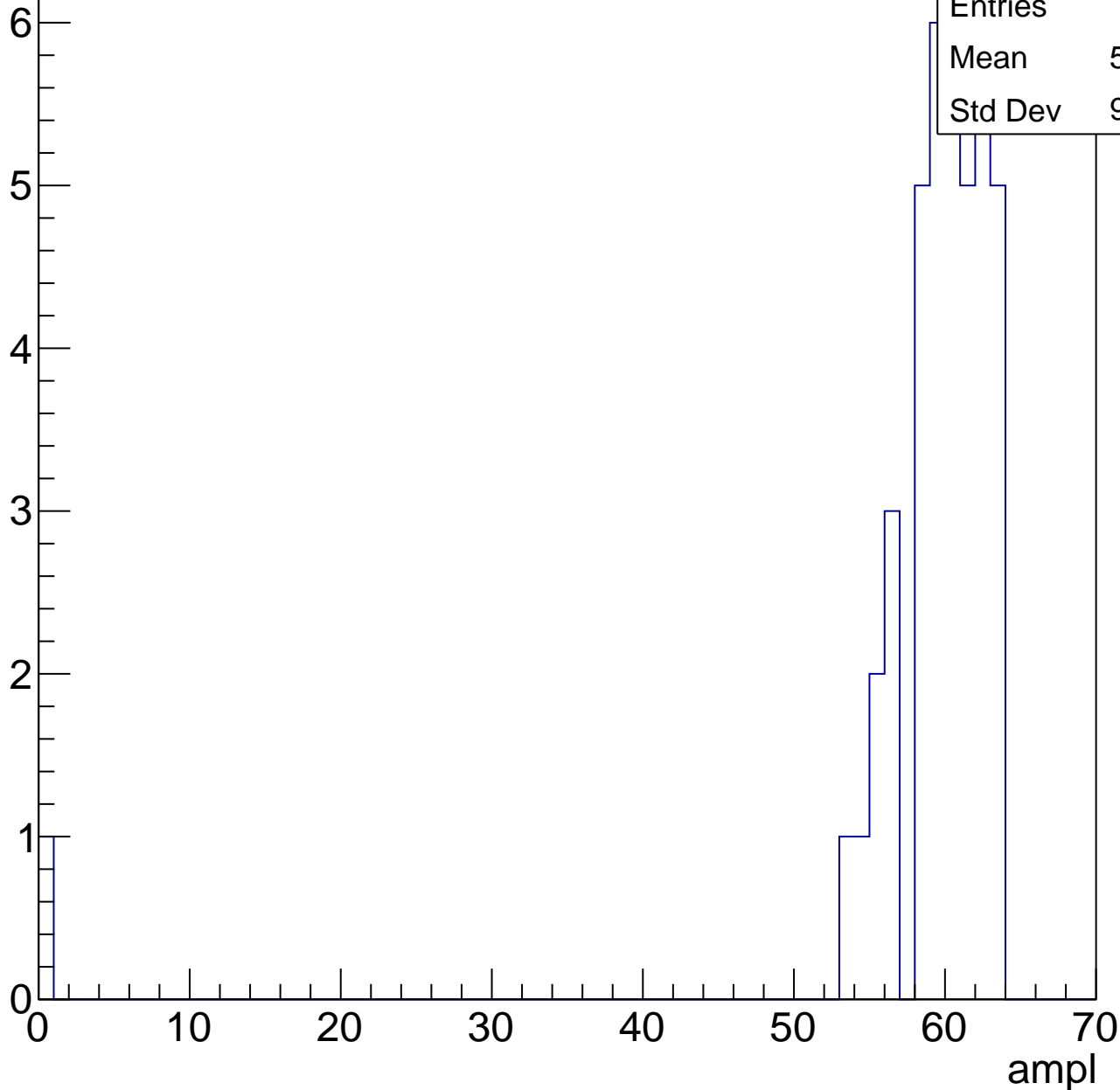


B1L101S, U12-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

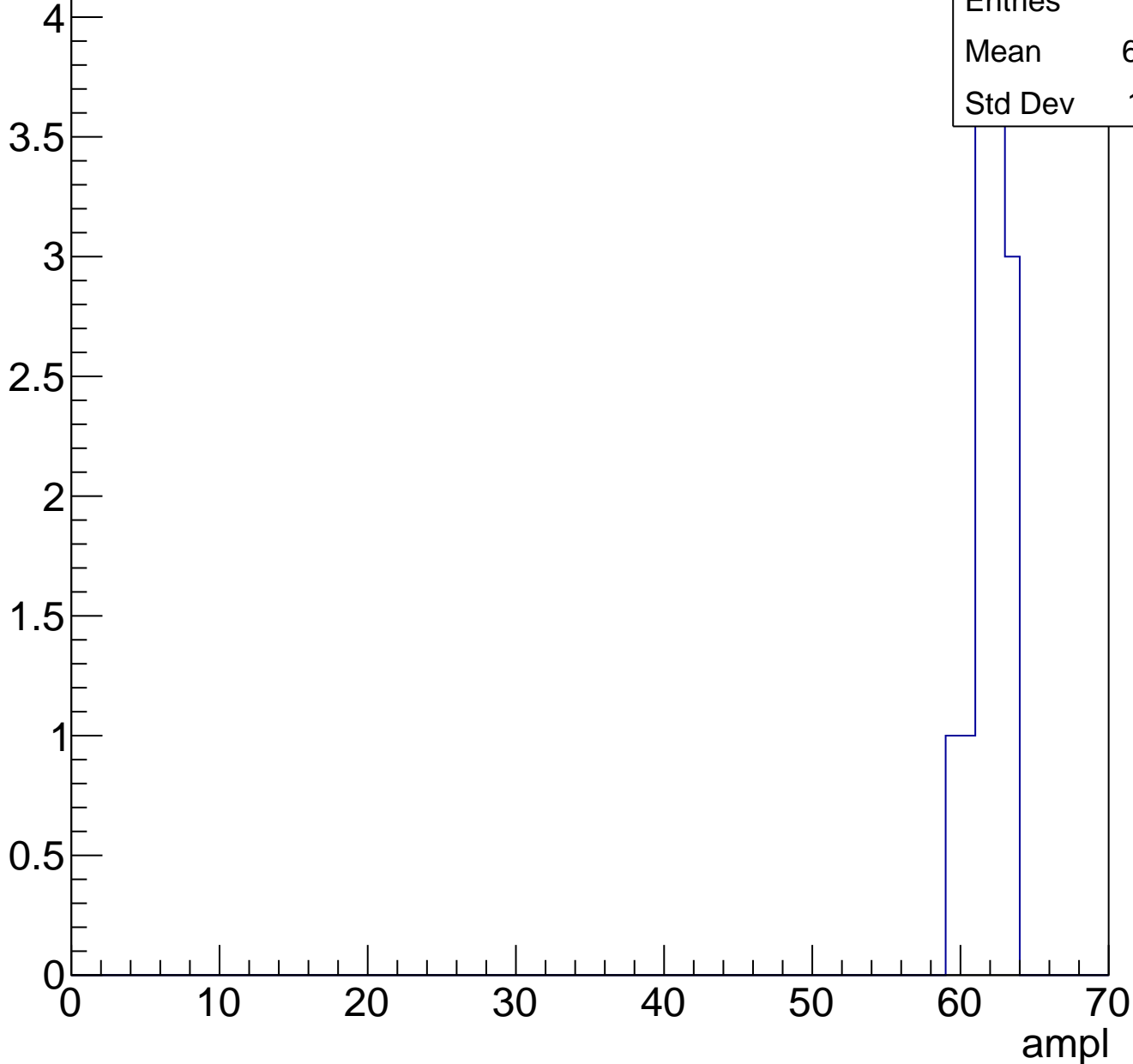
Entries	41
Mean	58.07
Std Dev	9.539



B1L101S, U12-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch73, adc0

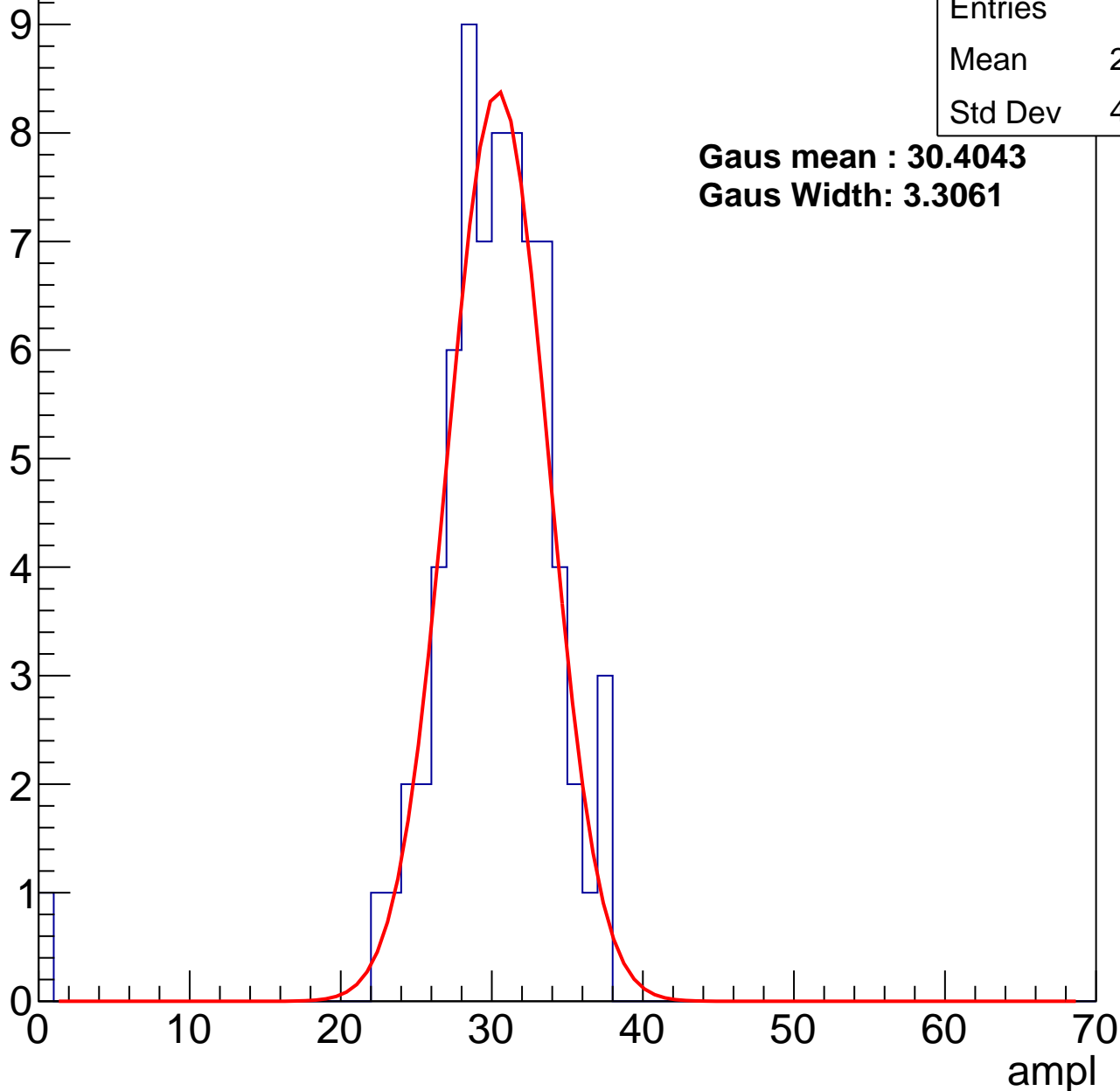
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.59
Std Dev	4.797

Gaus mean : 30.4043

Gaus Width: 3.3061



B1L101S, U12-ch73, adc1

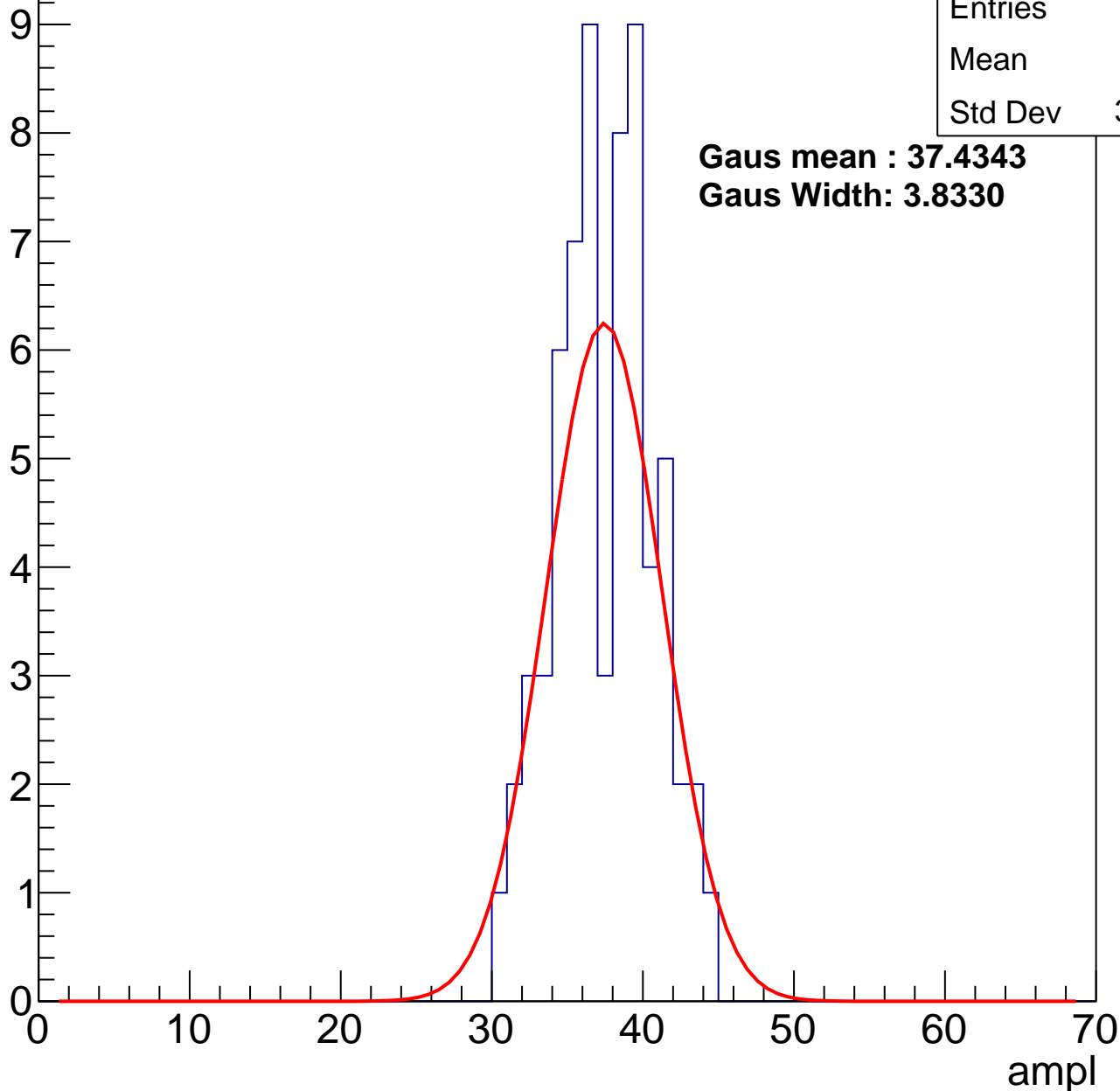
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37
Std Dev	3.201

Gaus mean : 37.4343

Gaus Width: 3.8330



B1L101S, U12-ch73, adc2

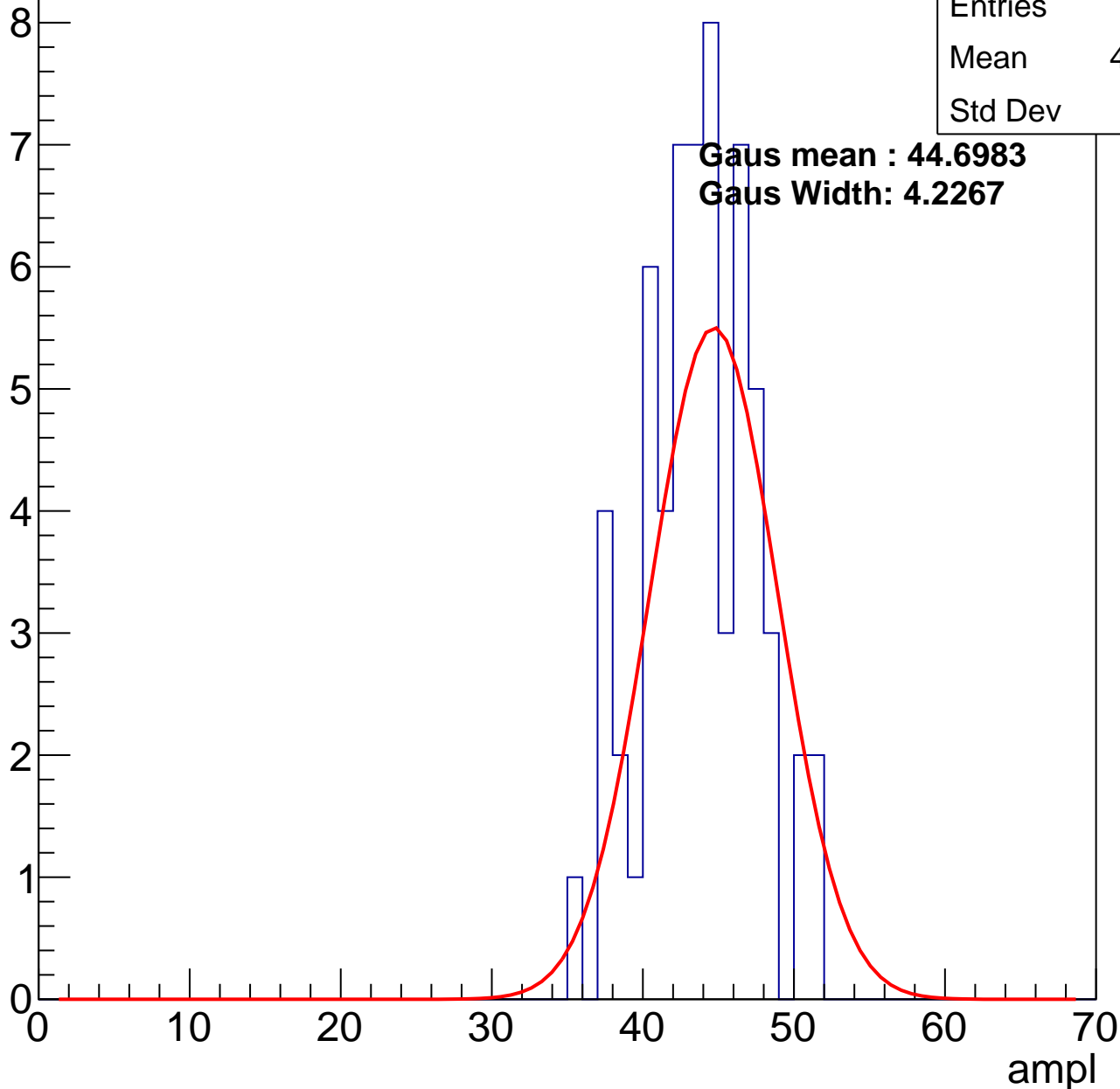
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.34
Std Dev	3.61

Gaus mean : 44.6983

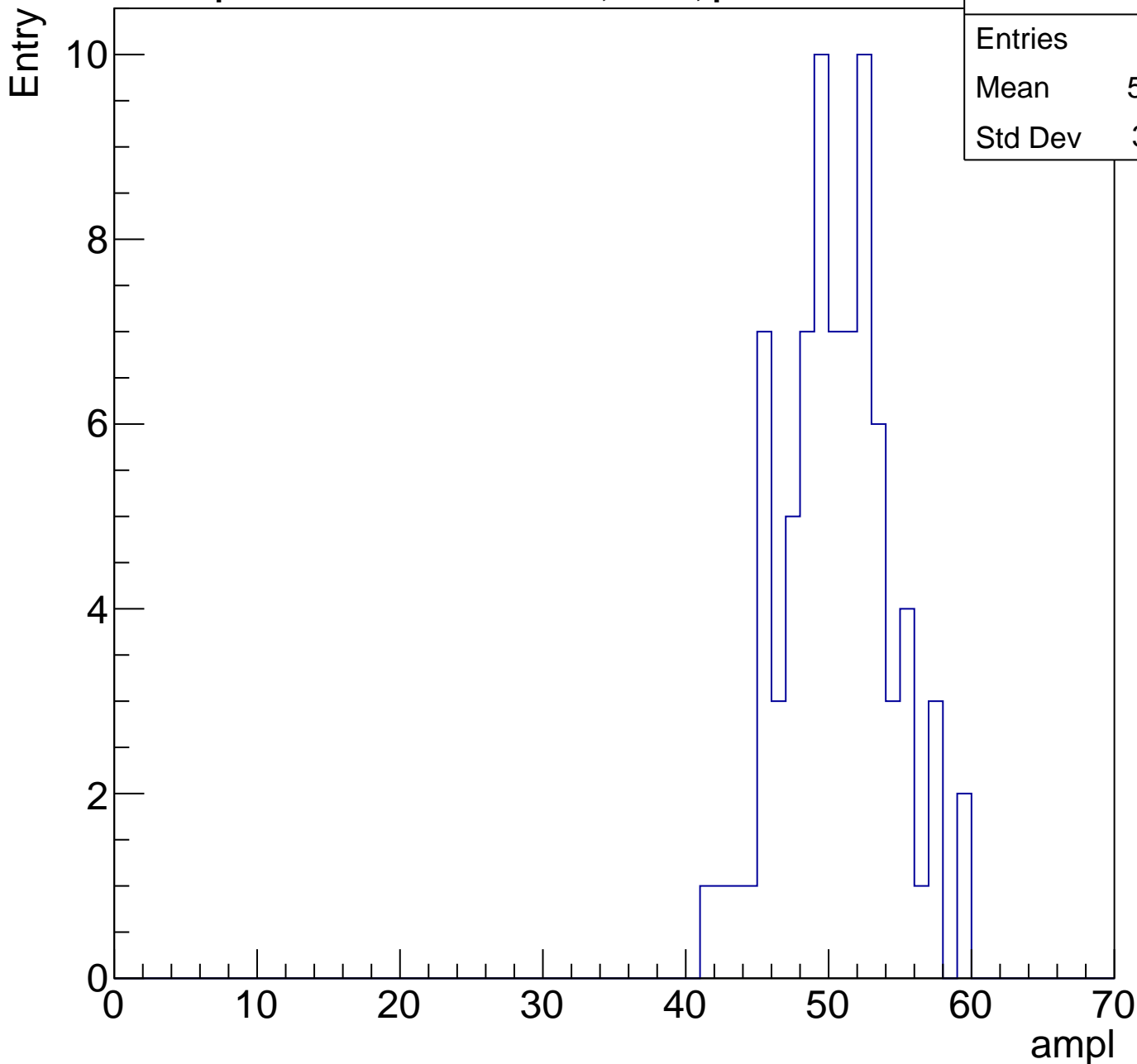
Gaus Width: 4.2267



B1L101S, U12-ch73, adc3

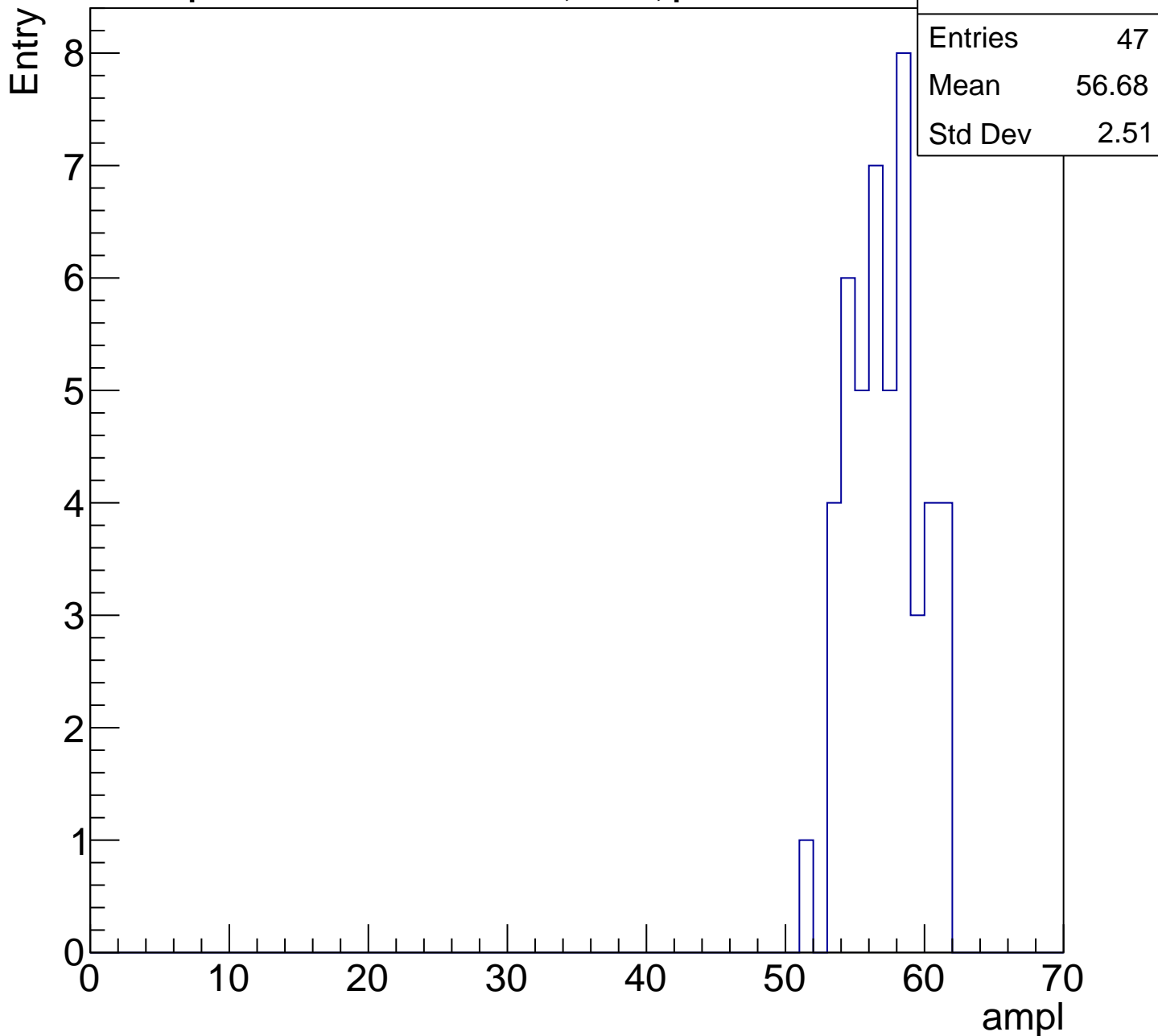
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	50.08
Std Dev	3.781



B1L101S, U12-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

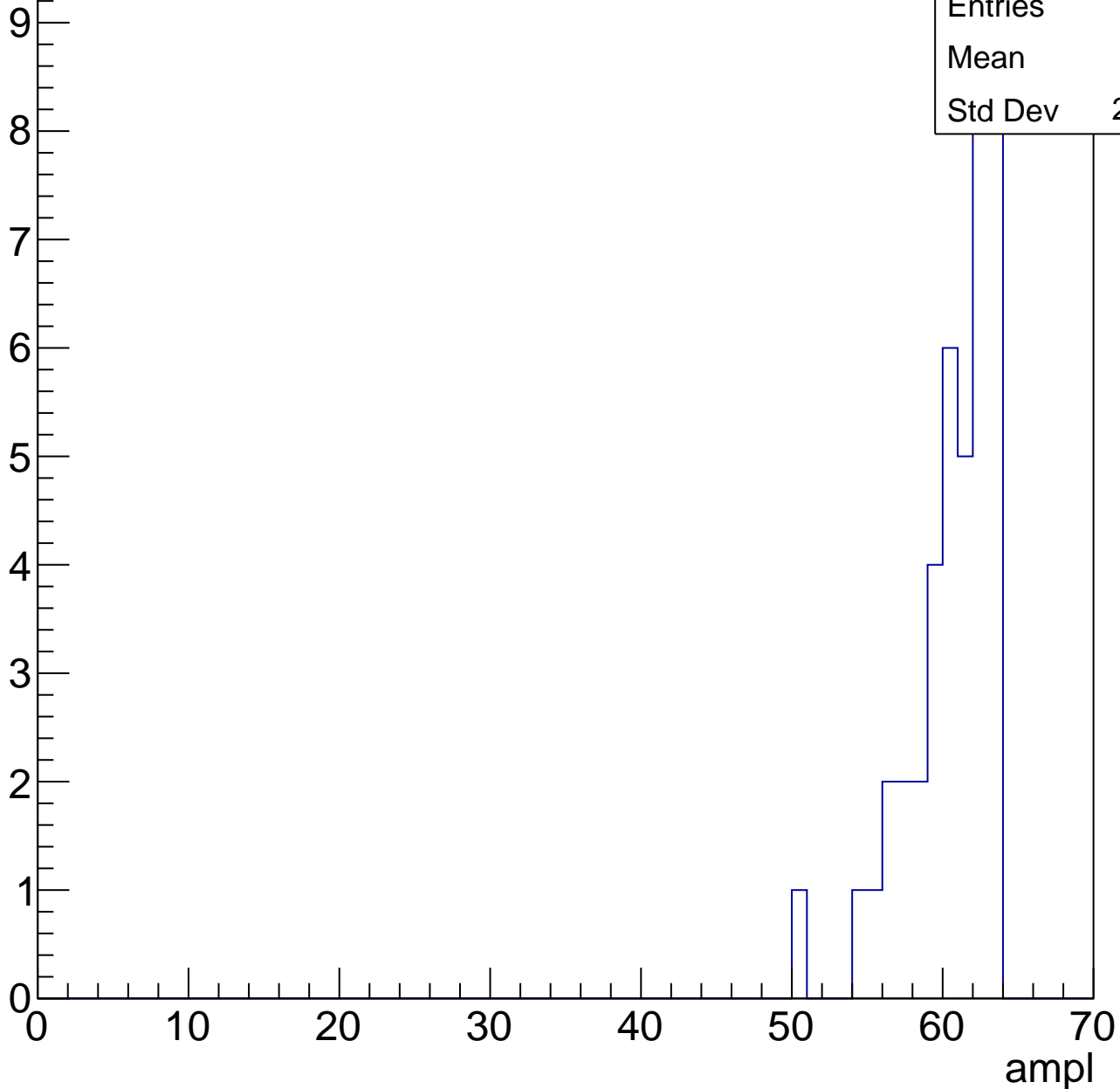


B1L101S, U12-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	60.1
Std Dev	2.861



B1L101S, U12-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

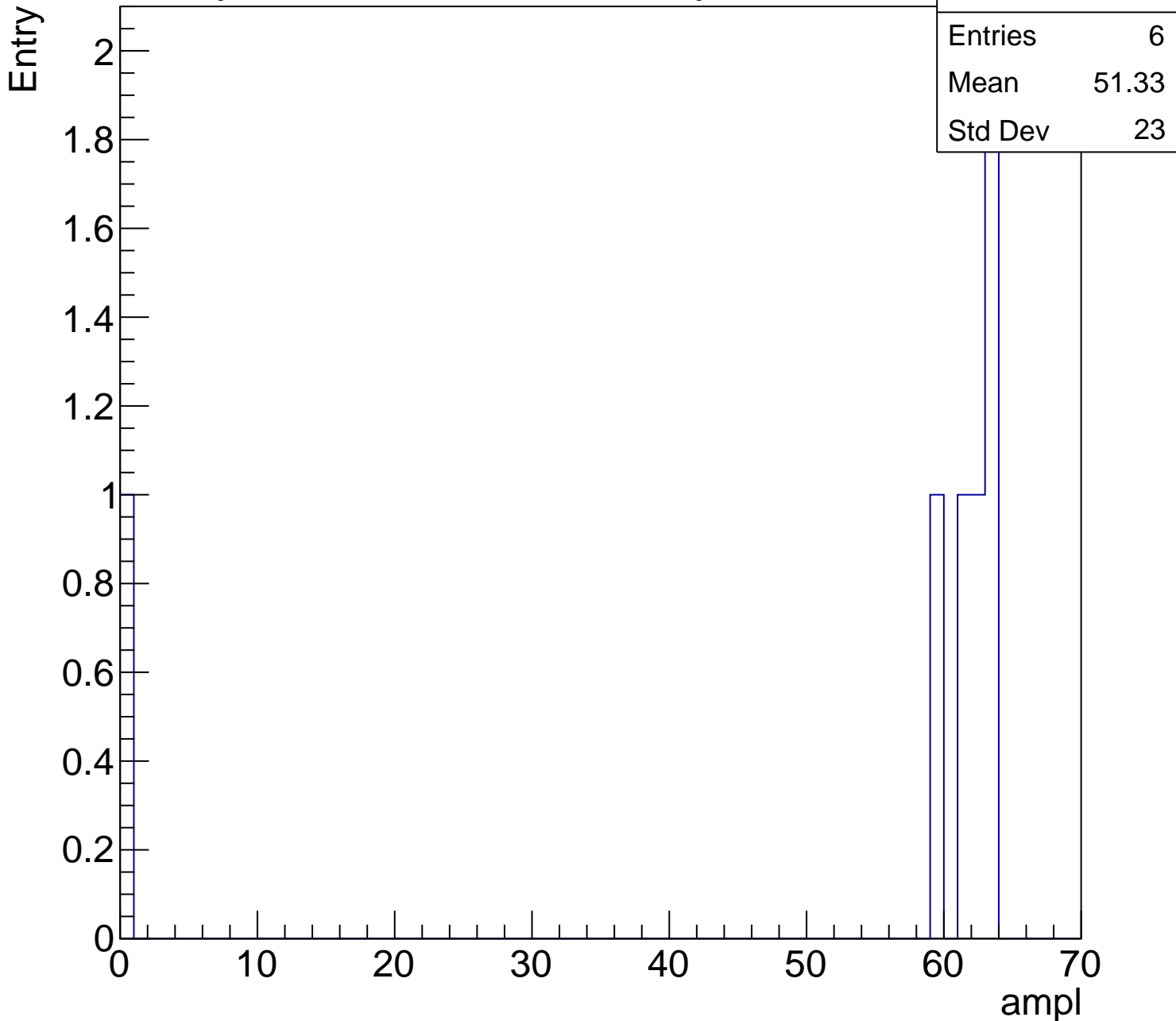
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	23

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch74, adc0

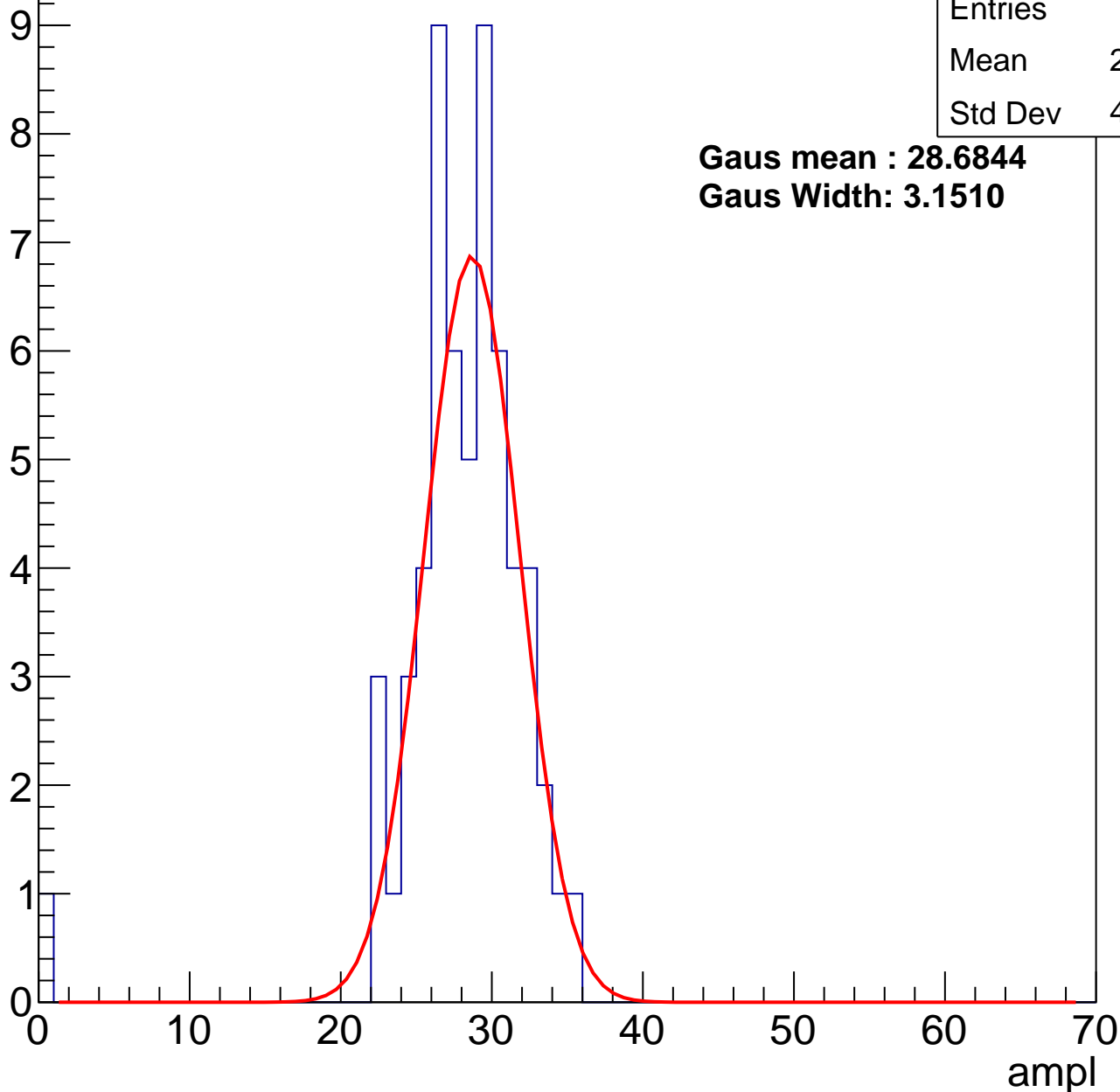
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.54
Std Dev	4.692

Gaus mean : 28.6844

Gaus Width: 3.1510



B1L101S, U12-ch74, adc1

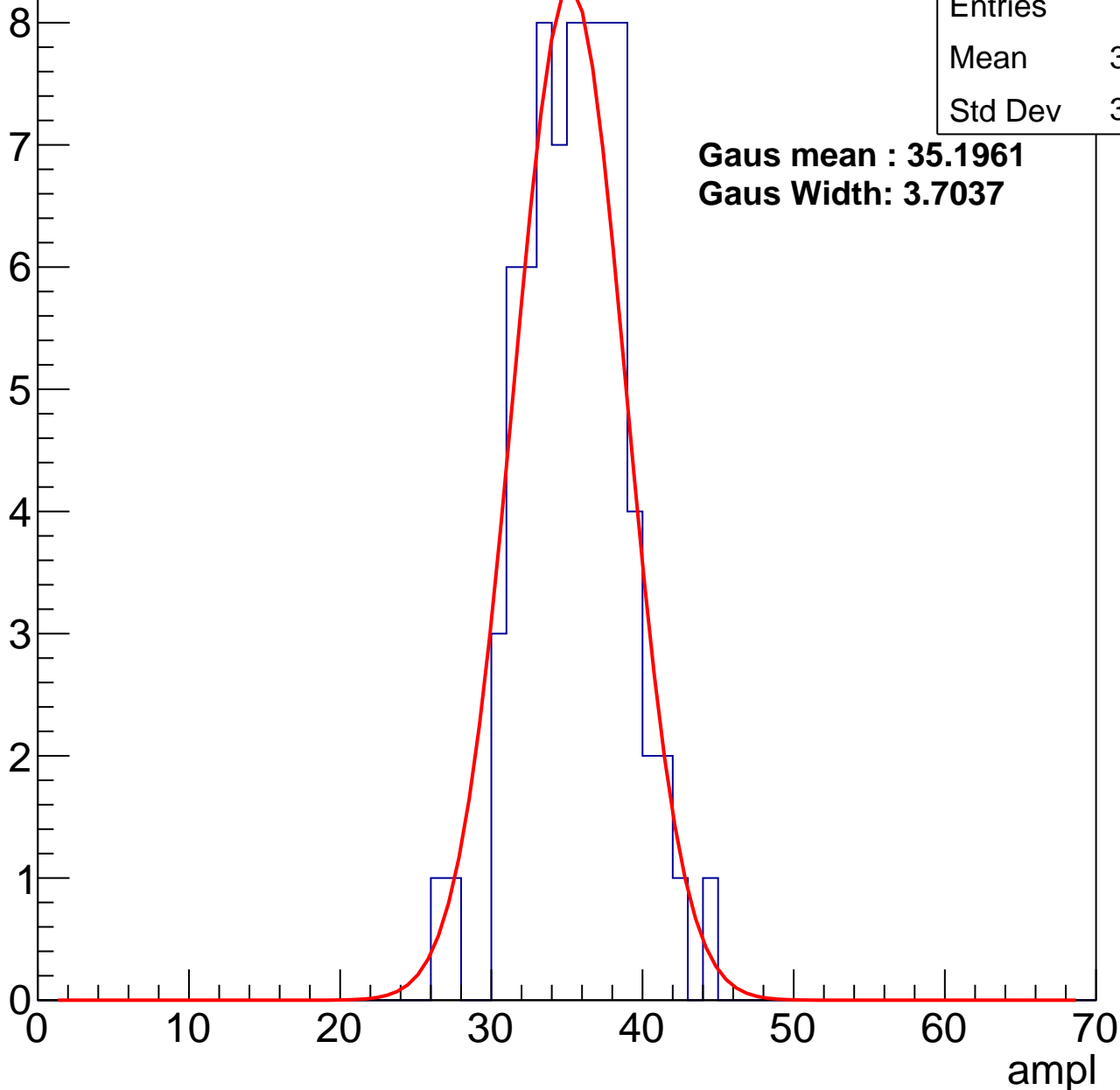
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.07
Std Dev	3.362

Gaus mean : 35.1961

Gaus Width: 3.7037



B1L101S, U12-ch74, adc2

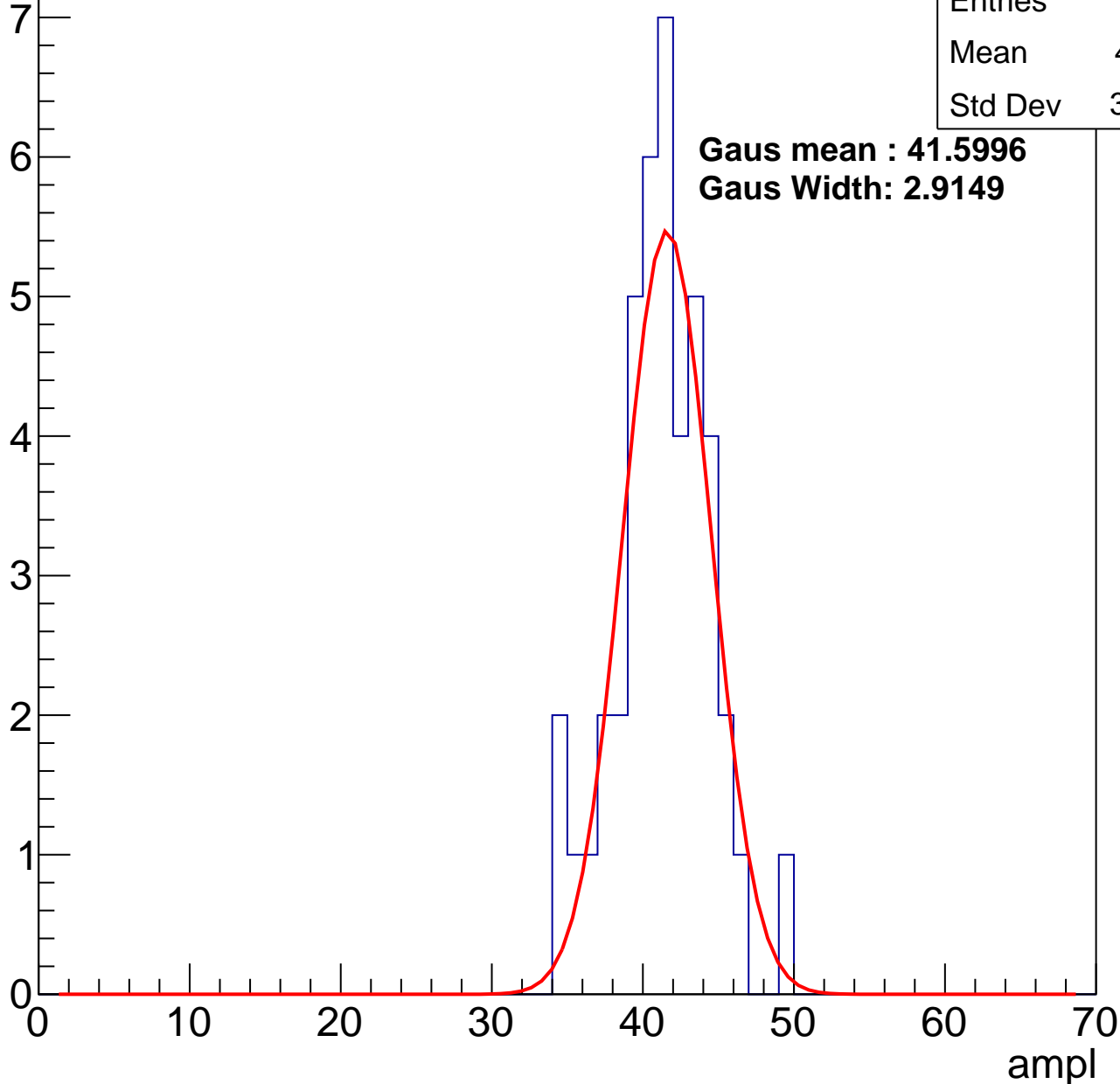
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	40.81
Std Dev	3.105

Gaus mean : 41.5996

Gaus Width: 2.9149



B1L101S, U12-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	47.34
Std Dev	4.112

Entry

10

8

6

4

2

0

0

10

20

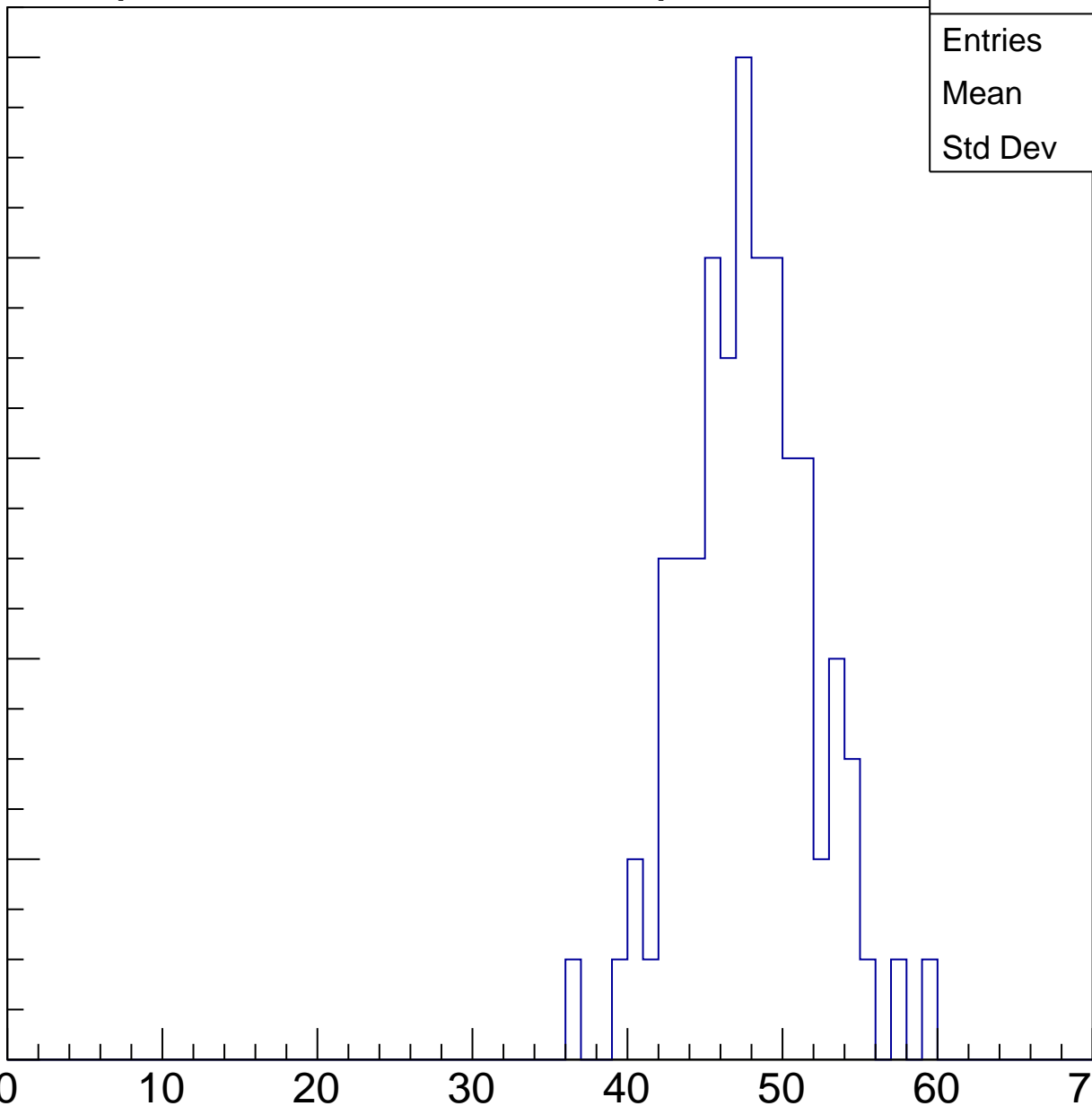
30

40

50

60

ampl

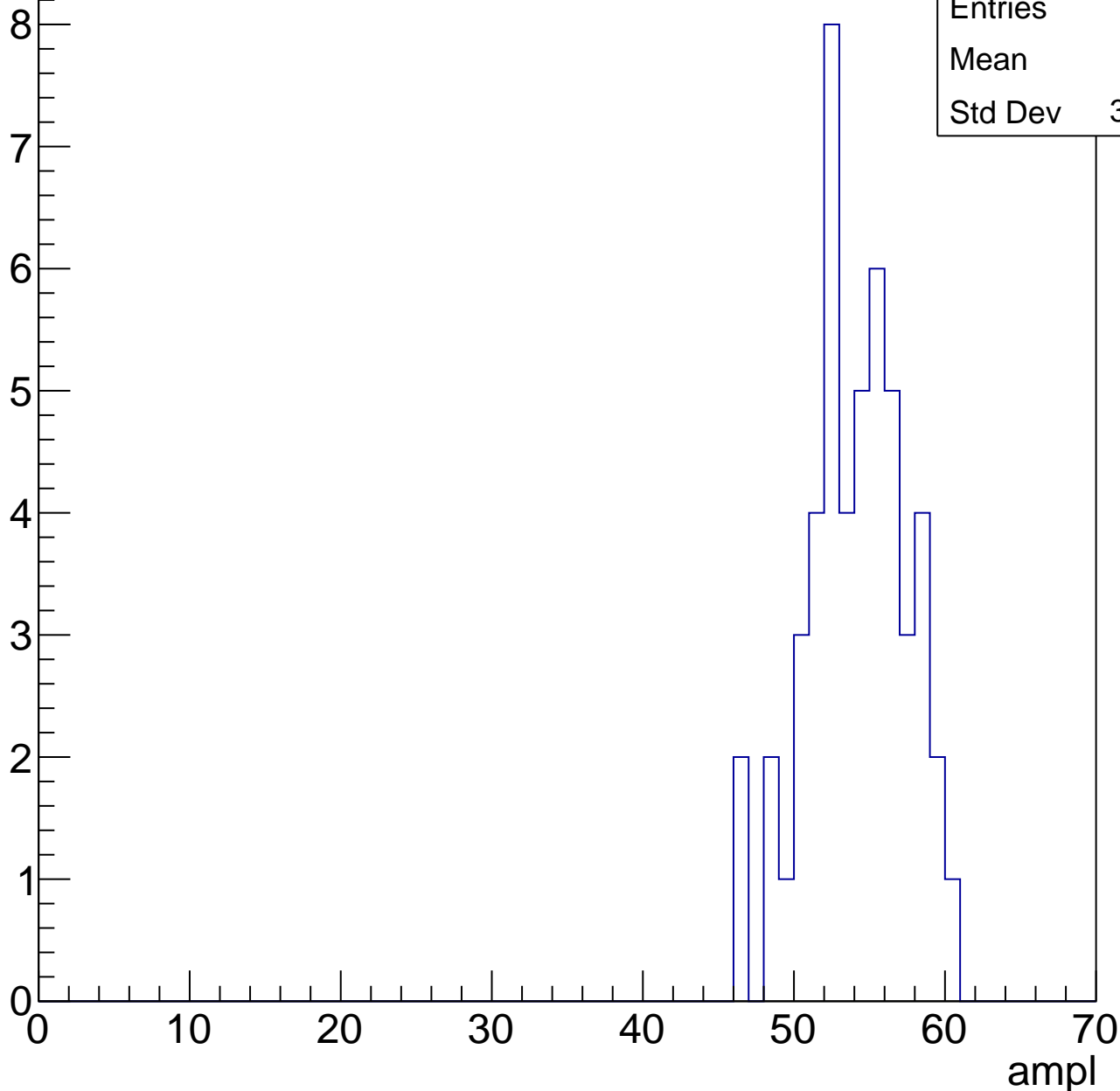


B1L101S, U12-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

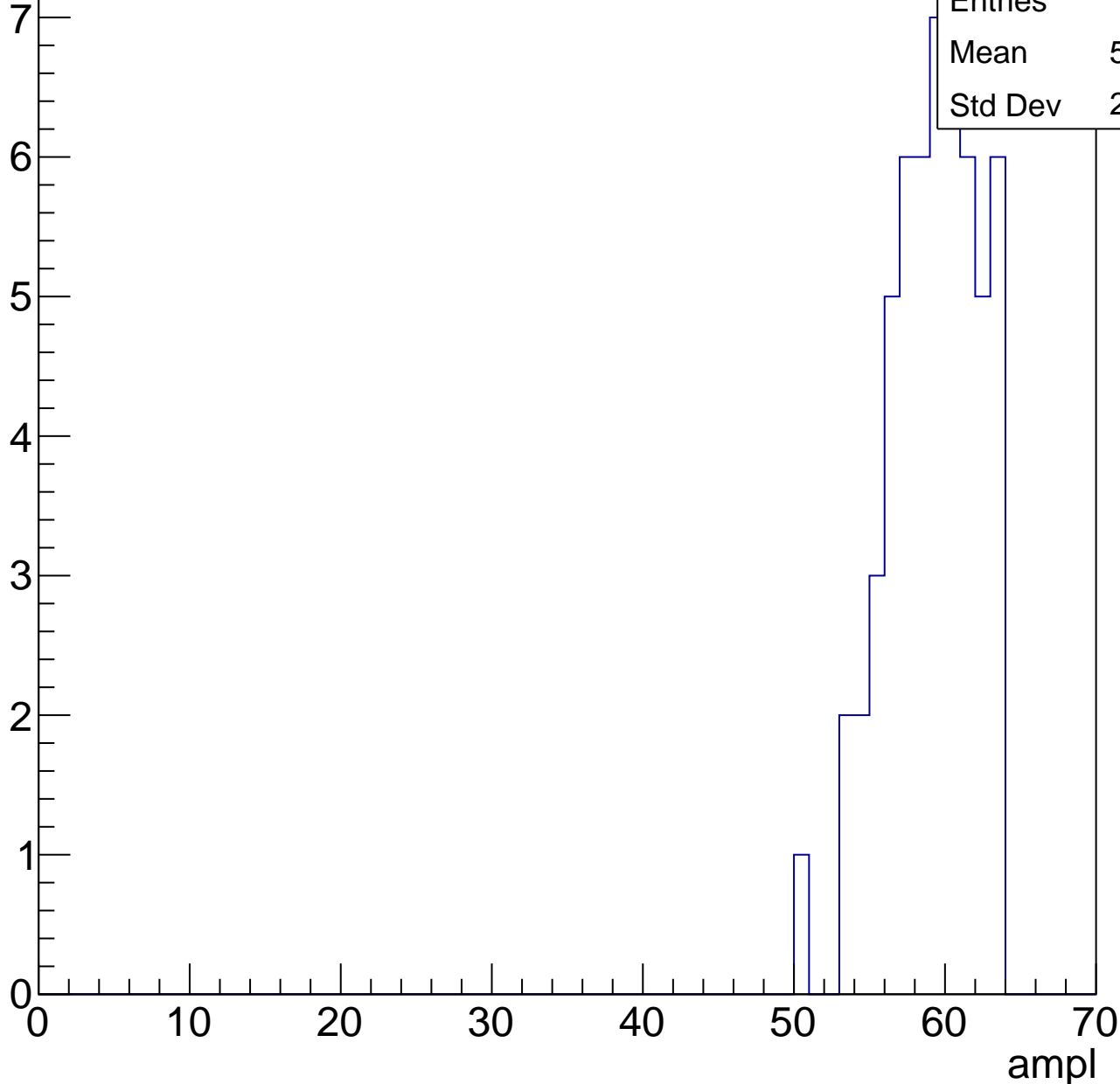
Entries	50
Mean	53.6
Std Dev	3.286



B1L101S, U12-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

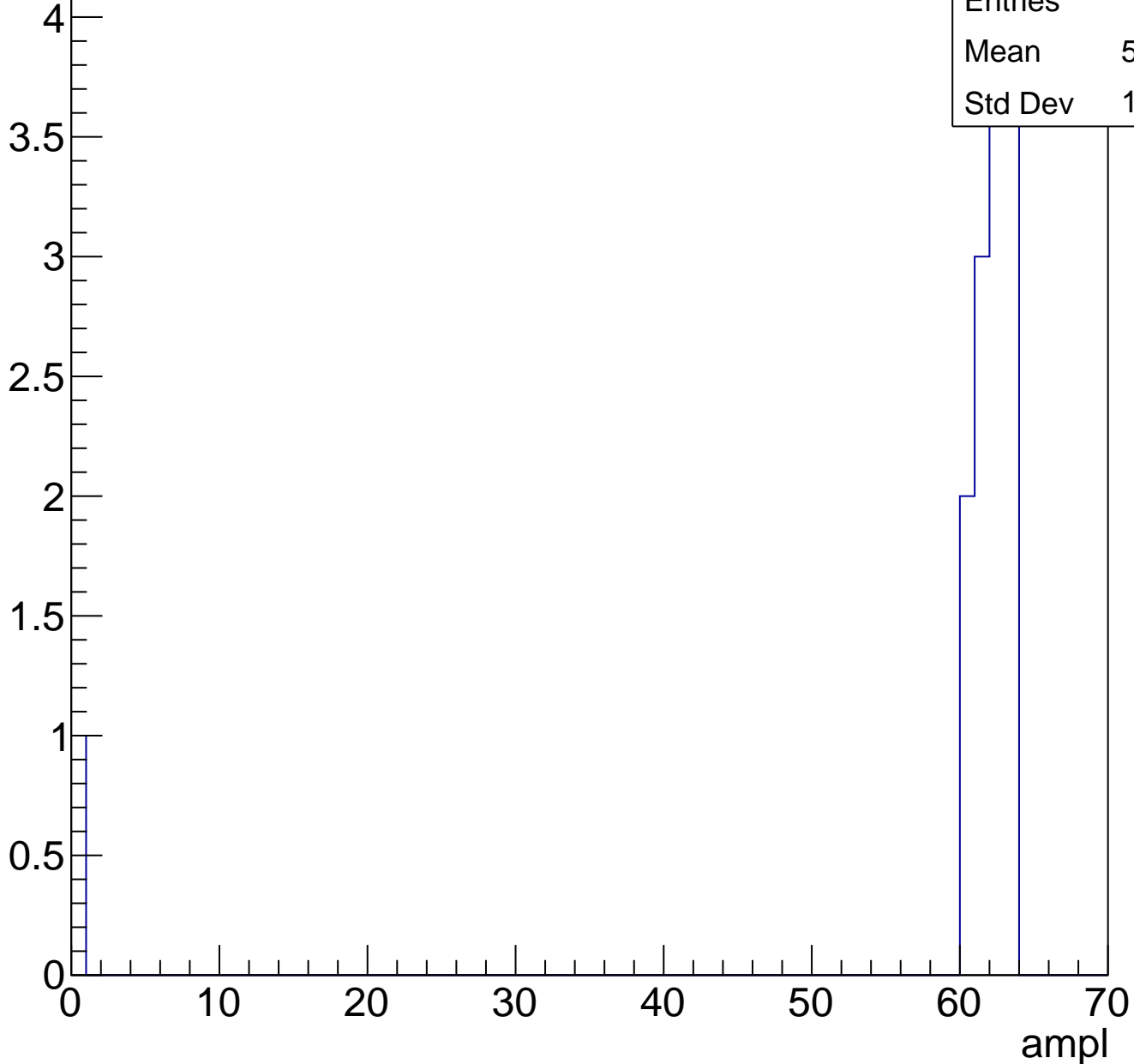


Entries	56
Mean	58.68
Std Dev	2.965

B1L101S, U12-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

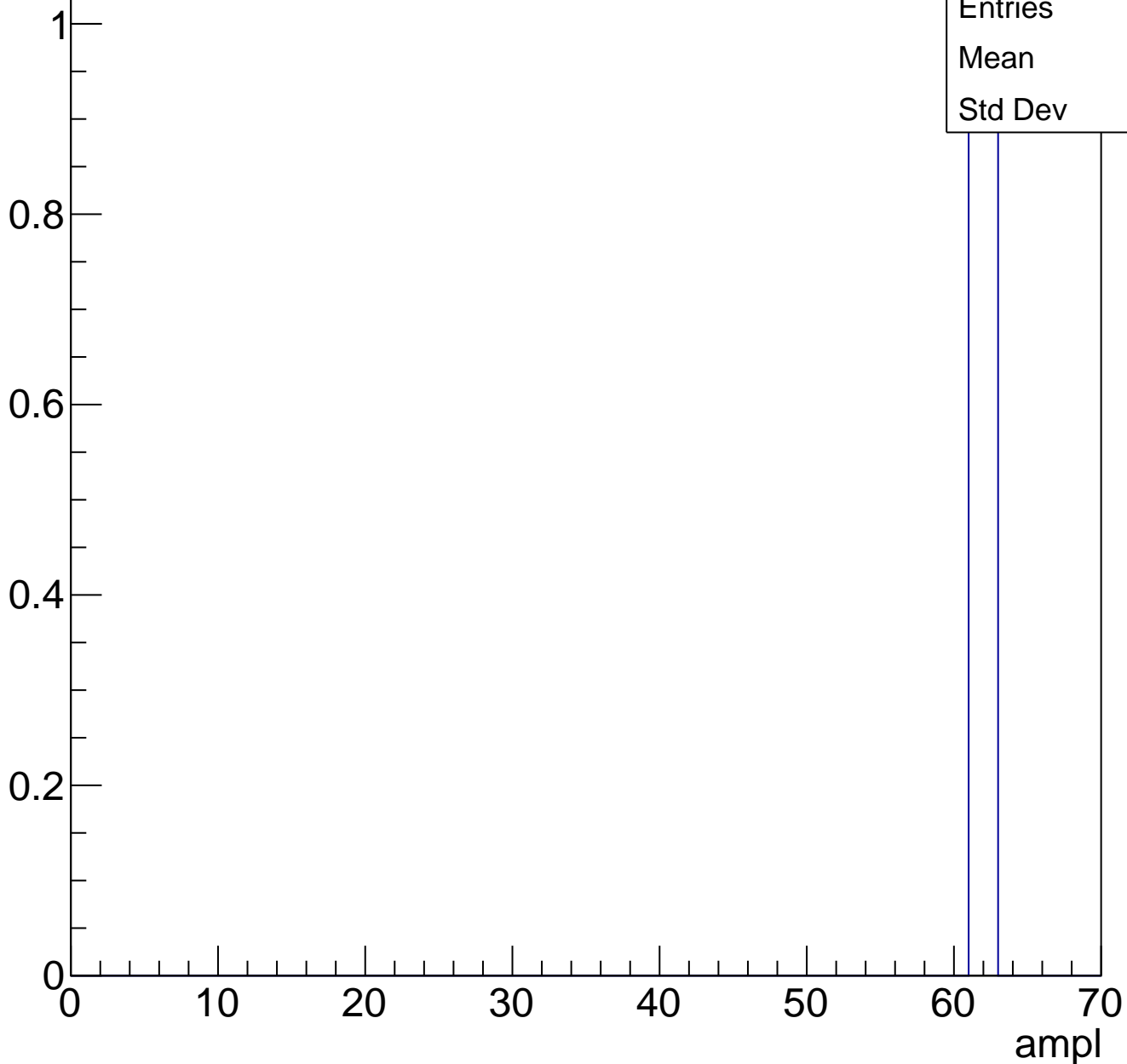
Entry



B1L101S, U12-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch75, adc0

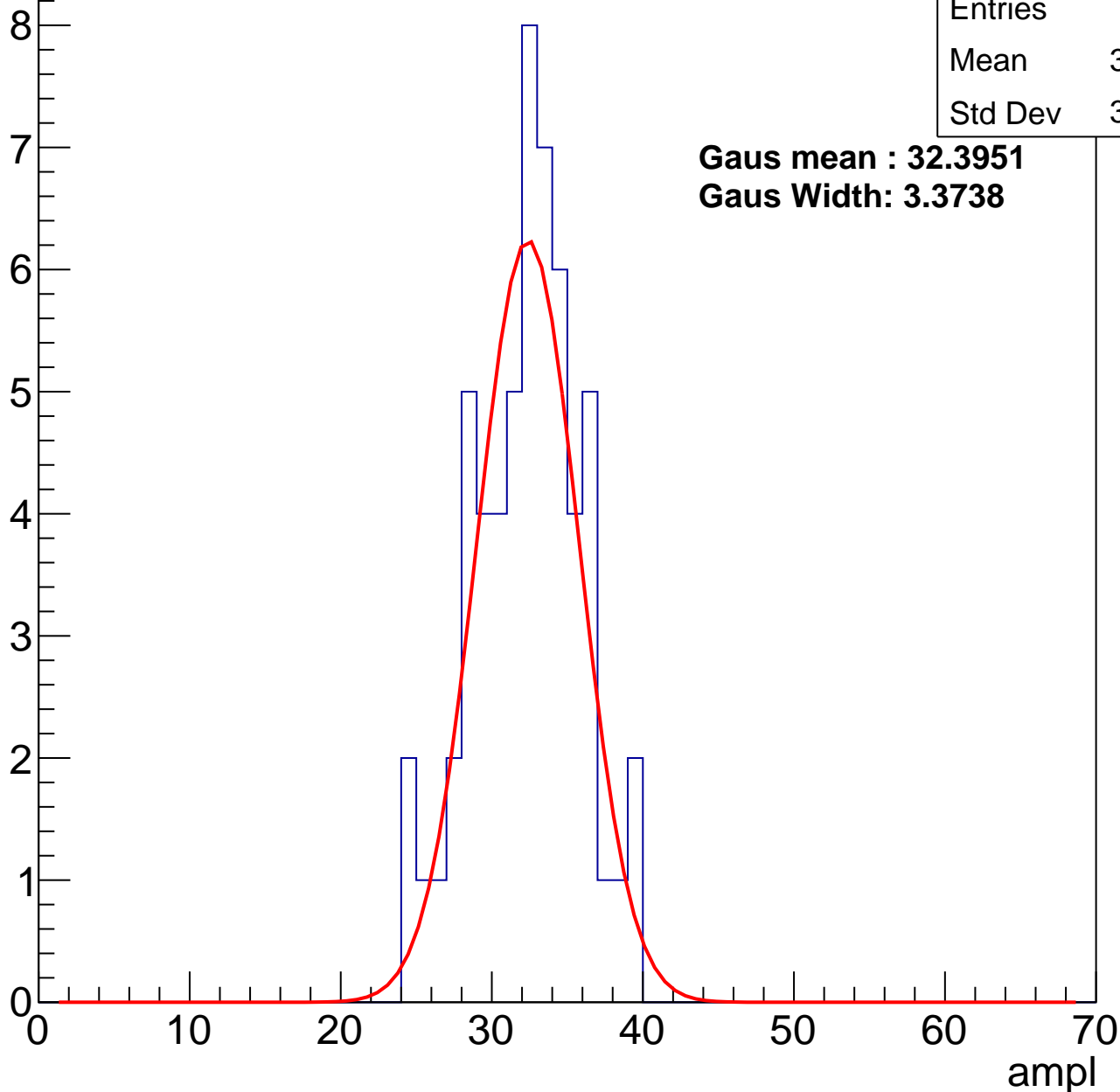
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	31.86
Std Dev	3.486

Gaus mean : 32.3951

Gaus Width: 3.3738



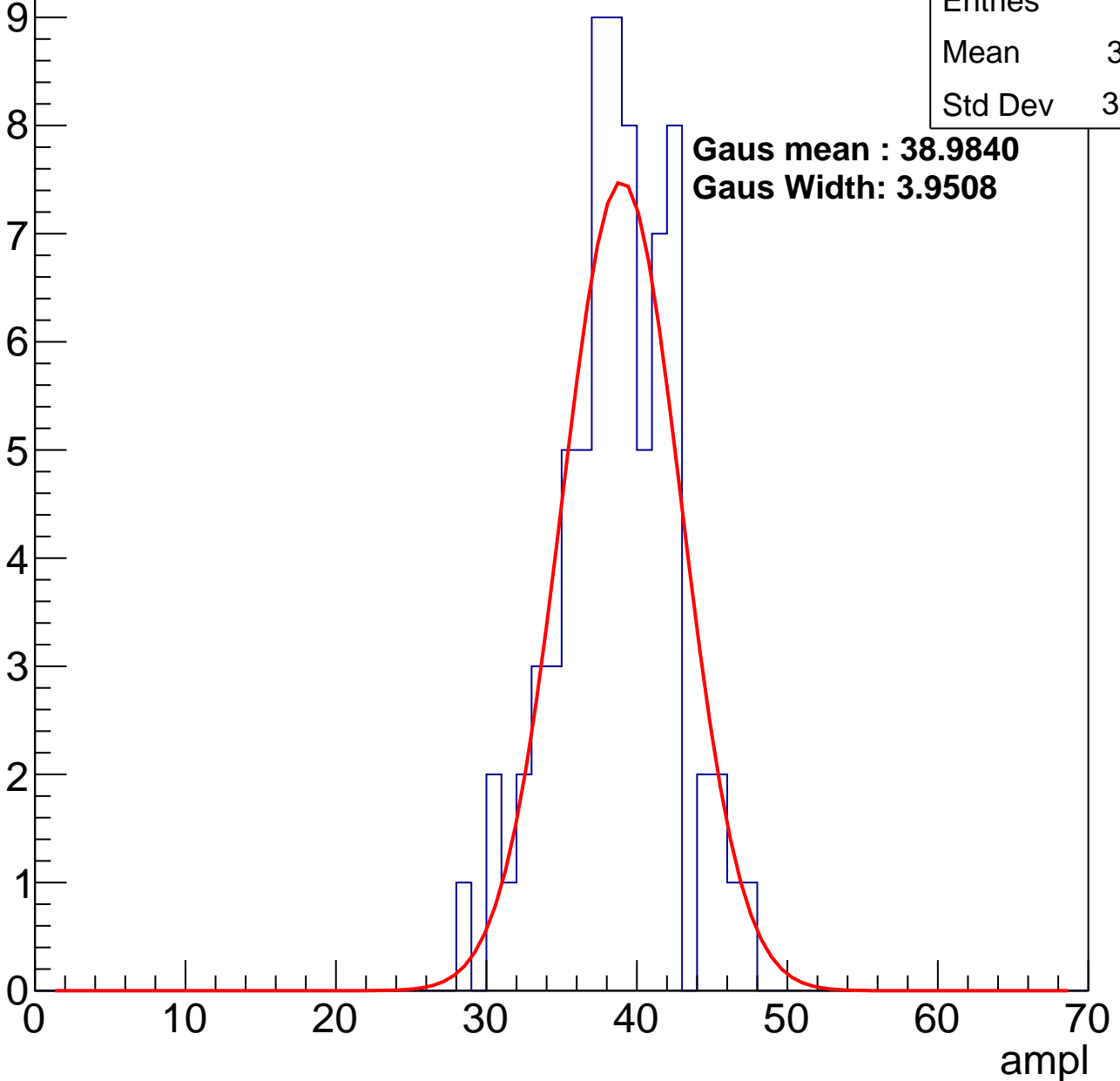
B1L101S, U12-ch75, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	38.11
Std Dev	3.819

Gaus mean : 38.9840
Gaus Width: 3.9508



B1L101S, U12-ch75, adc2

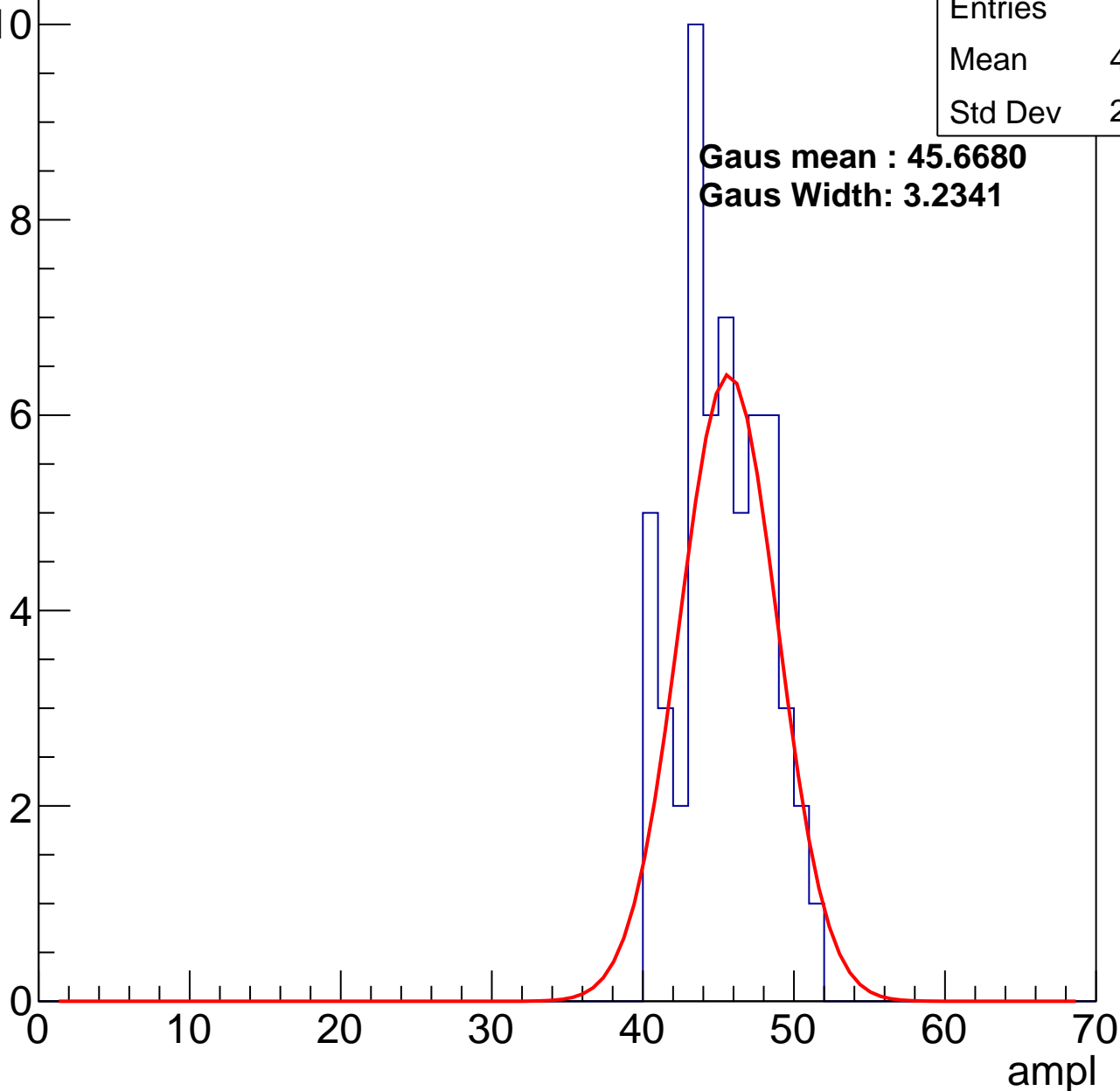
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	44.89
Std Dev	2.845

Gaus mean : 45.6680

Gaus Width: 3.2341

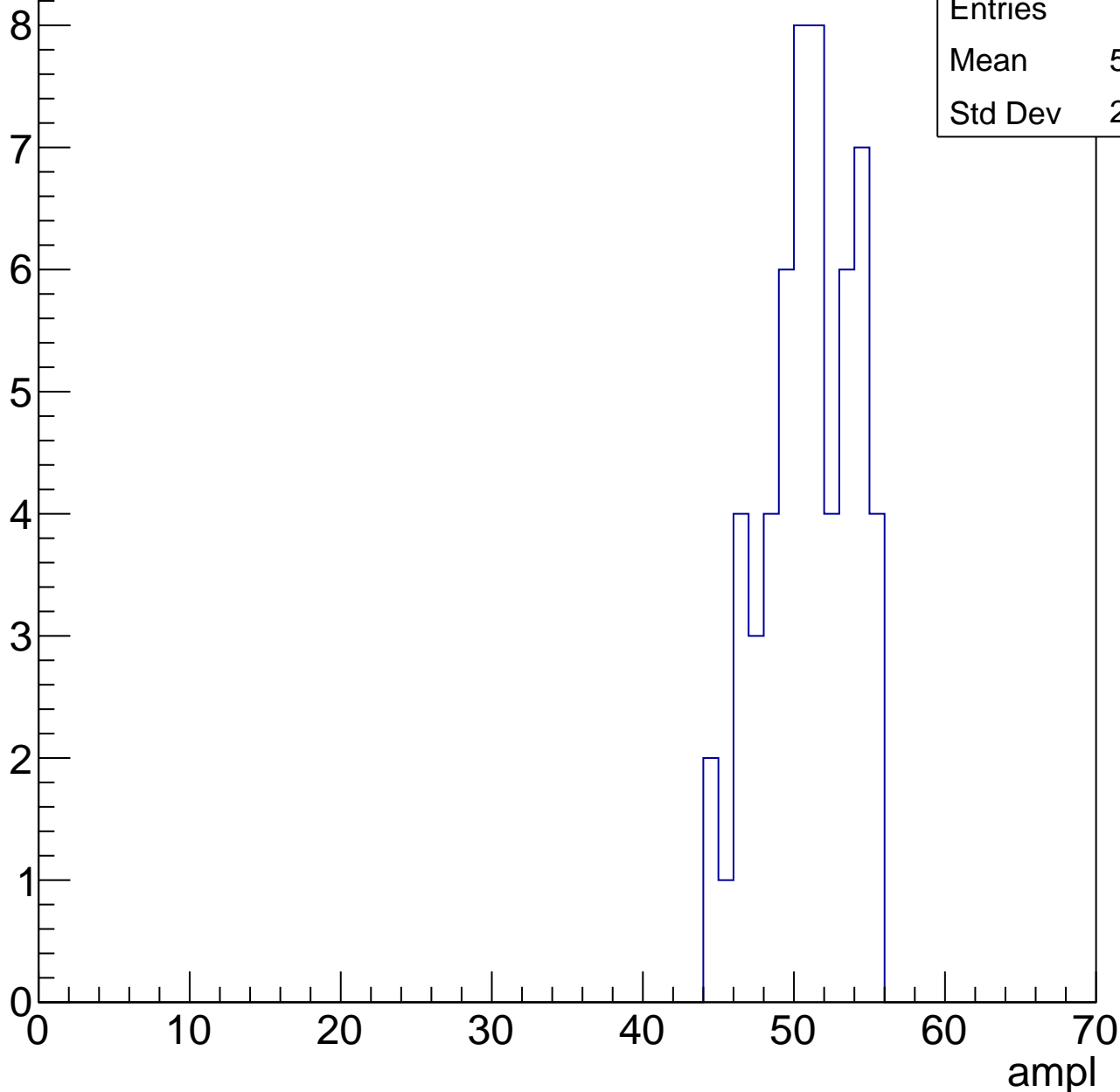


B1L101S, U12-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

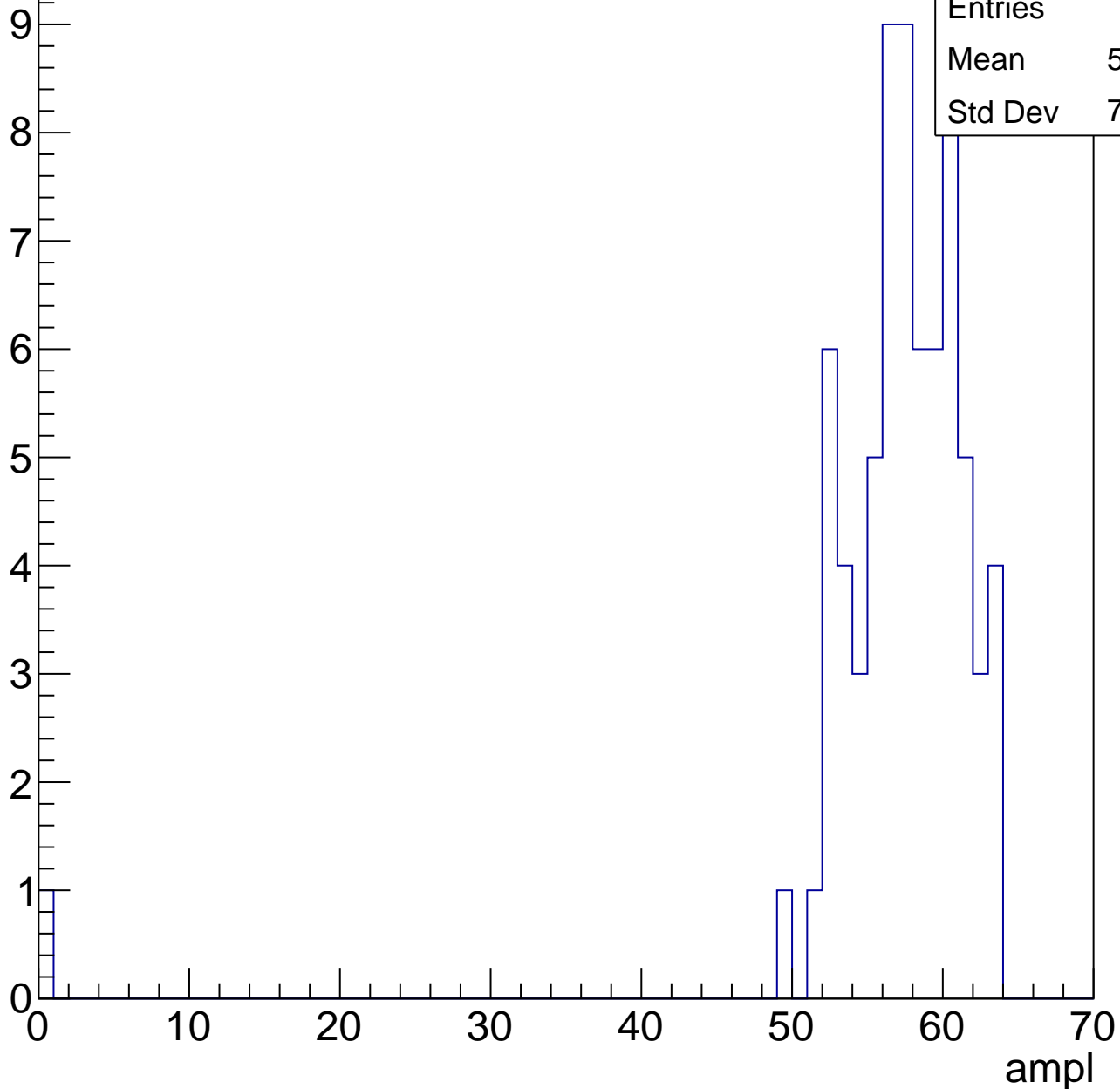
Entries	57
Mean	50.46
Std Dev	2.926



B1L101S, U12-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



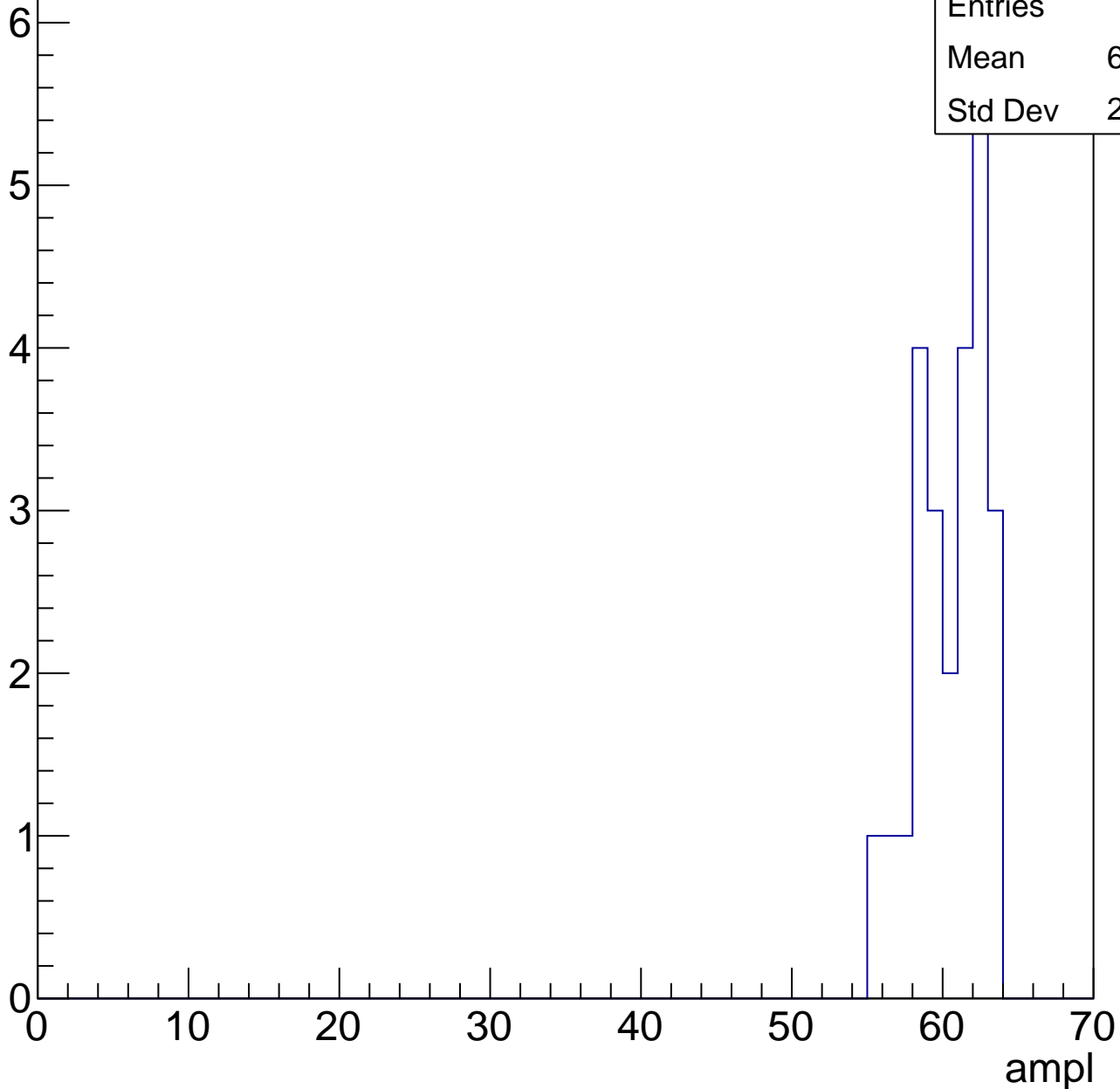
Entries	71
Mean	56.38
Std Dev	7.499

B1L101S, U12-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	60.08
Std Dev	2.226



B1L101S, U12-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L101S, U12-ch76, adc0

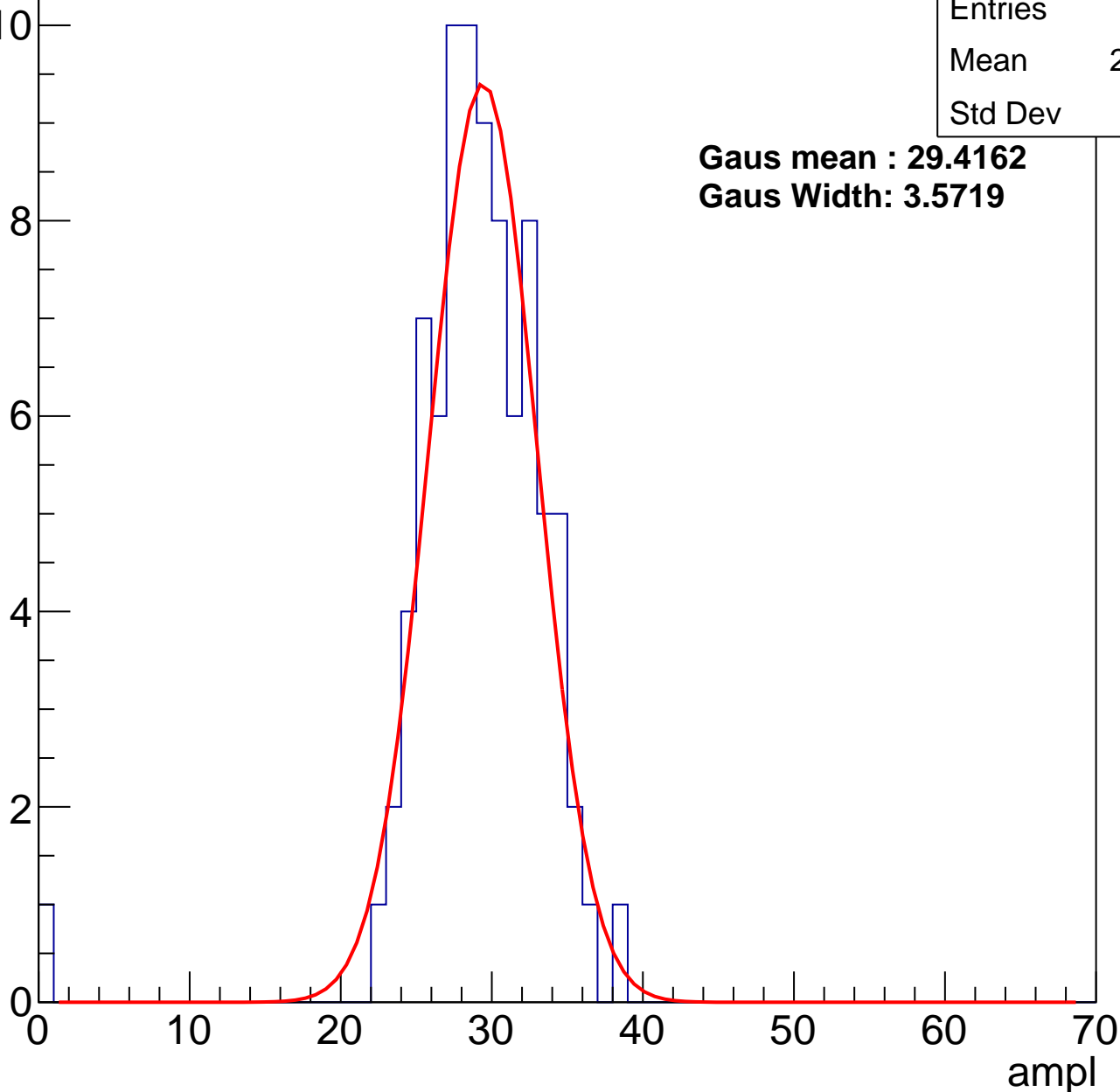
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	28.69
Std Dev	4.55

Gaus mean : 29.4162

Gaus Width: 3.5719



B1L101S, U12-ch76, adc1

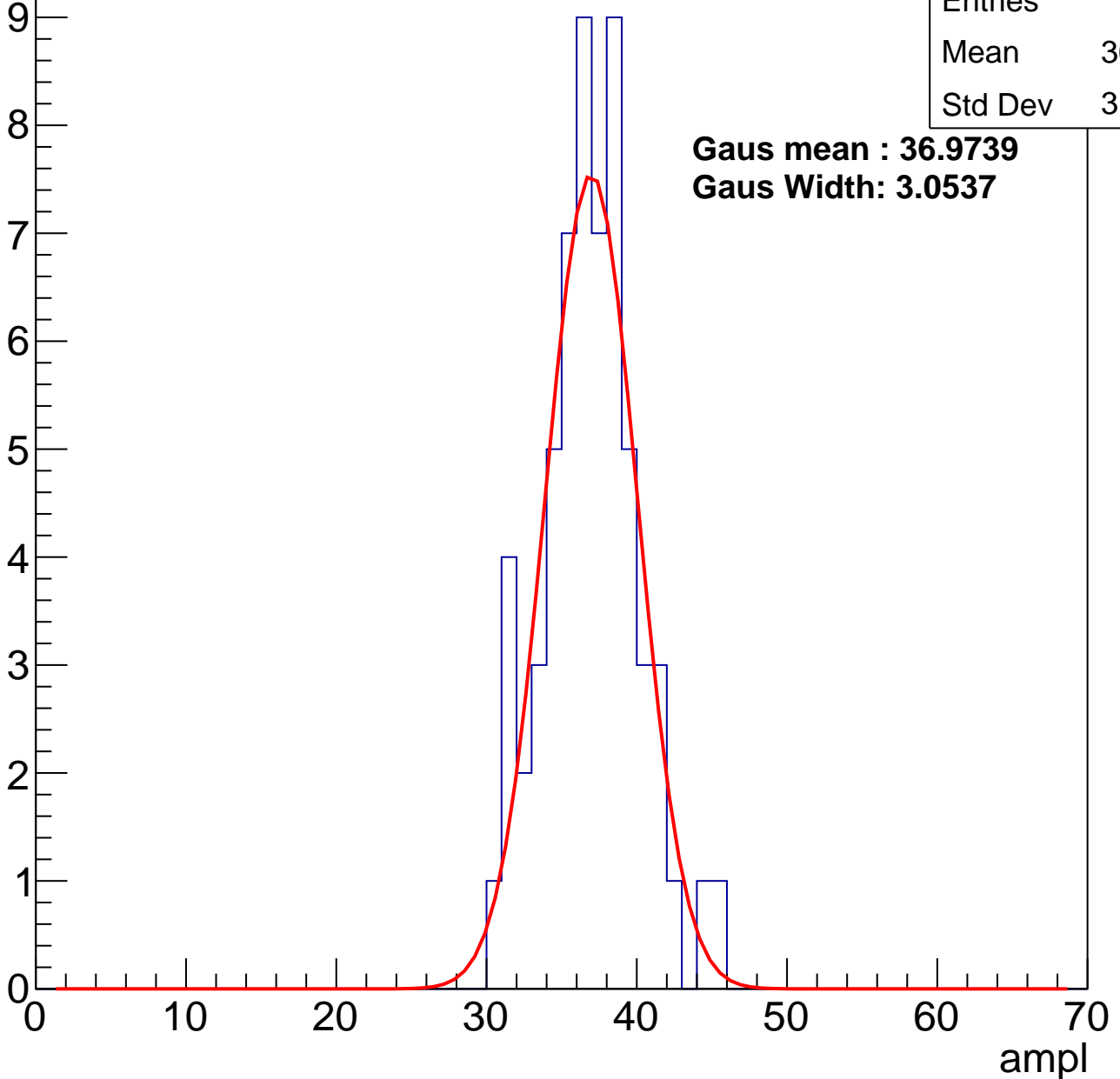
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.49
Std Dev	3.134

Gaus mean : 36.9739

Gaus Width: 3.0537



B1L101S, U12-ch76, adc2

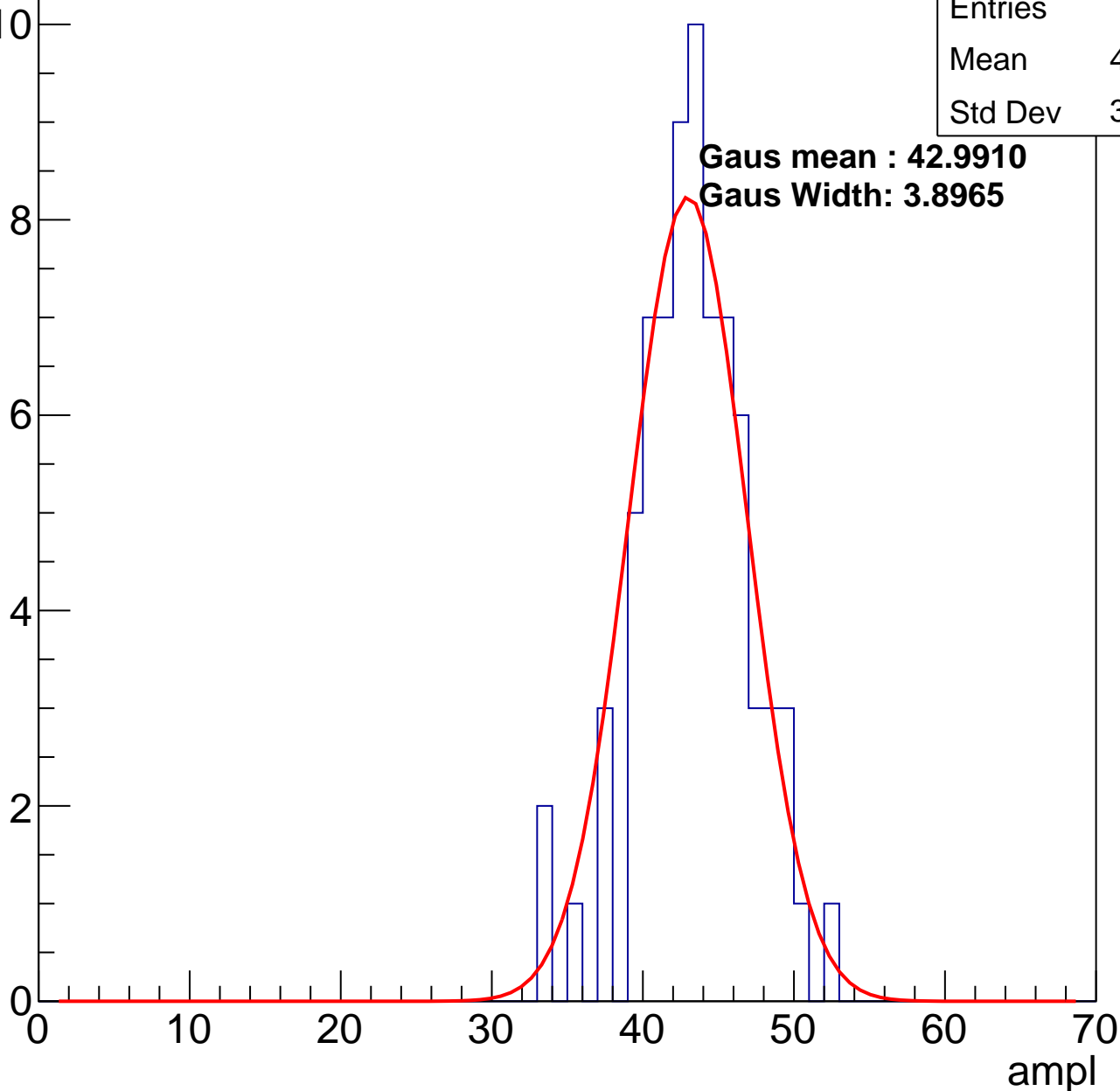
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	42.87
Std Dev	3.653

Gaus mean : 42.9910

Gaus Width: 3.8965

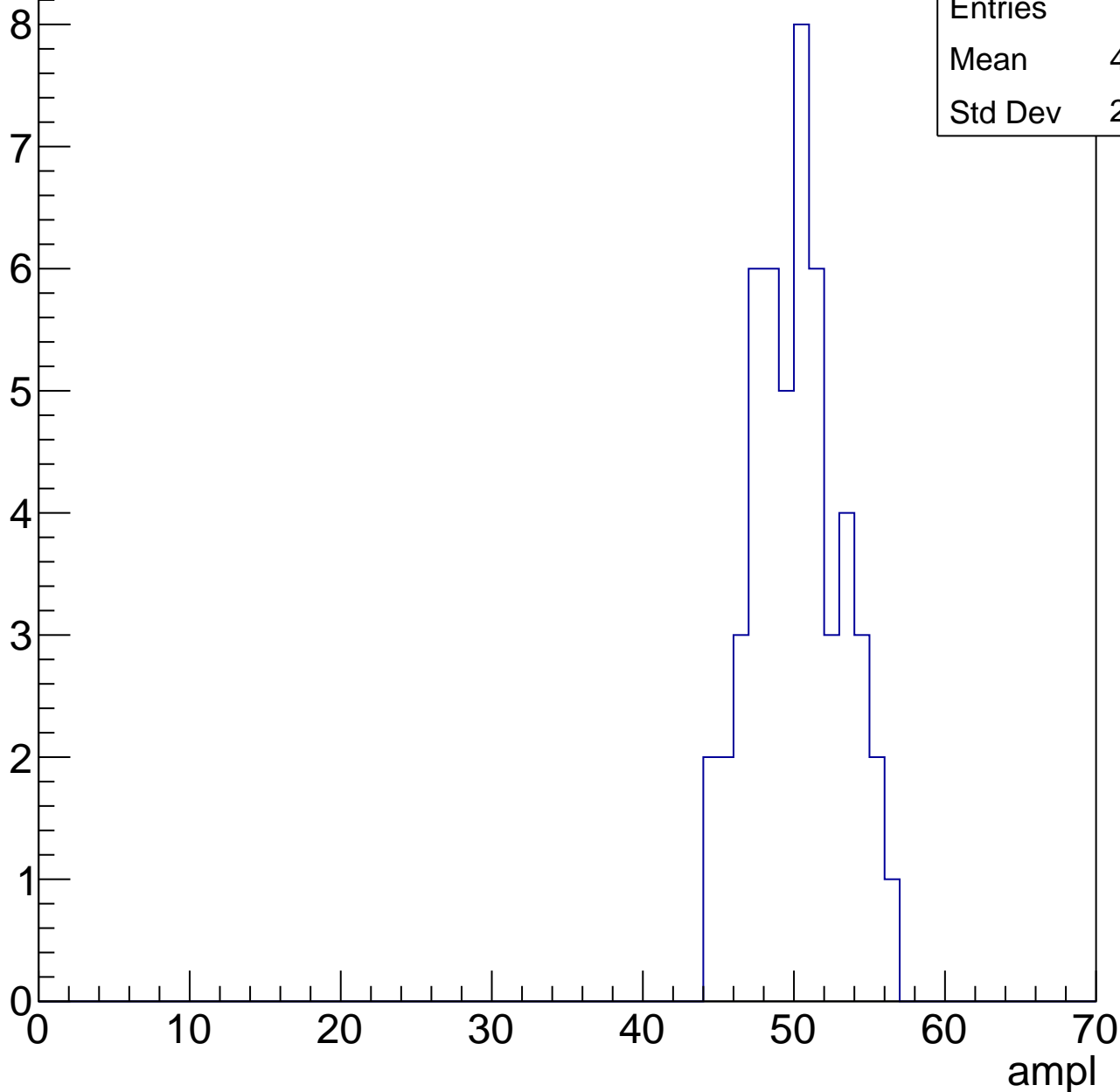


B1L101S, U12-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

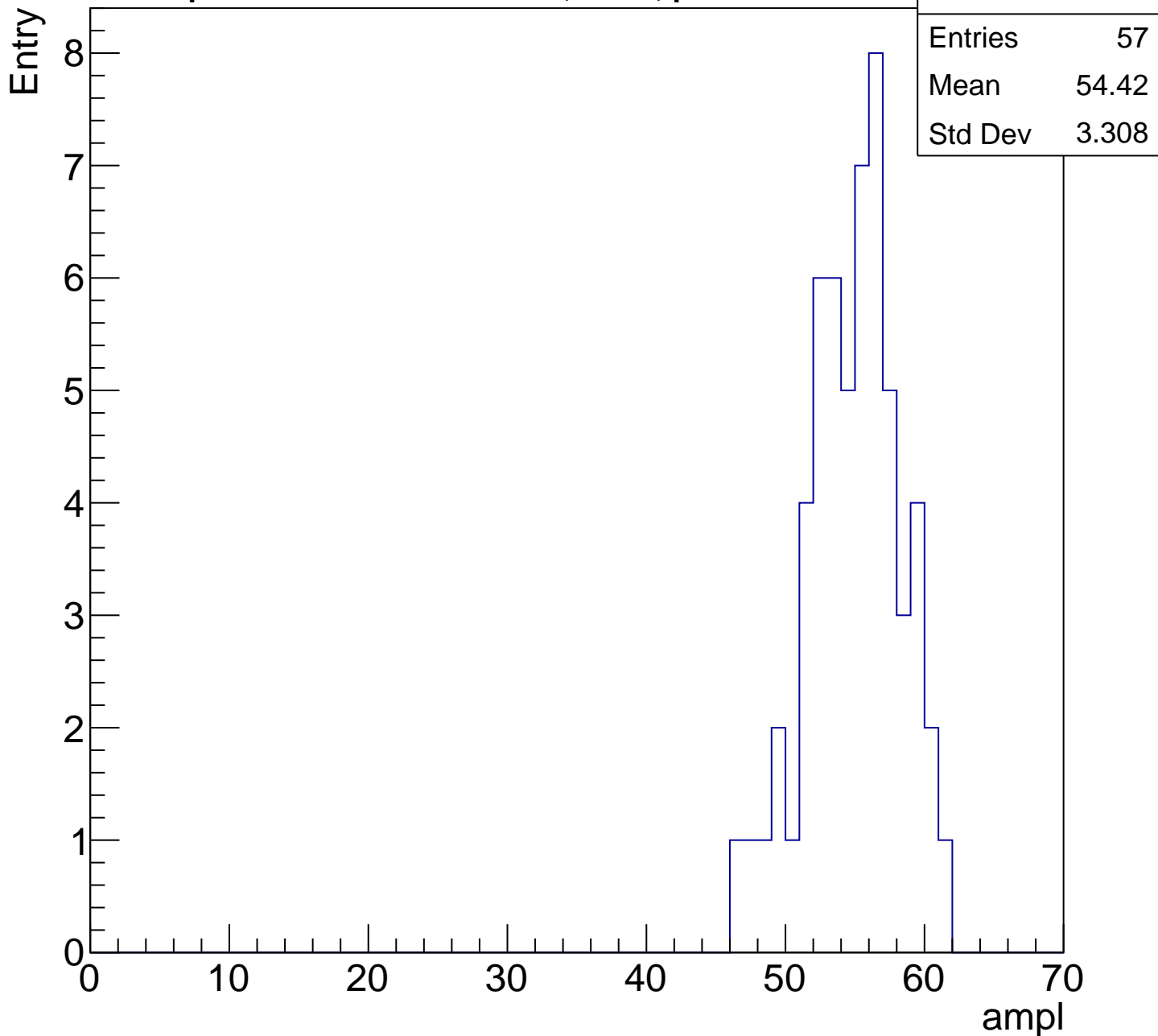
Entry

Entries	51
Mean	49.67
Std Dev	2.922



B1L101S, U12-ch76, adc4

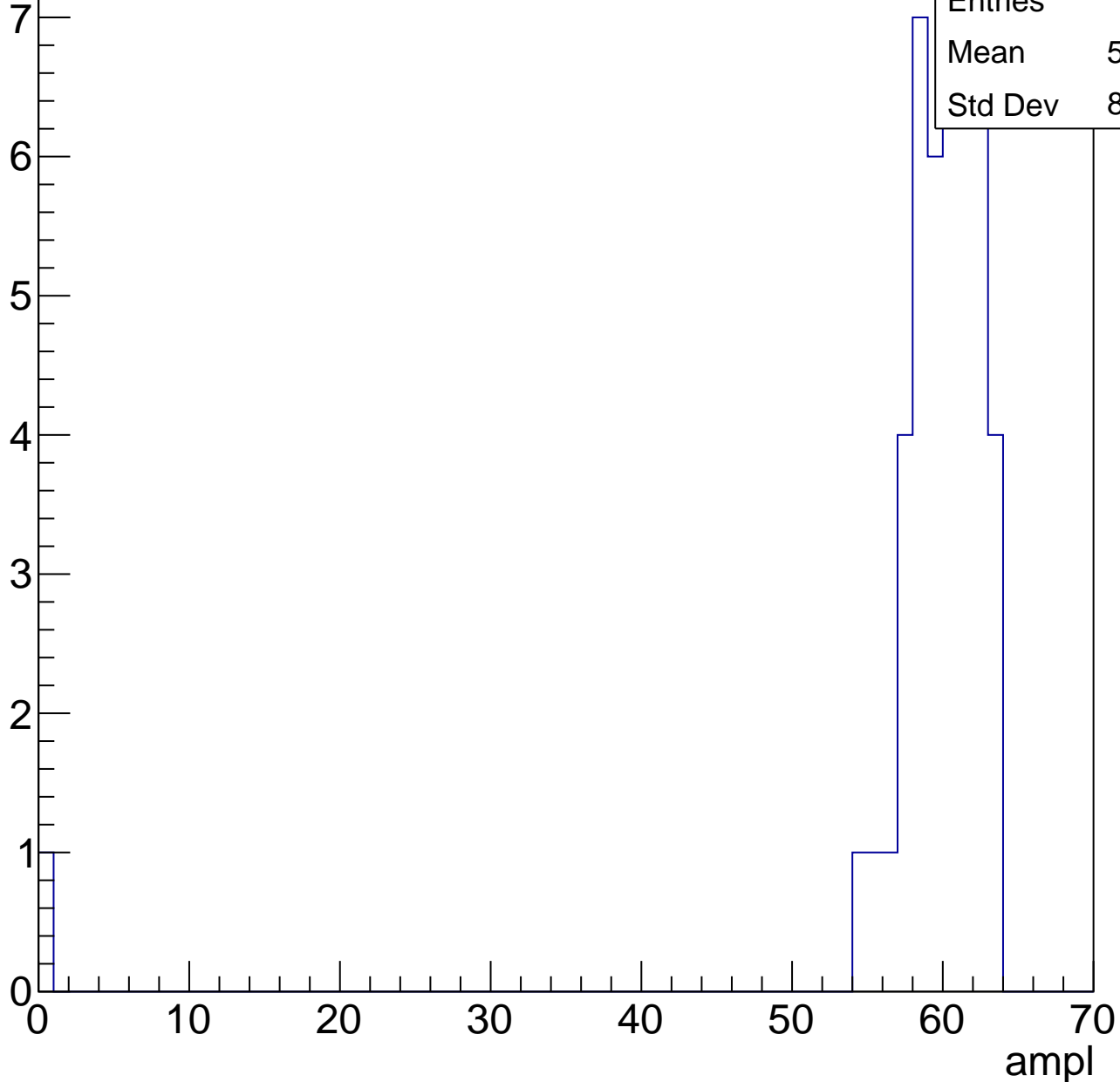
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

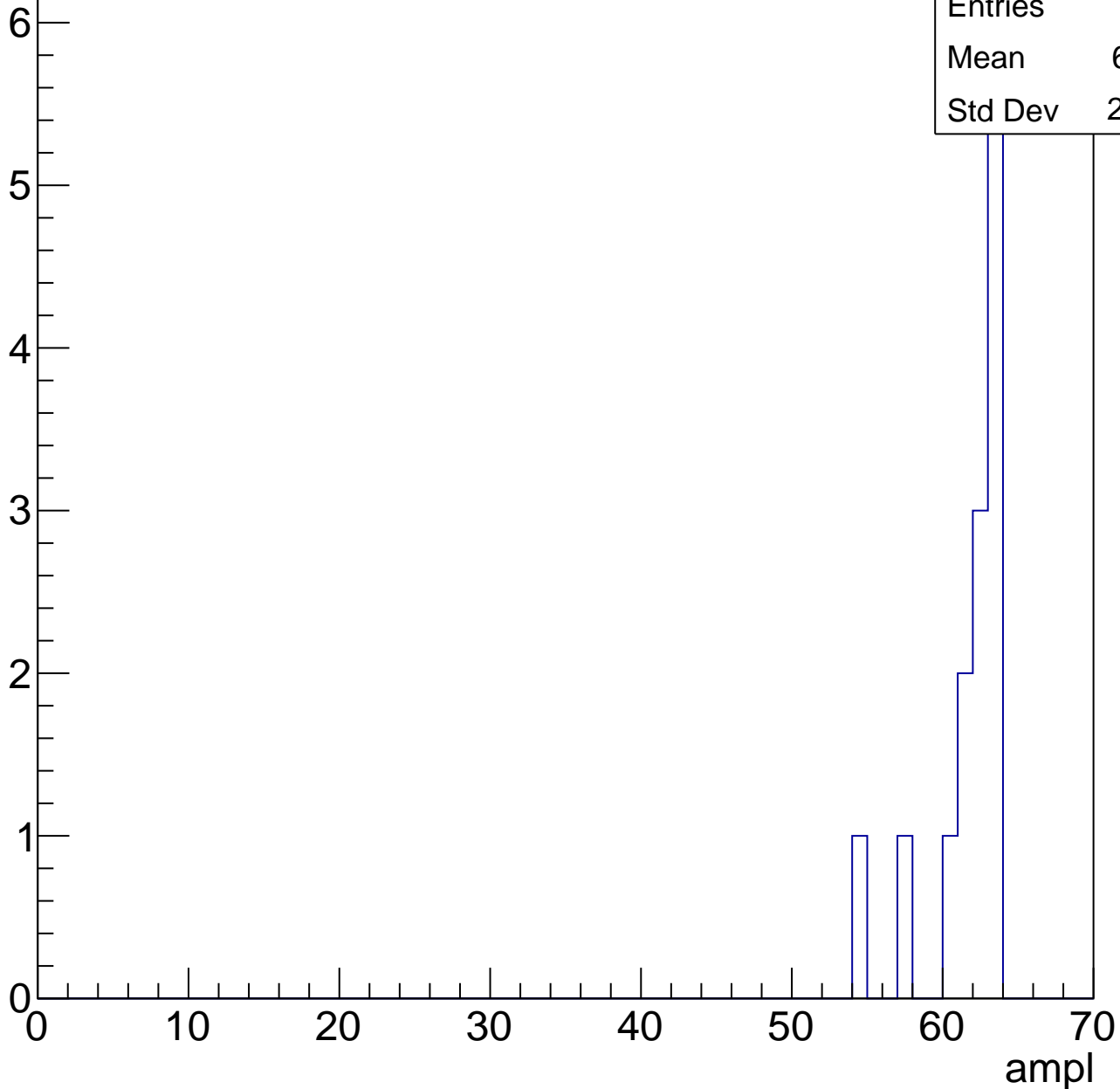


B1L101S, U12-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.21
Std Dev	2.568



B1L101S, U12-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch77, adc0

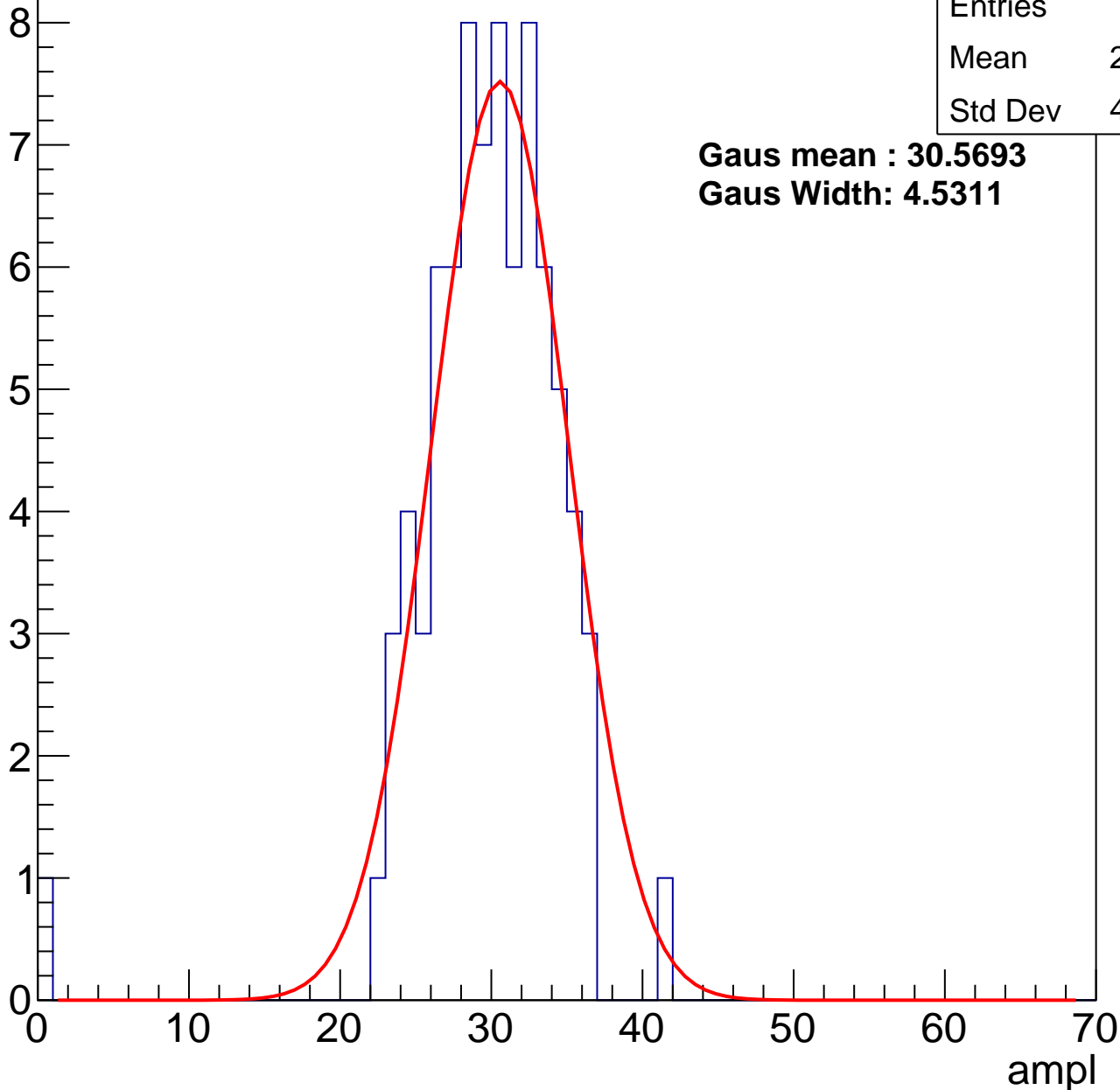
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	29.32
Std Dev	4.979

Gaus mean : 30.5693

Gaus Width: 4.5311



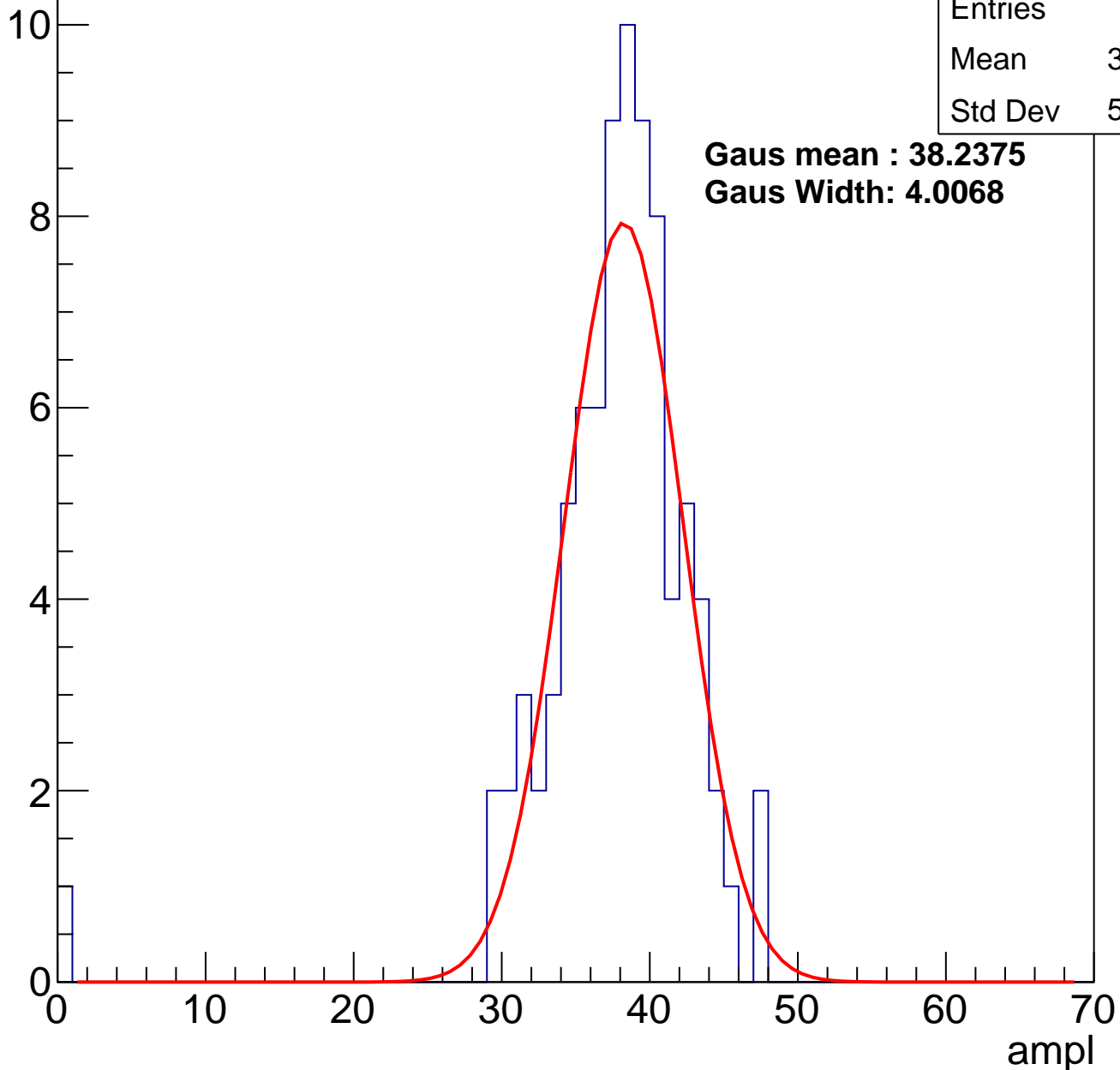
B1L101S, U12-ch77, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	37.23
Std Dev	5.653

Gaus mean : 38.2375
Gaus Width: 4.0068

Entry



B1L101S, U12-ch77, adc2

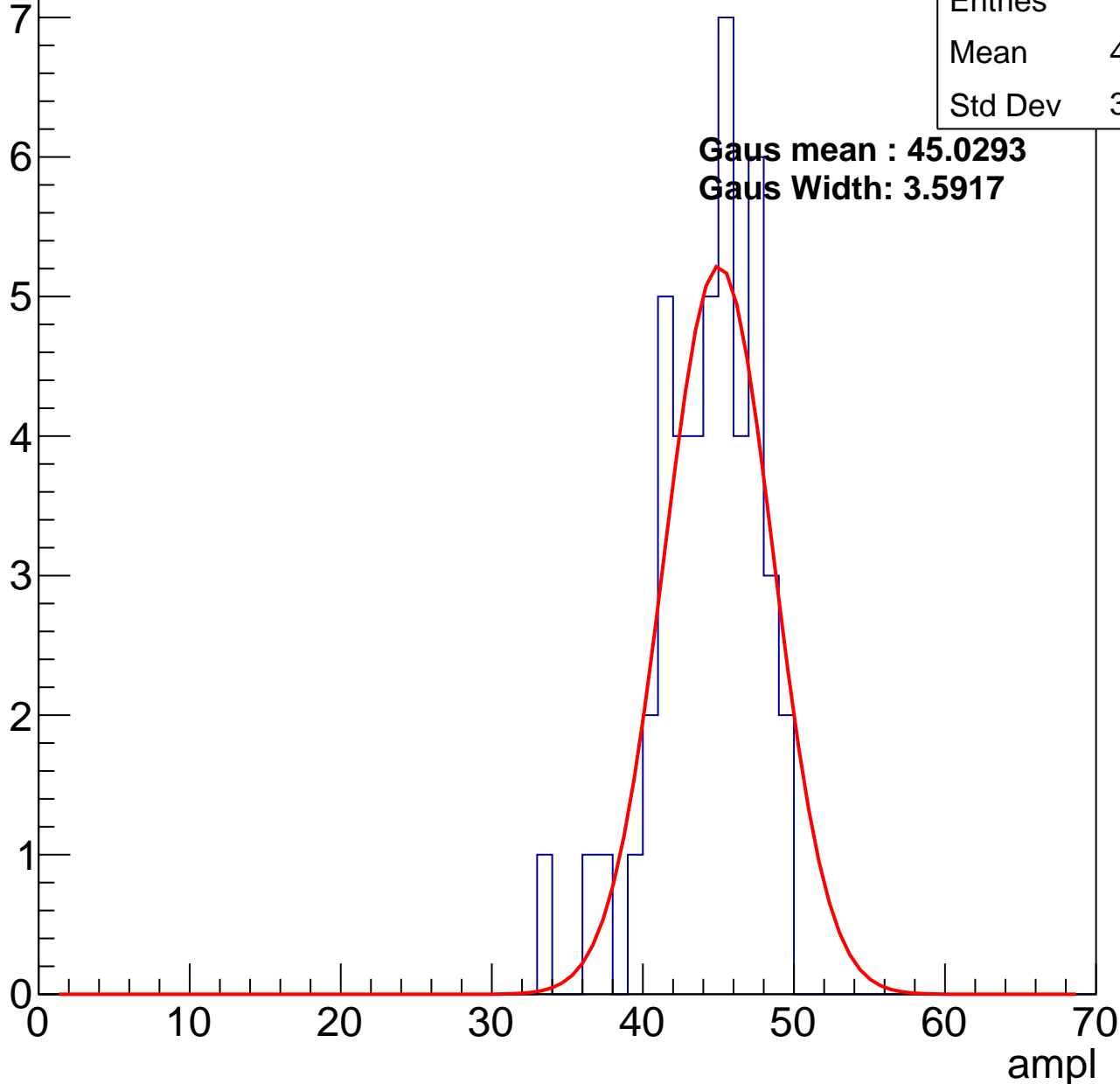
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	43.76
Std Dev	3.389

Gaus mean : 45.0293

Gaus Width: 3.5917



B1L101S, U12-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

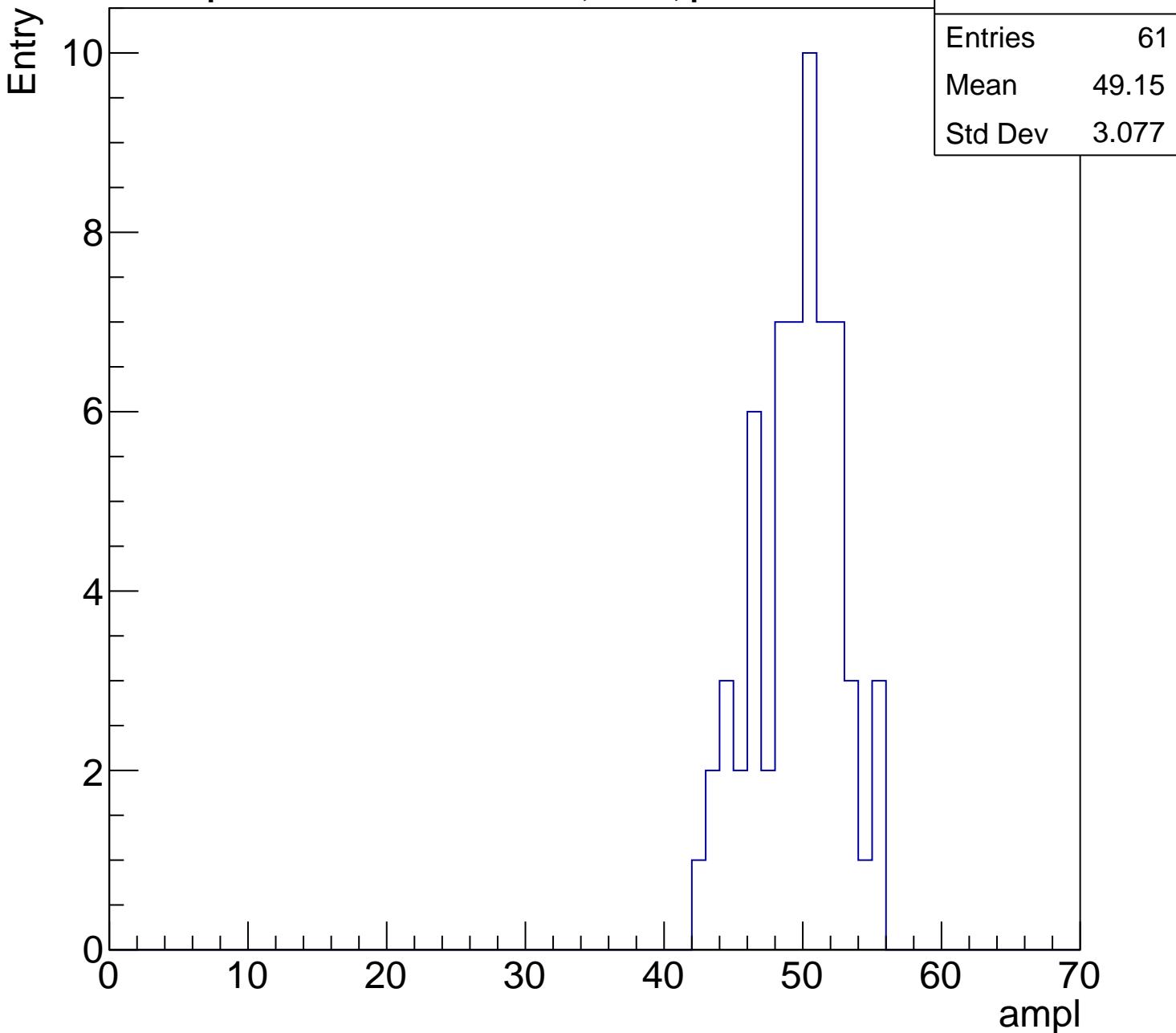
Entries	61
Mean	49.15
Std Dev	3.077

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

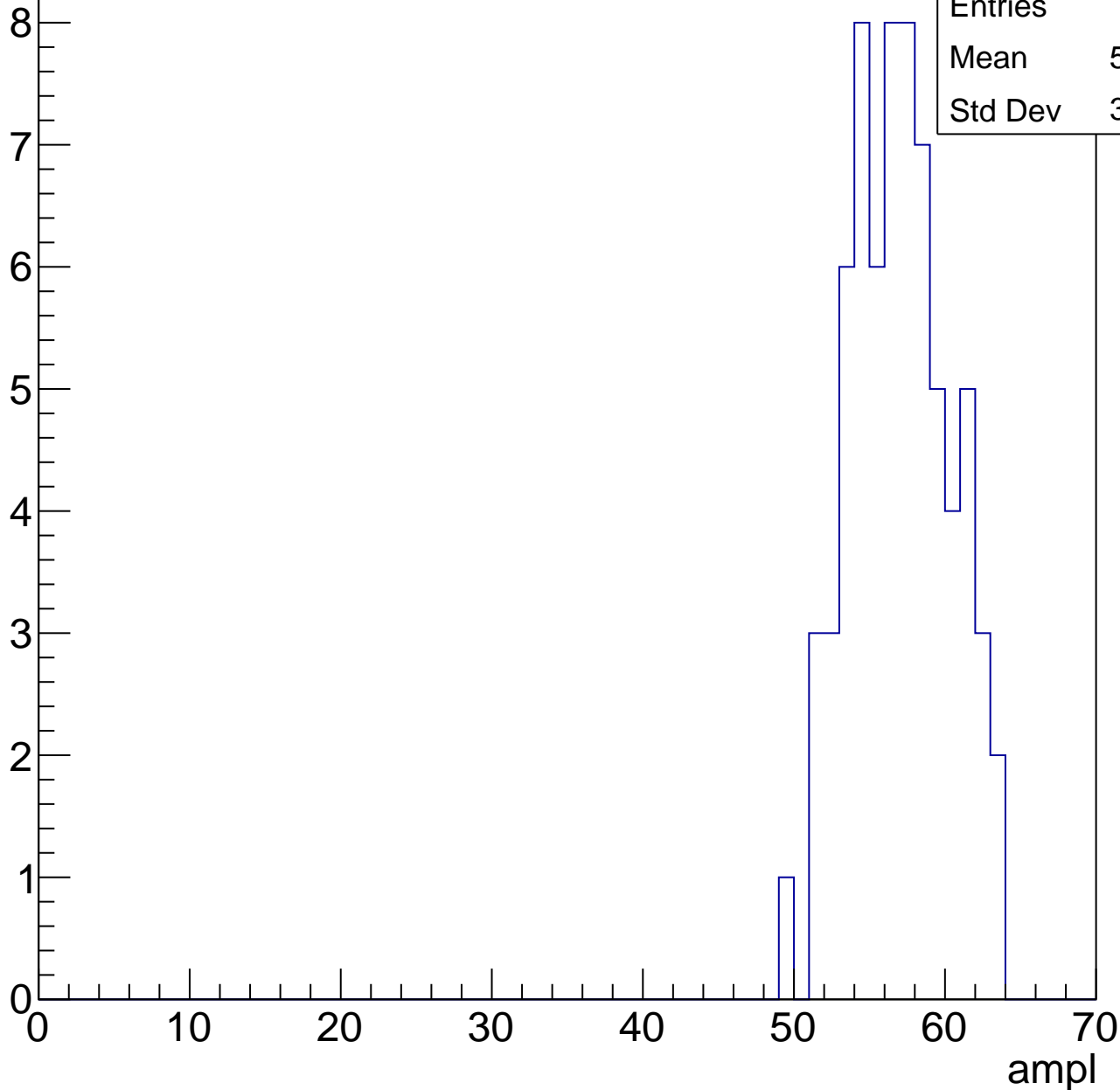


B1L101S, U12-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	56.52
Std Dev	3.233



B1L101S, U12-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	58.46
Std Dev	9.597

ampl

B1L101S, U12-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch78, adc0

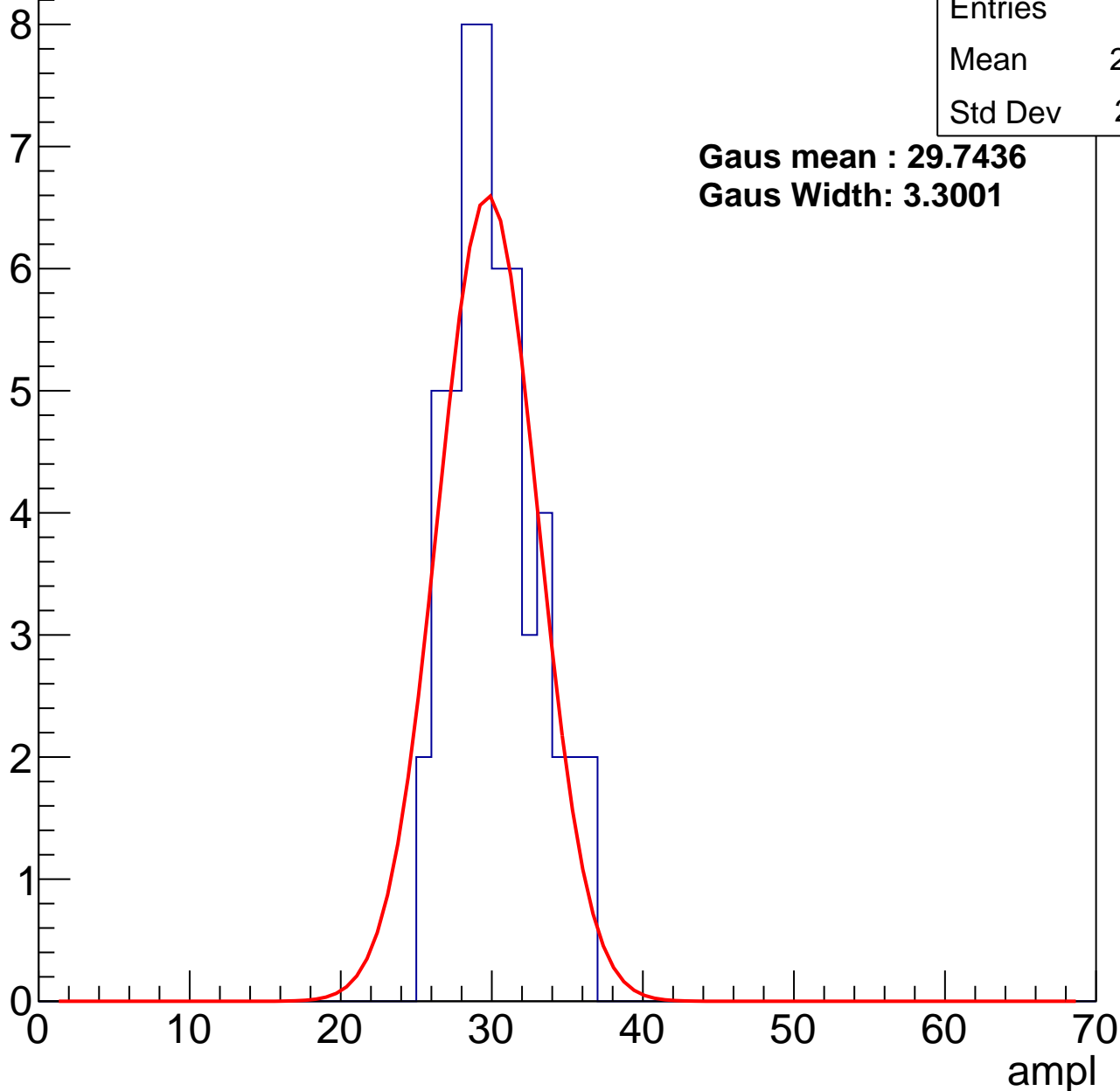
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.72
Std Dev	2.811

Gaus mean : 29.7436

Gaus Width: 3.3001



B1L101S, U12-ch78, adc1

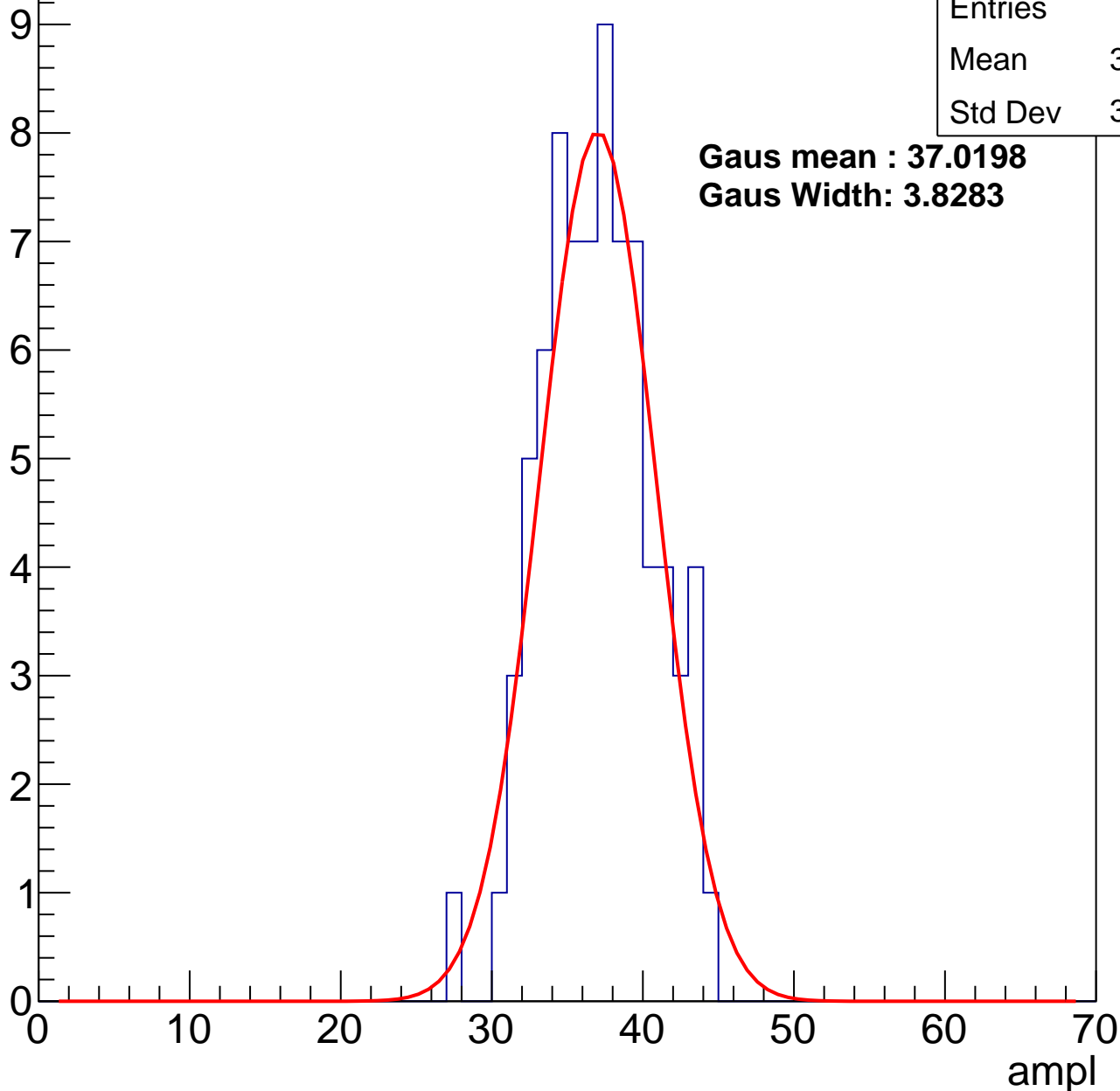
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.56
Std Dev	3.555

Gaus mean : 37.0198

Gaus Width: 3.8283



B1L101S, U12-ch78, adc2

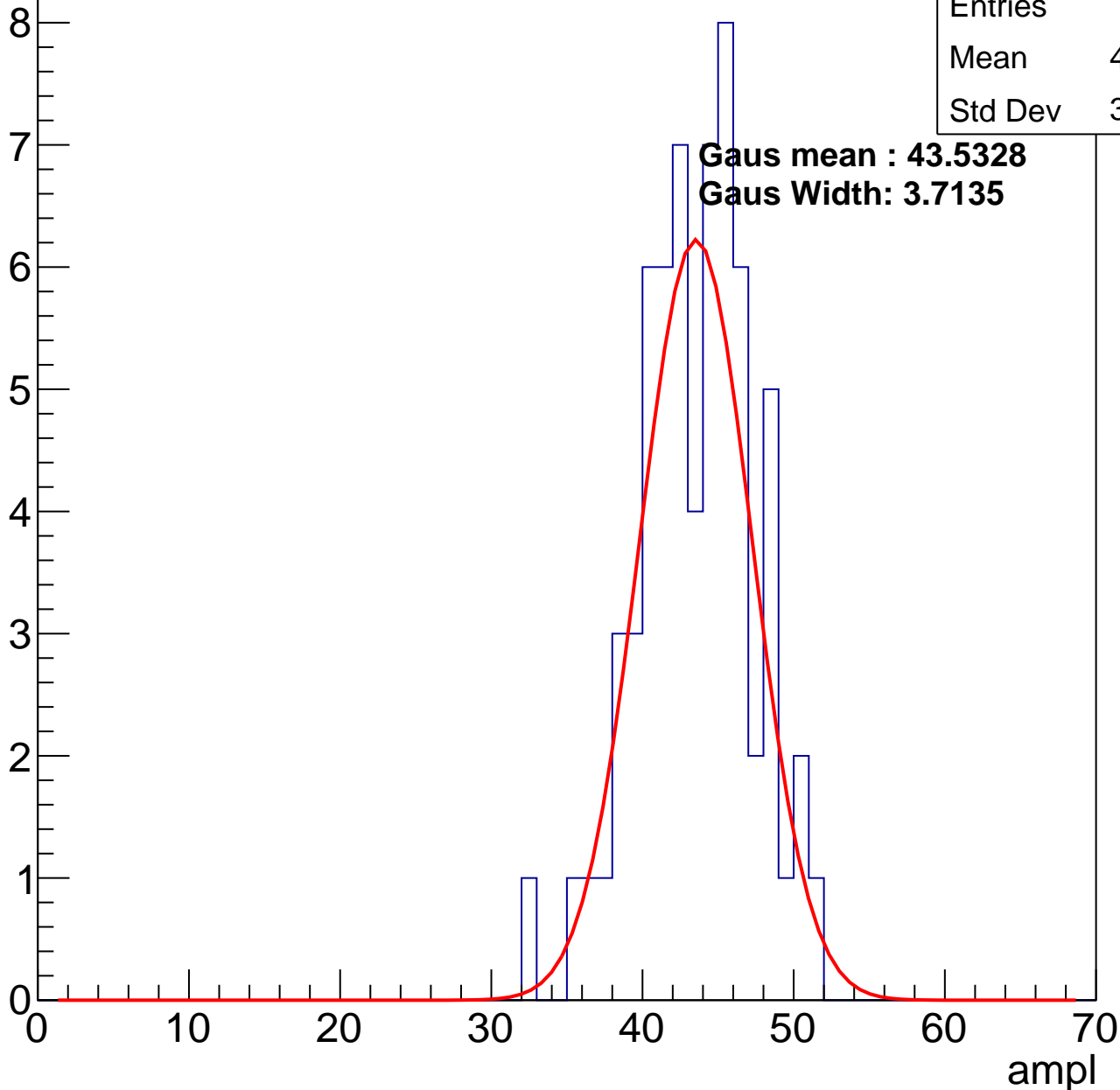
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.09
Std Dev	3.773

Gaus mean : 43.5328

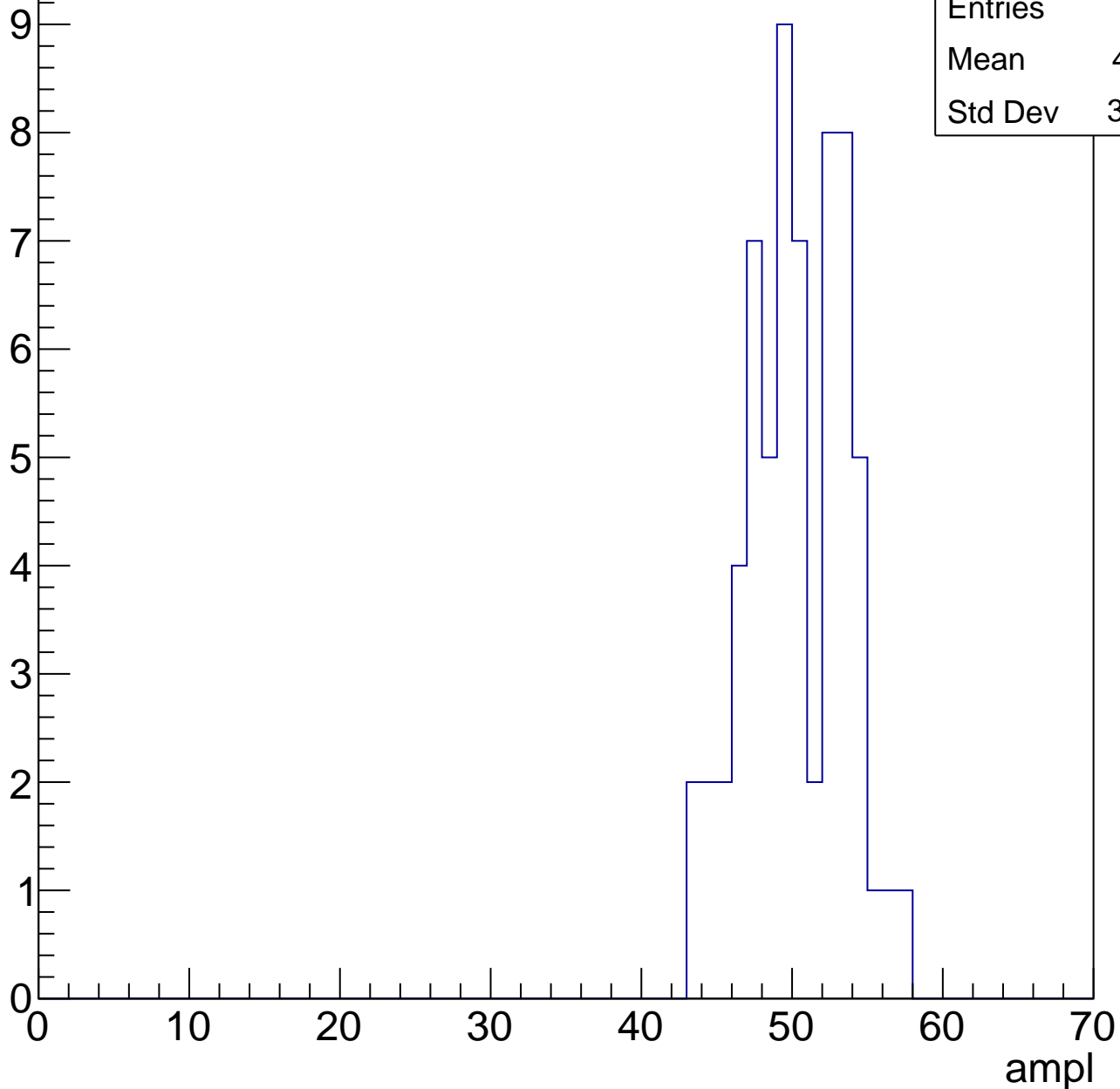
Gaus Width: 3.7135



B1L101S, U12-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

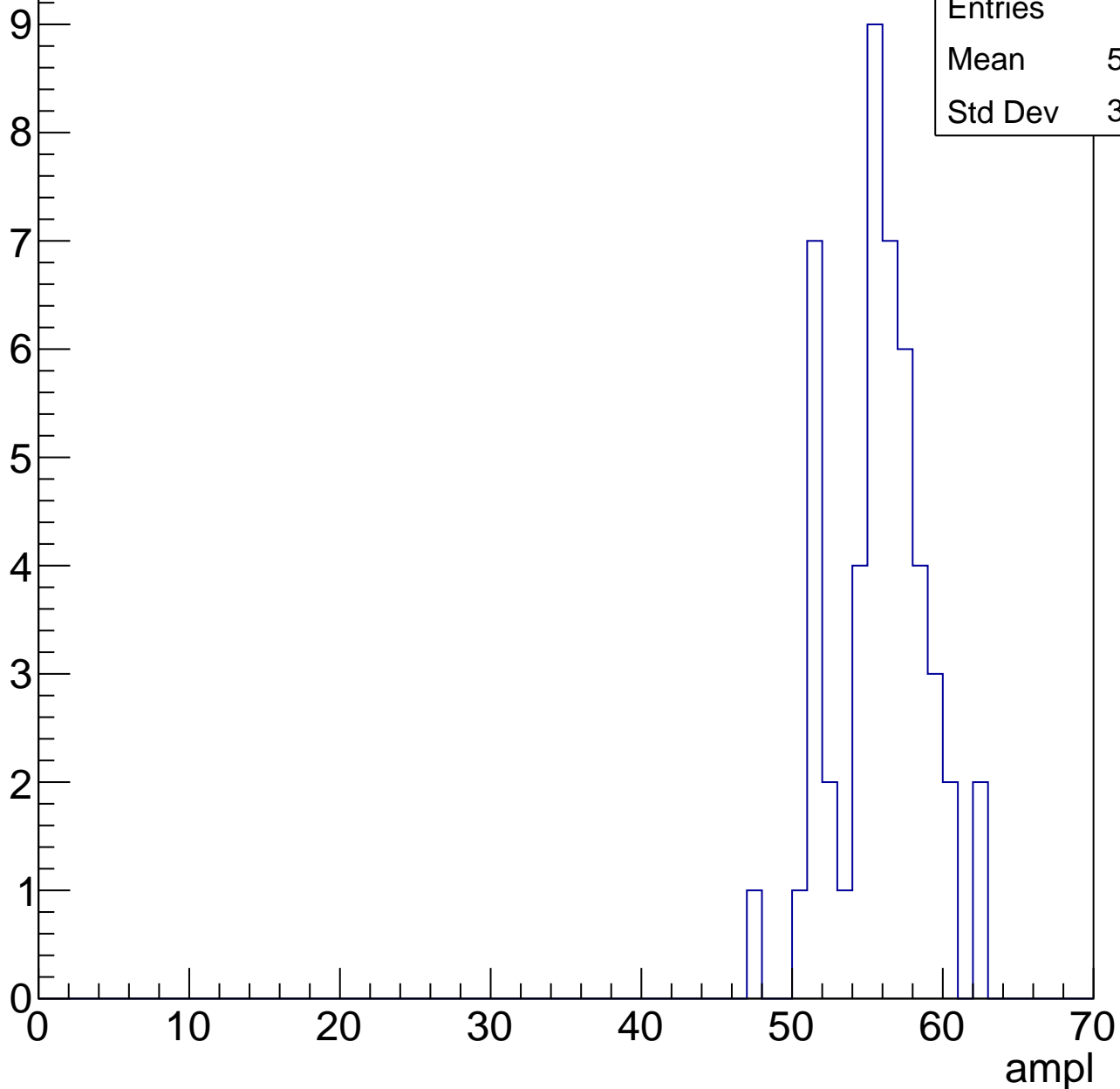


Entries	64
Mean	49.81
Std Dev	3.235

B1L101S, U12-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	49
Mean	55.29
Std Dev	3.149

B1L101S, U12-ch78, adc5

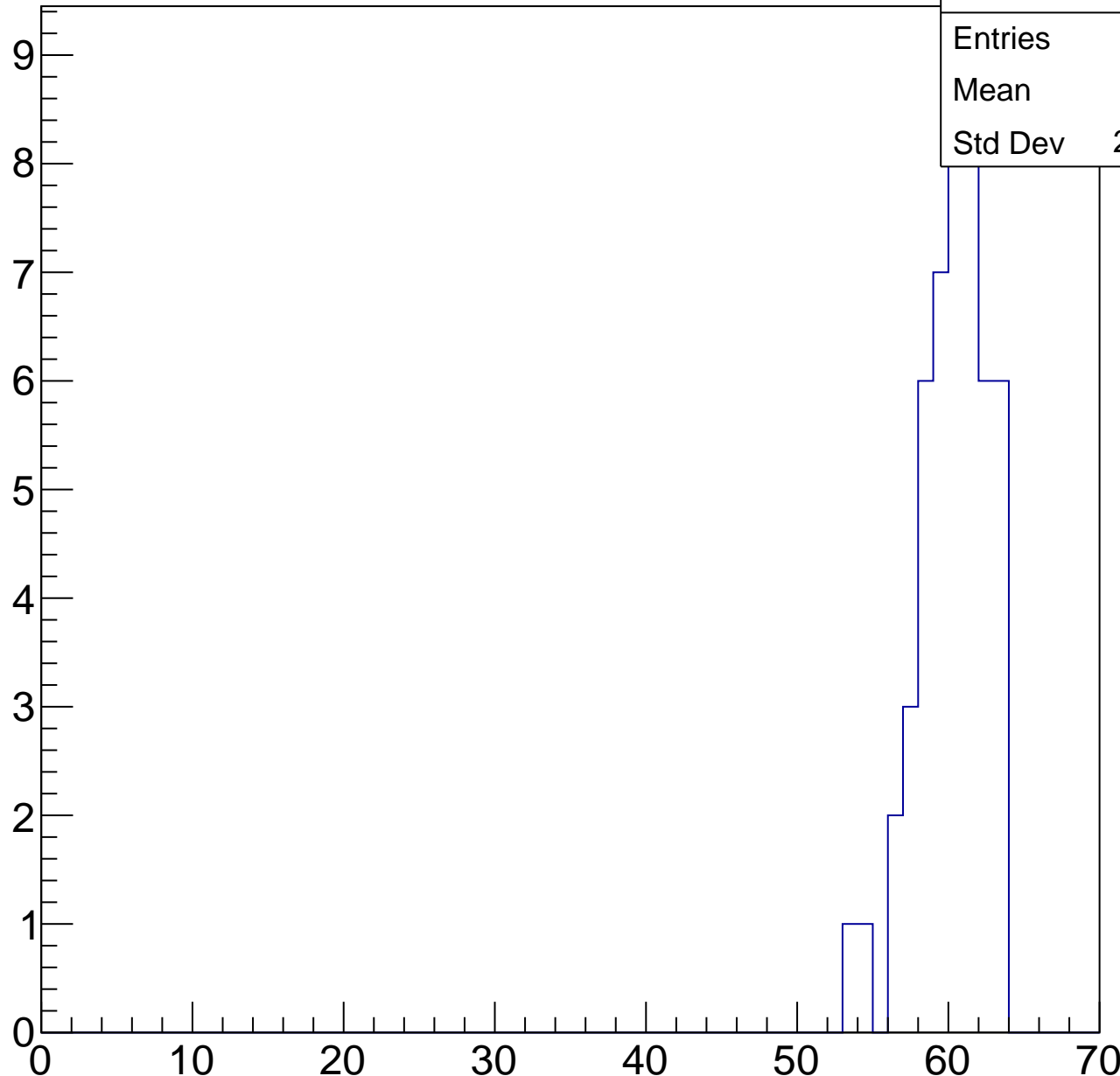
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.8
Std Dev	2.303

ampl



B1L101S, U12-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

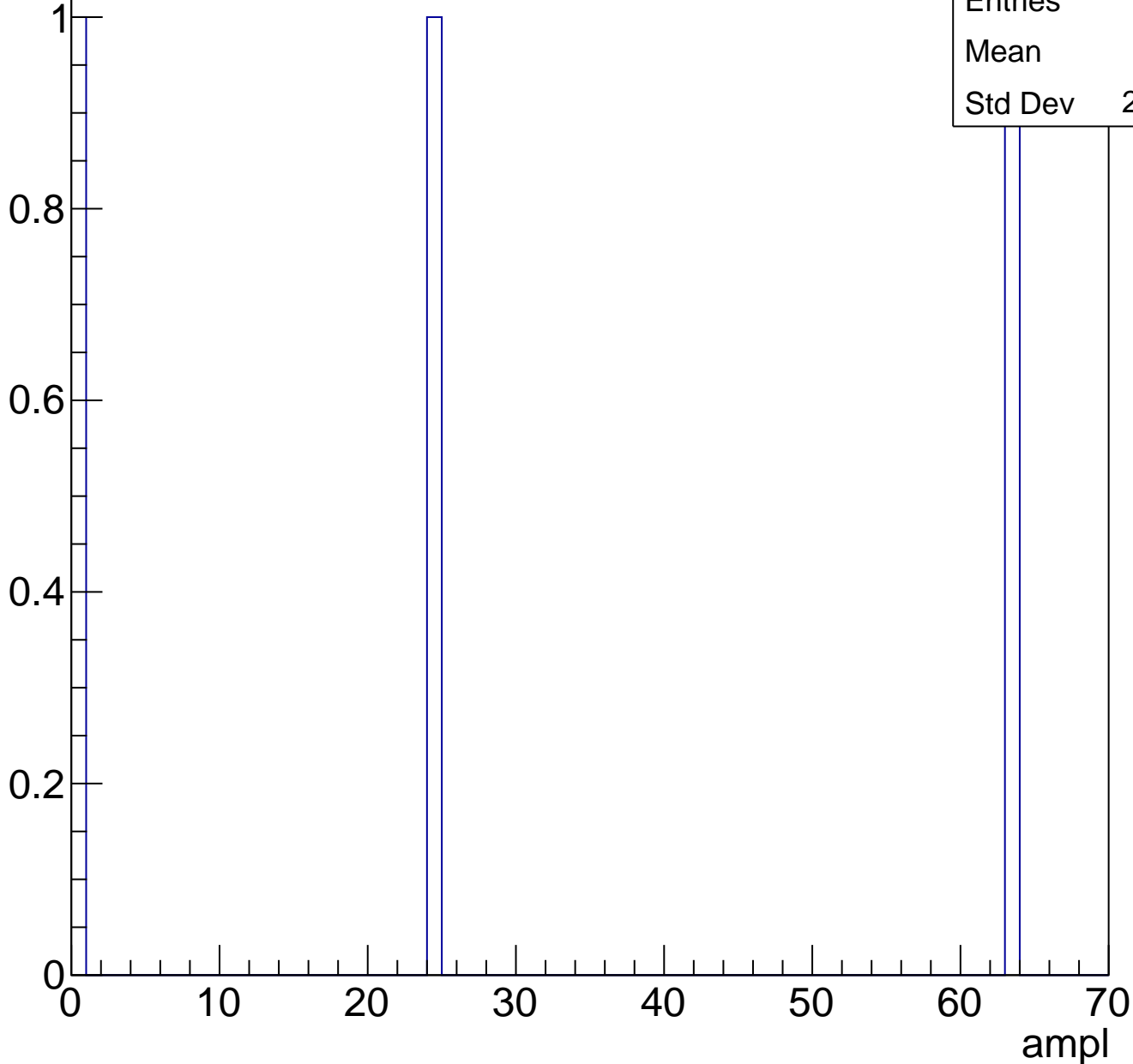
Entry



B1L101S, U12-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch79, adc0

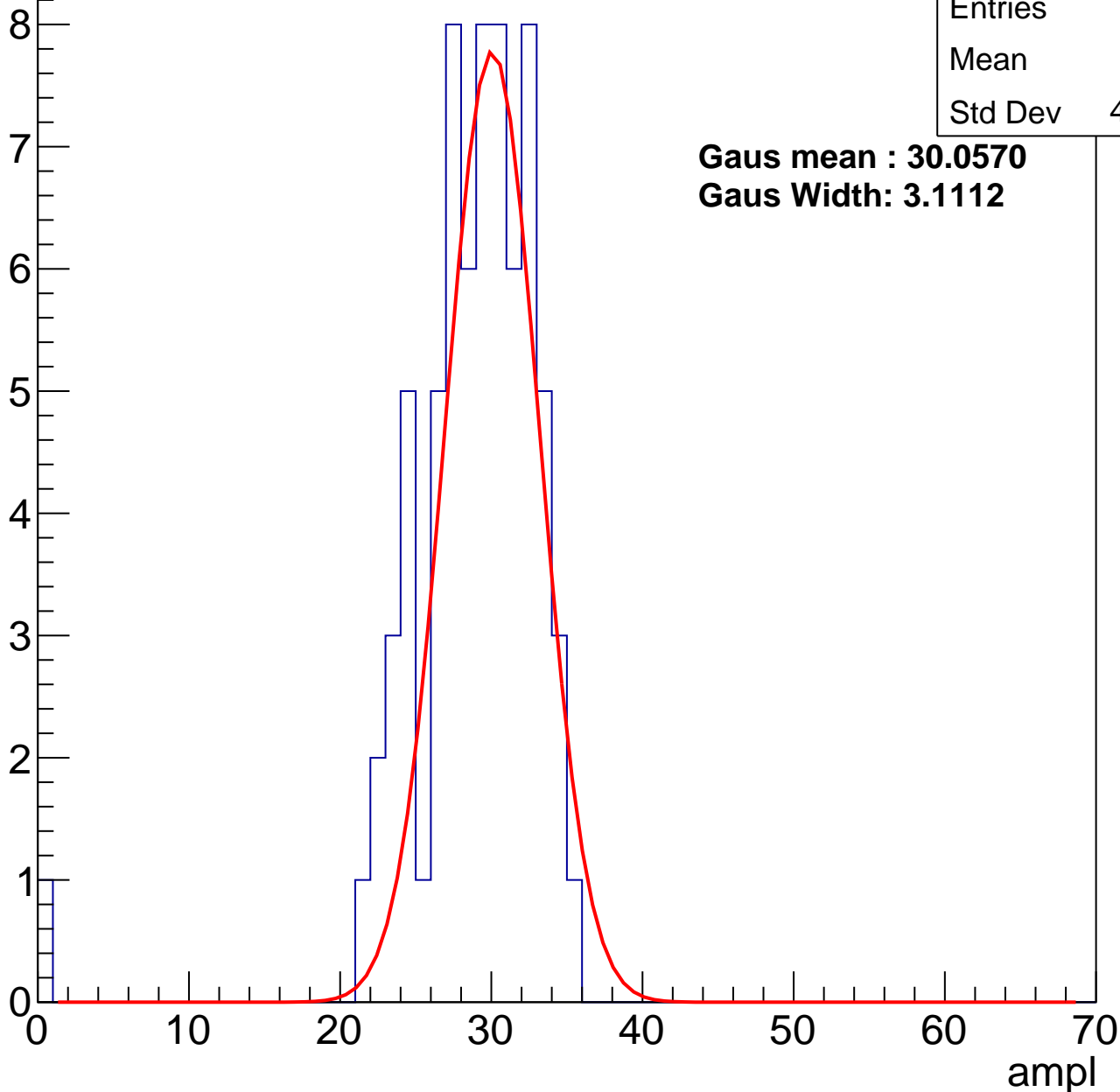
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.3
Std Dev	4.739

Gaus mean : 30.0570

Gaus Width: 3.1112



B1L101S, U12-ch79, adc1

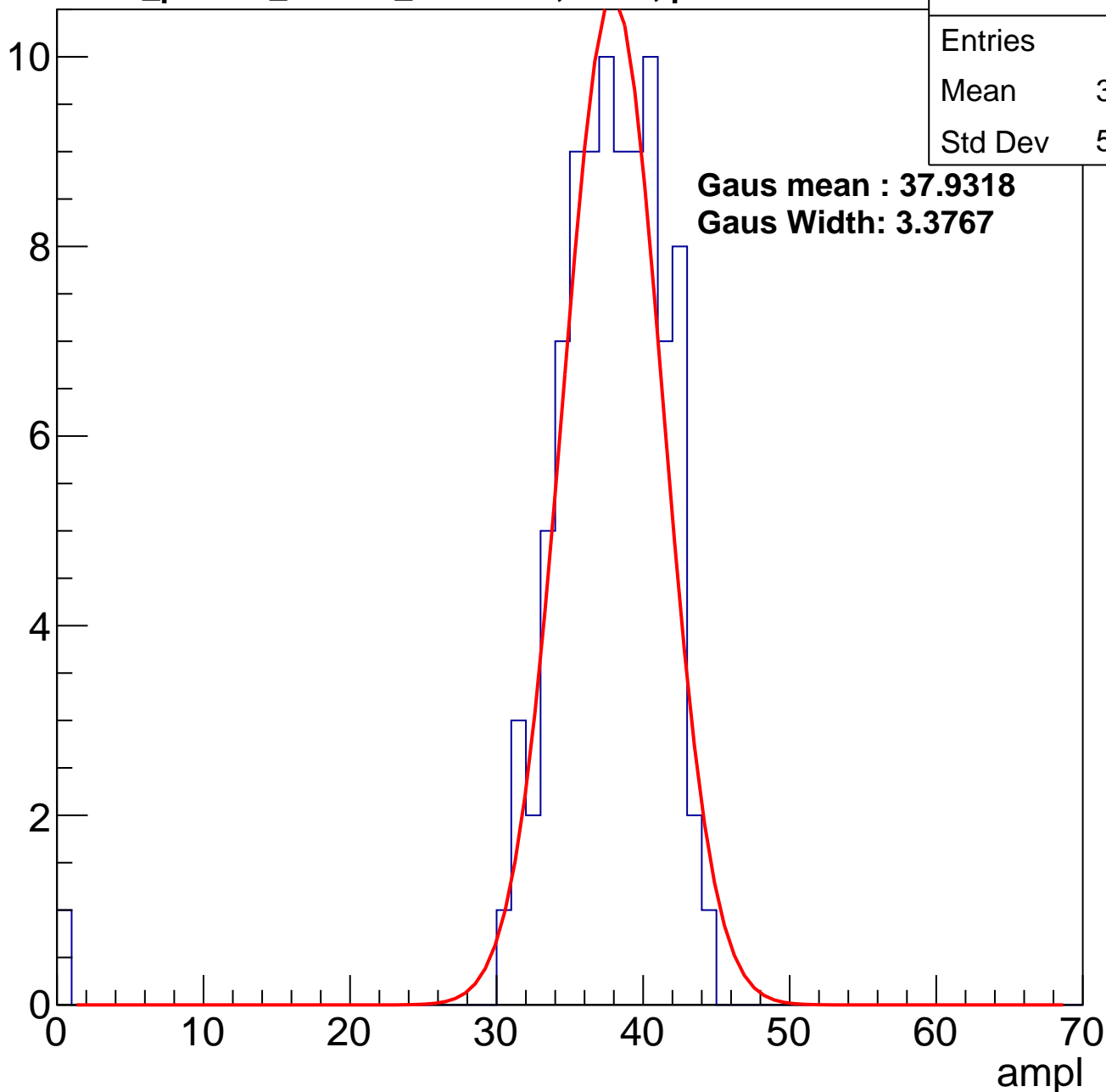
calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	37.04
Std Dev	5.007

Gaus mean : 37.9318

Gaus Width: 3.3767

Entry



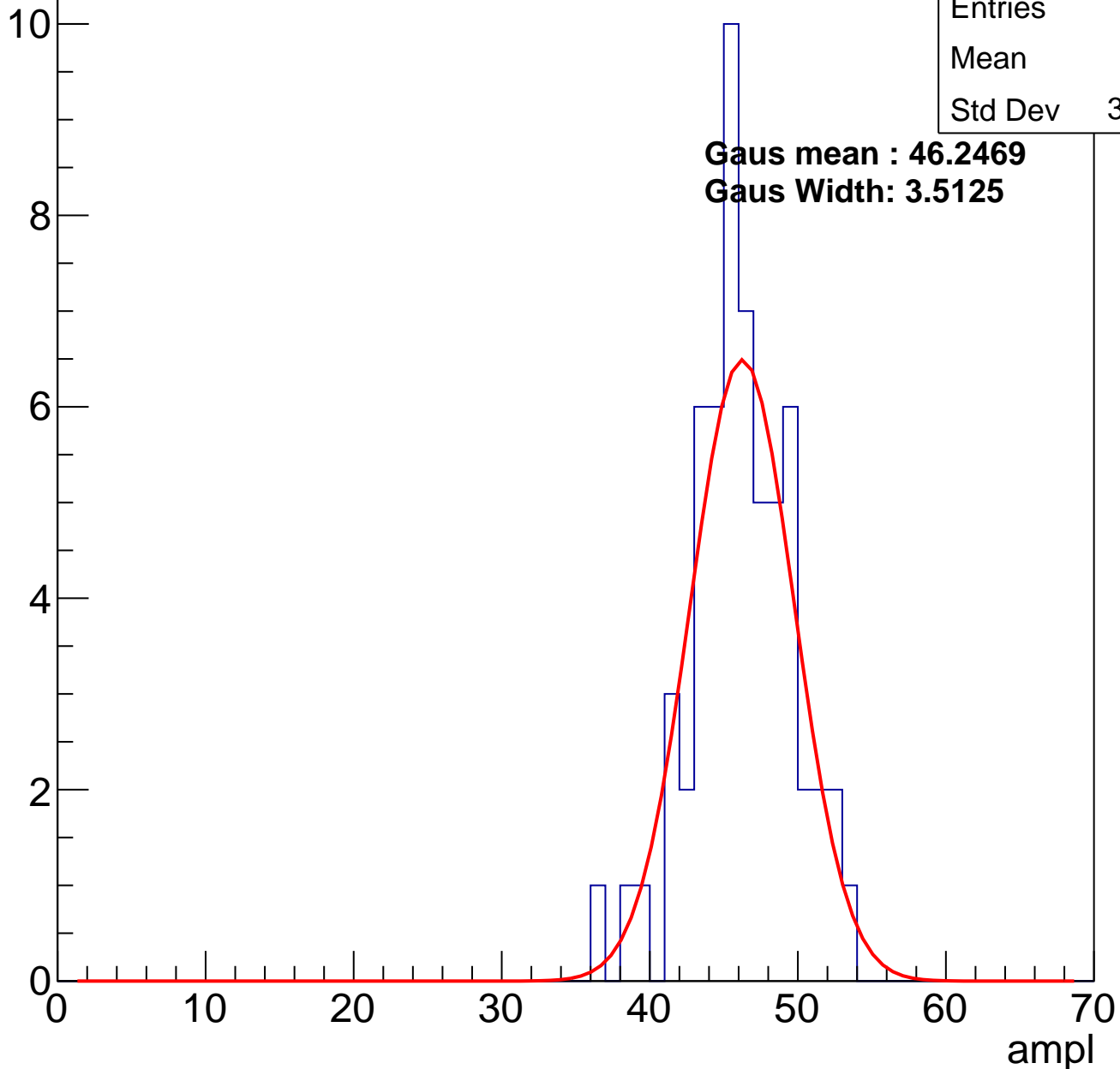
B1L101S, U12-ch79, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	45.7
Std Dev	3.378

Gaus mean : 46.2469
Gaus Width: 3.5125

Entry

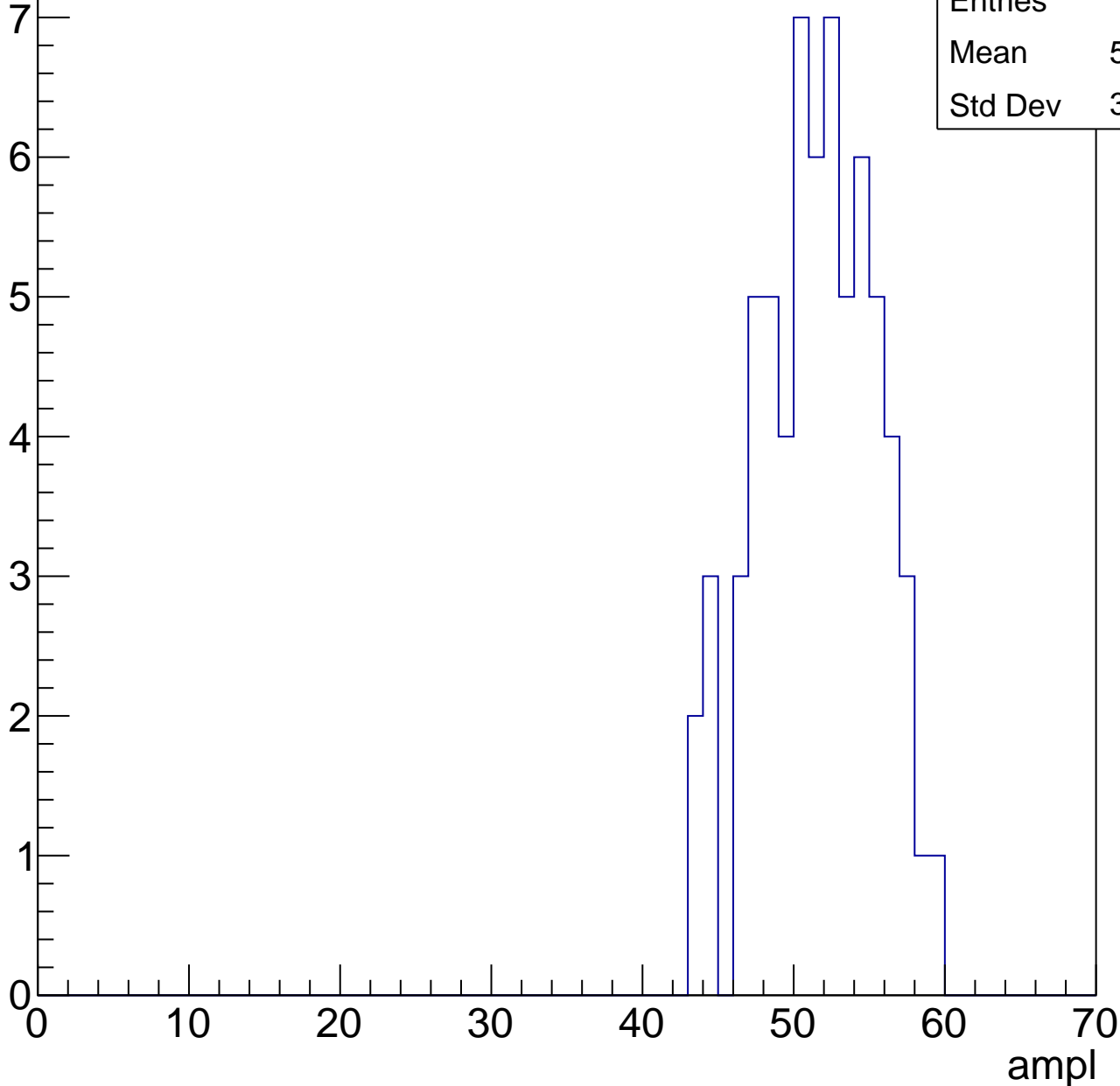


B1L101S, U12-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

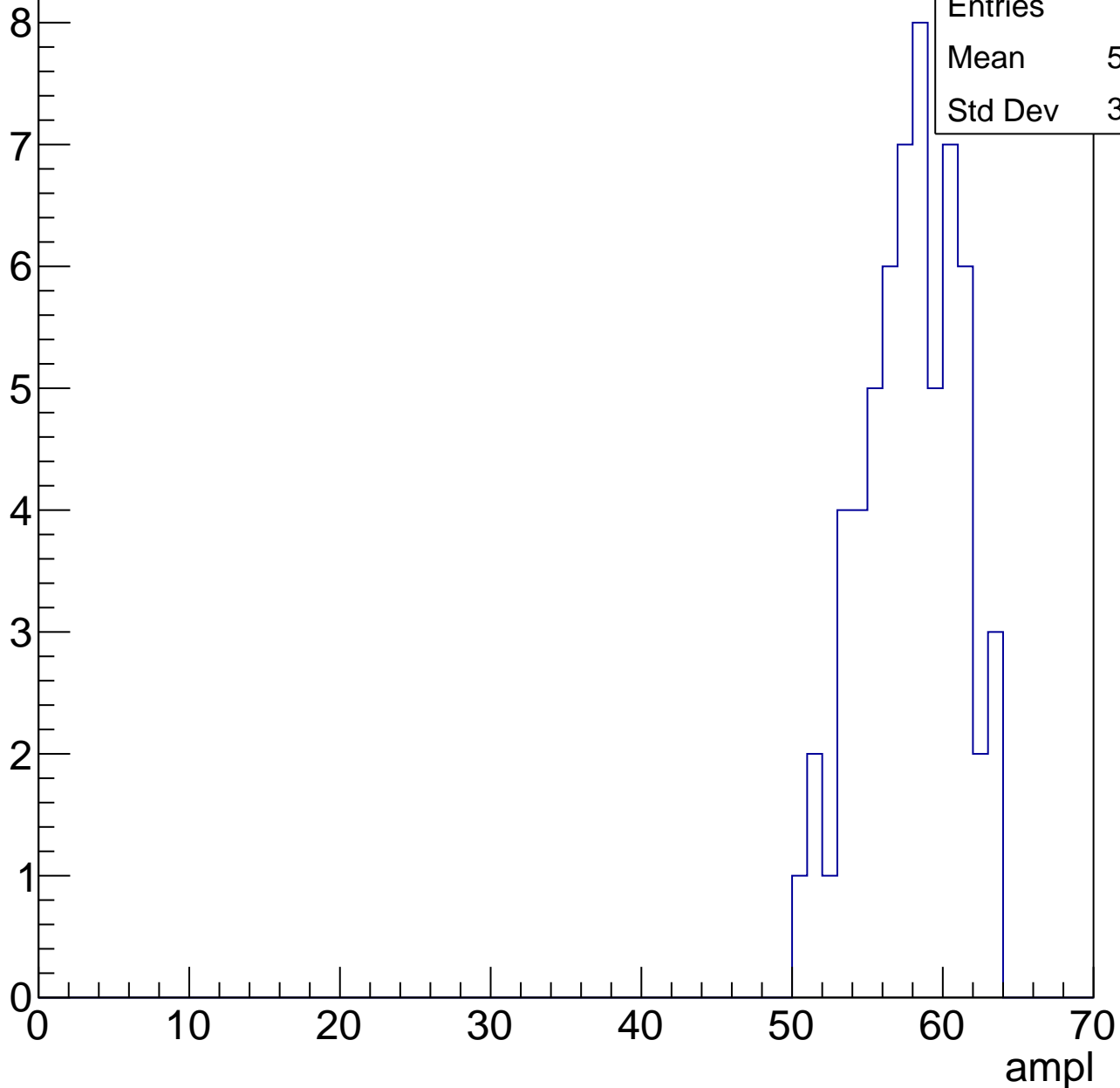
Entries	67
Mean	51.09
Std Dev	3.823



B1L101S, U12-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

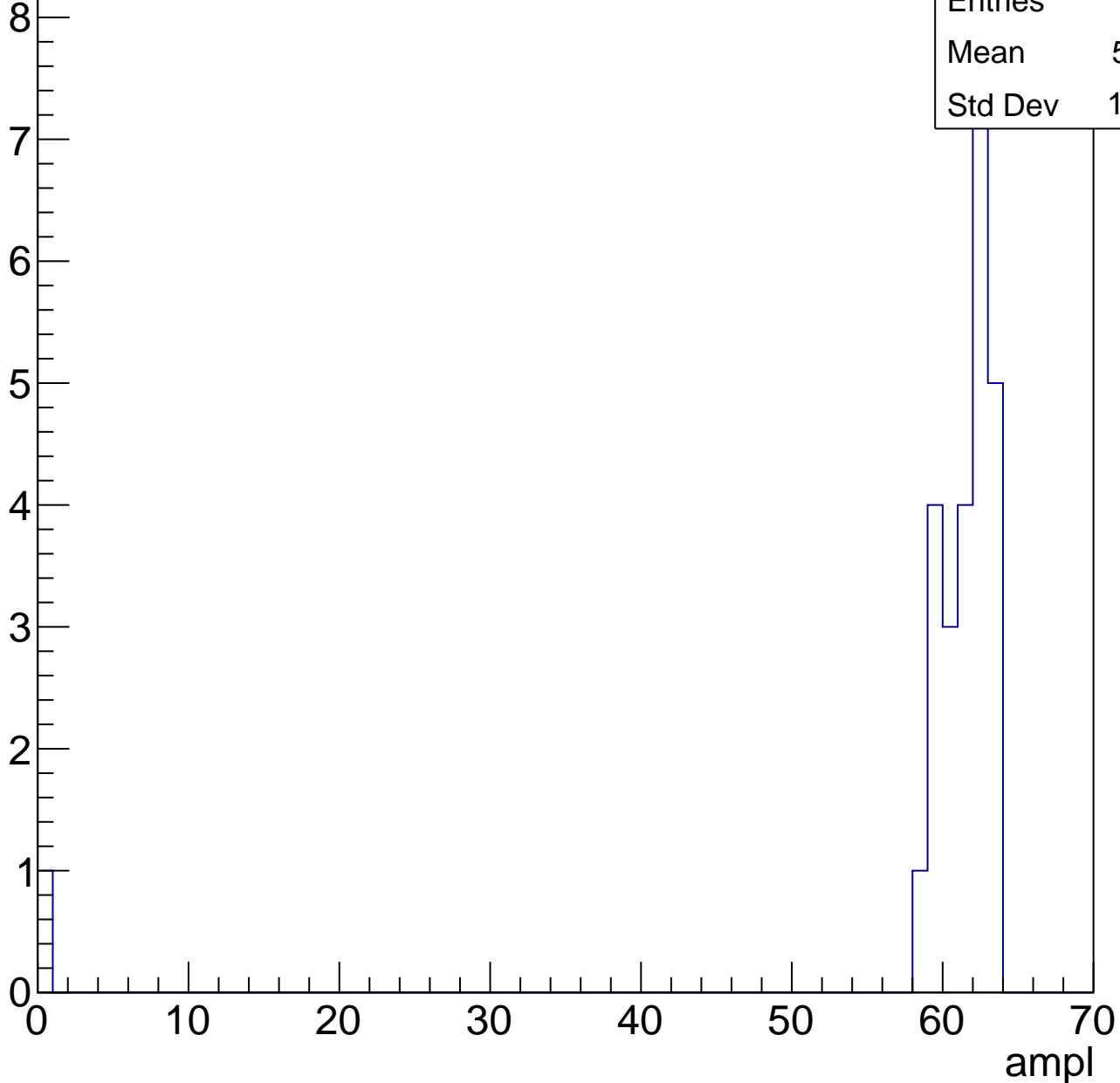


B1L101S, U12-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.81
Std Dev	11.85



B1L101S, U12-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

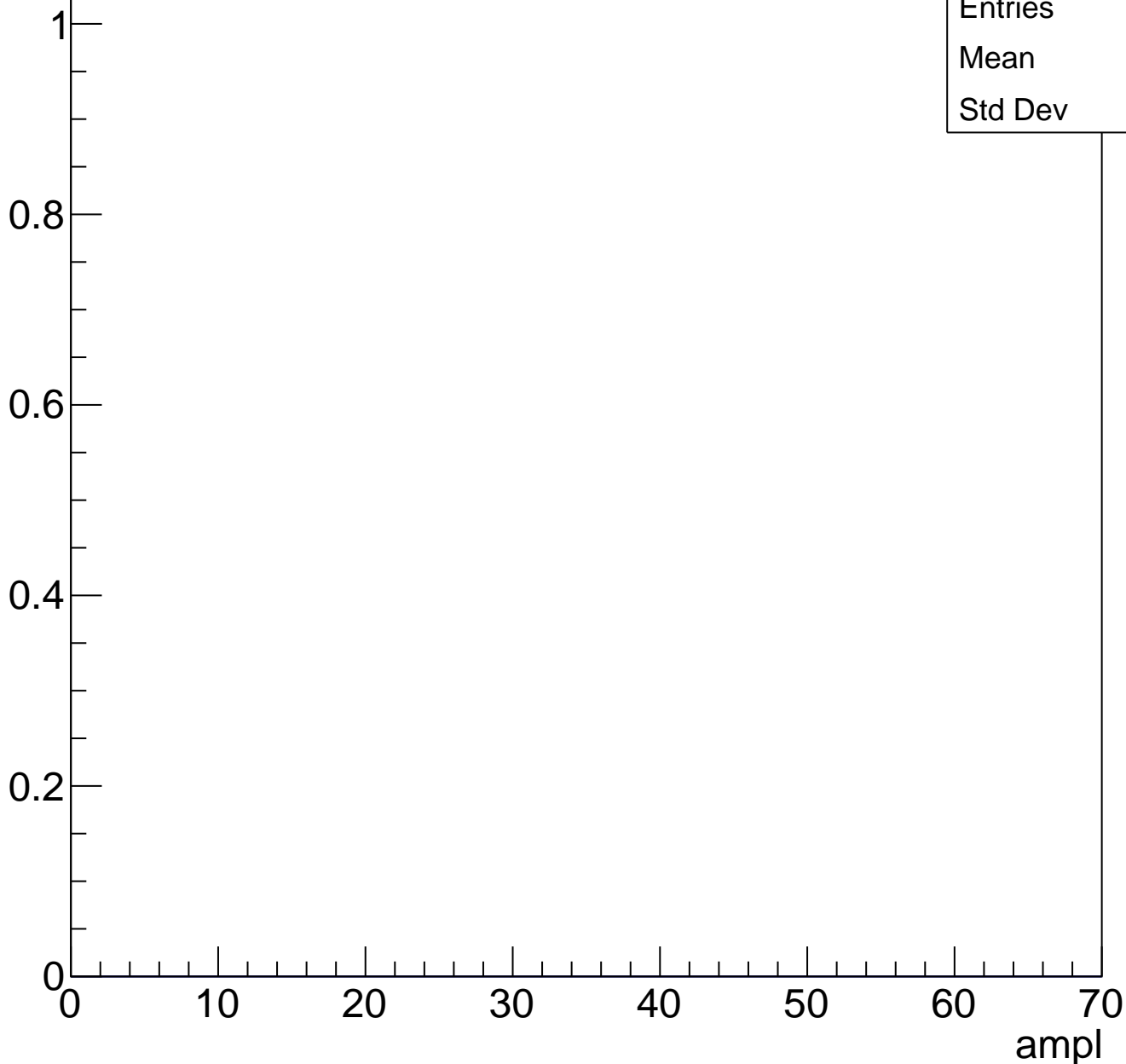
0 10 20 30 40 50 60 70

ampl

B1L101S, U12-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch80, adc0

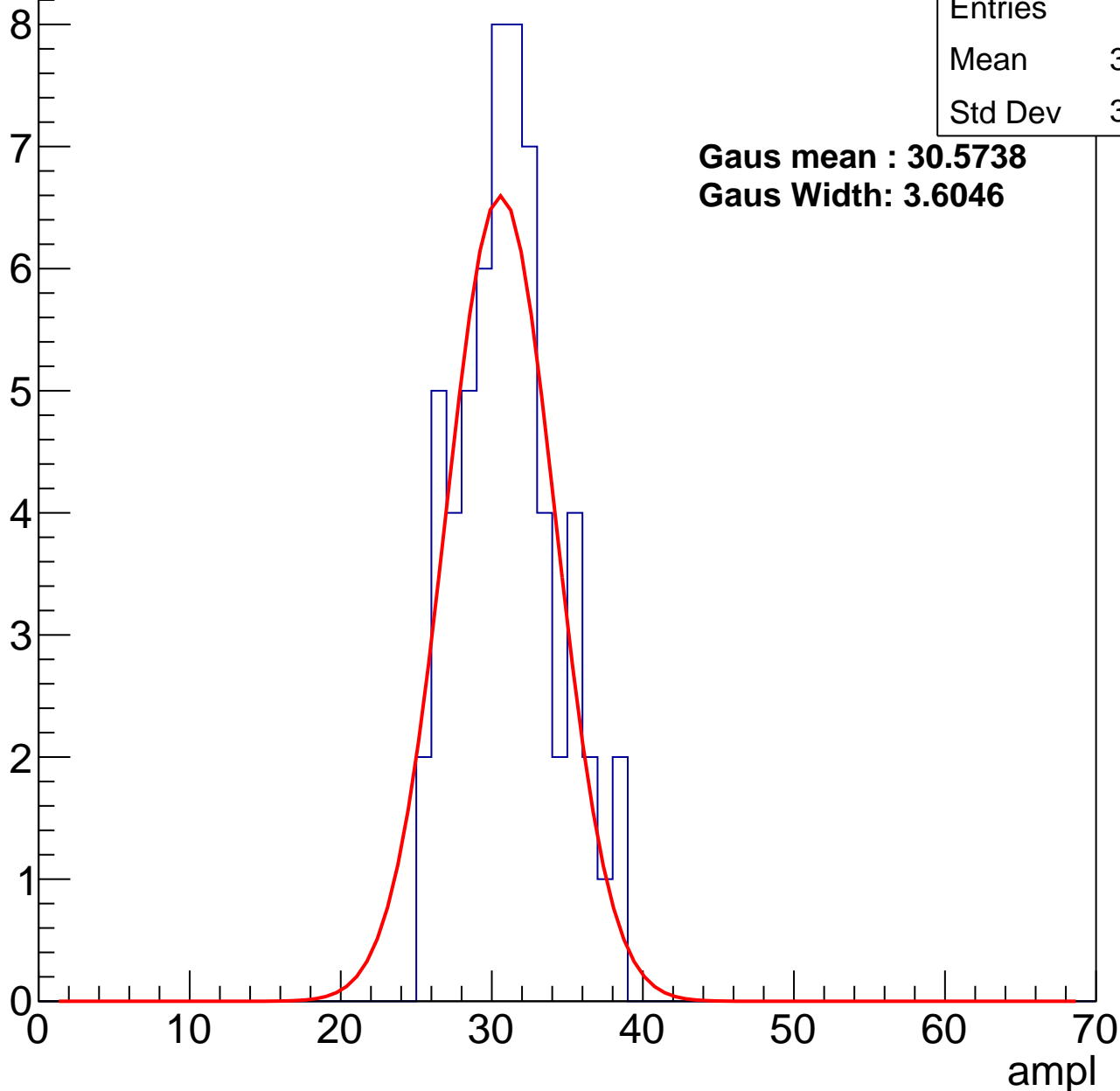
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.65
Std Dev	3.214

Gaus mean : 30.5738

Gaus Width: 3.6046



B1L101S, U12-ch80, adc1

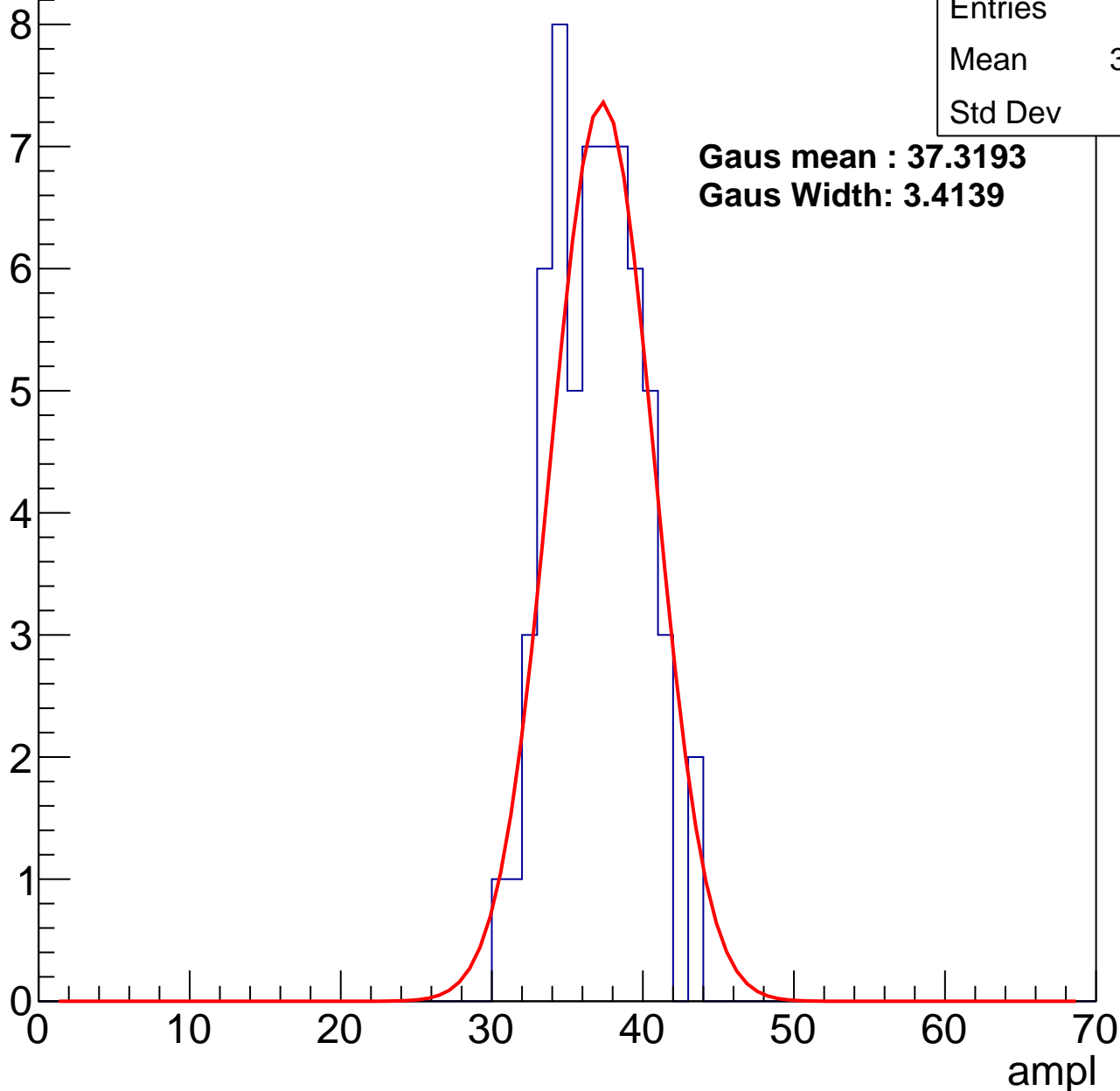
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.43
Std Dev	2.95

Gaus mean : 37.3193

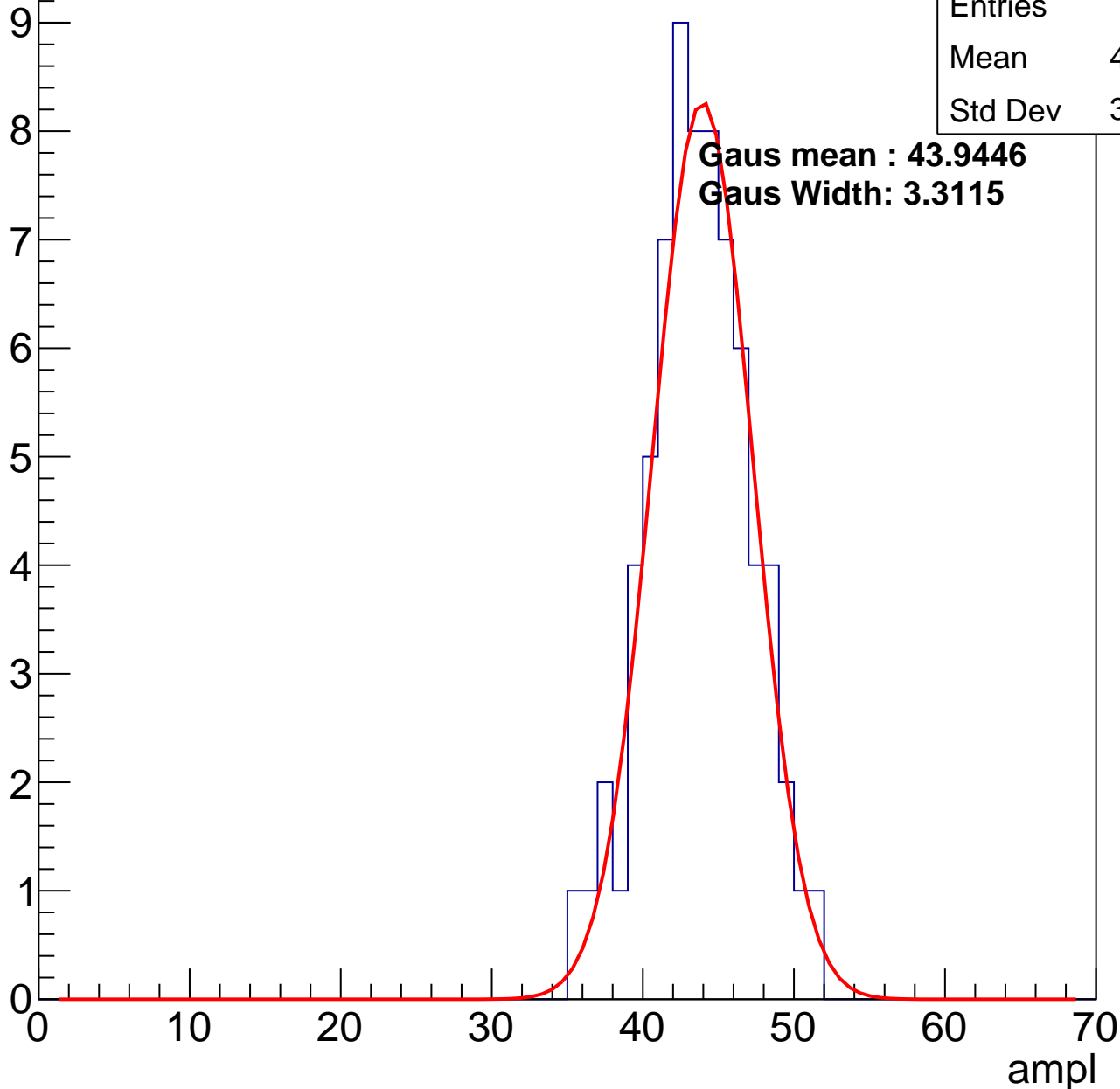
Gaus Width: 3.4139



B1L101S, U12-ch80, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry



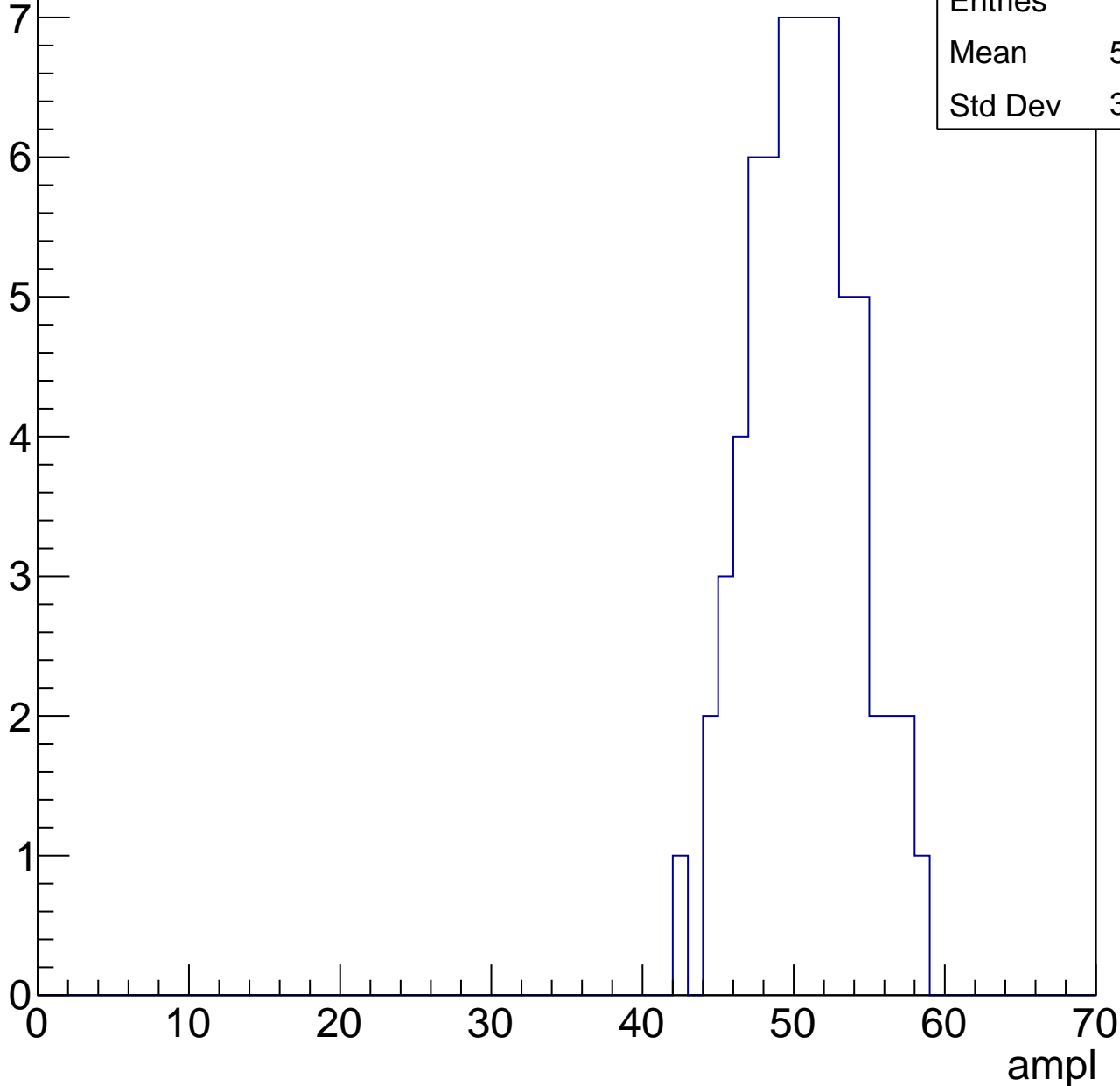
Entries	71
Mean	43.24
Std Dev	3.338

B1L101S, U12-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.18
Std Dev	3.459

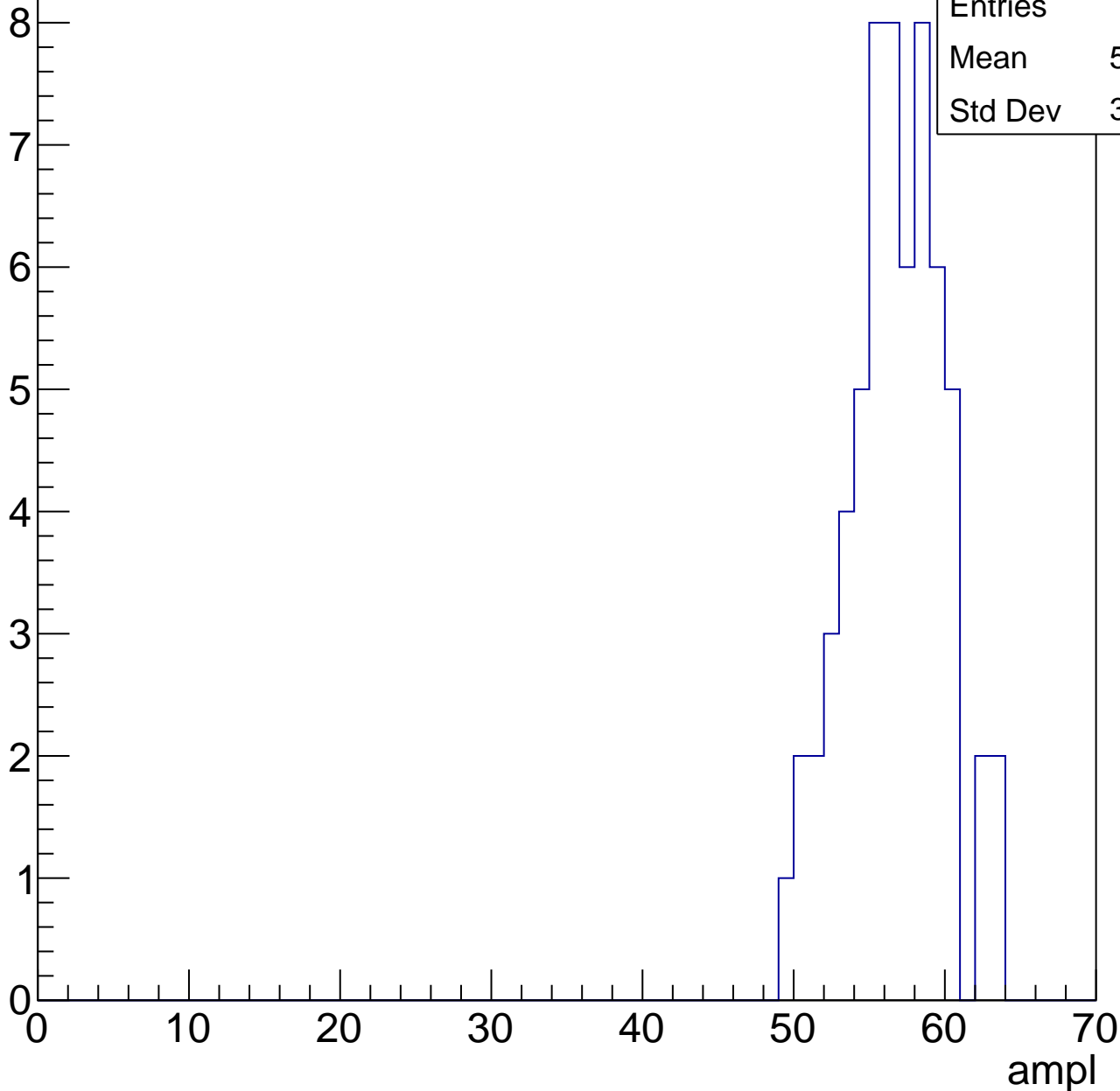


B1L101S, U12-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.24
Std Dev	3.166

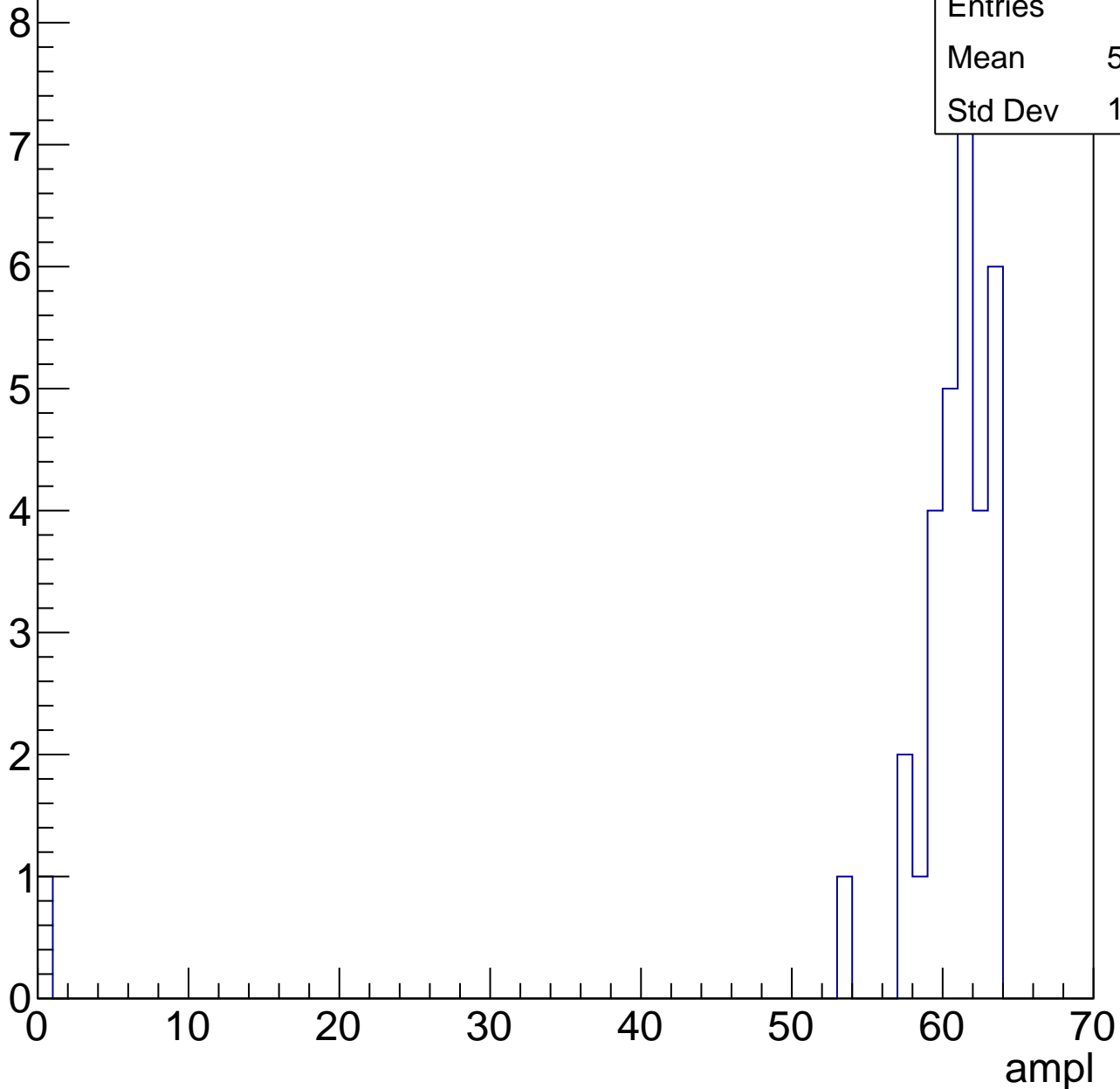


B1L101S, U12-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

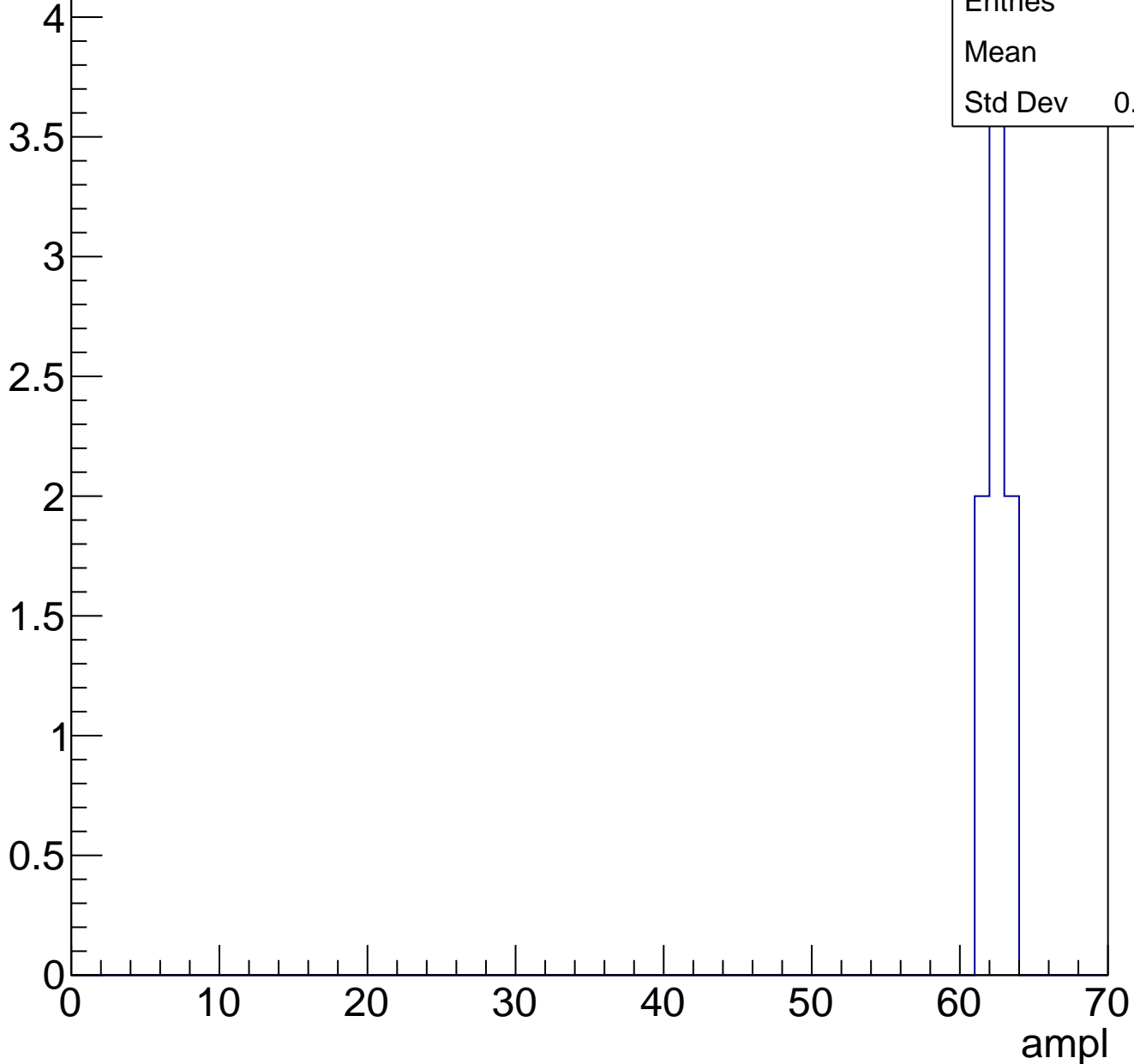
Entries	32
Mean	58.59
Std Dev	10.74



B1L101S, U12-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch81, adc0

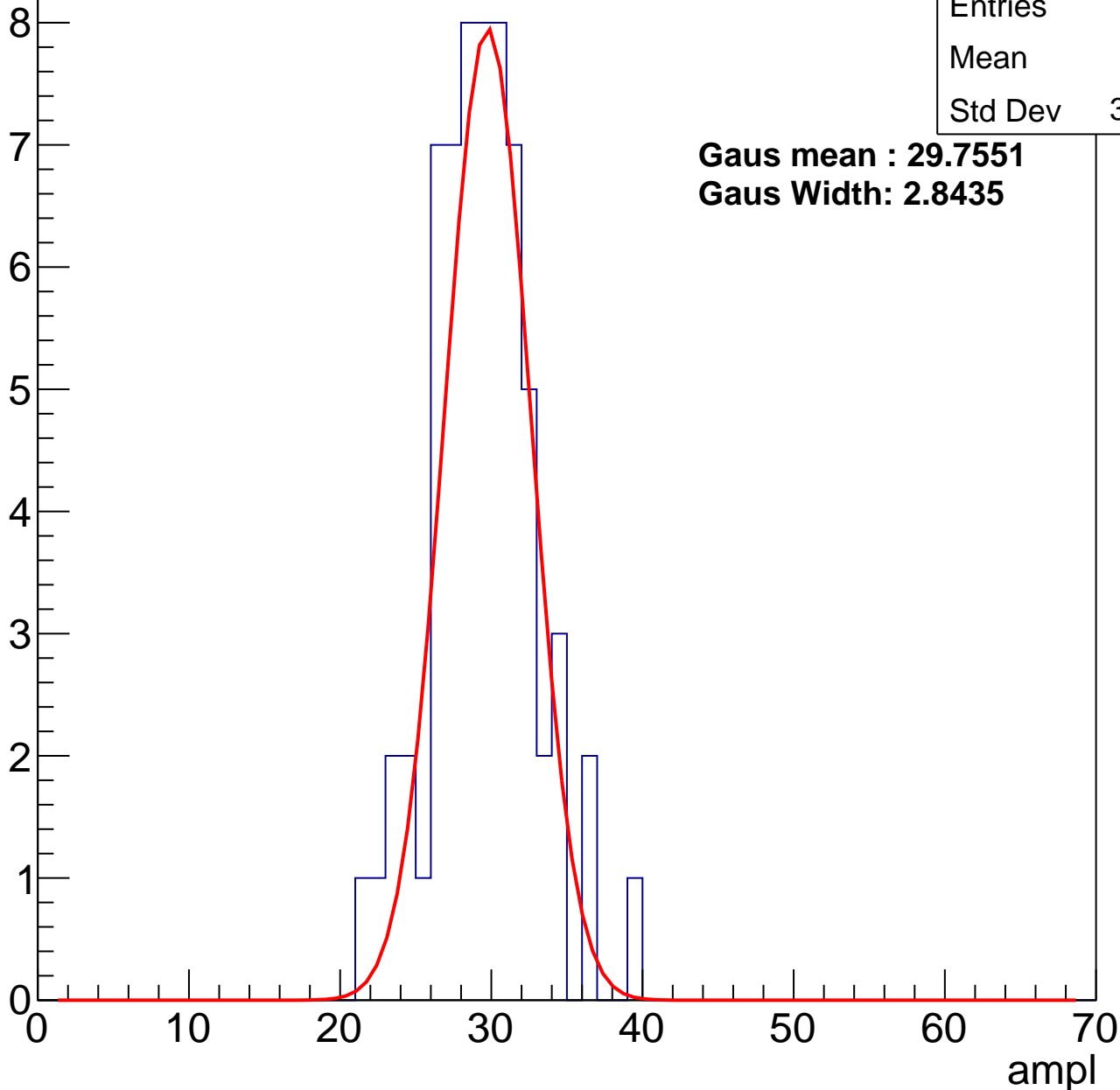
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29
Std Dev	3.365

Gaus mean : 29.7551

Gaus Width: 2.8435



B1L101S, U12-ch81, adc1

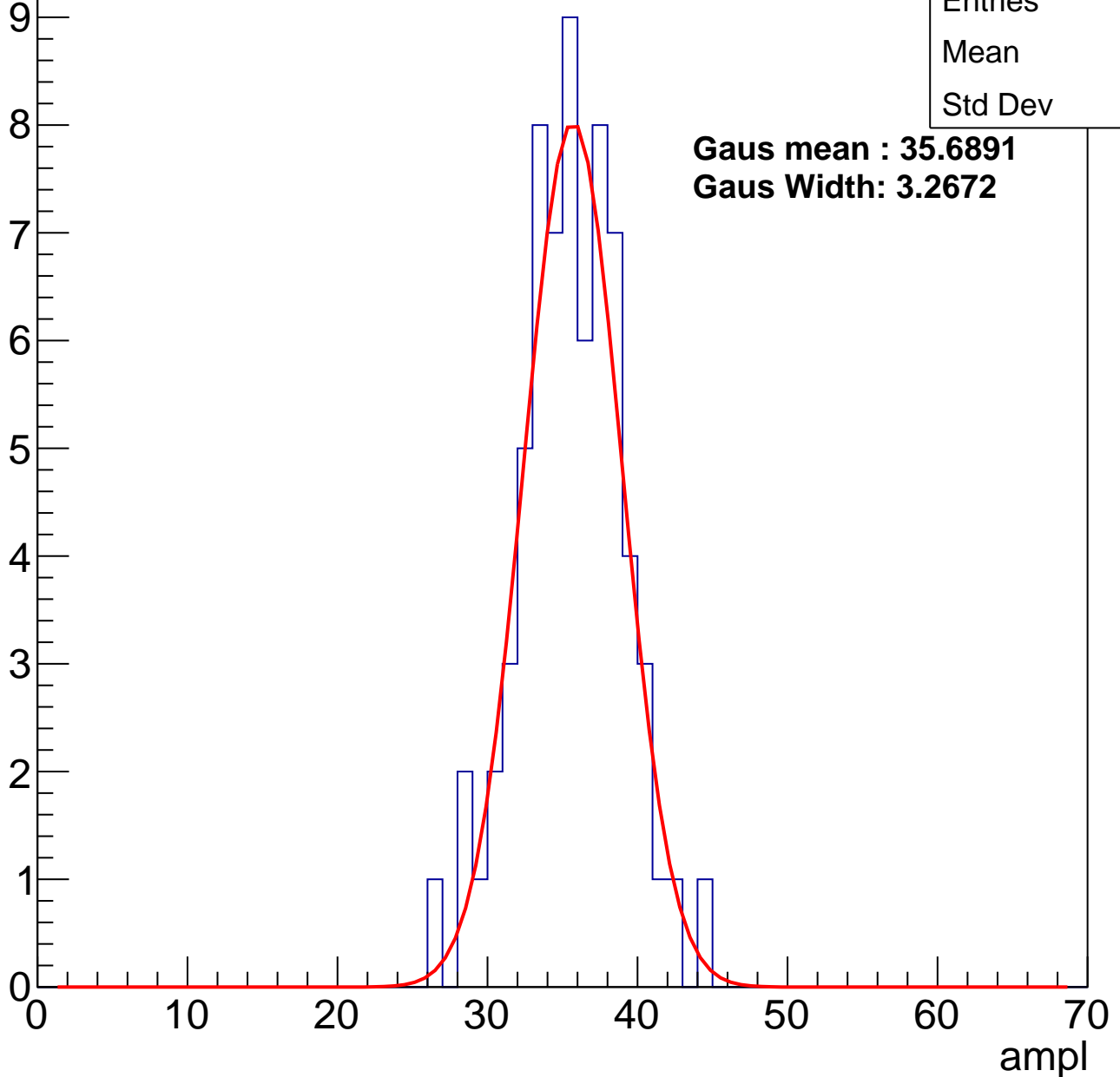
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.1
Std Dev	3.41

Gaus mean : 35.6891

Gaus Width: 3.2672



B1L101S, U12-ch81, adc2

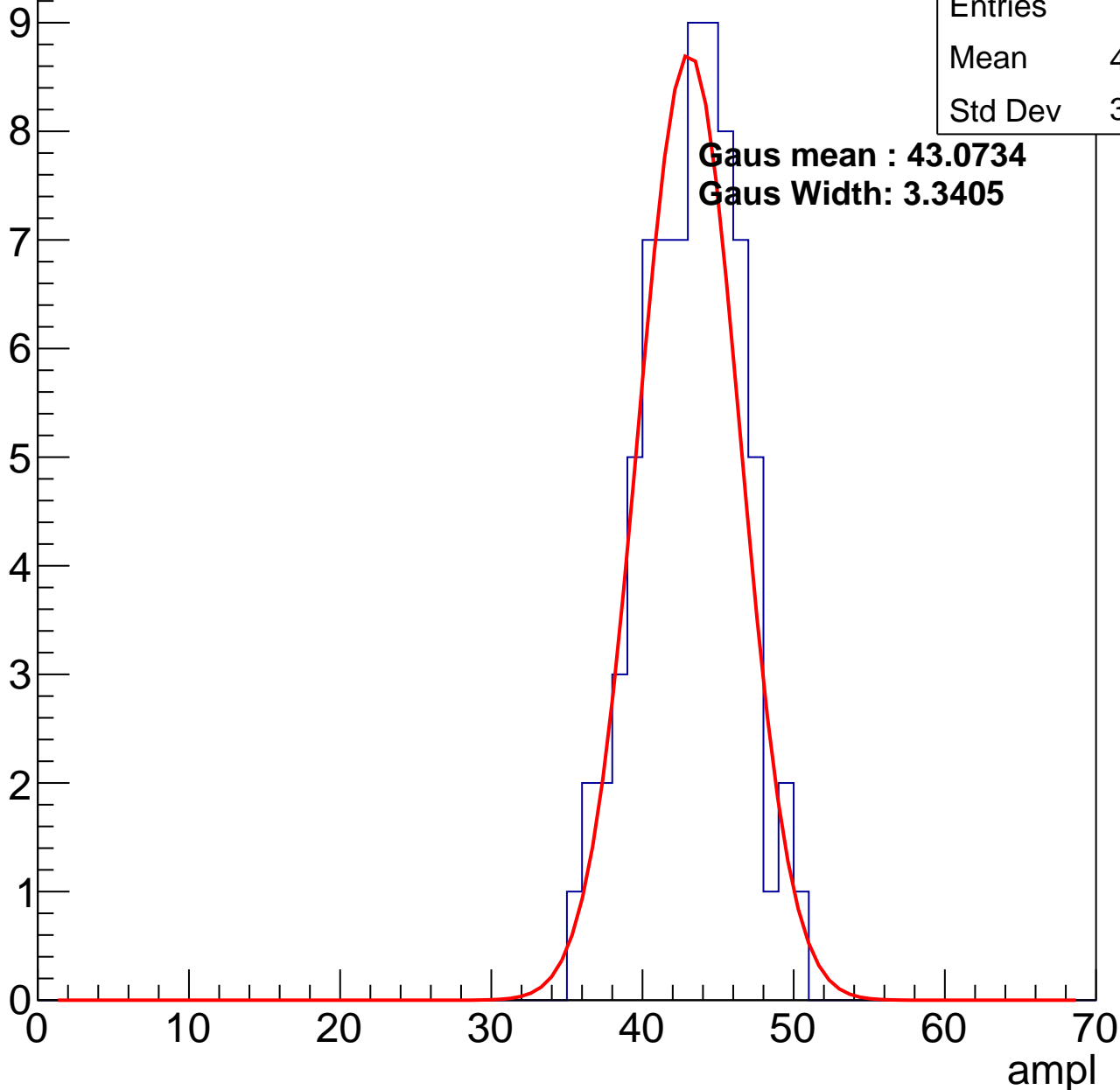
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	42.72
Std Dev	3.267

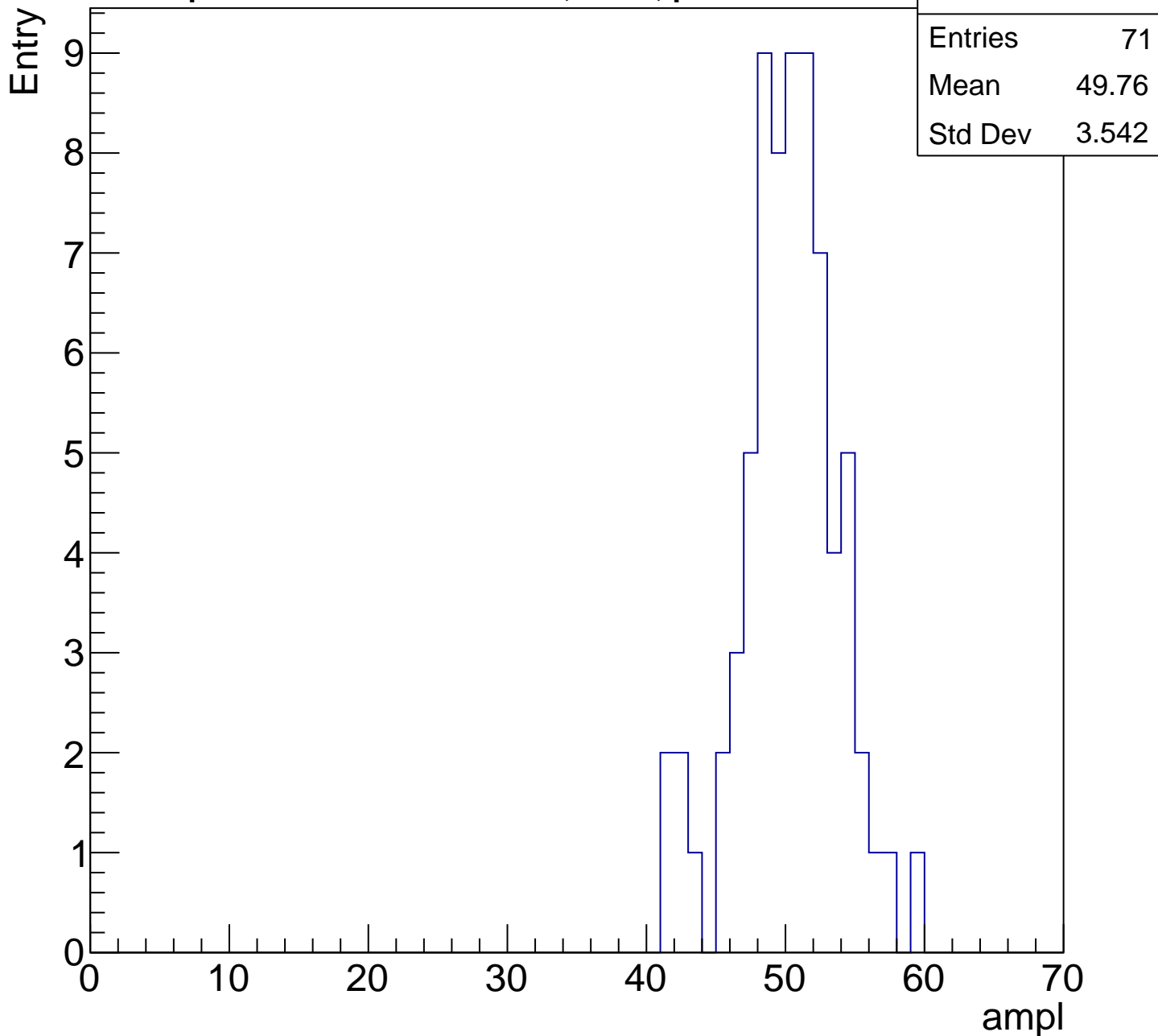
Gaus mean : 43.0734

Gaus Width: 3.3405



B1L101S, U12-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

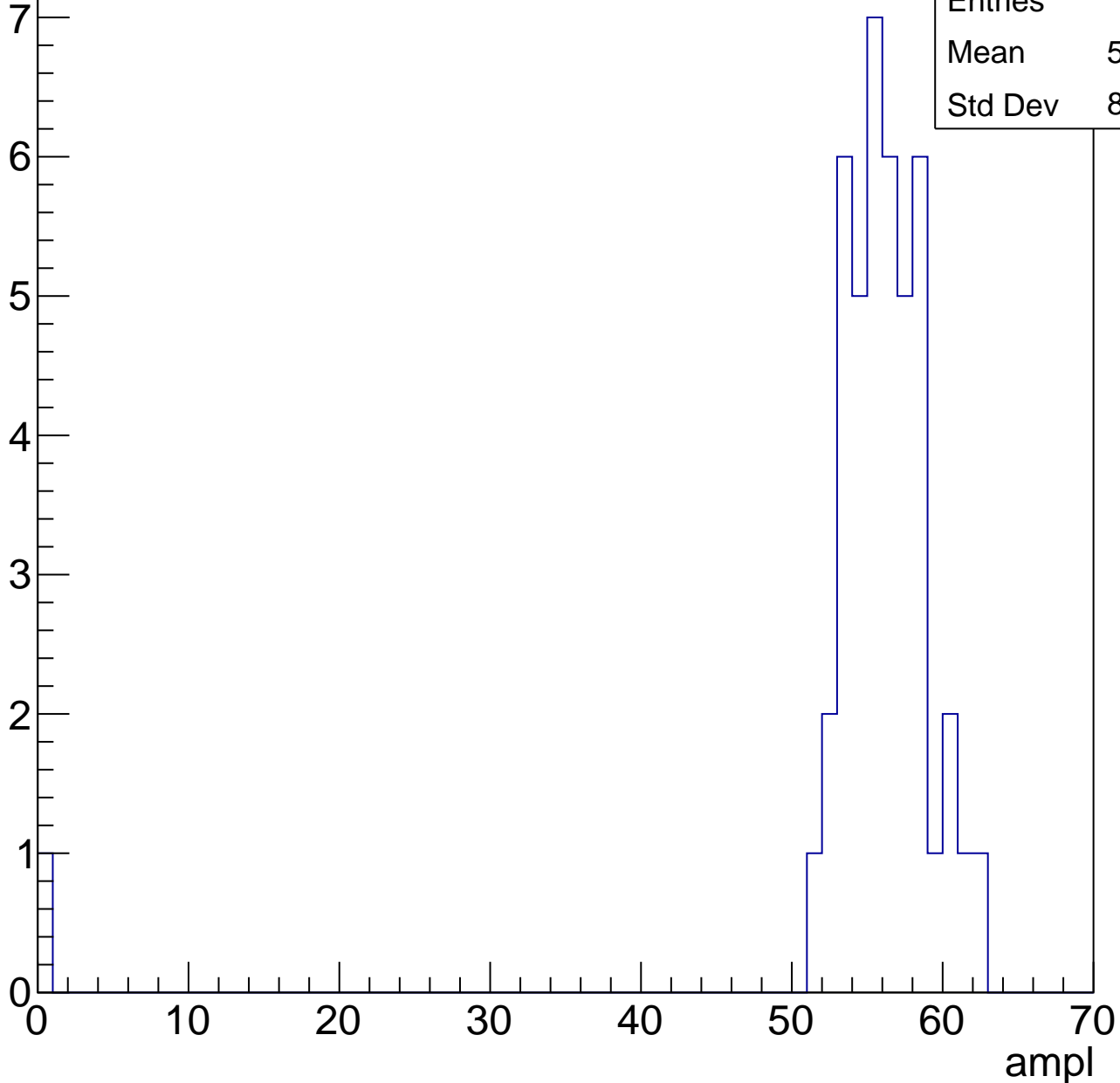


B1L101S, U12-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	54.52
Std Dev	8.672



B1L101S, U12-ch81, adc5

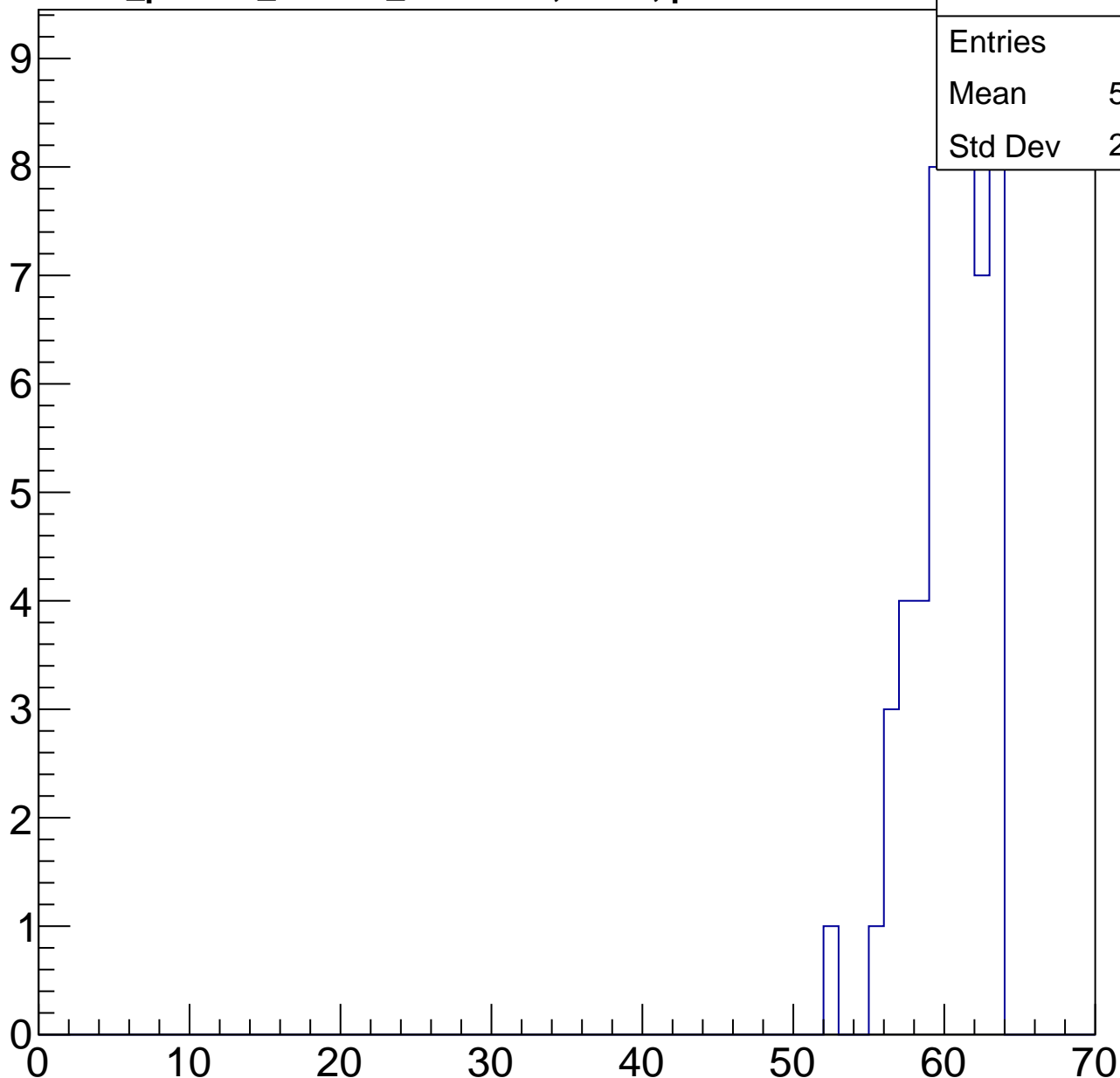
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.92
Std Dev	2.433

ampl



B1L101S, U12-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	62
Std Dev	0.7071

B1L101S, U12-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch82, adc0

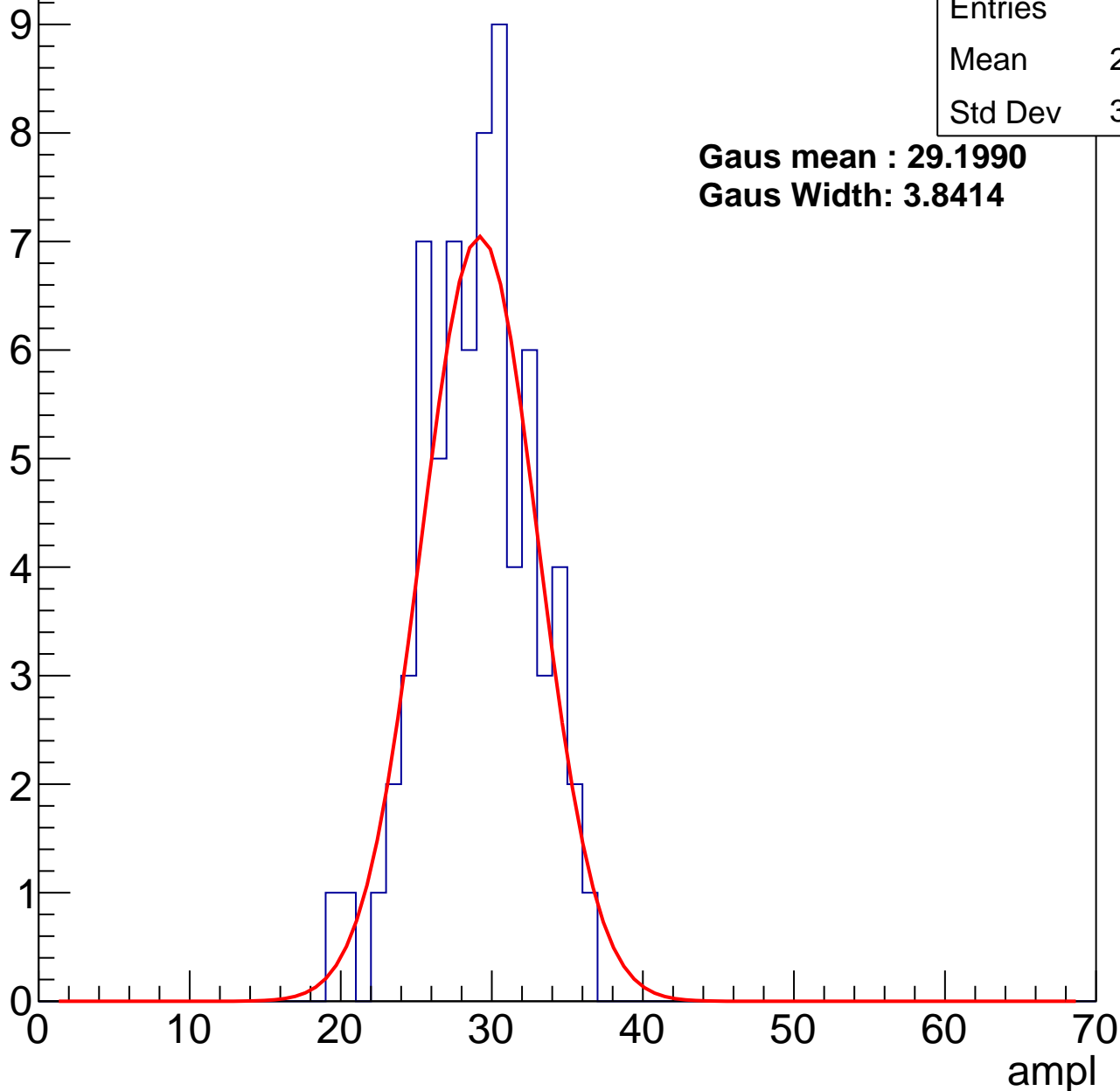
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.57
Std Dev	3.592

Gaus mean : 29.1990

Gaus Width: 3.8414



B1L101S, U12-ch82, adc1

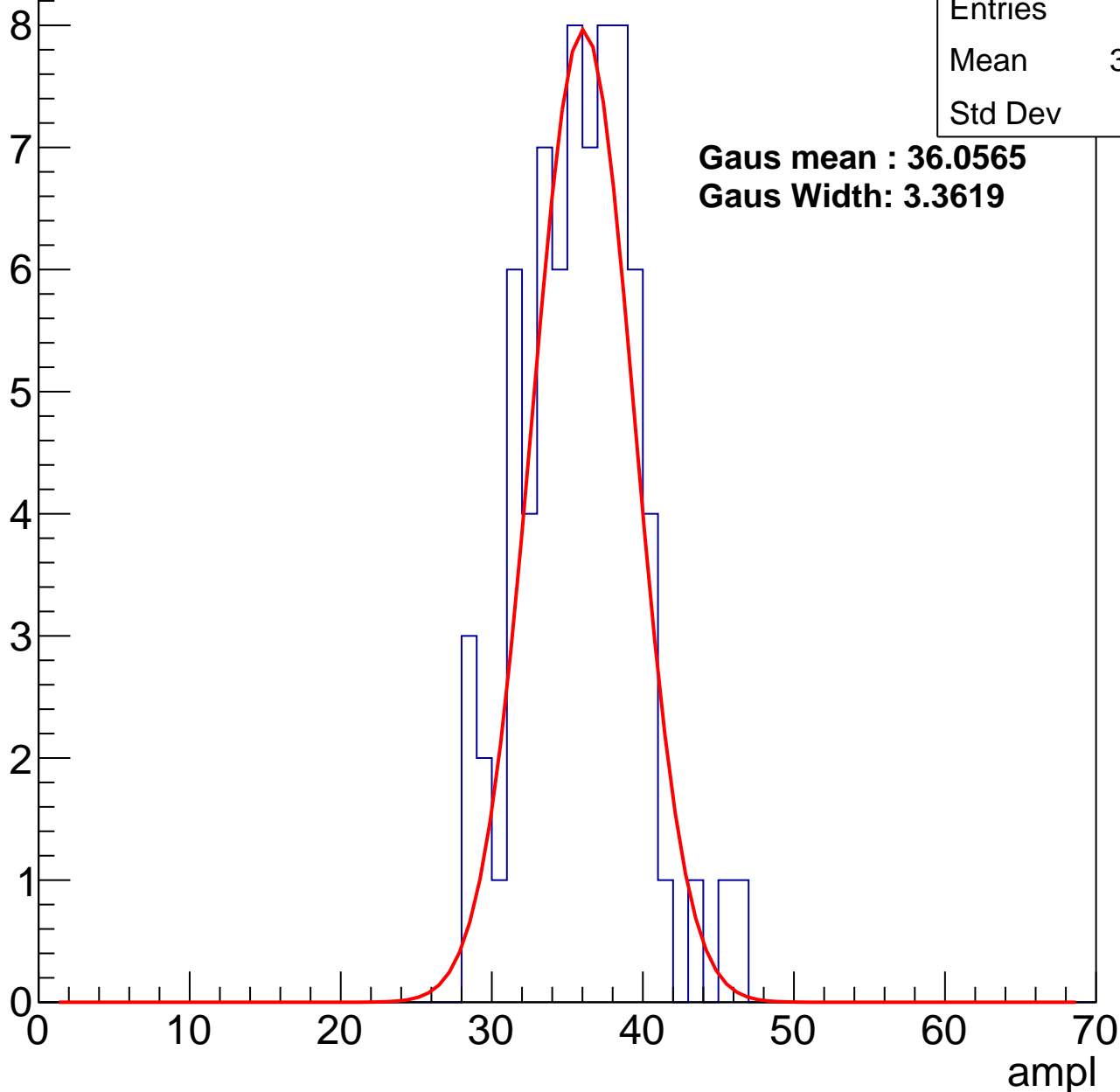
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.43
Std Dev	3.72

Gaus mean : 36.0565

Gaus Width: 3.3619



B1L101S, U12-ch82, adc2

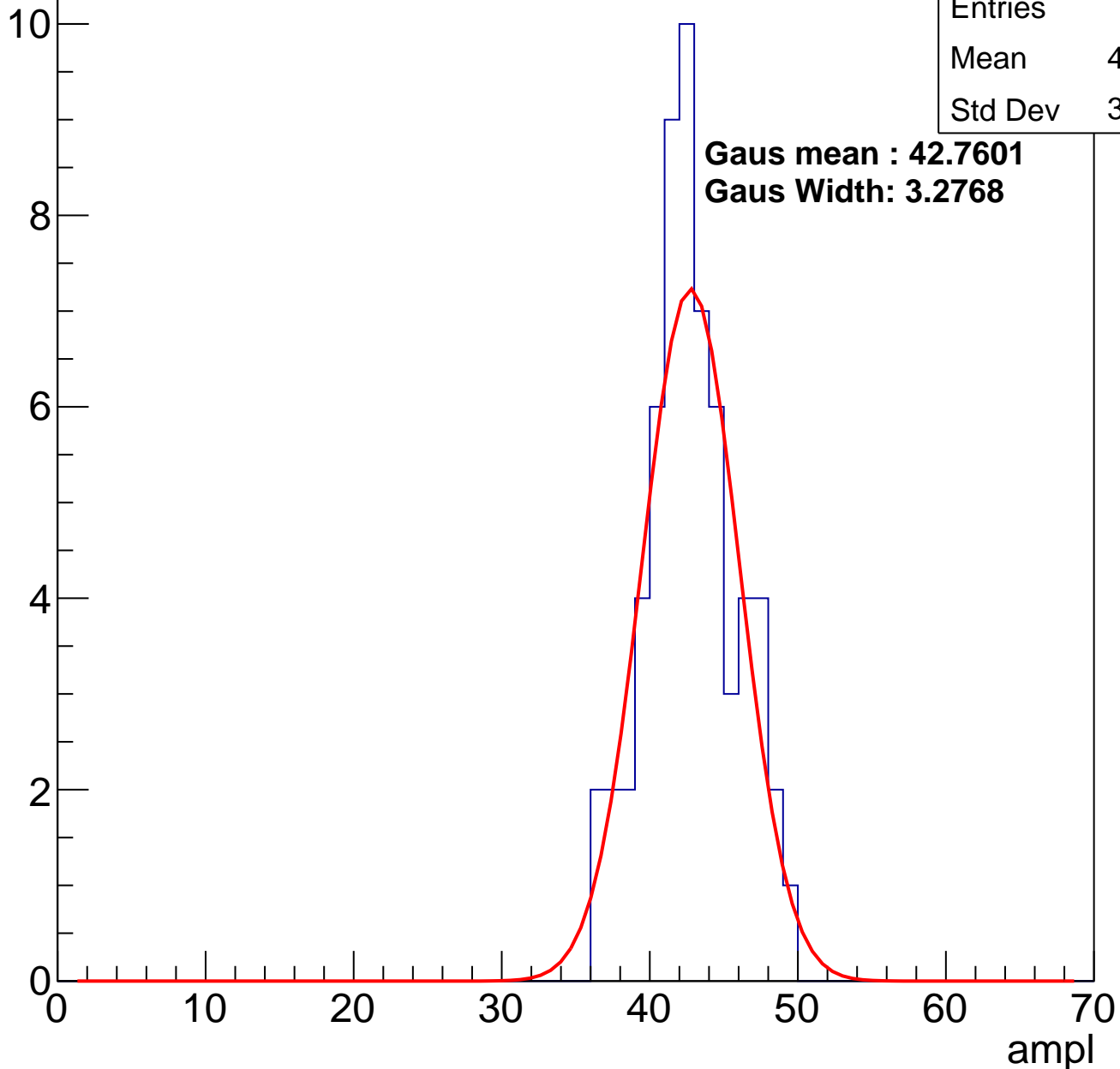
calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	42.32
Std Dev	3.004

Gaus mean : 42.7601

Gaus Width: 3.2768

Entry

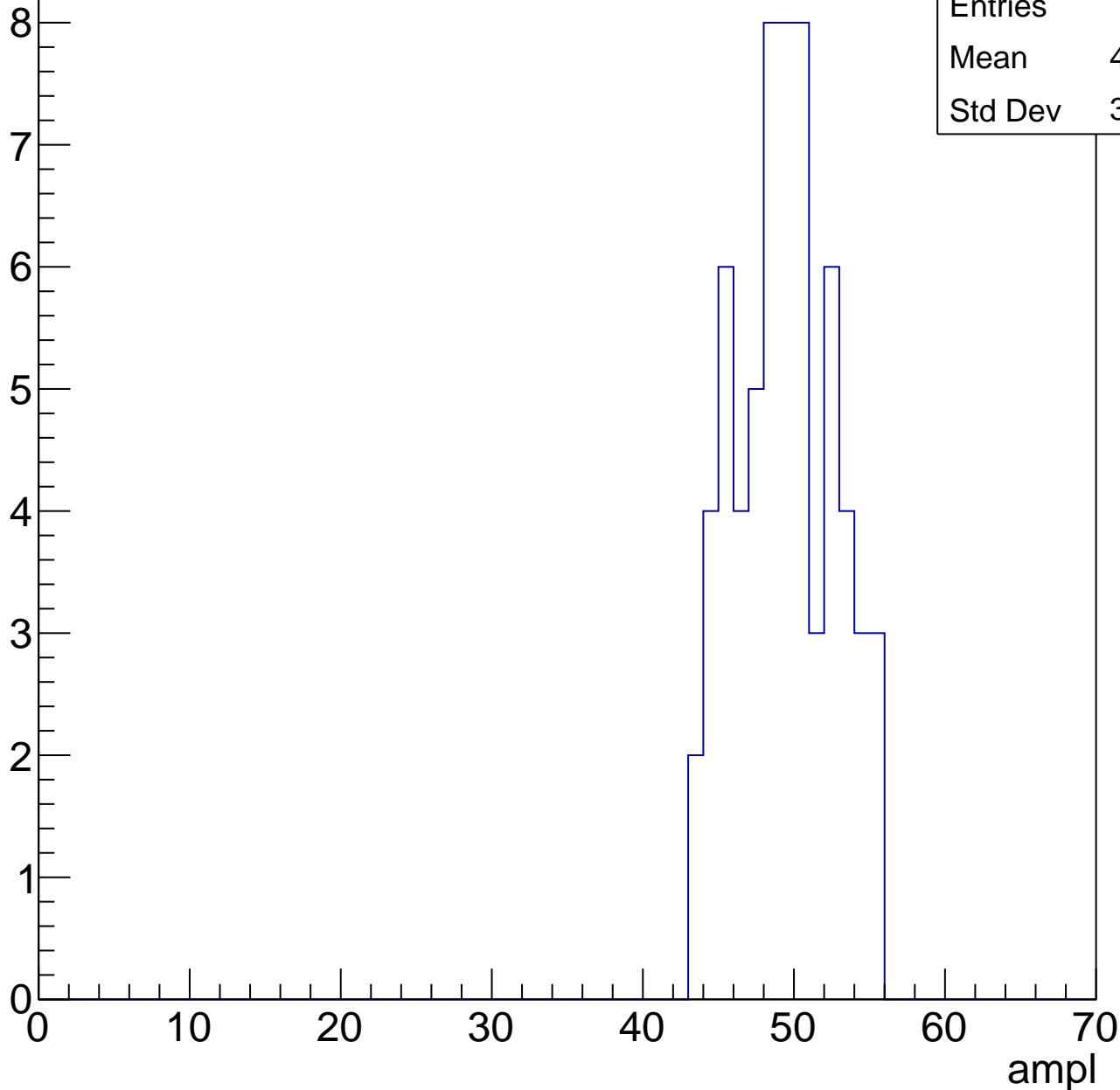


B1L101S, U12-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	48.92
Std Dev	3.193

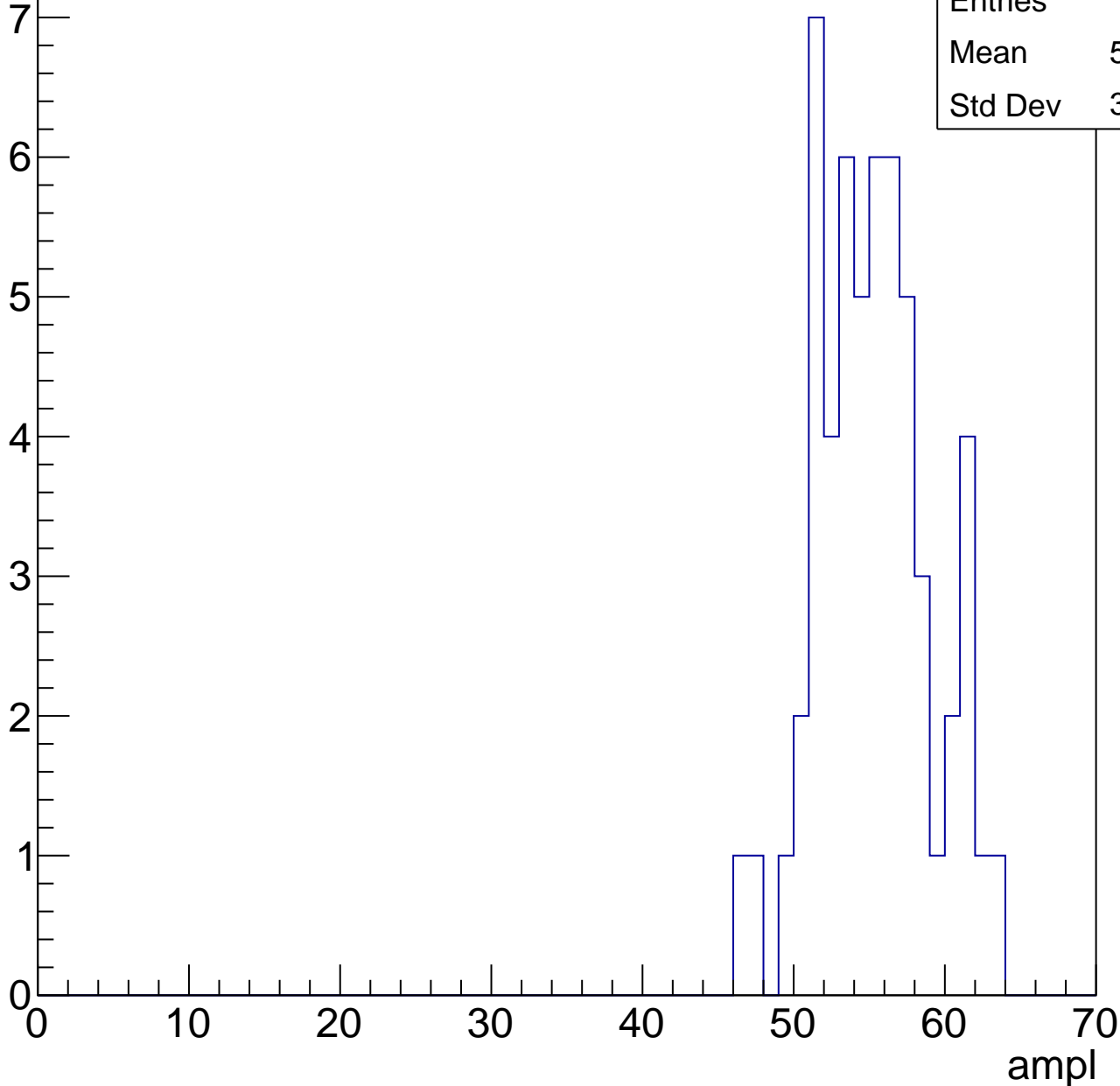


B1L101S, U12-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

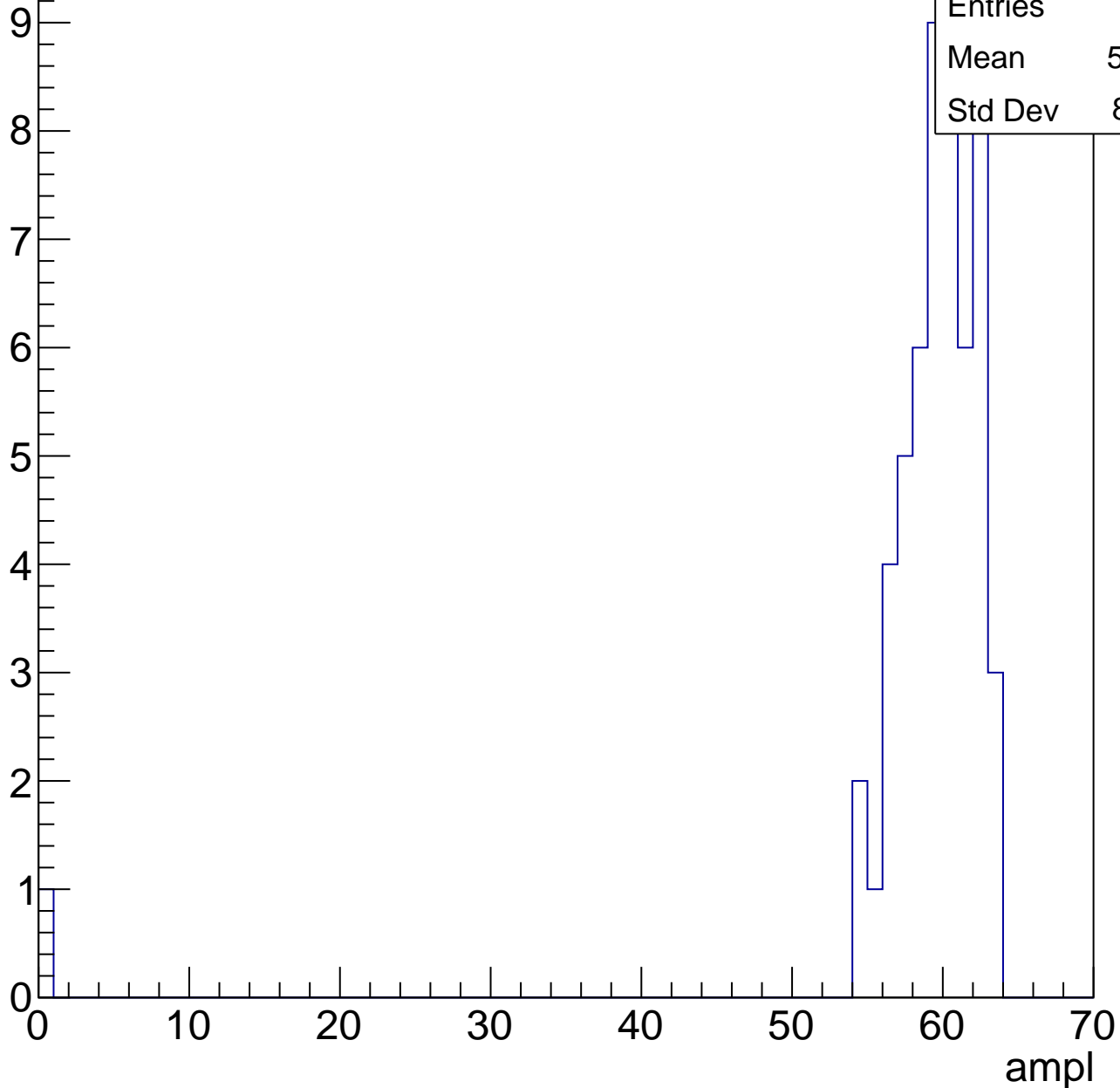
Entries	56
Mean	54.79
Std Dev	3.736



B1L101S, U12-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

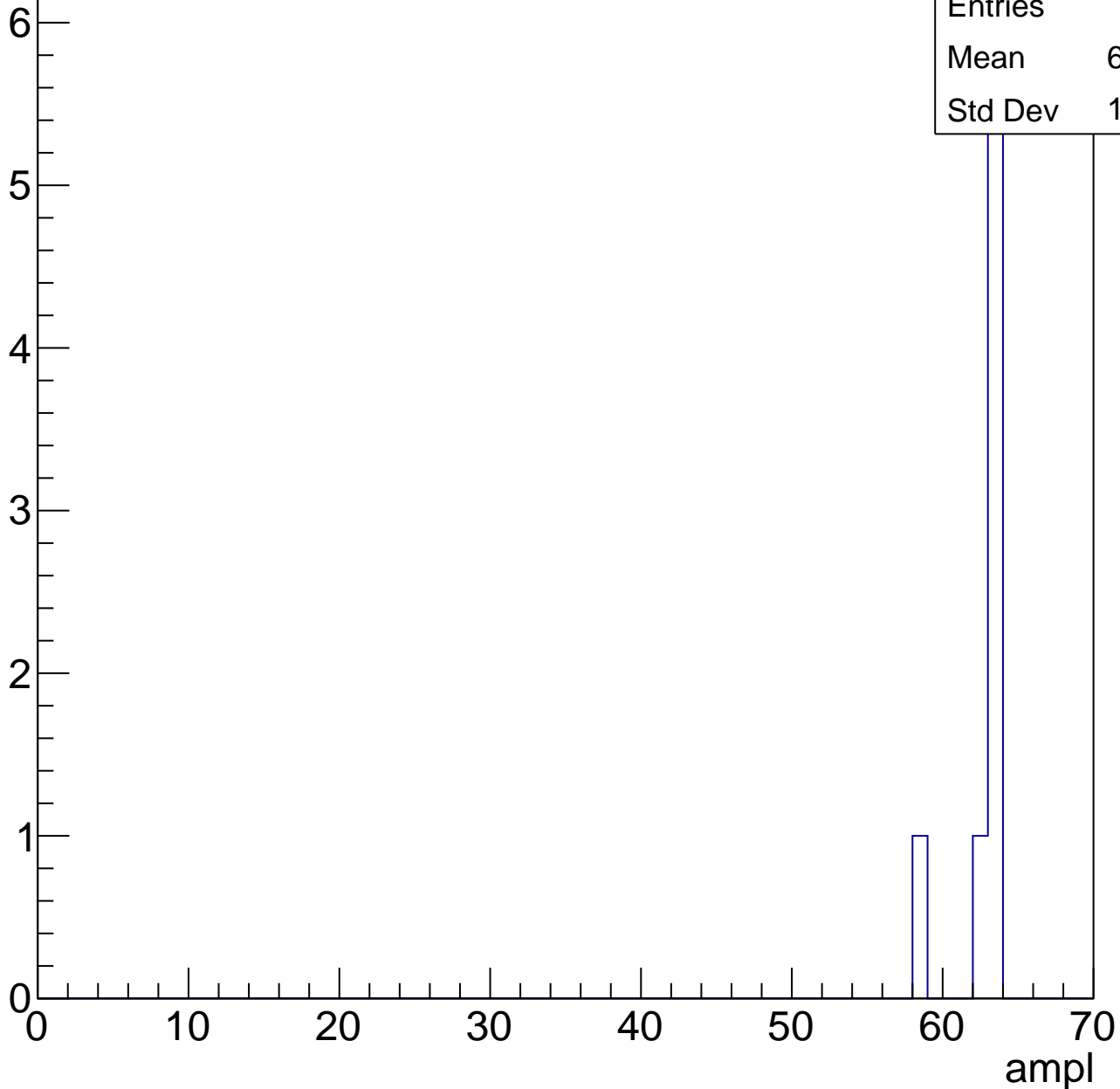


B1L101S, U12-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.25
Std Dev	1.639



B1L101S, U12-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch83, adc0

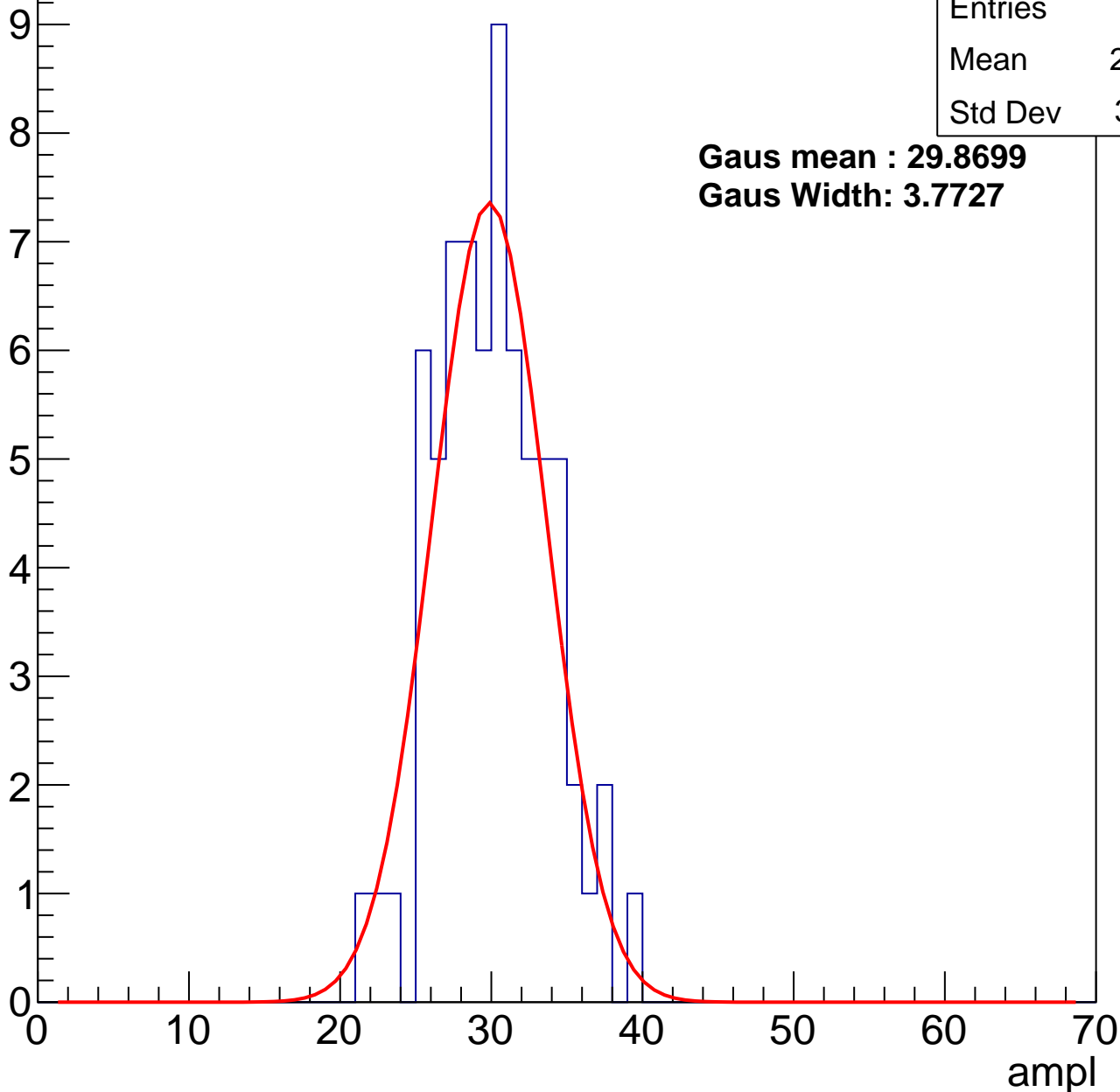
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.64
Std Dev	3.641

Gaus mean : 29.8699

Gaus Width: 3.7727



B1L101S, U12-ch83, adc1

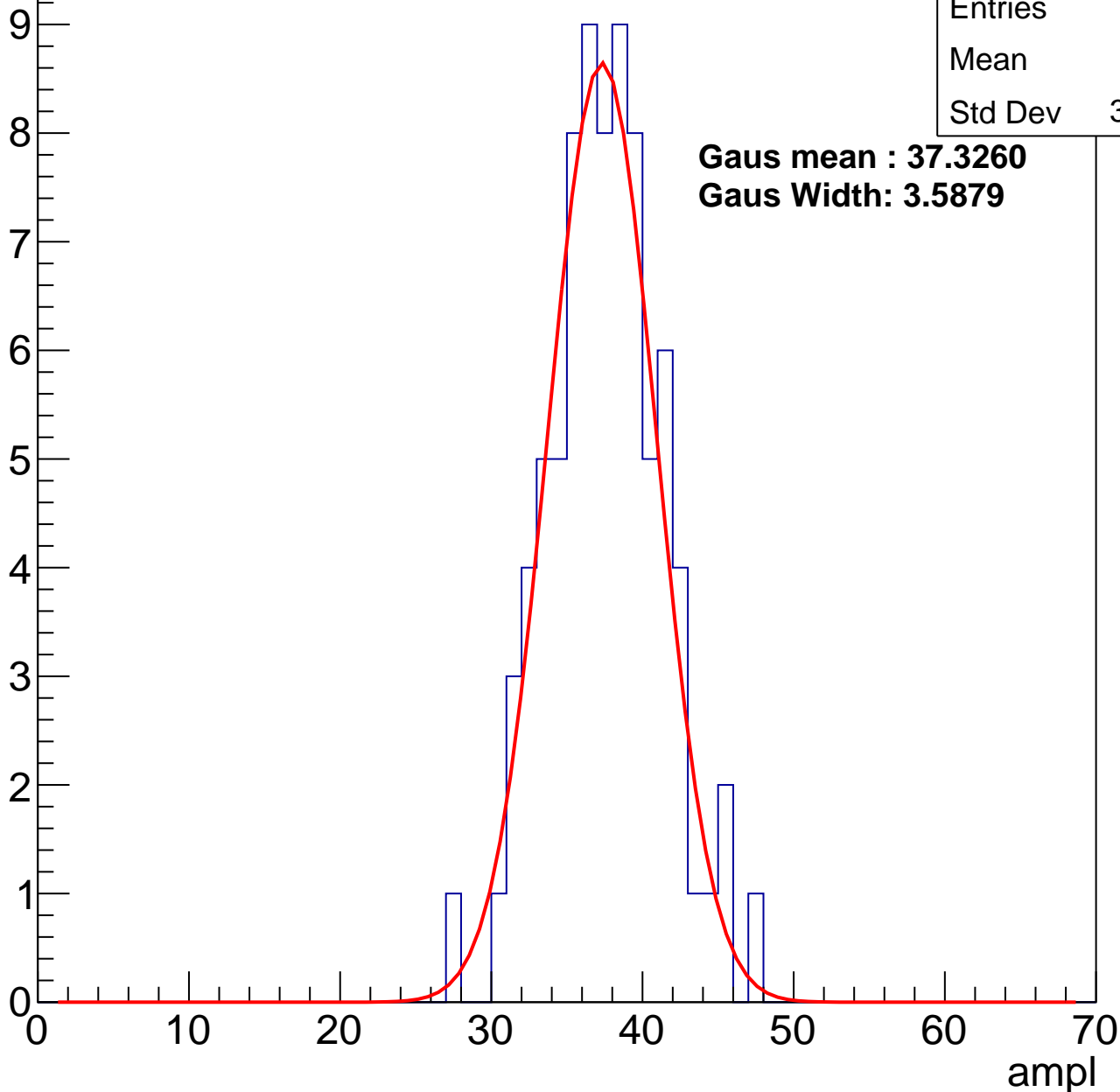
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.1
Std Dev	3.714

Gaus mean : 37.3260

Gaus Width: 3.5879



B1L101S, U12-ch83, adc2

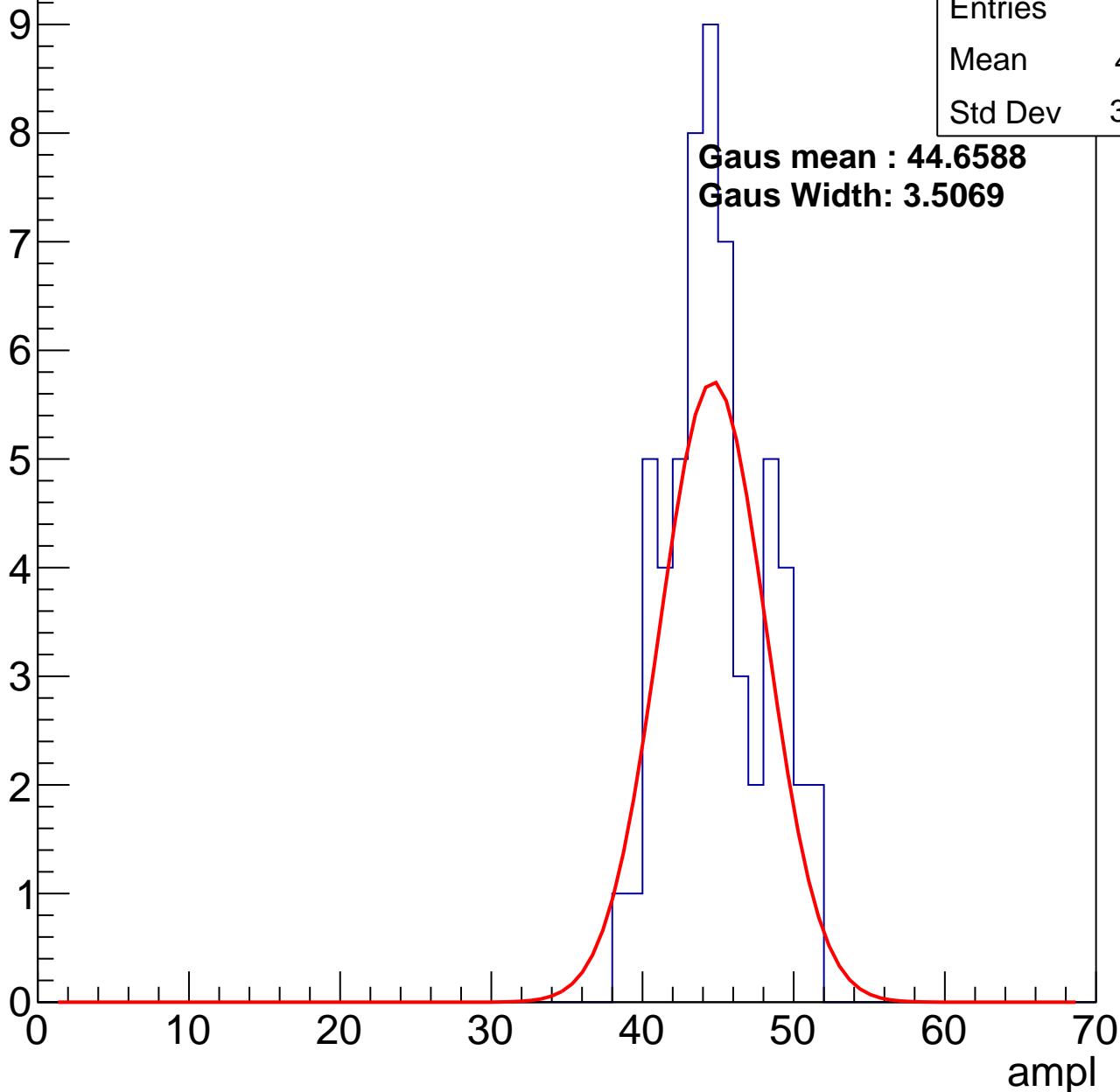
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.41
Std Dev	3.168

Gaus mean : 44.6588

Gaus Width: 3.5069

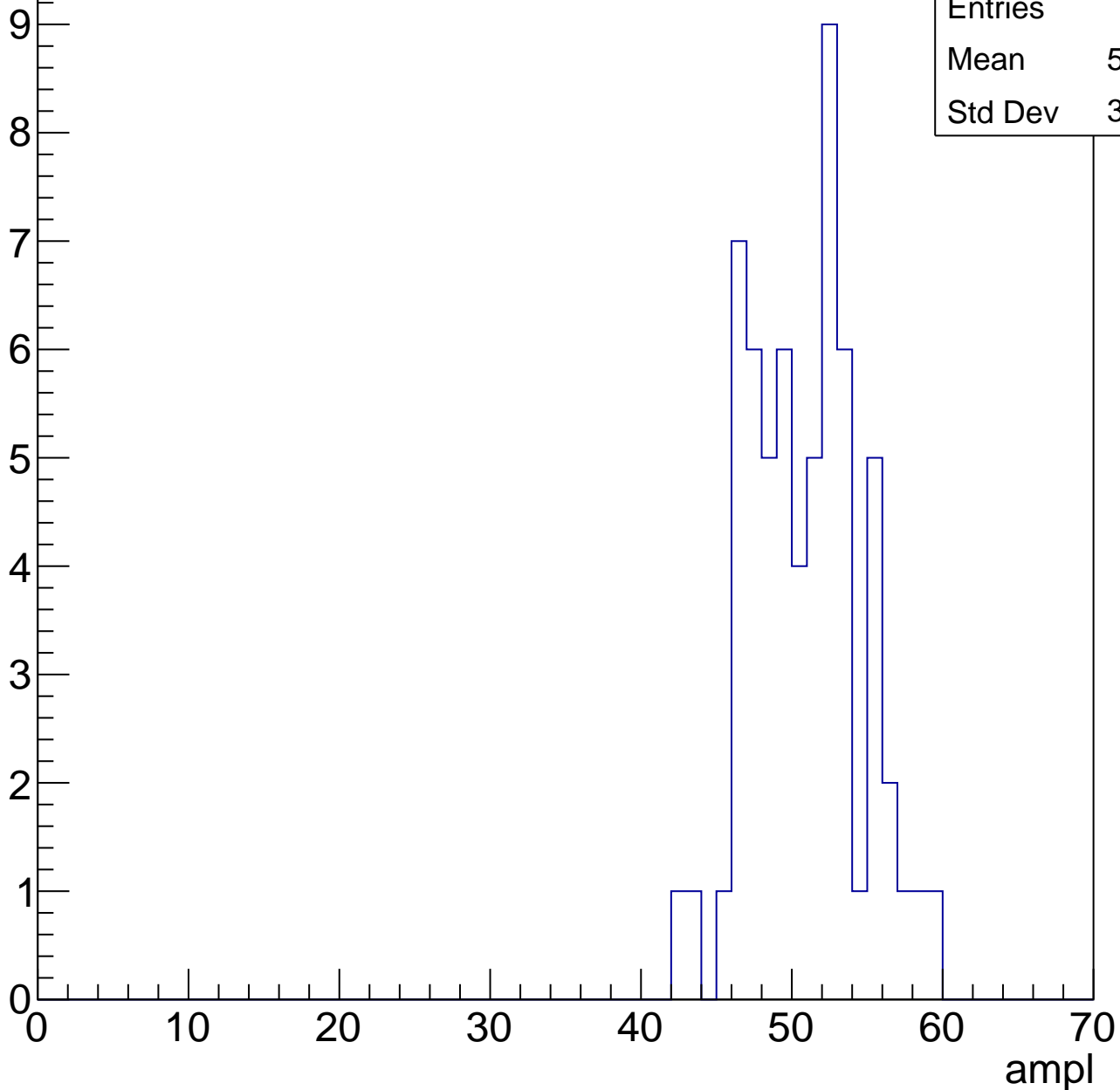


B1L101S, U12-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	50.39
Std Dev	3.643

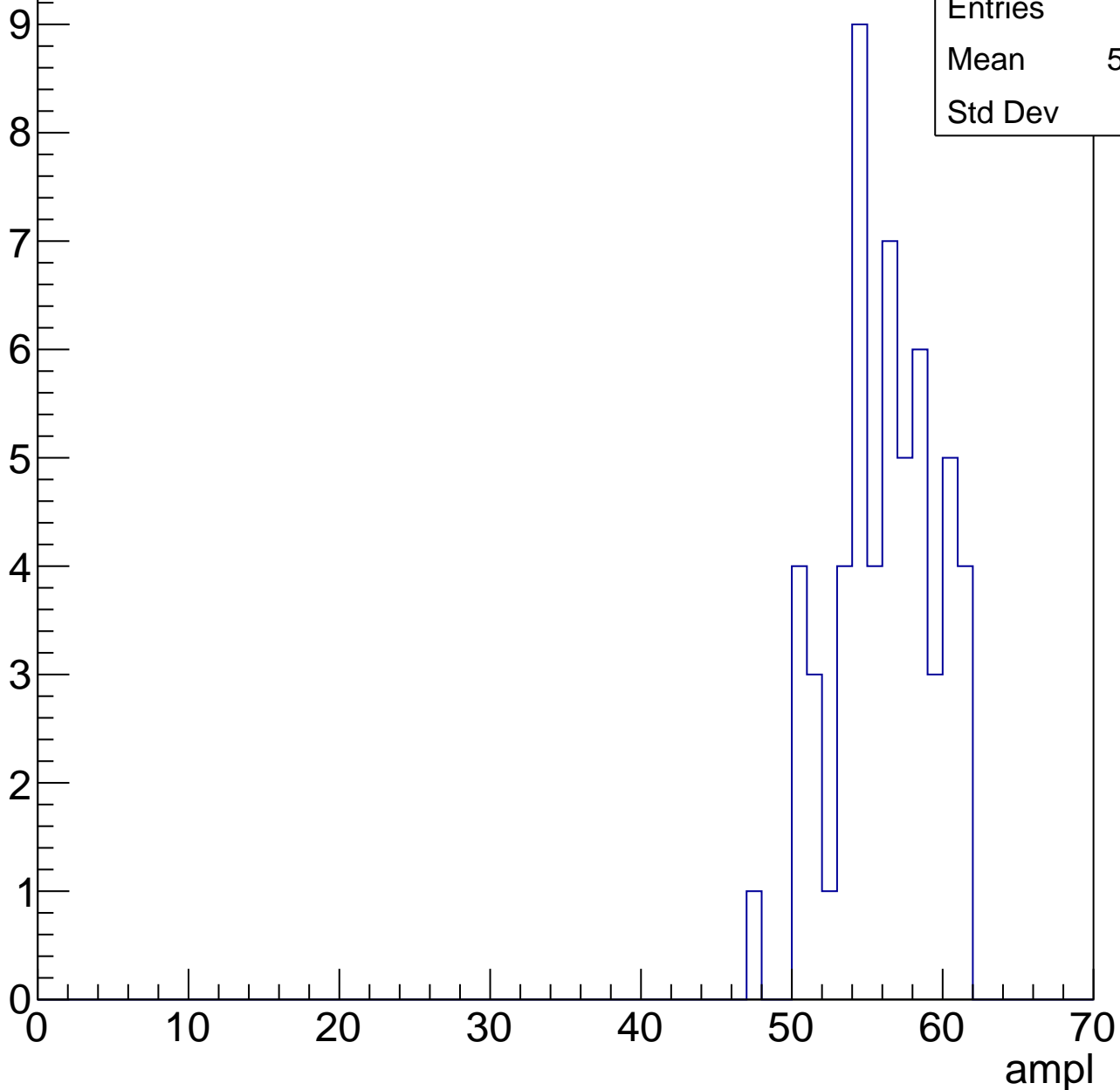


B1L101S, U12-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.64
Std Dev	3.33

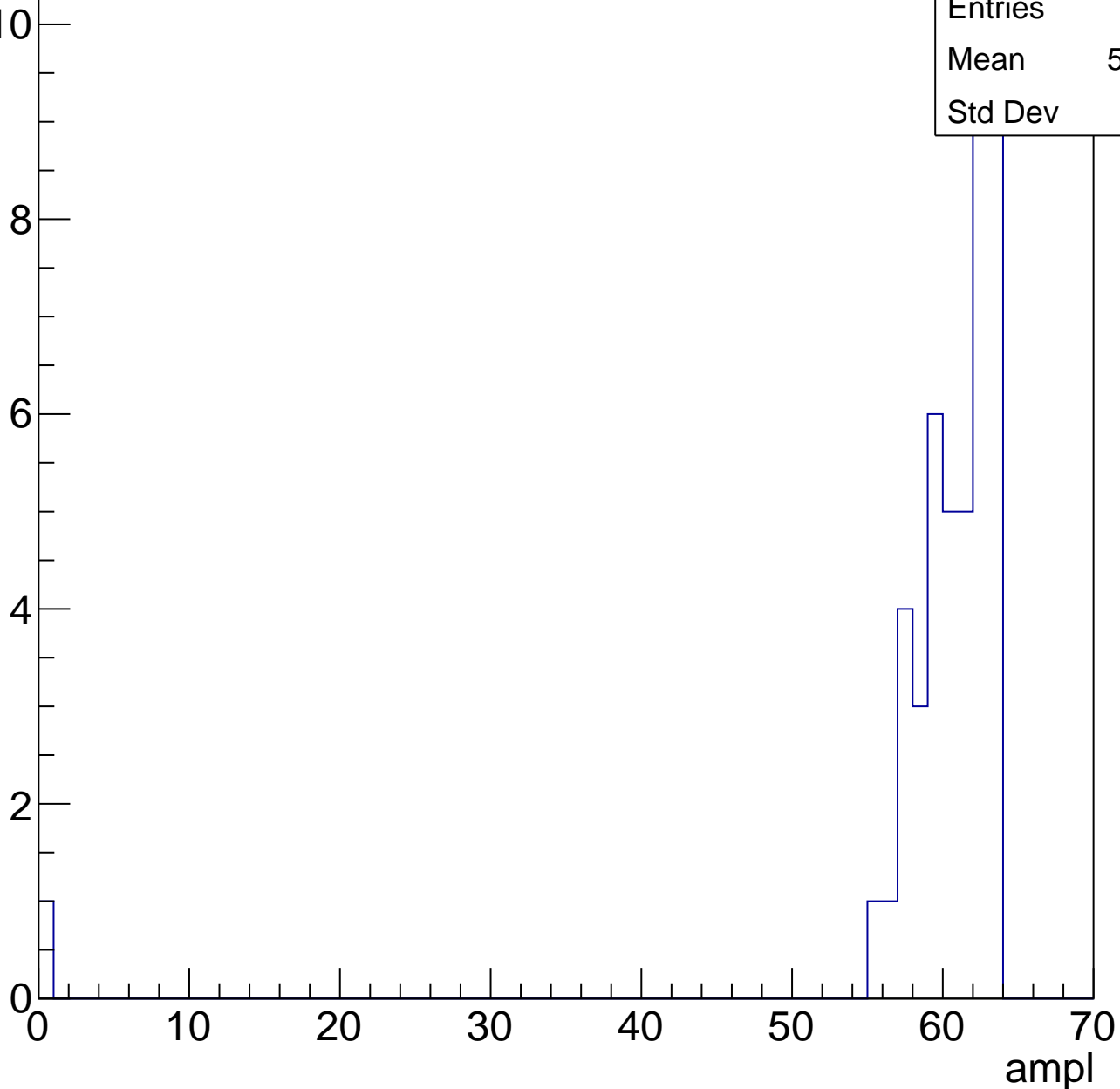


B1L101S, U12-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.09
Std Dev	9.17



B1L101S, U12-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch84, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	27.48
Std Dev	5.651

Gaus mean : 28.7839

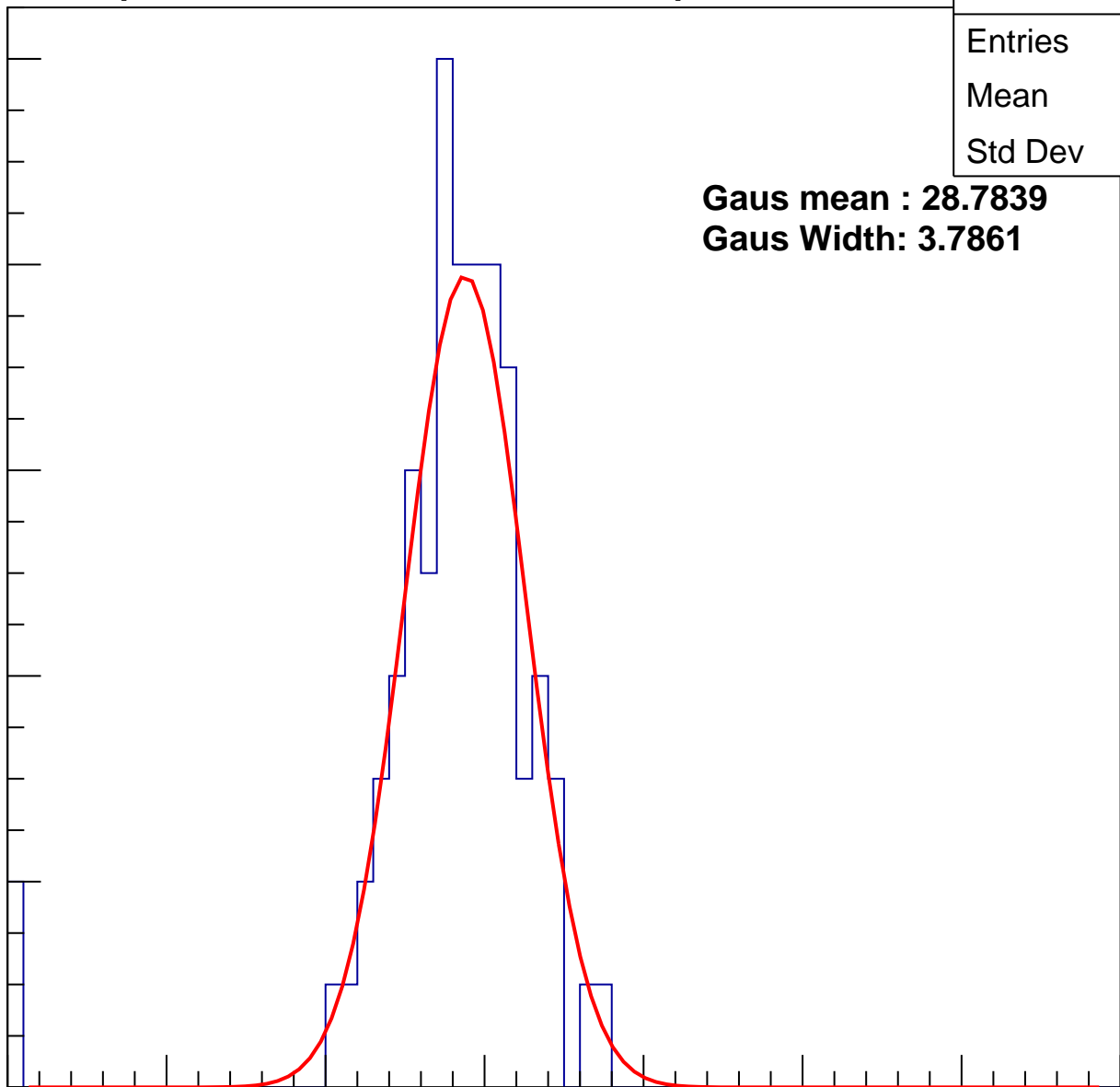
Gaus Width: 3.7861

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch84, adc1

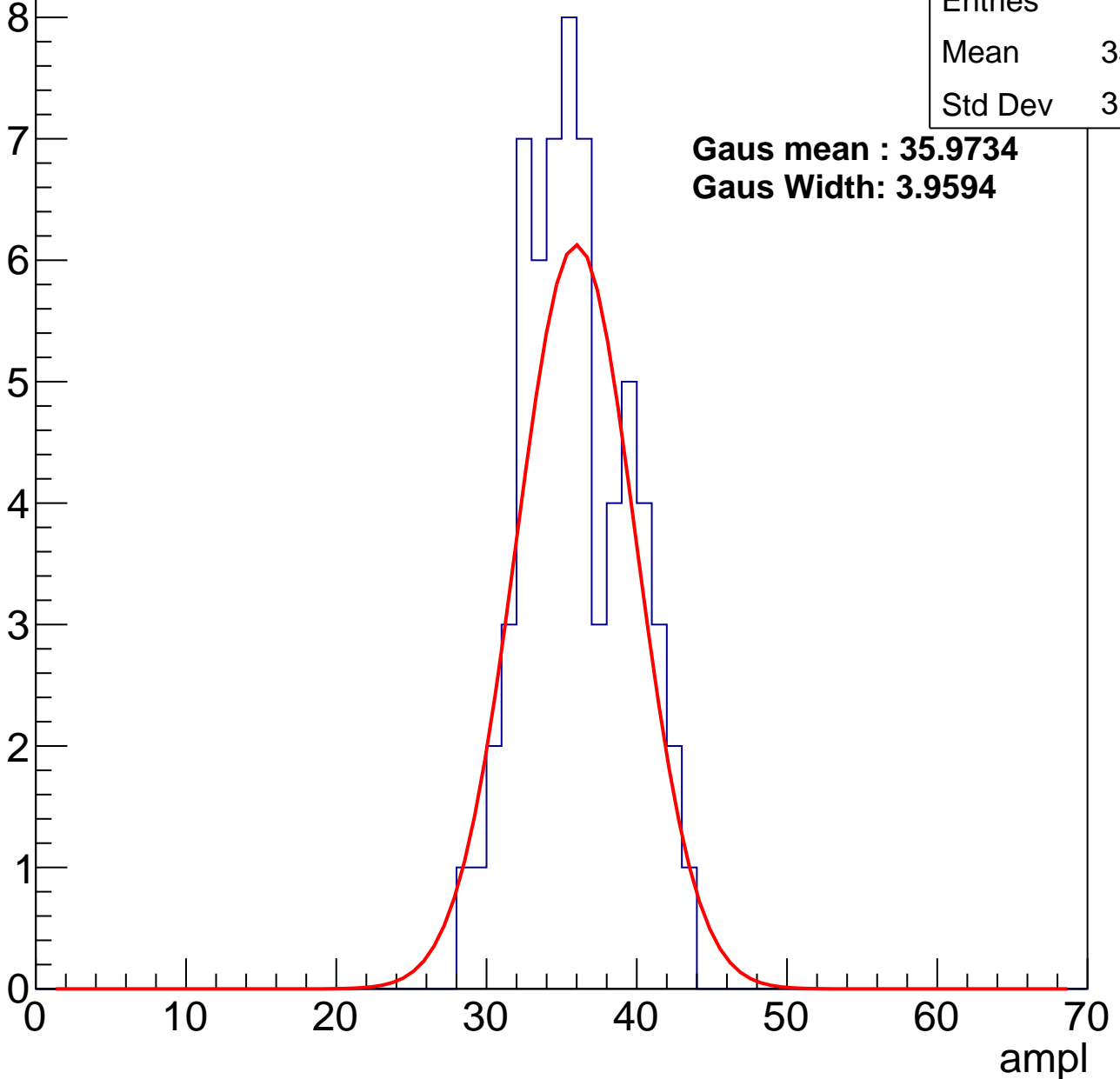
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.47
Std Dev	3.464

Gaus mean : 35.9734

Gaus Width: 3.9594



B1L101S, U12-ch84, adc2

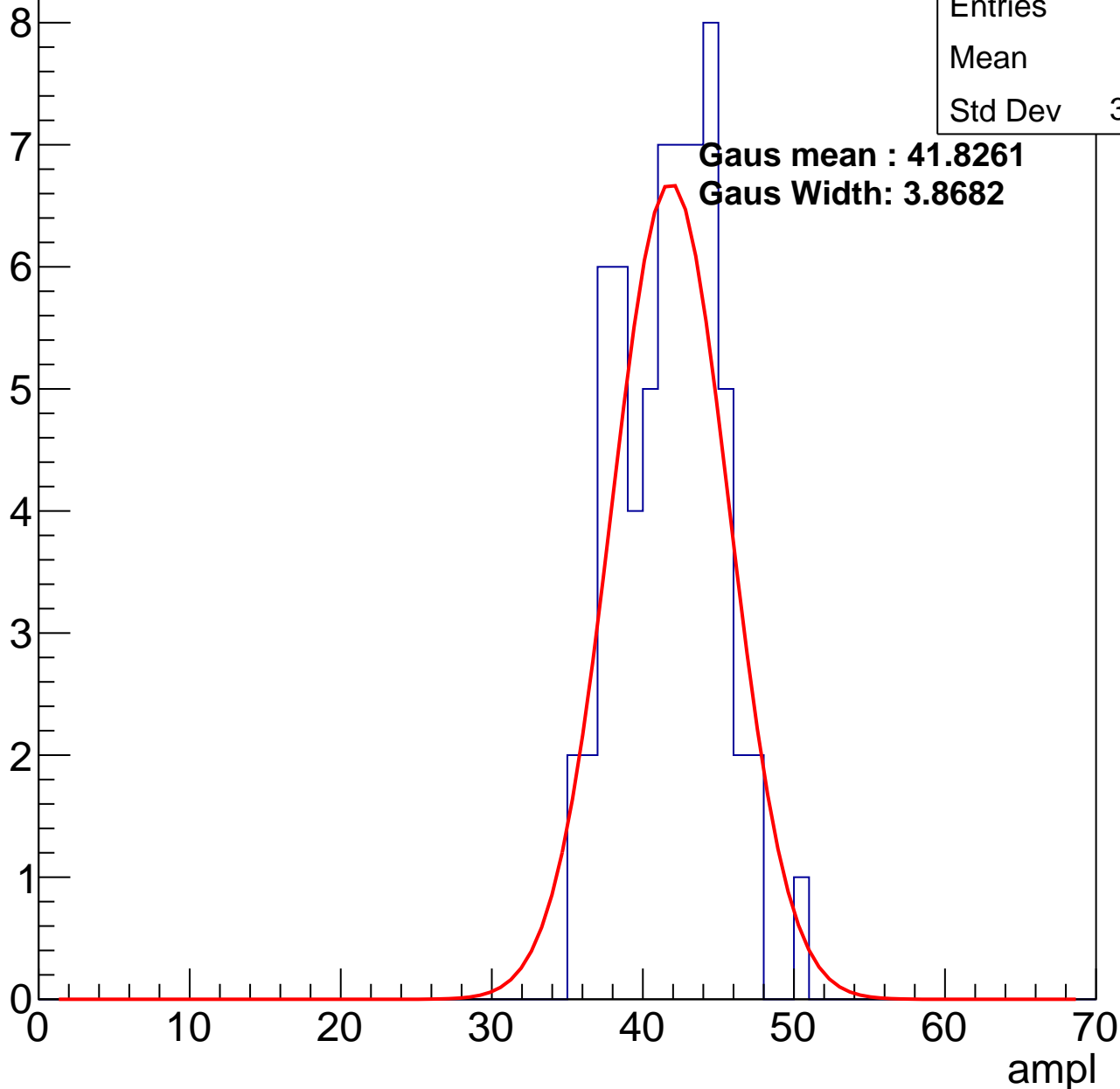
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	41.3
Std Dev	3.253

Gaus mean : 41.8261

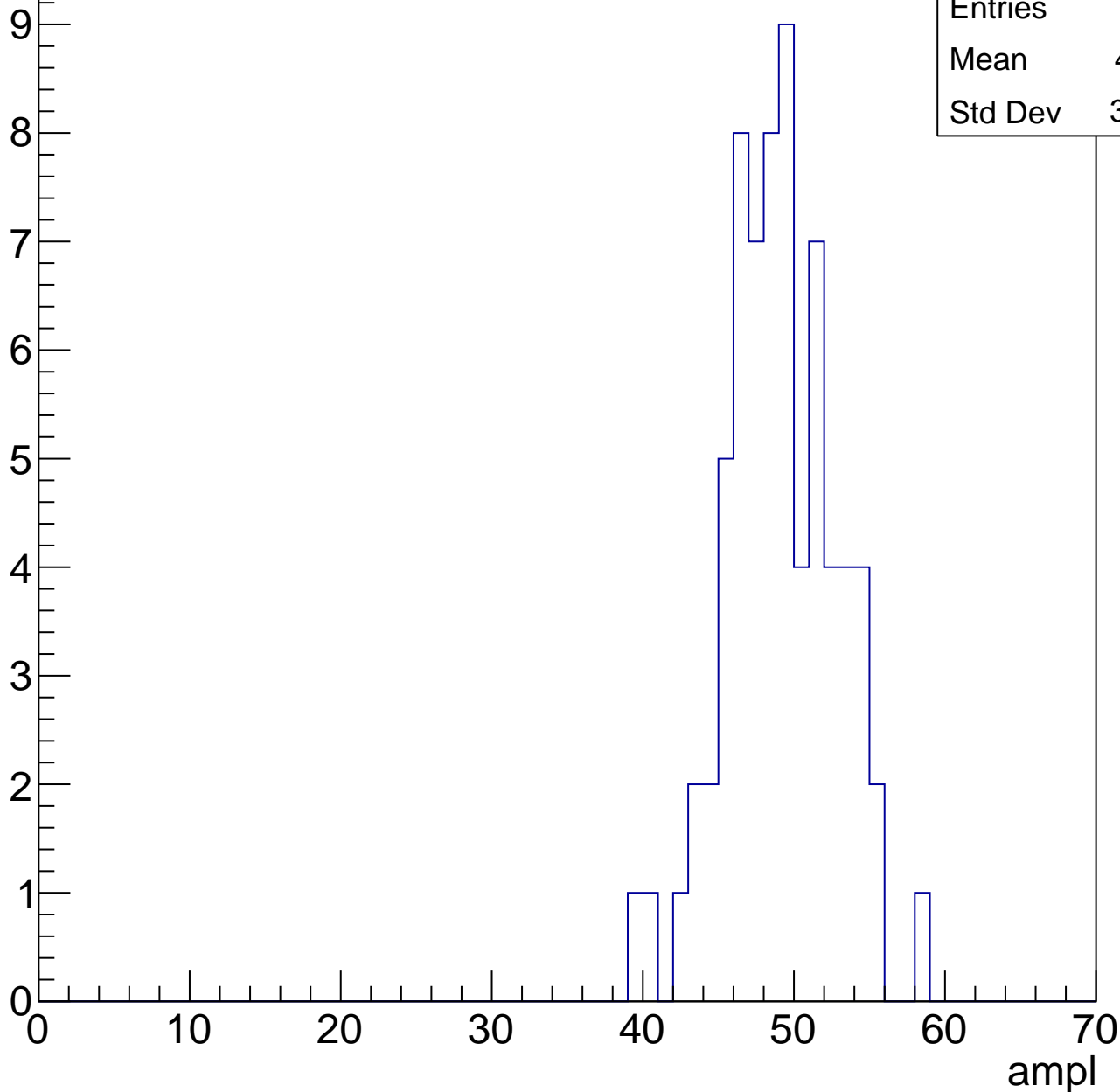
Gaus Width: 3.8682



B1L101S, U12-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

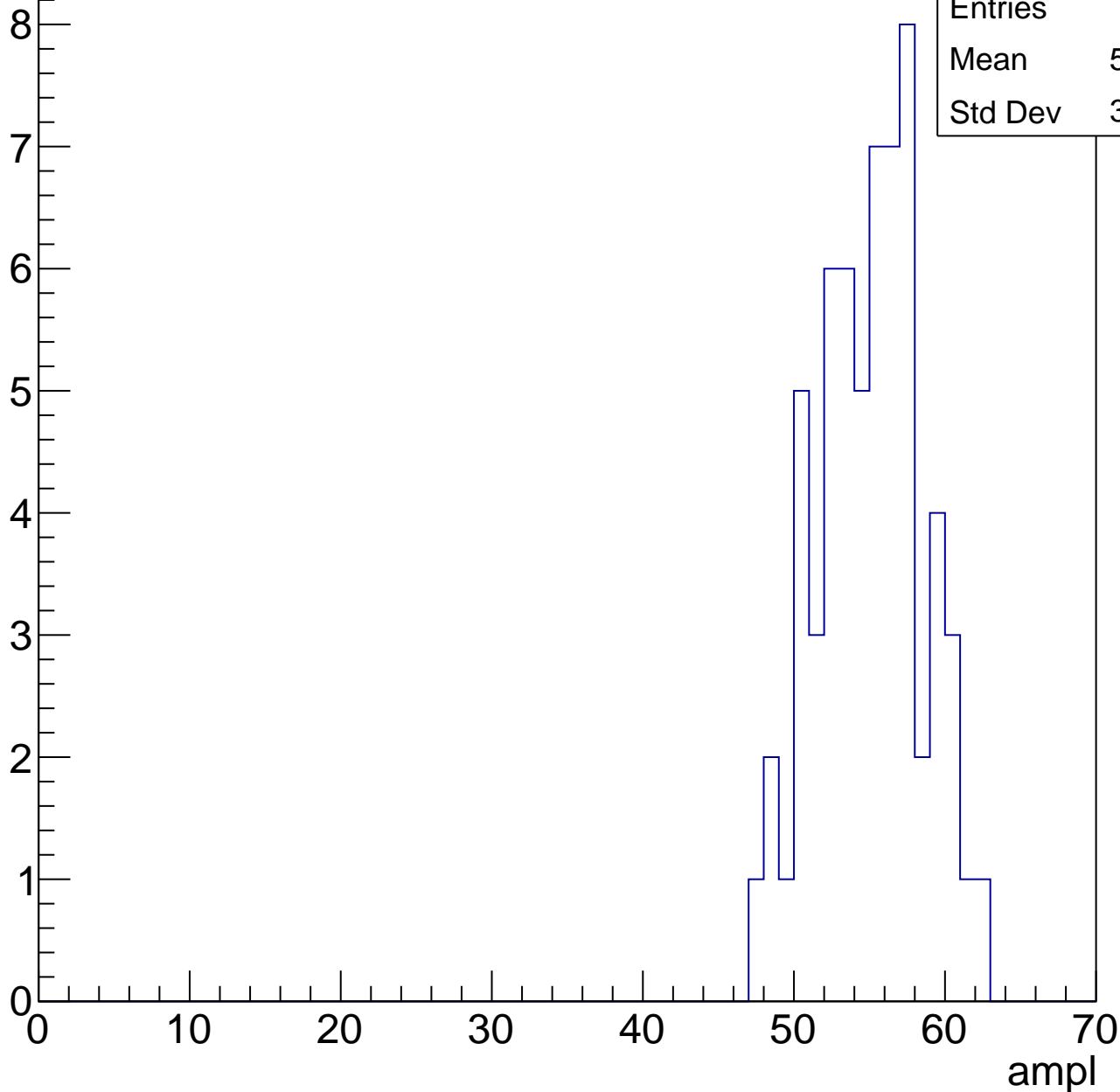
Entry



B1L101S, U12-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

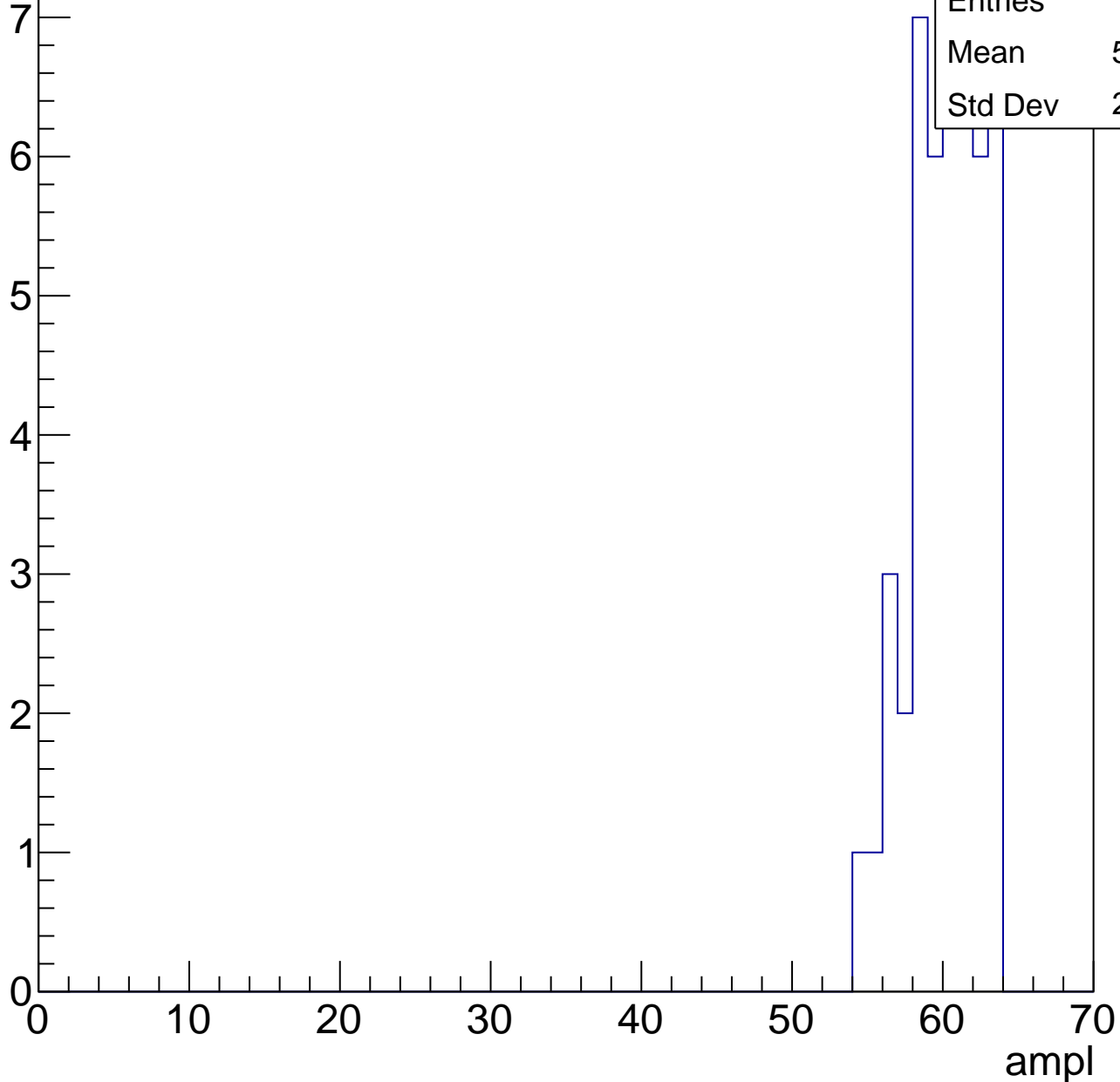
Entry



B1L101S, U12-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

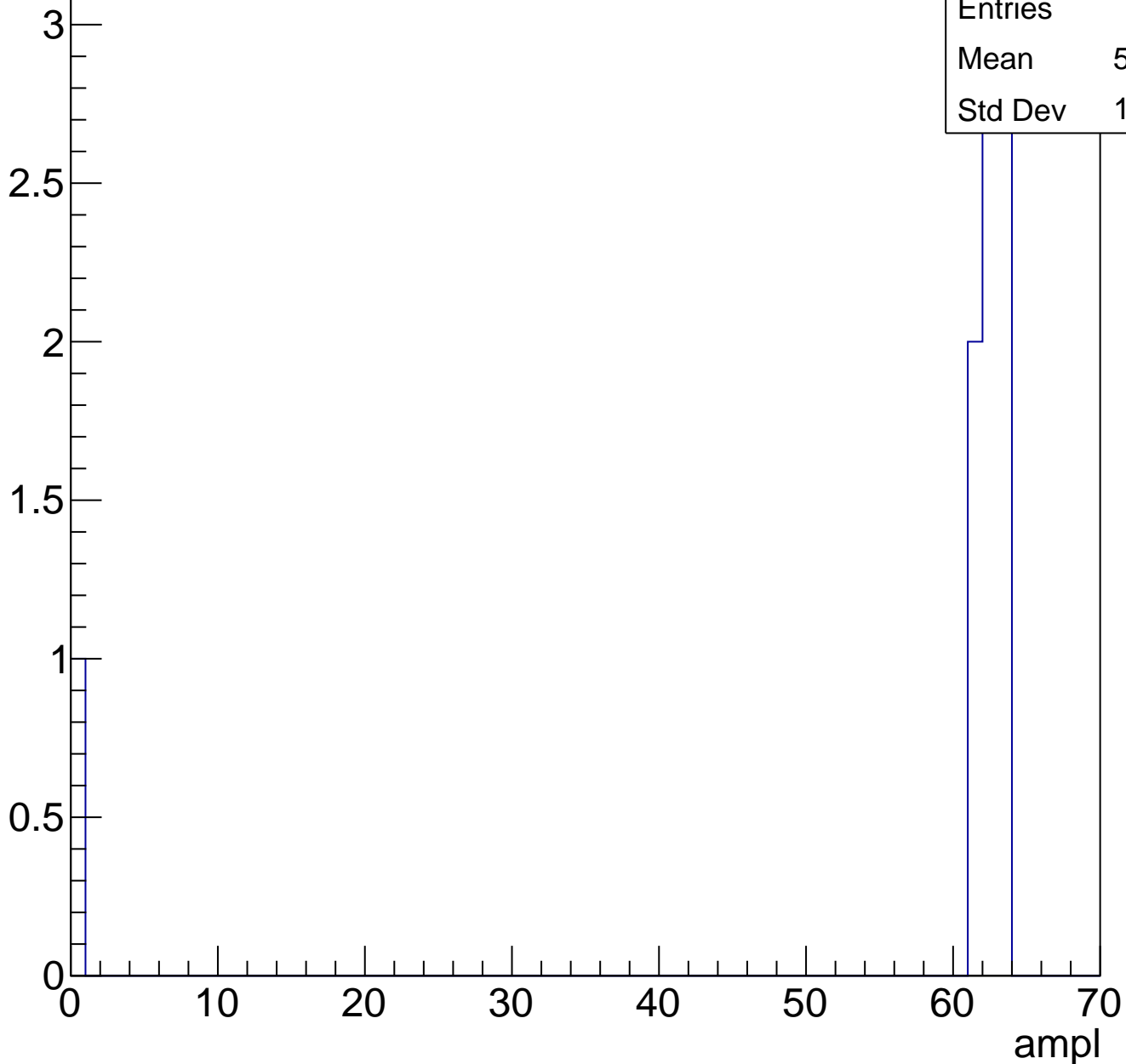
Entry



B1L101S, U12-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L101S, U12-ch85, adc0

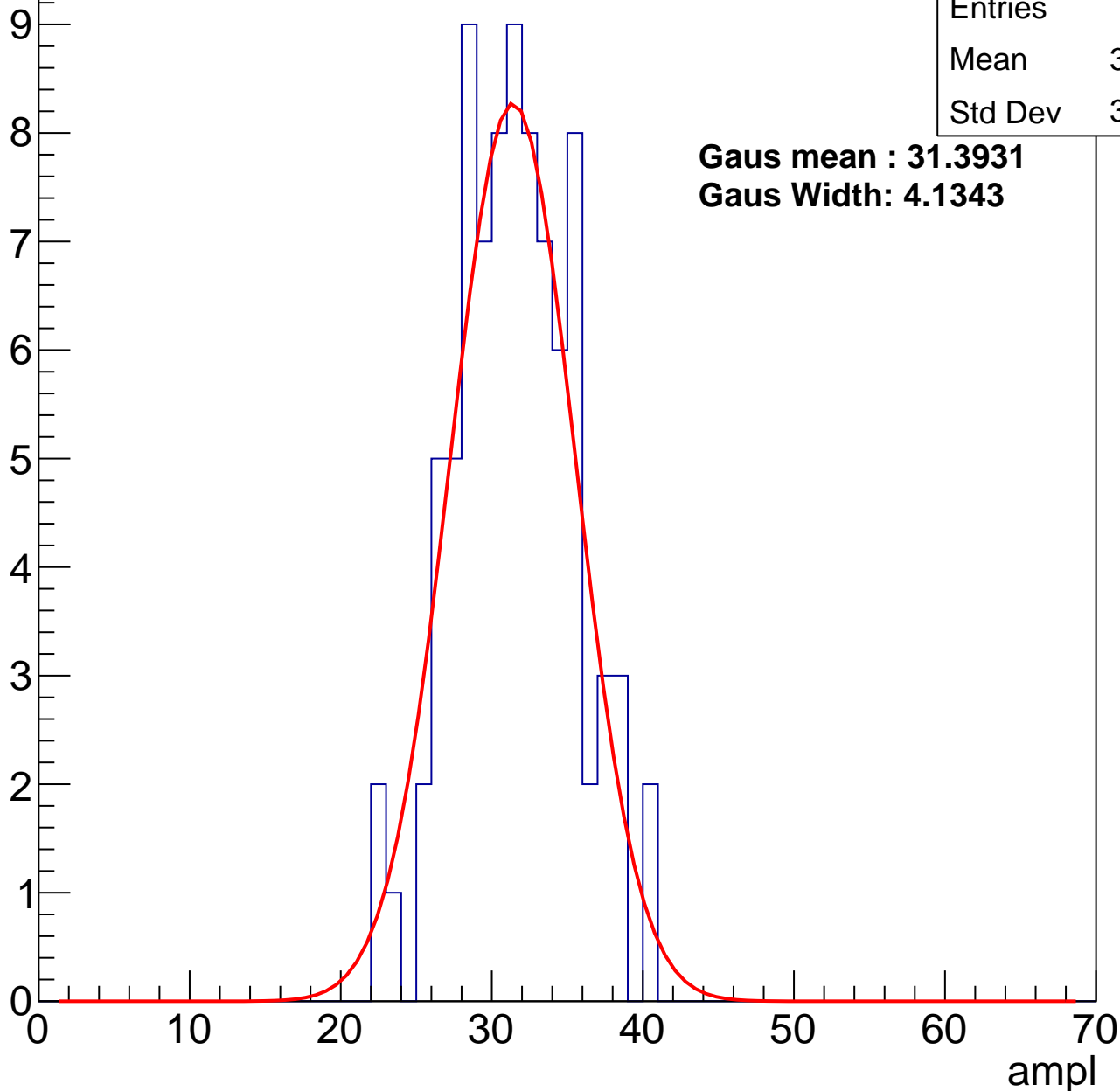
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	31.08
Std Dev	3.872

Gaus mean : 31.3931

Gaus Width: 4.1343



B1L101S, U12-ch85, adc1

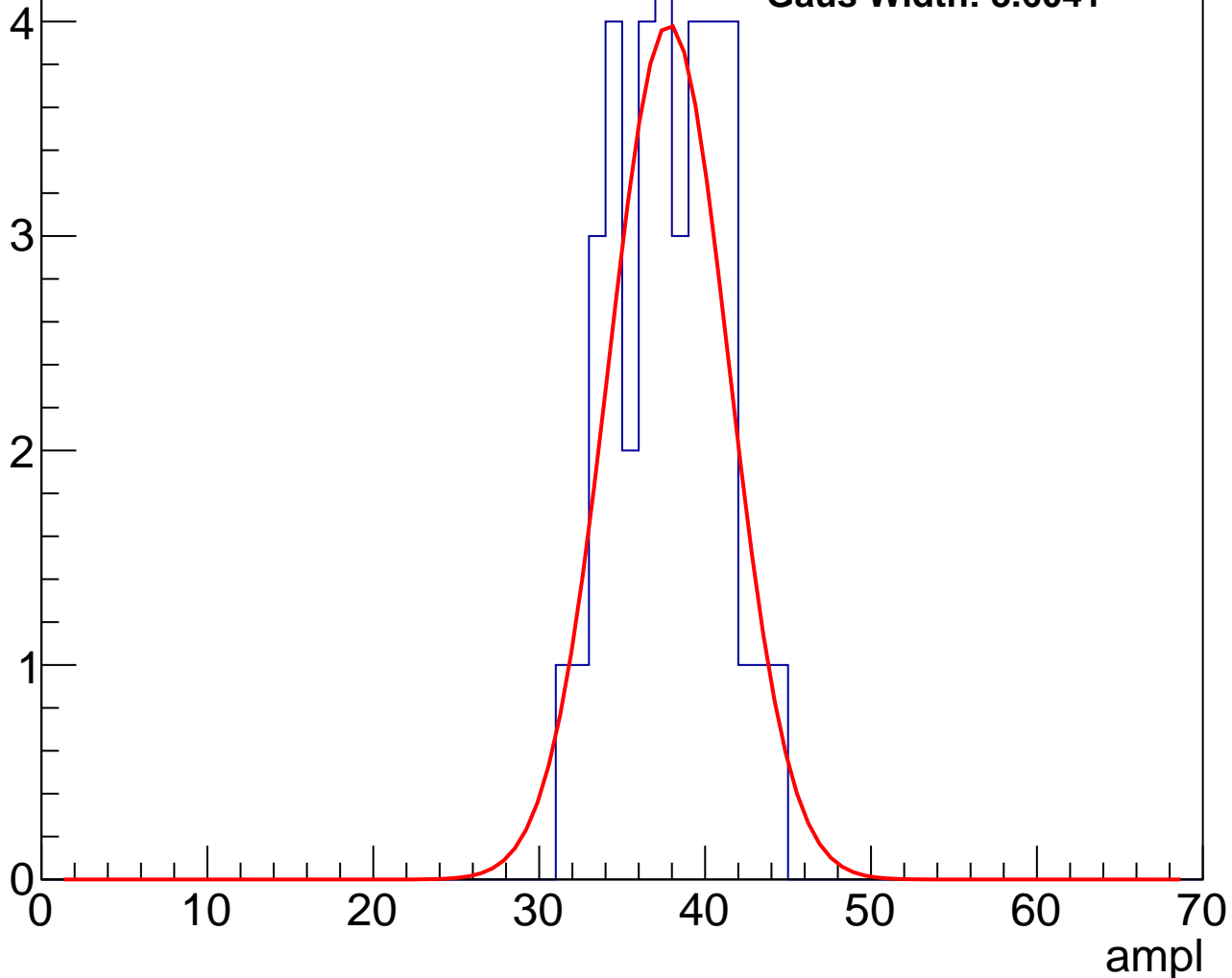
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	37.37
Std Dev	3.166

Gaus mean : 37.8098

Gaus Width: 3.6041



B1L101S, U12-ch85, adc2

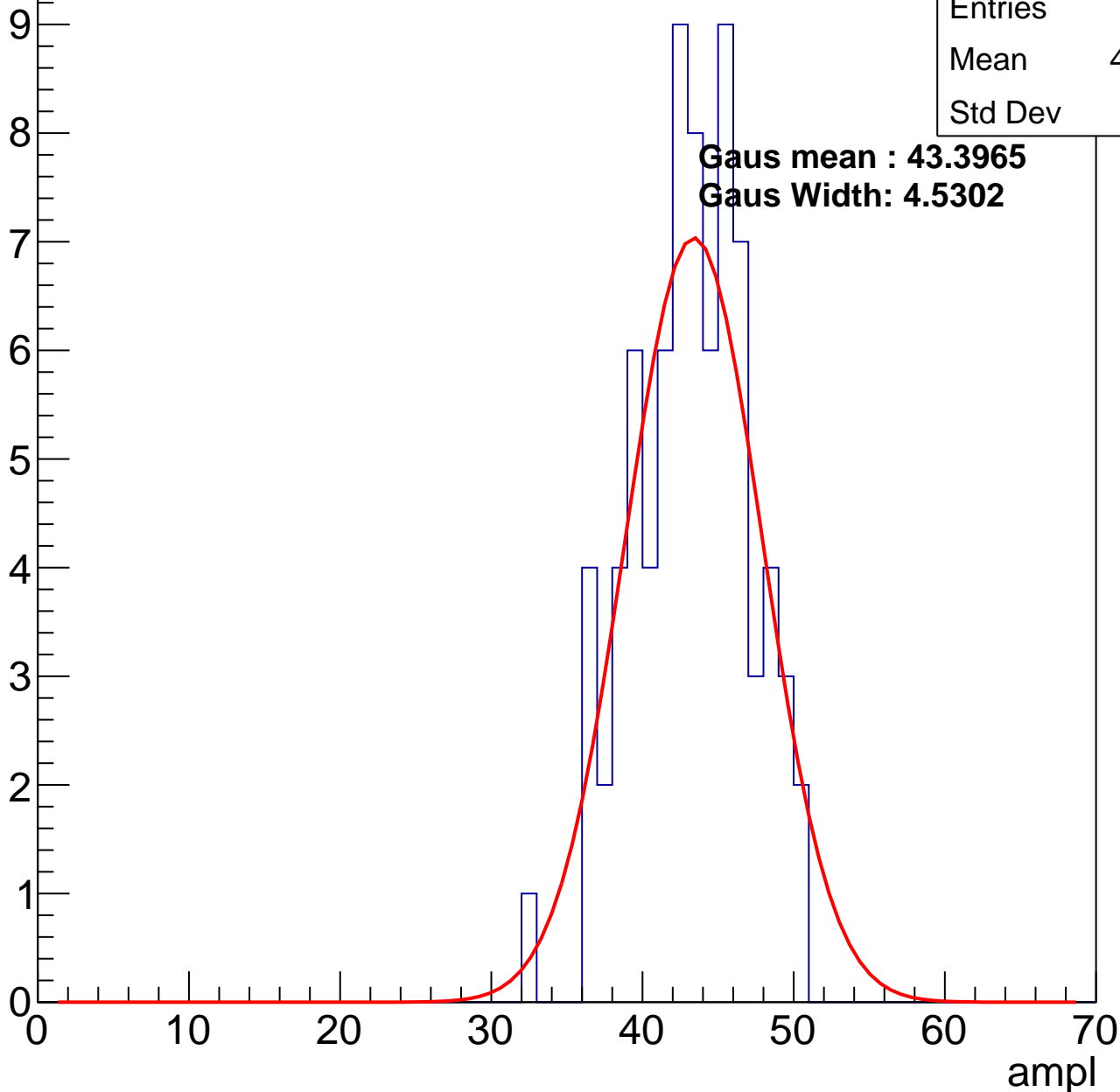
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	42.76
Std Dev	3.77

Gaus mean : 43.3965

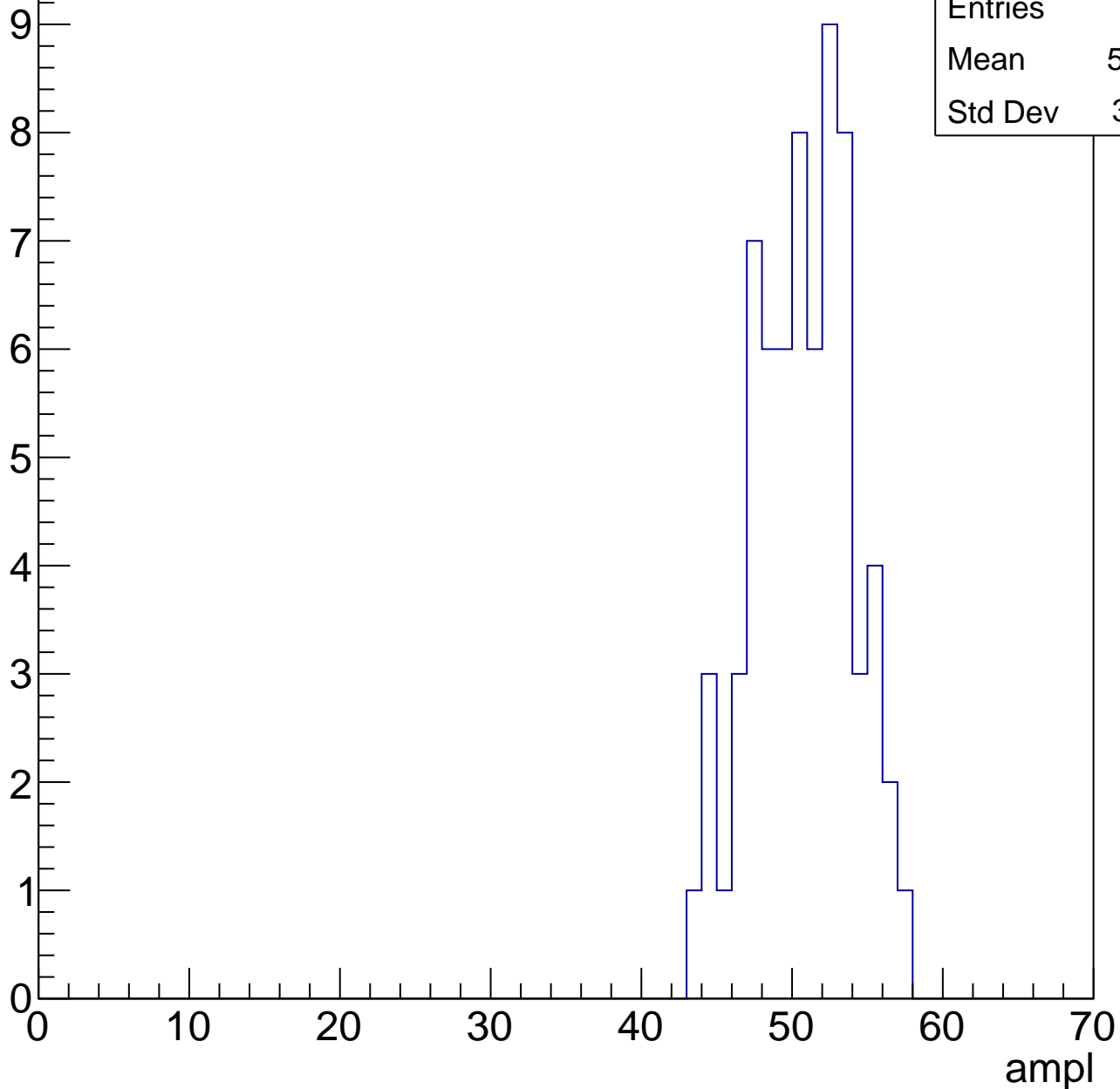
Gaus Width: 4.5302



B1L101S, U12-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

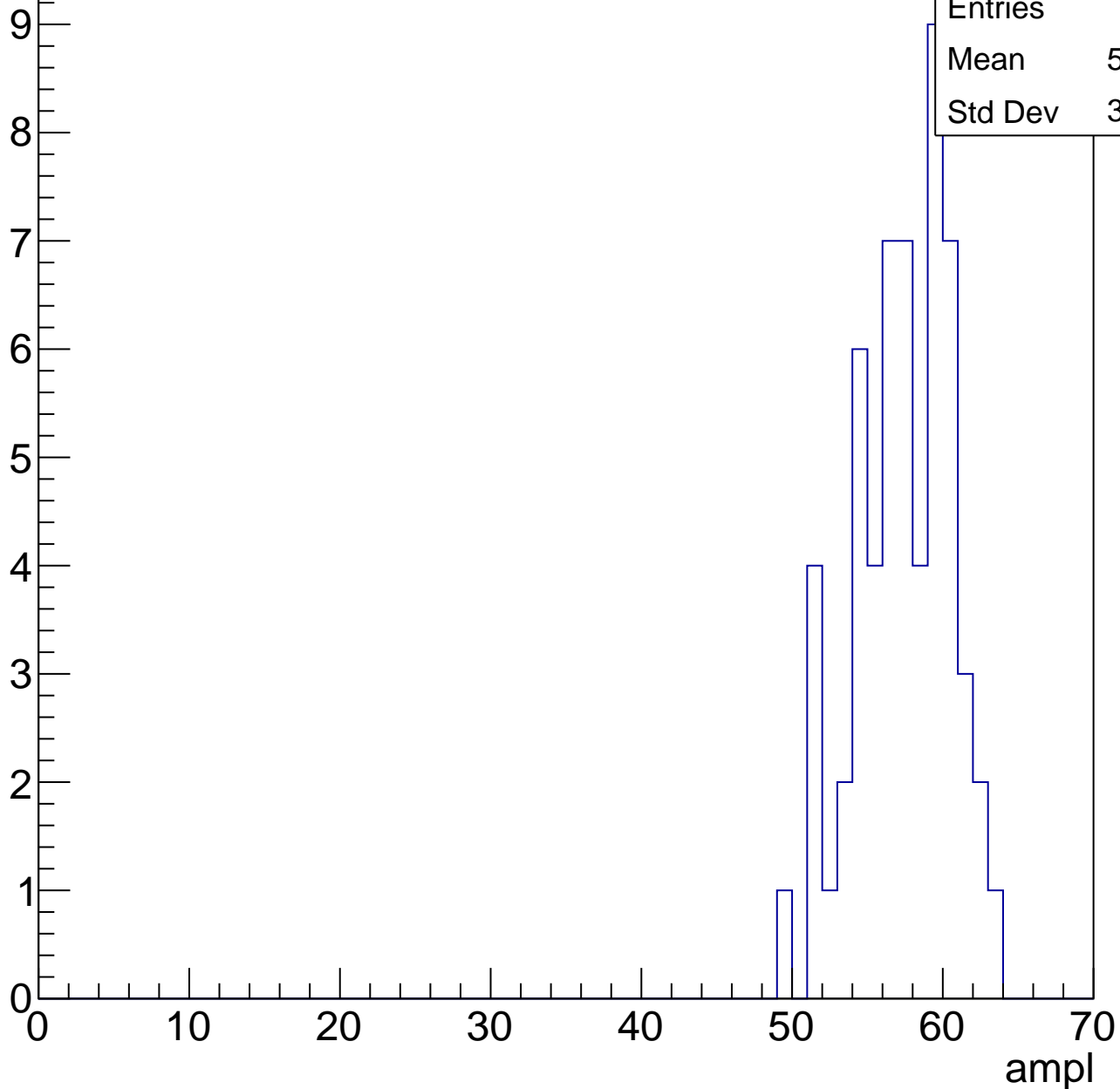


Entries	68
Mean	50.26
Std Dev	3.211

B1L101S, U12-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

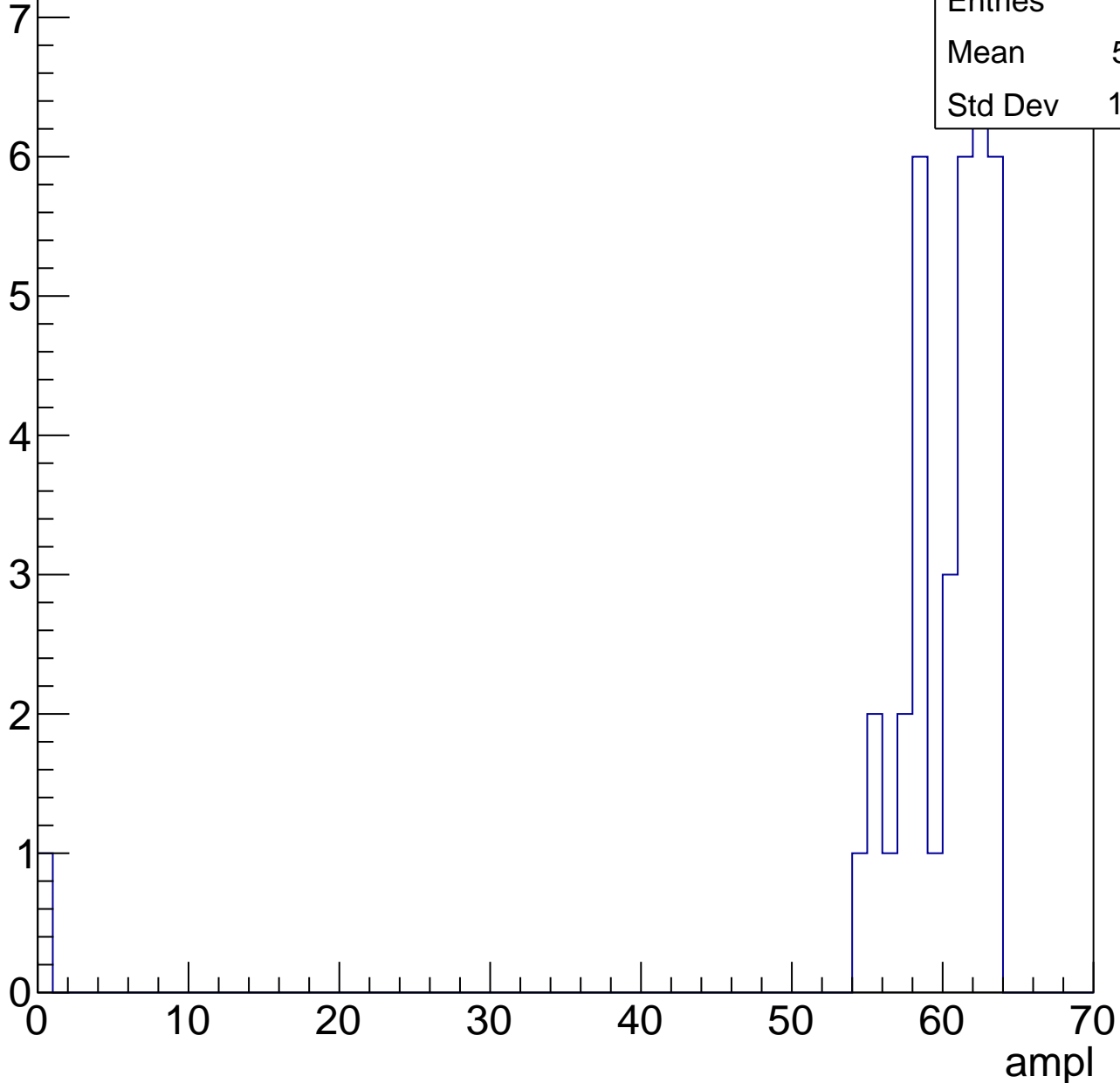


B1L101S, U12-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.31
Std Dev	10.18



B1L101S, U12-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

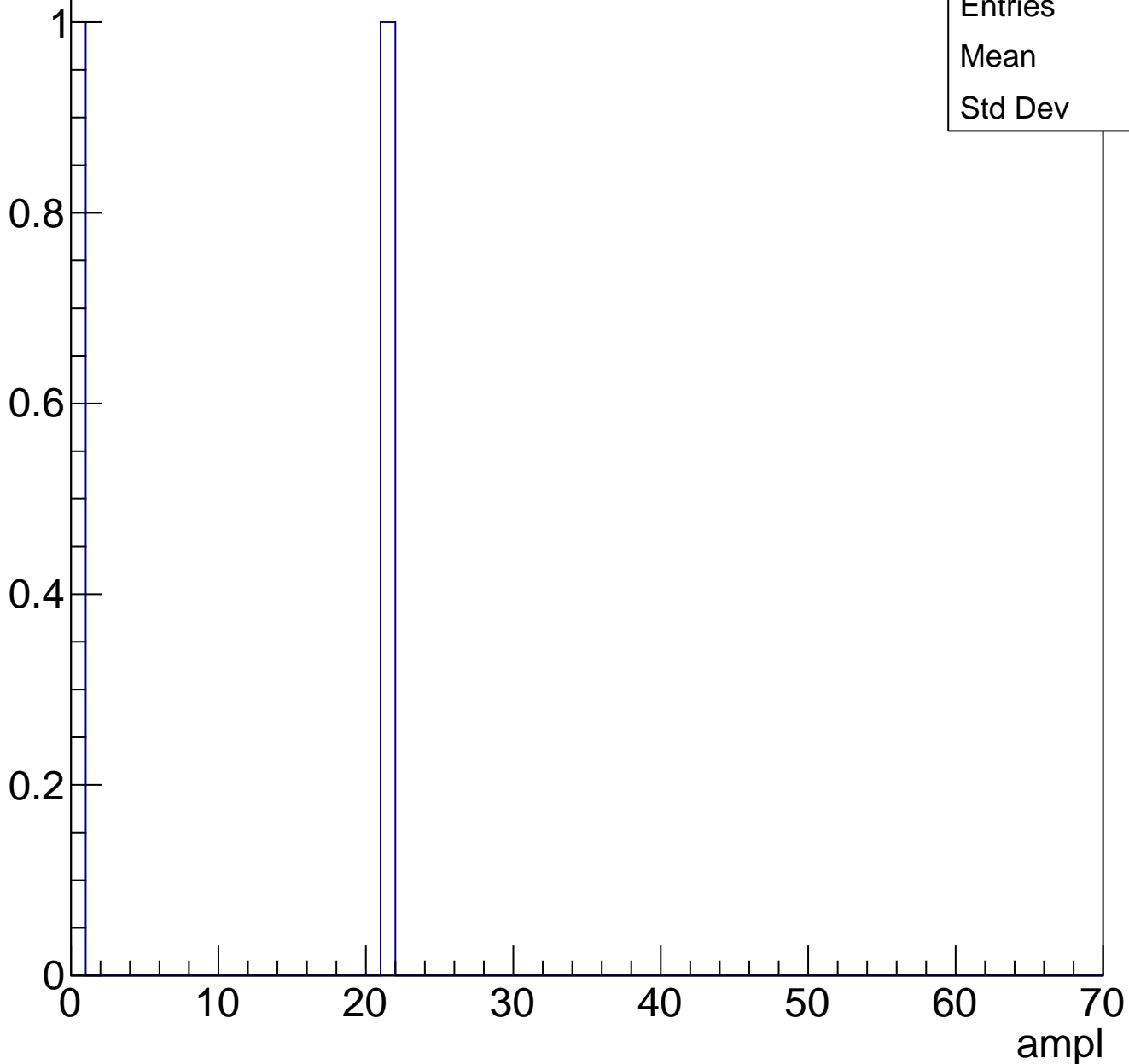
Entry



B1L101S, U12-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch86, adc0

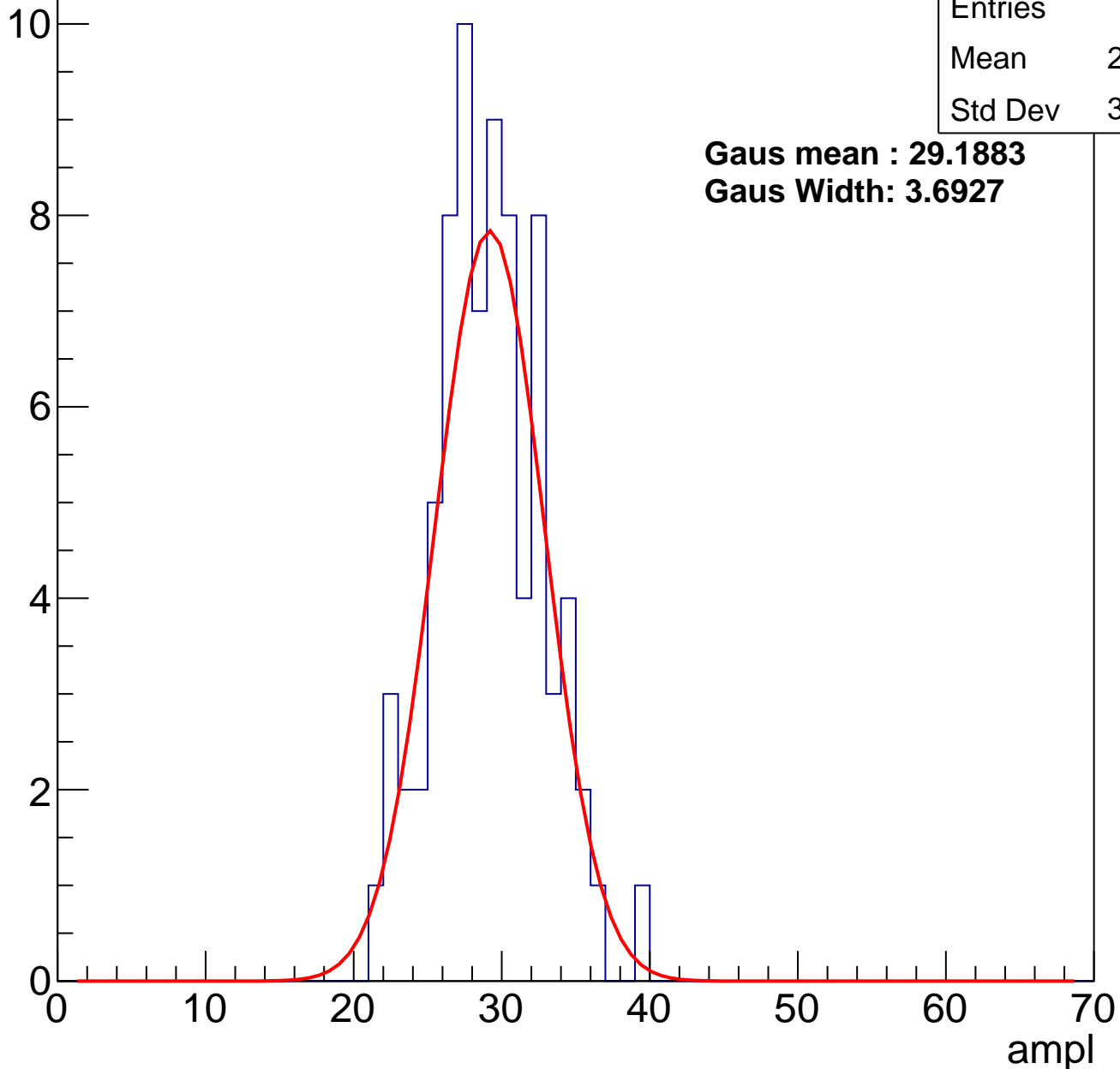
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	28.73
Std Dev	3.565

Gaus mean : 29.1883

Gaus Width: 3.6927

Entry



B1L101S, U12-ch86, adc1

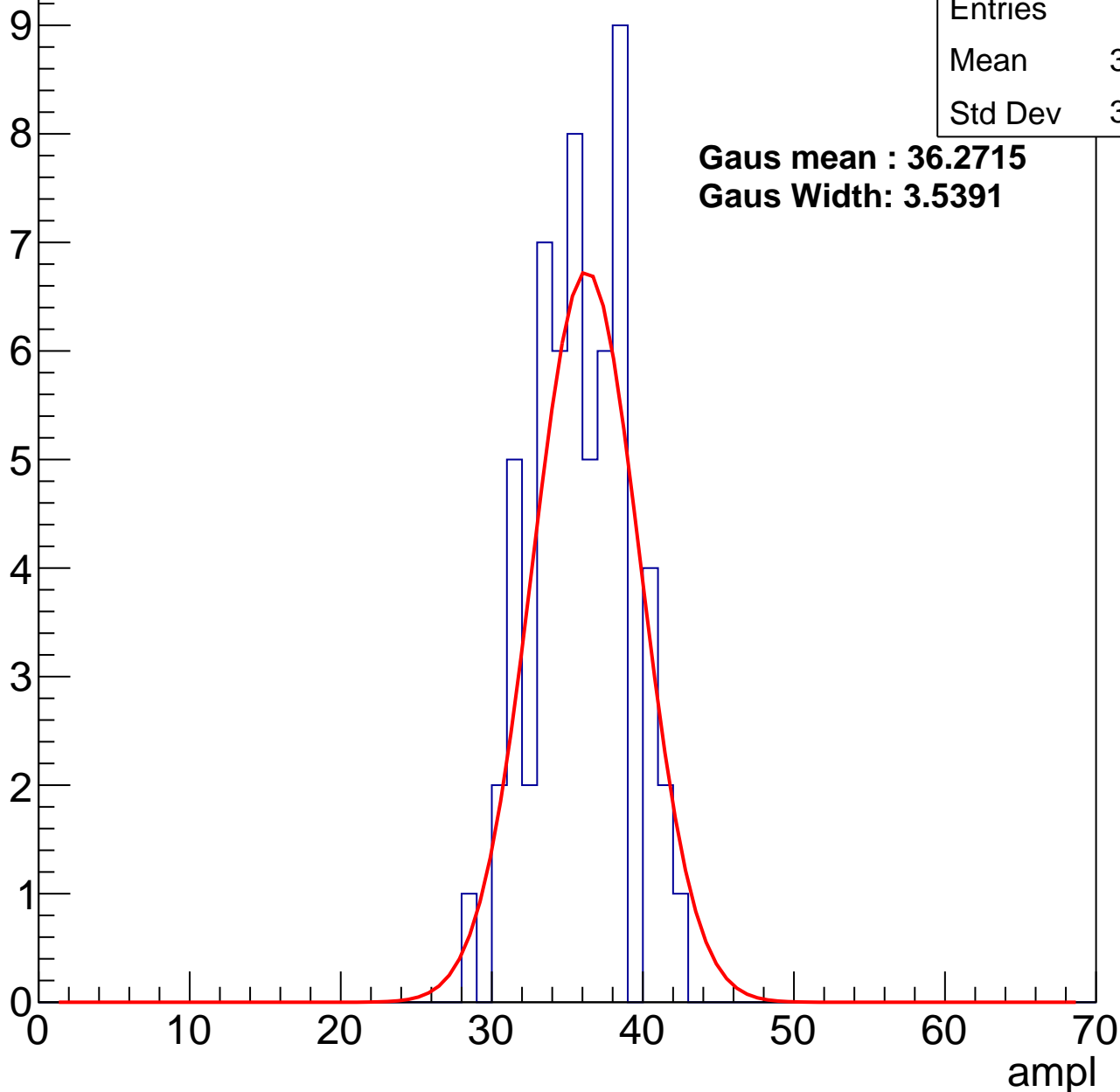
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.34
Std Dev	3.094

Gaus mean : 36.2715

Gaus Width: 3.5391



B1L101S, U12-ch86, adc2

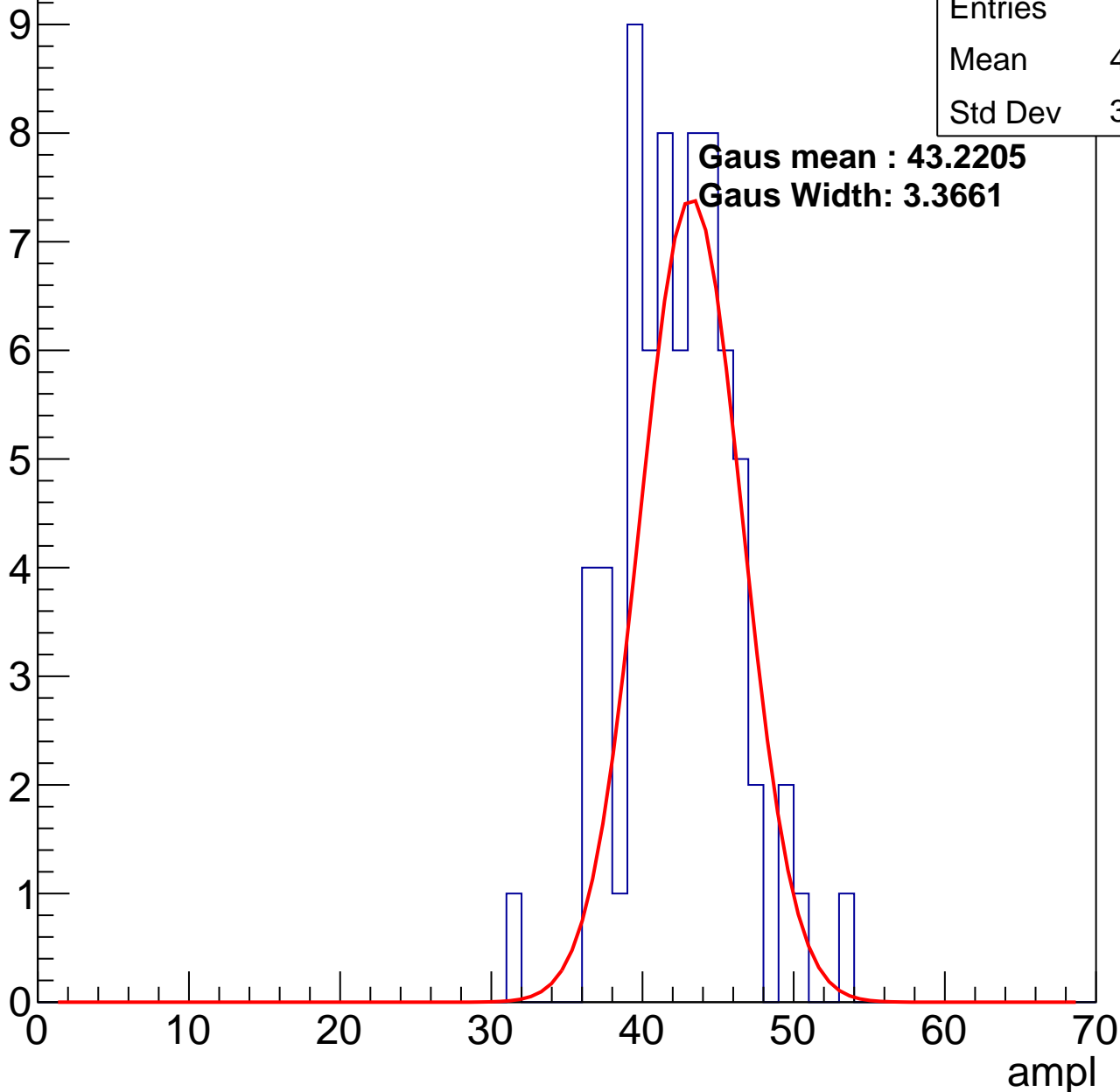
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	41.99
Std Dev	3.729

Gaus mean : 43.2205

Gaus Width: 3.3661

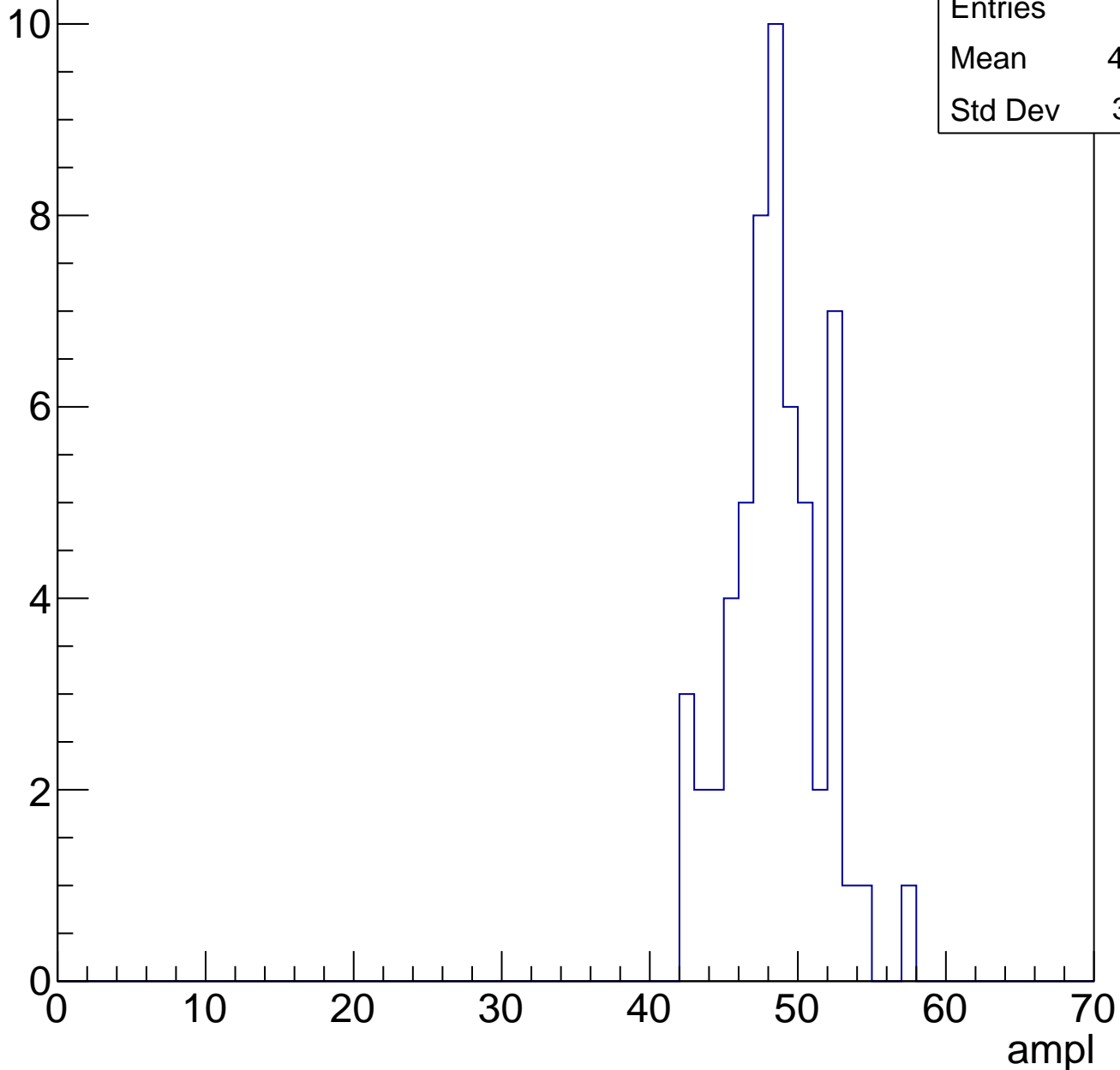


B1L101S, U12-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	48.07
Std Dev	3.111

Entry

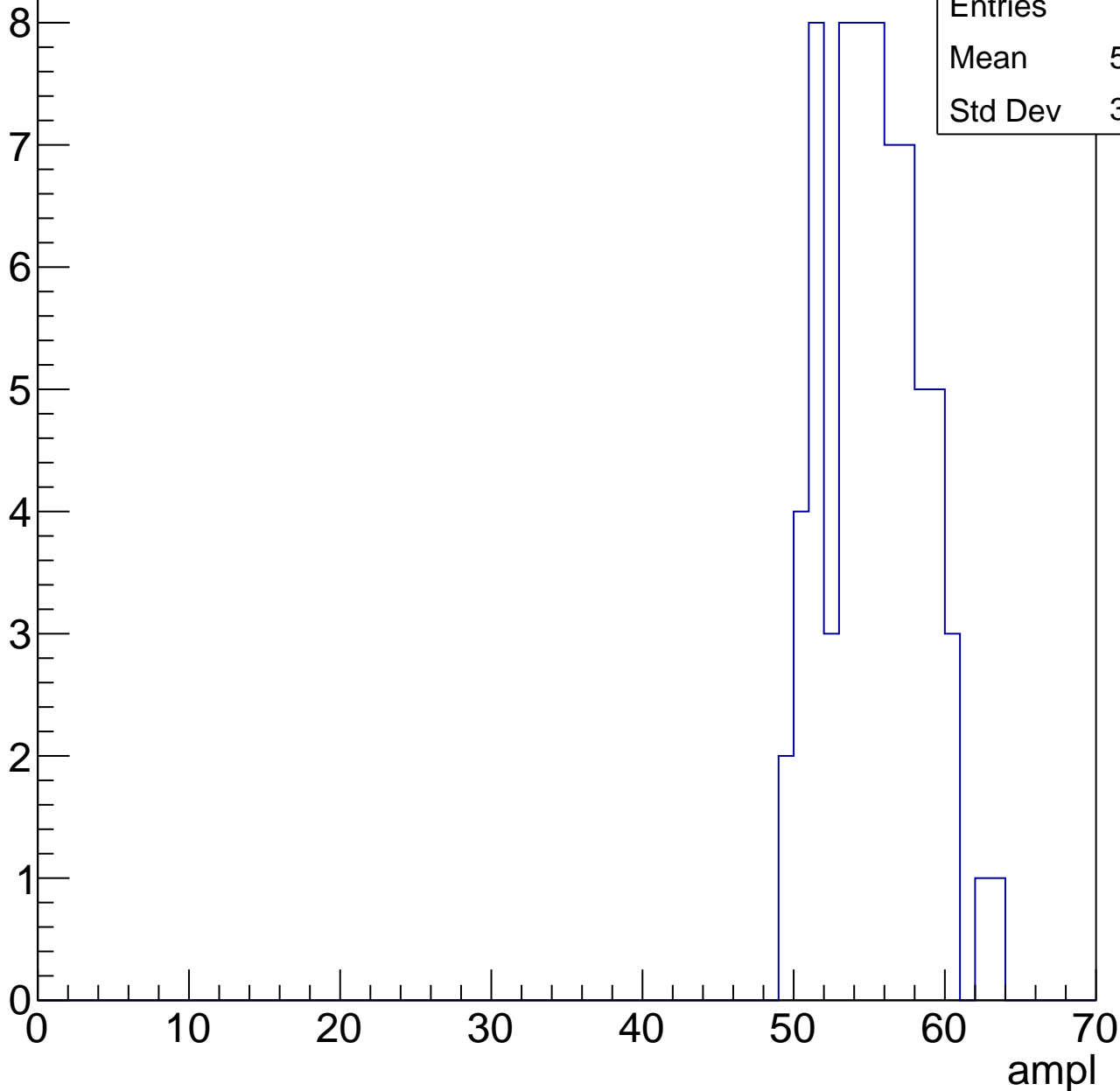


B1L101S, U12-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	54.84
Std Dev	3.192

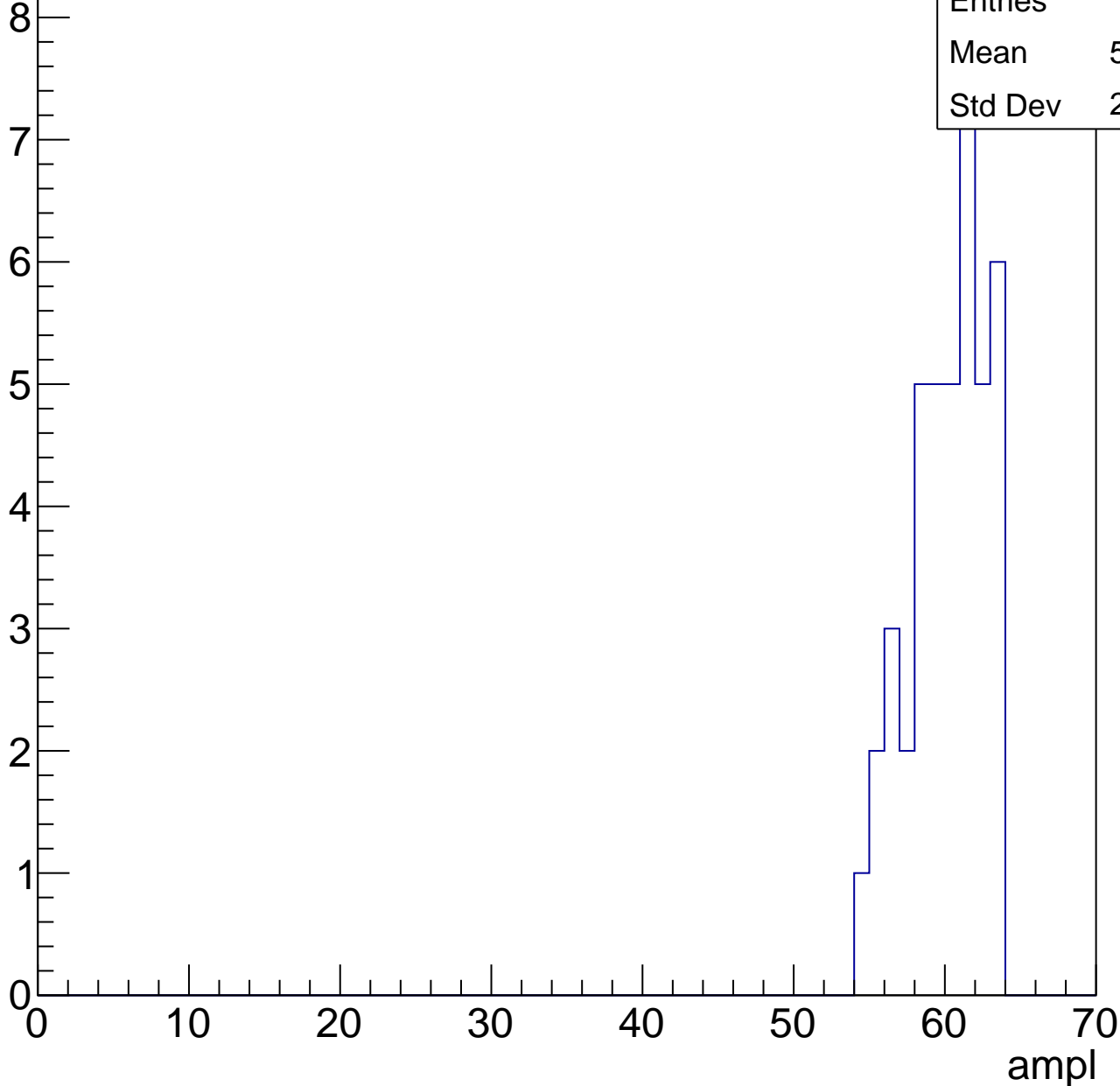


B1L101S, U12-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

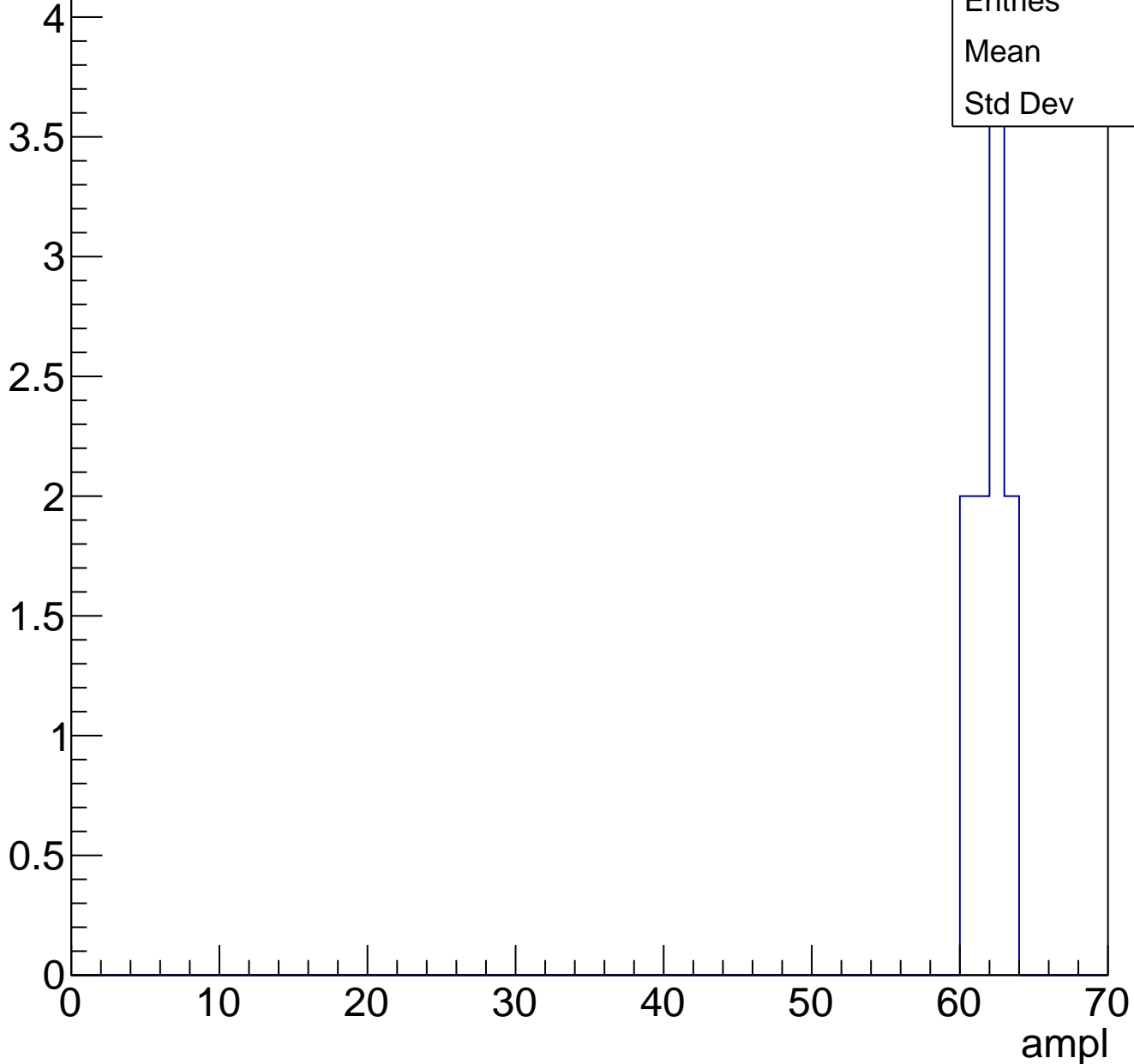
Entries	42
Mean	59.69
Std Dev	2.464



B1L101S, U12-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch87, adc0

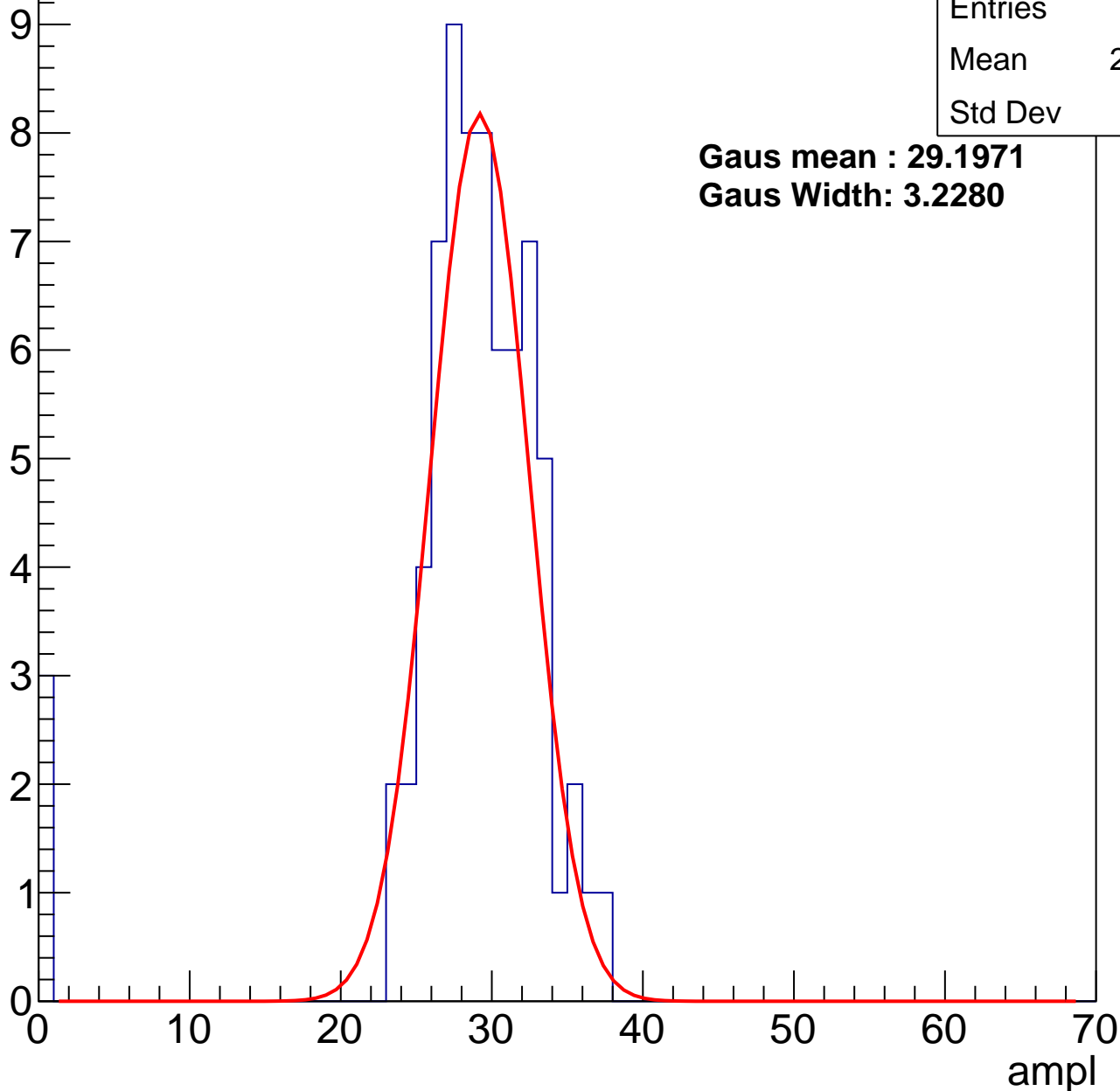
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.88
Std Dev	6.57

Gaus mean : 29.1971

Gaus Width: 3.2280



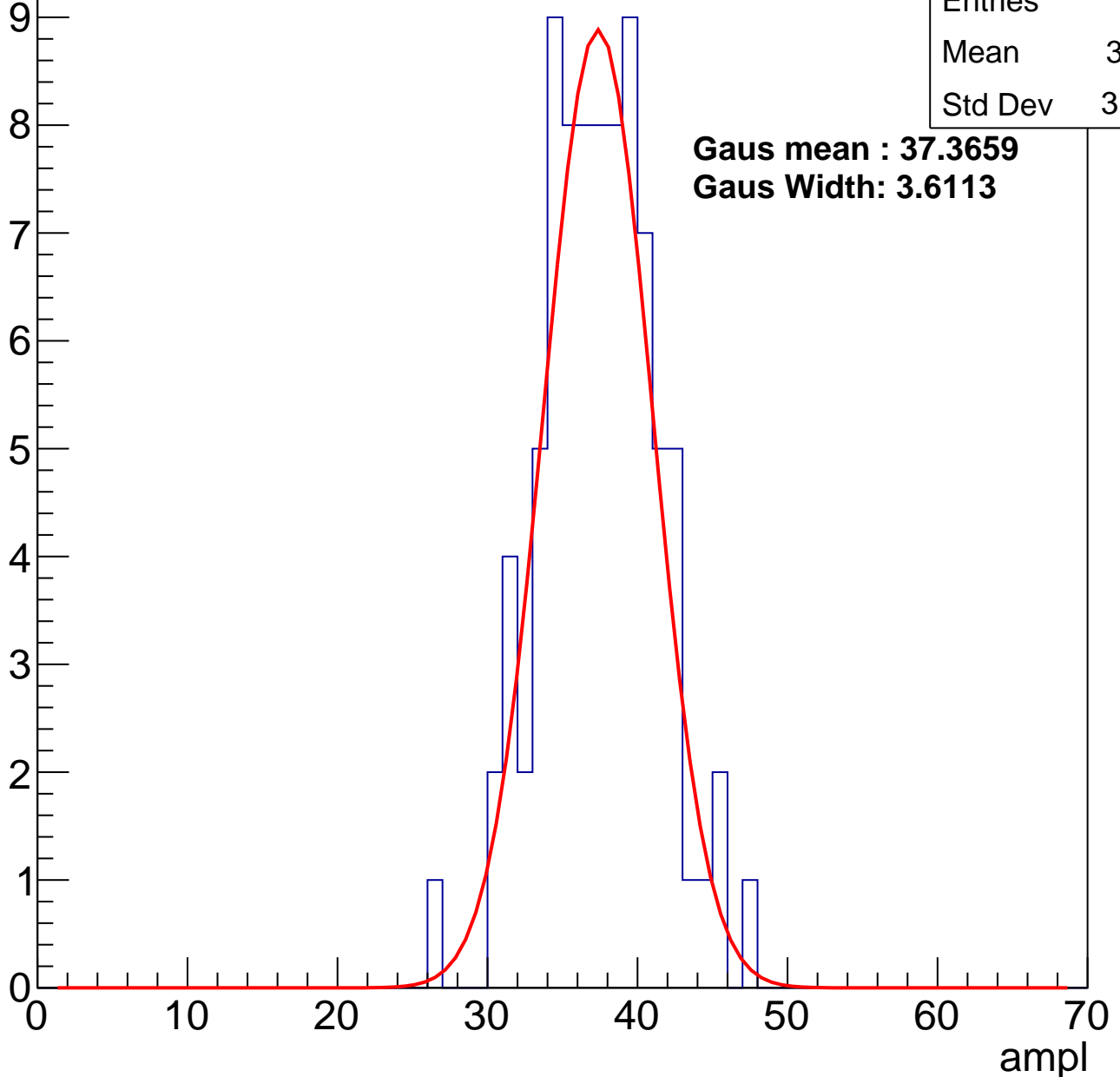
B1L101S, U12-ch87, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	37.01
Std Dev	3.796

Gaus mean : 37.3659
Gaus Width: 3.6113



B1L101S, U12-ch87, adc2

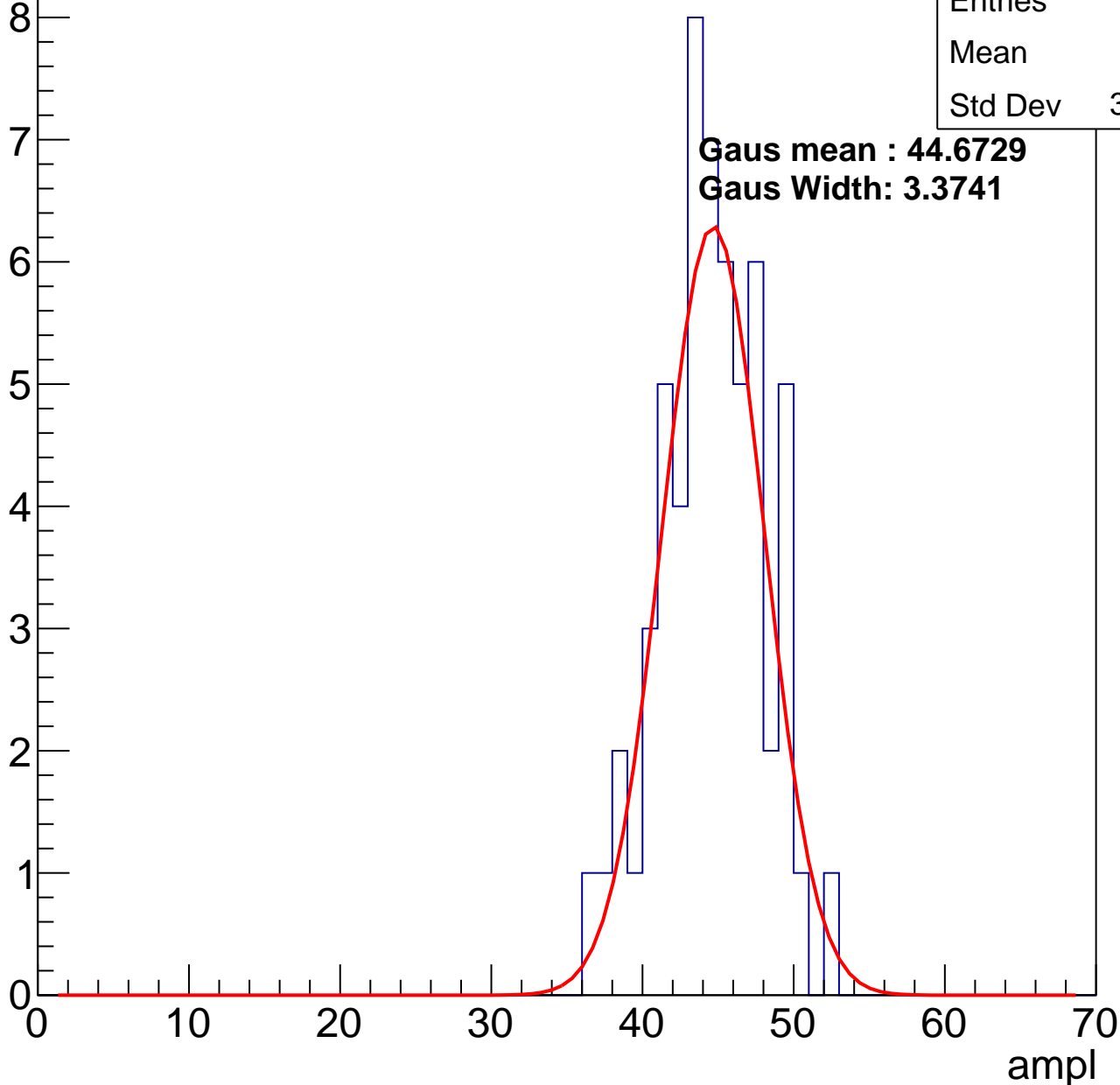
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.1
Std Dev	3.382

Gaus mean : 44.6729

Gaus Width: 3.3741

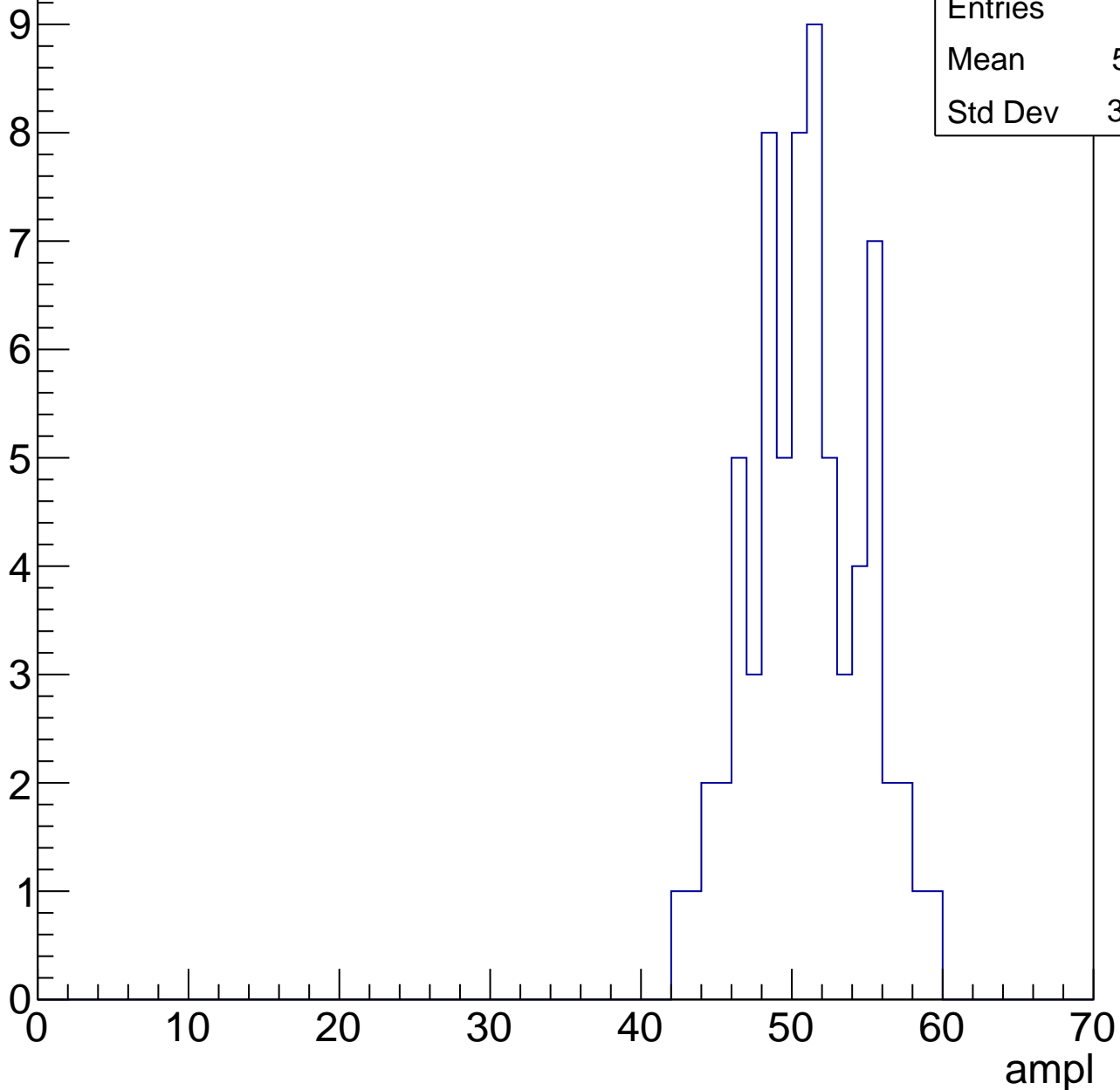


B1L101S, U12-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.51
Std Dev	3.752

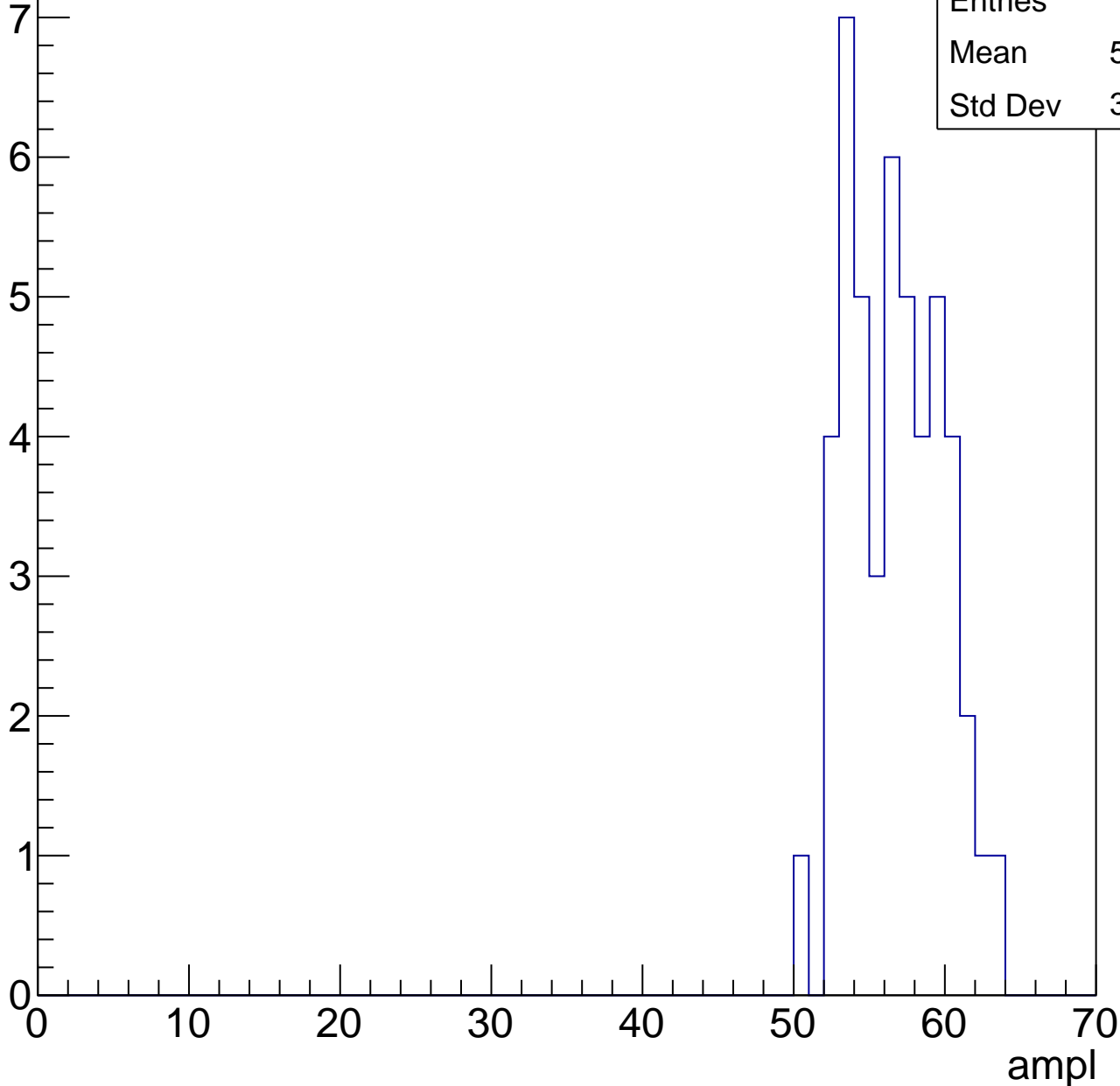


B1L101S, U12-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	56.23
Std Dev	3.057

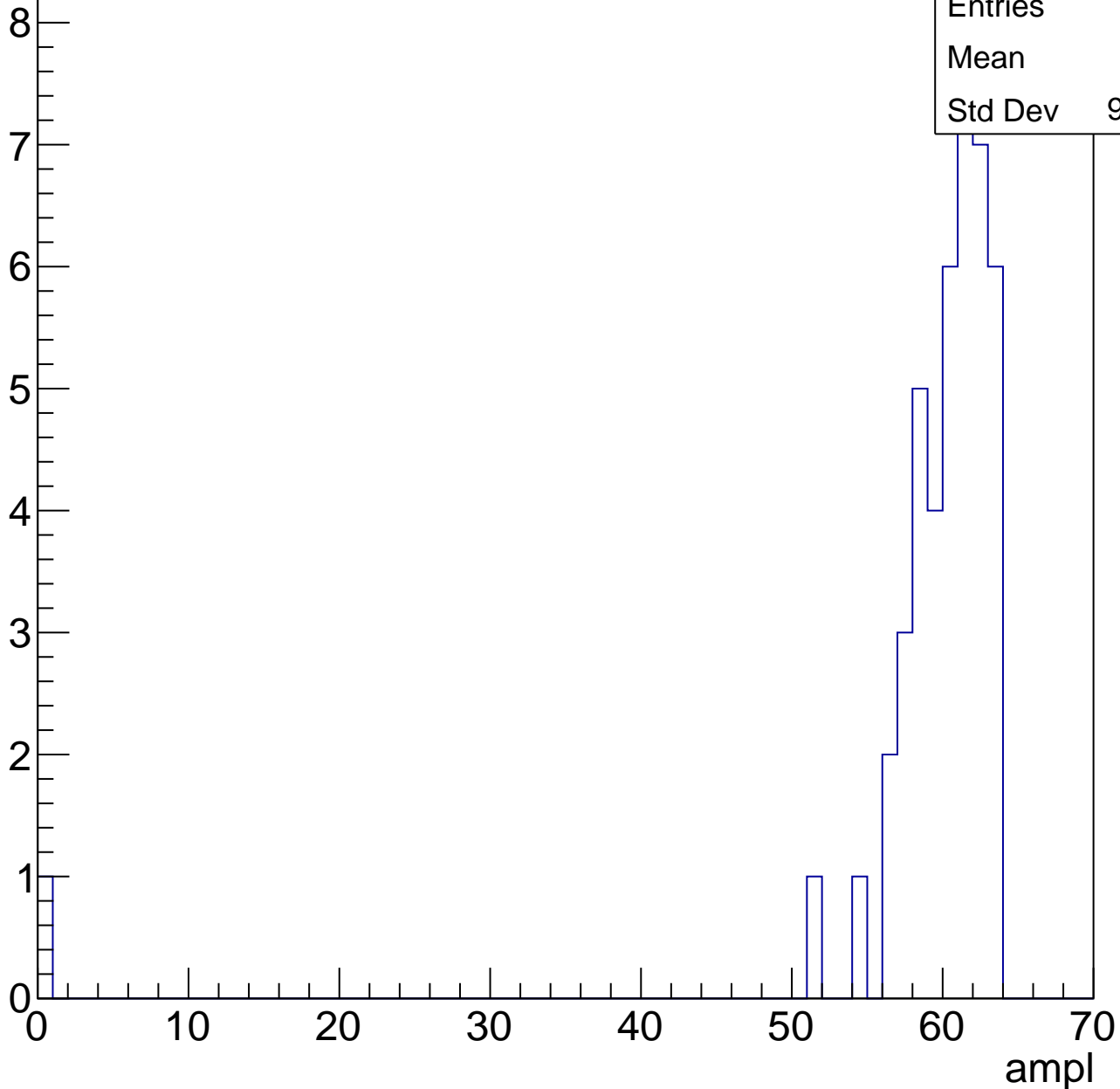


B1L101S, U12-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.5
Std Dev	9.282



B1L101S, U12-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch88, adc0

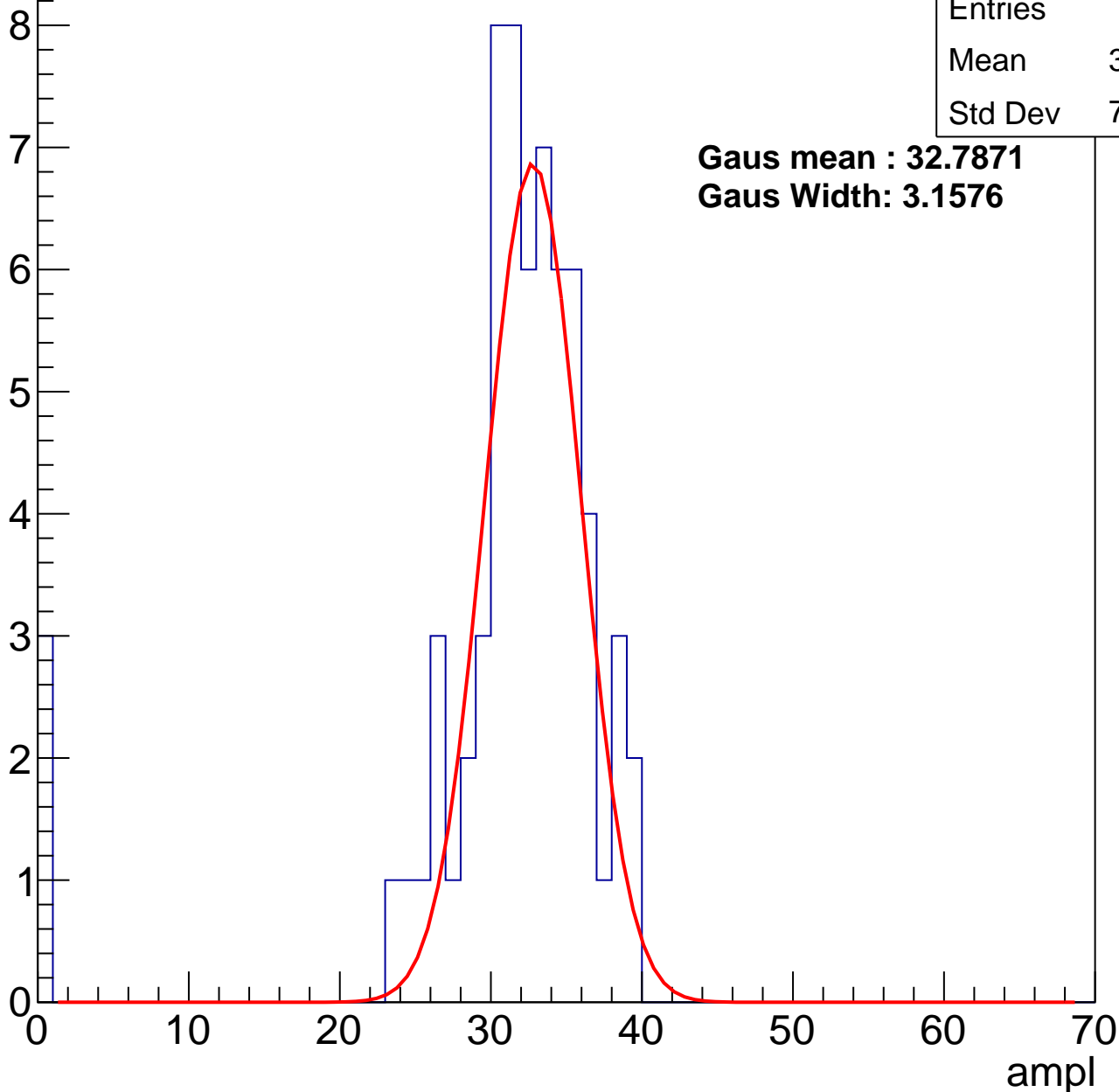
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.58
Std Dev	7.532

Gaus mean : 32.7871

Gaus Width: 3.1576



B1L101S, U12-ch88, adc1

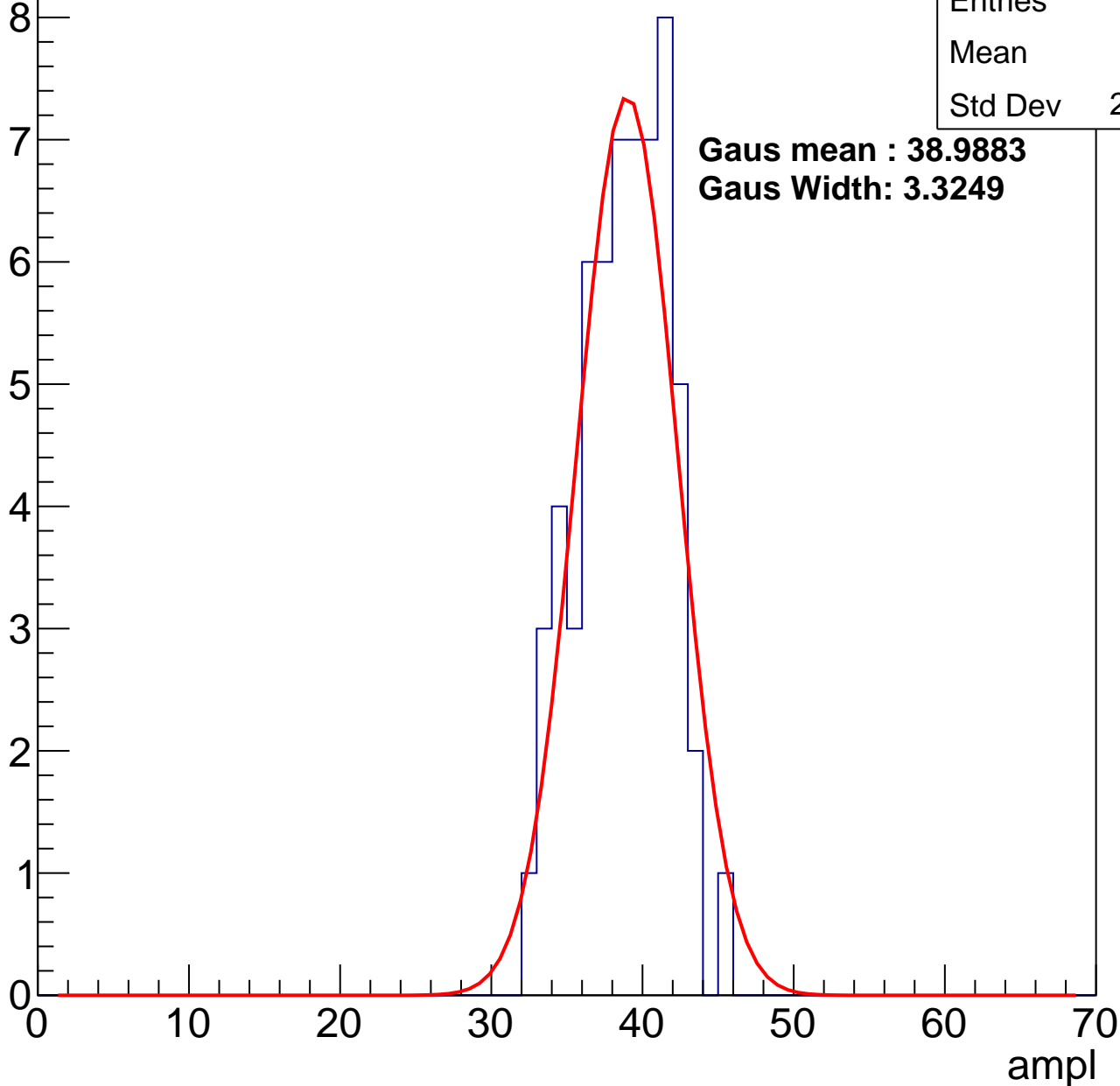
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.3
Std Dev	2.923

Gaus mean : 38.9883

Gaus Width: 3.3249



B1L101S, U12-ch88, adc2

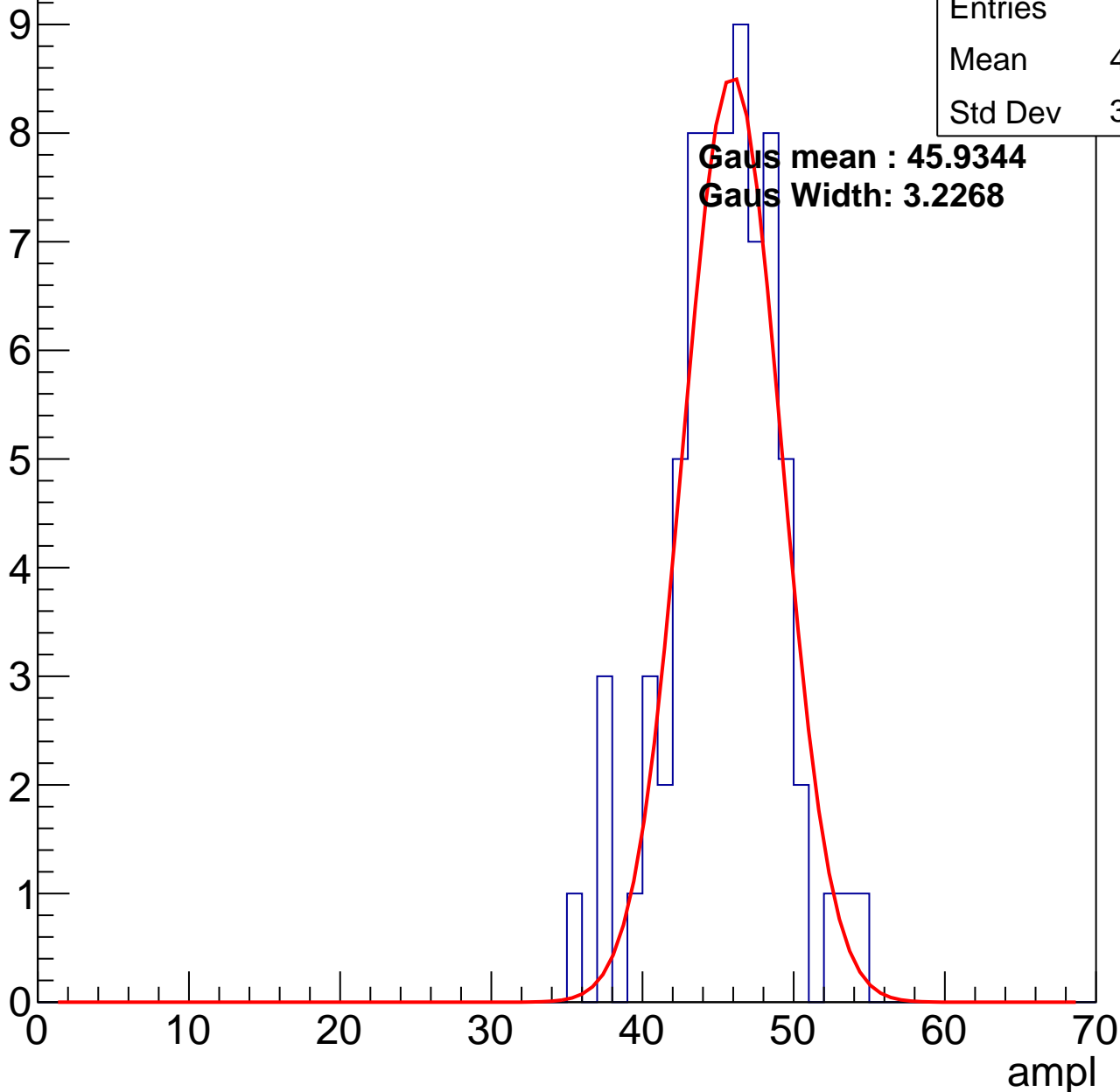
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	44.99
Std Dev	3.613

Gaus mean : 45.9344

Gaus Width: 3.2268

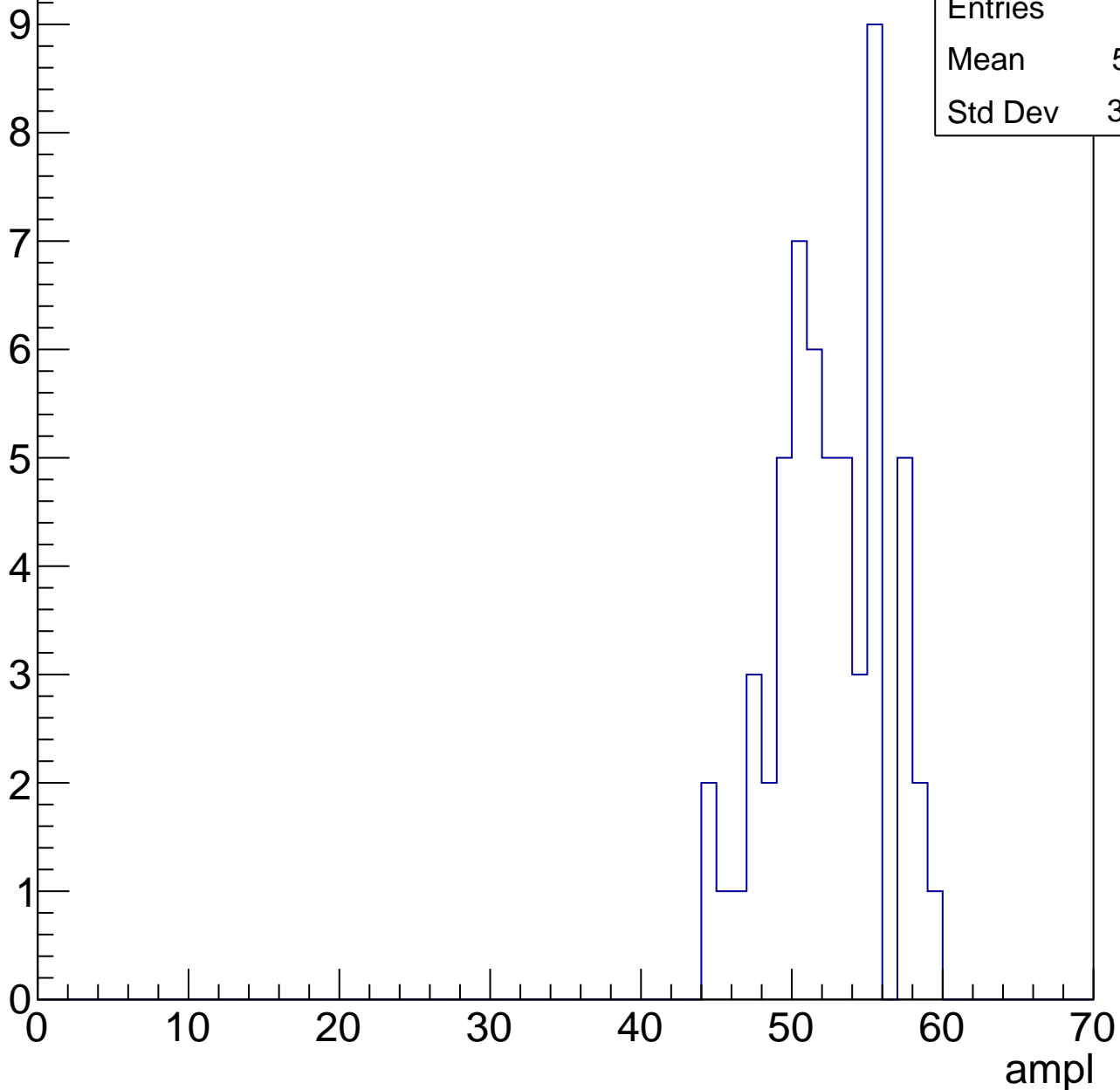


B1L101S, U12-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

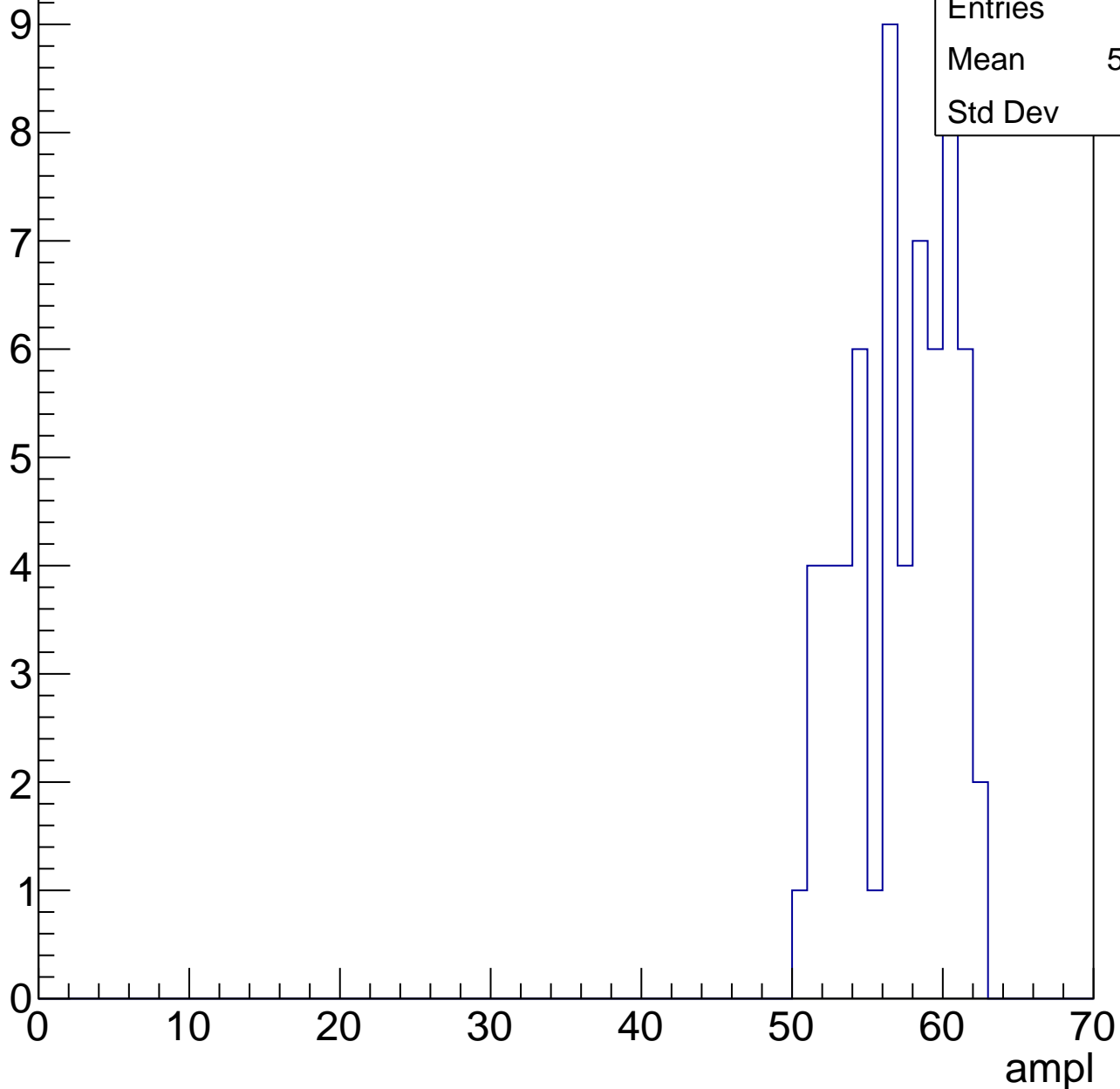
Entries	57
Mean	51.91
Std Dev	3.609



B1L101S, U12-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

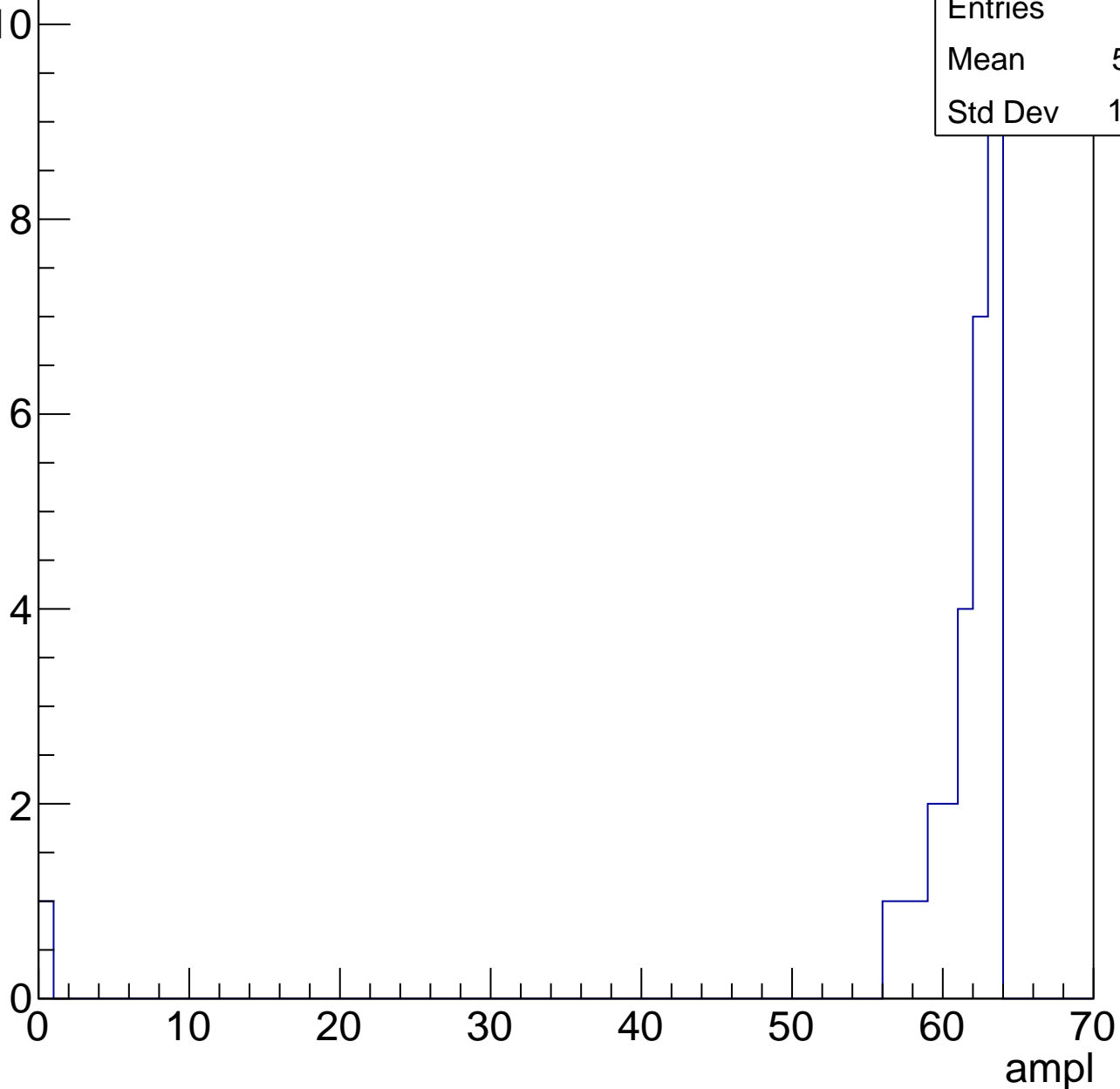


B1L101S, U12-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

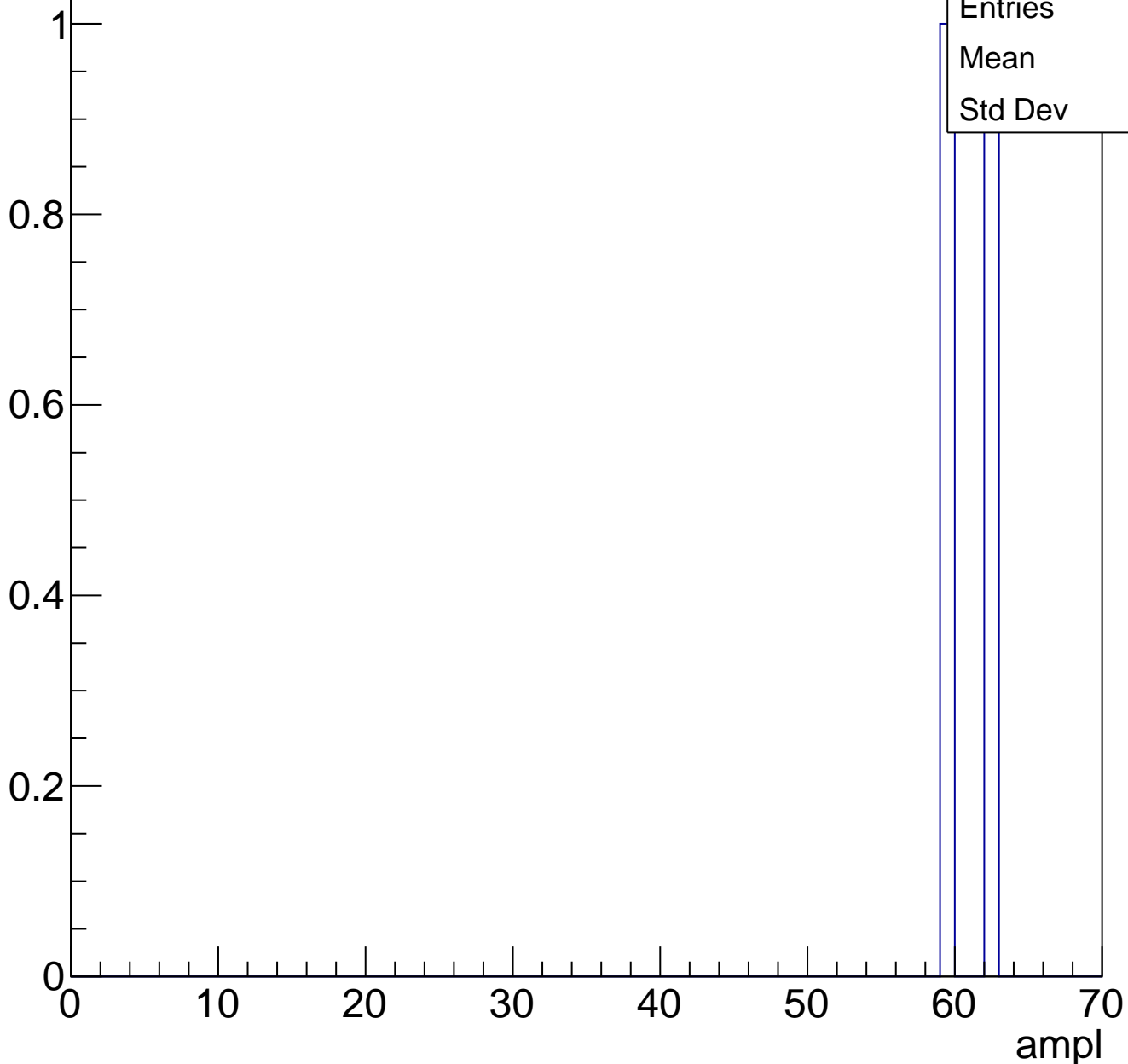
Entries	29
Mean	59.21
Std Dev	11.35



B1L101S, U12-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	60.5
Std Dev	1.5

B1L101S, U12-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch89, adc0

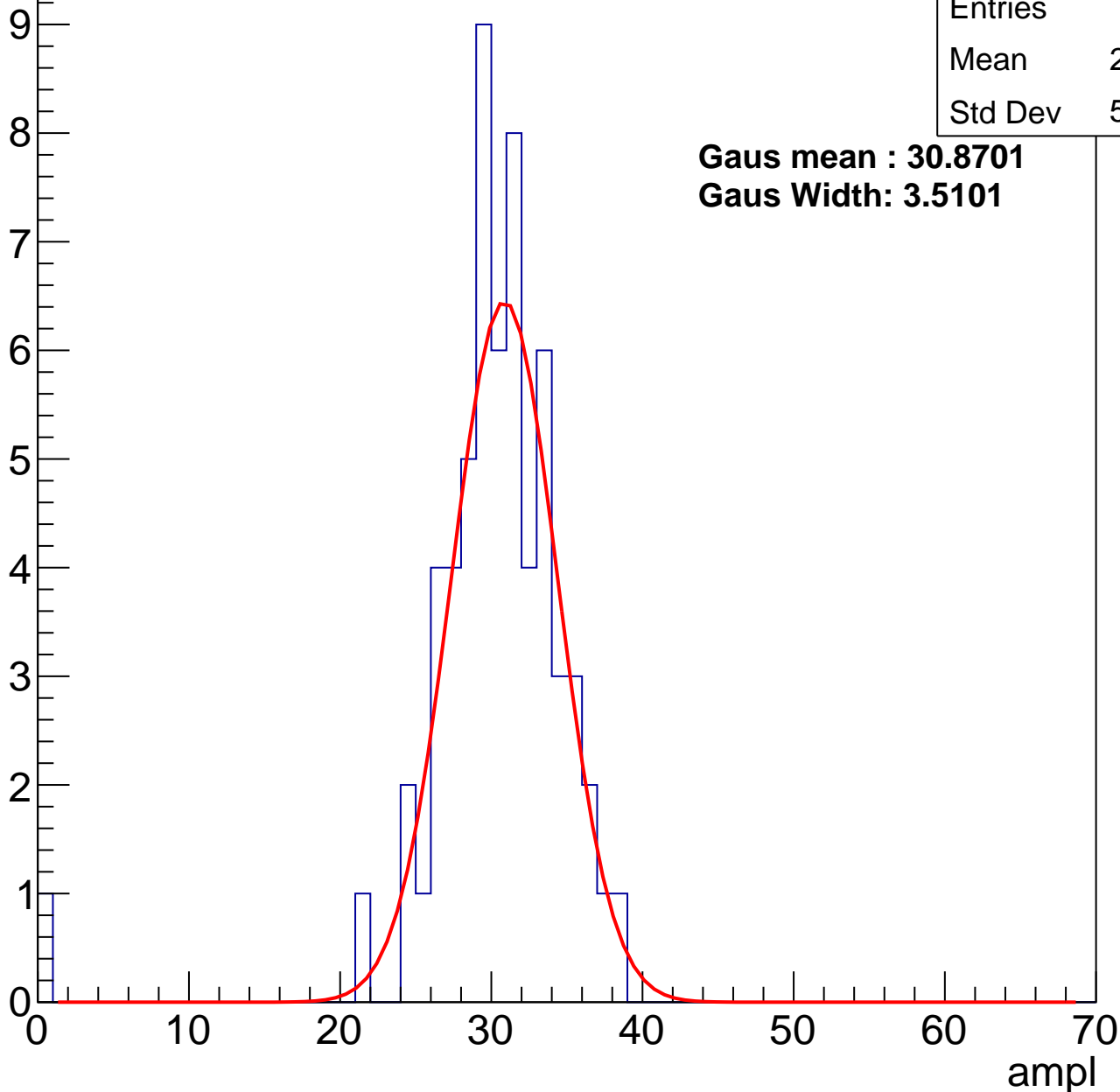
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.75
Std Dev	5.104

Gaus mean : 30.8701

Gaus Width: 3.5101



B1L101S, U12-ch89, adc1

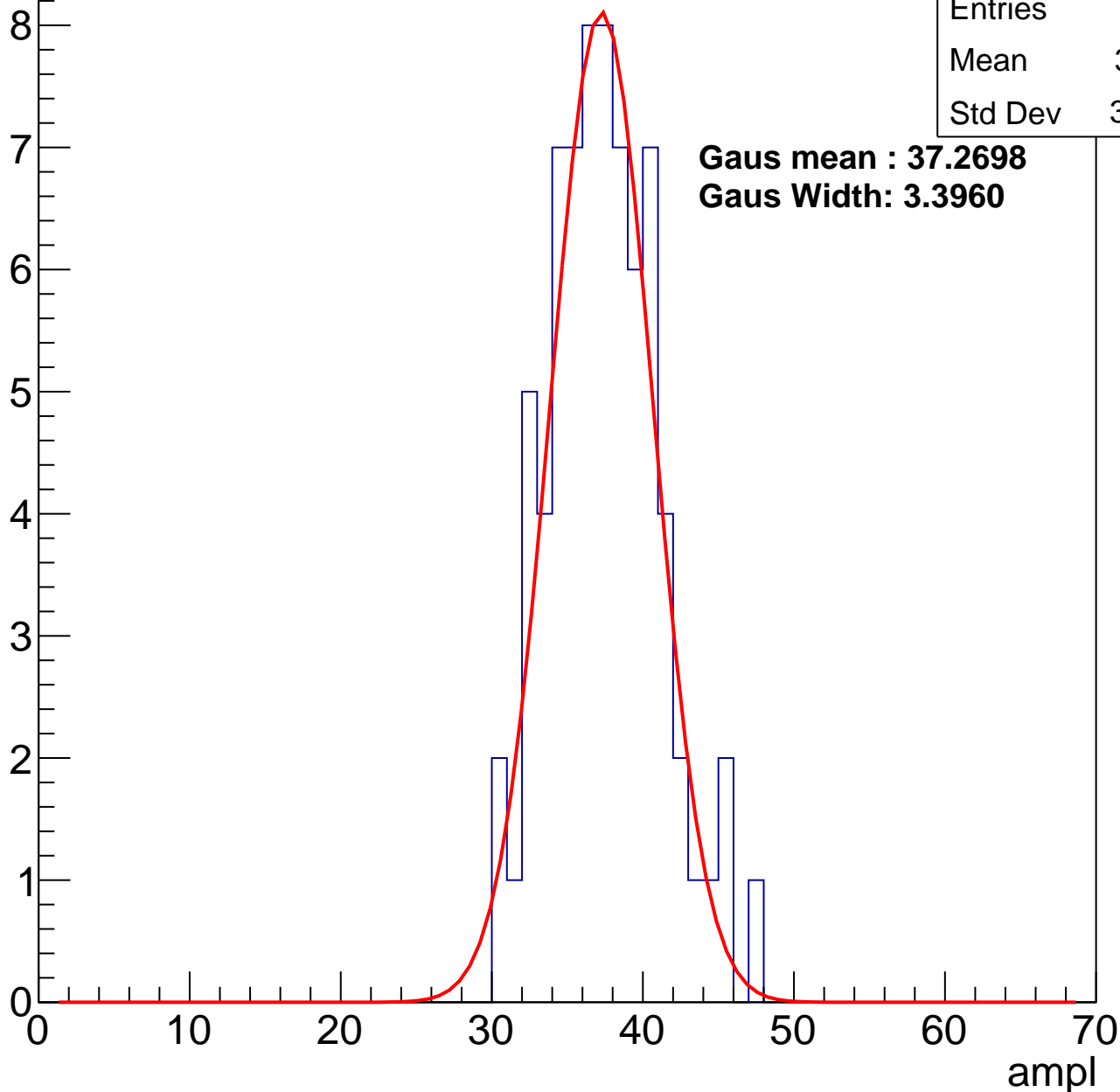
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	37.01
Std Dev	3.594

Gaus mean : 37.2698

Gaus Width: 3.3960



B1L101S, U12-ch89, adc2

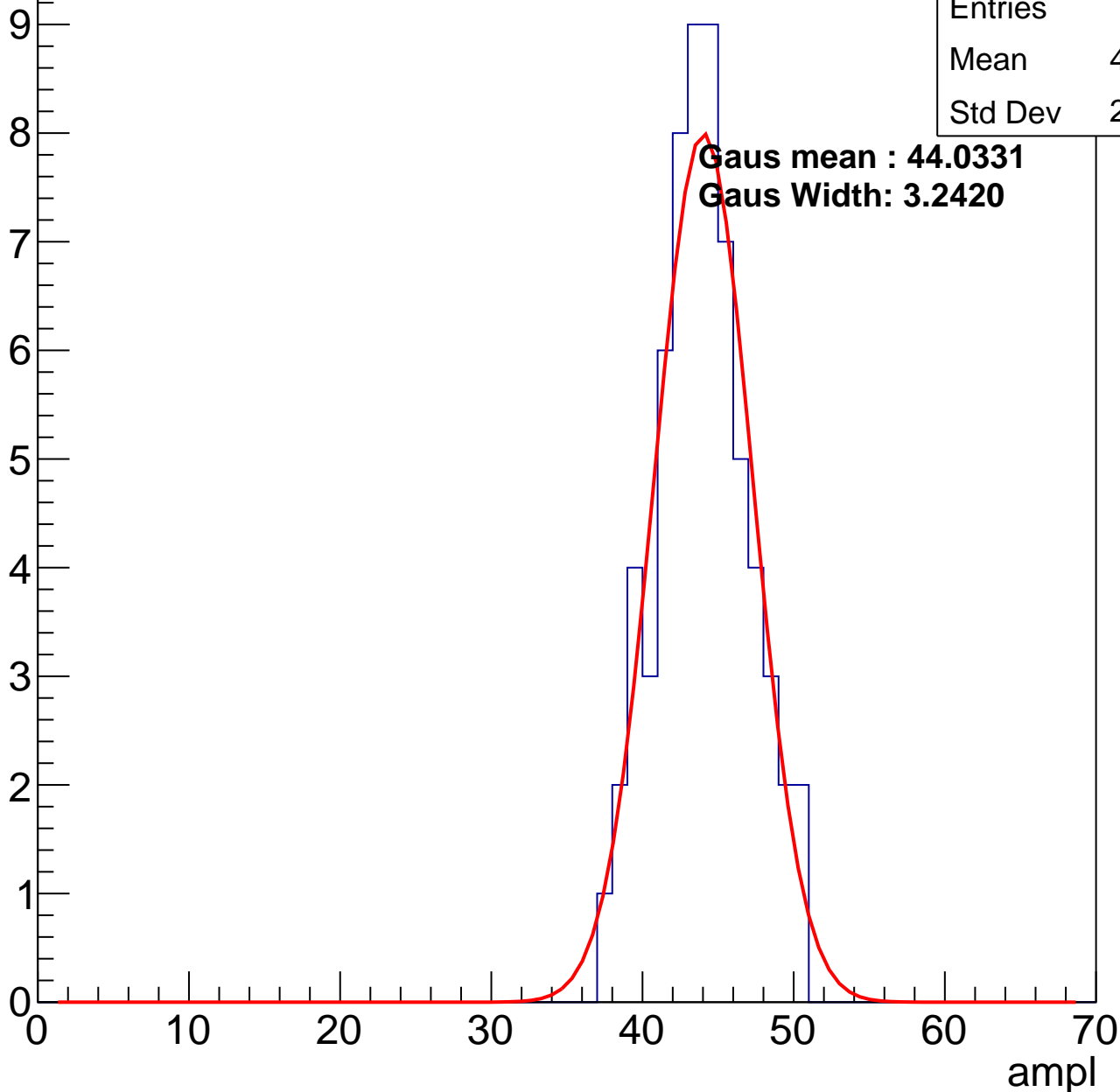
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.52
Std Dev	2.993

Gaus mean : 44.0331

Gaus Width: 3.2420

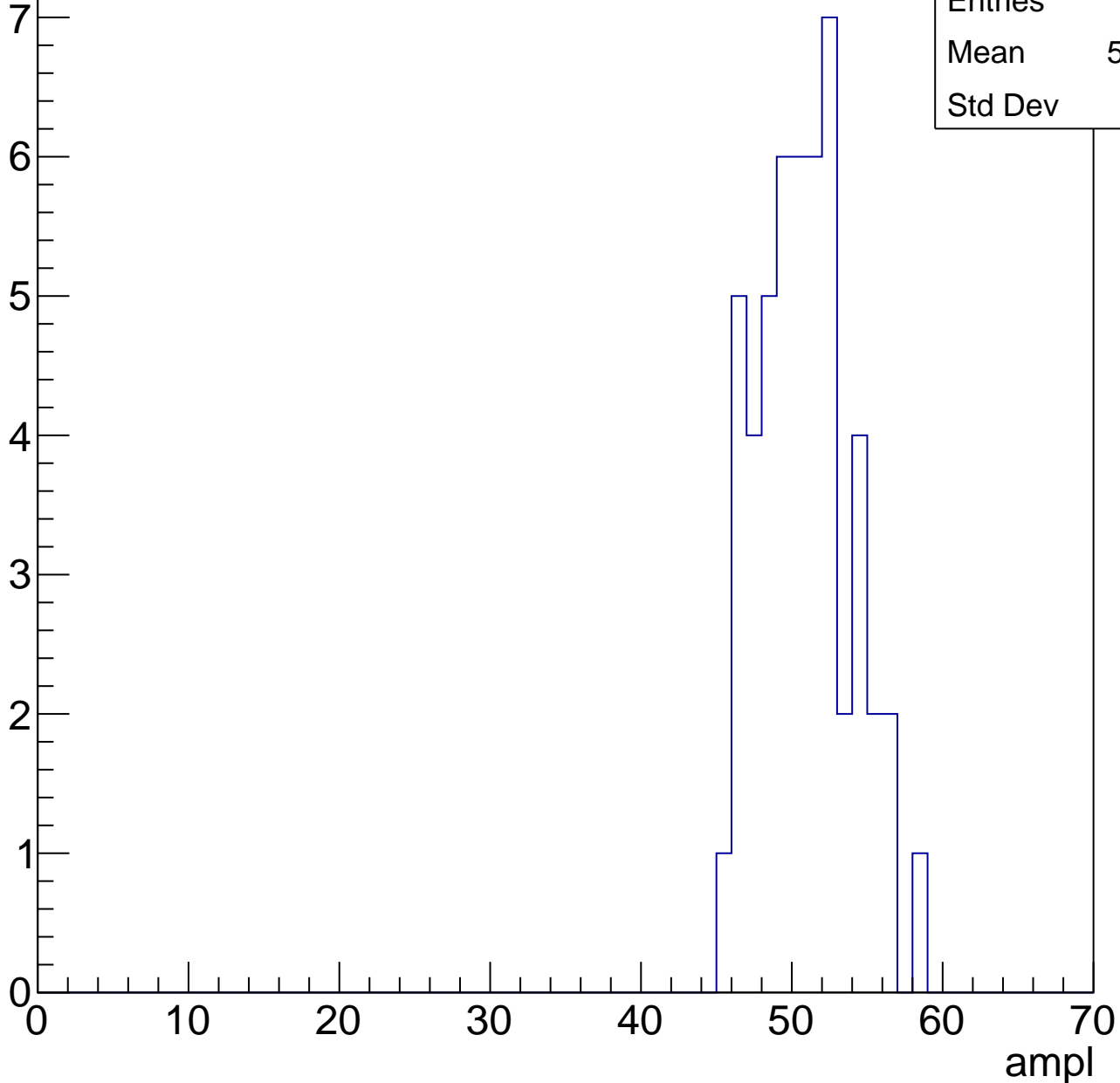


B1L101S, U12-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	50.37
Std Dev	3.01

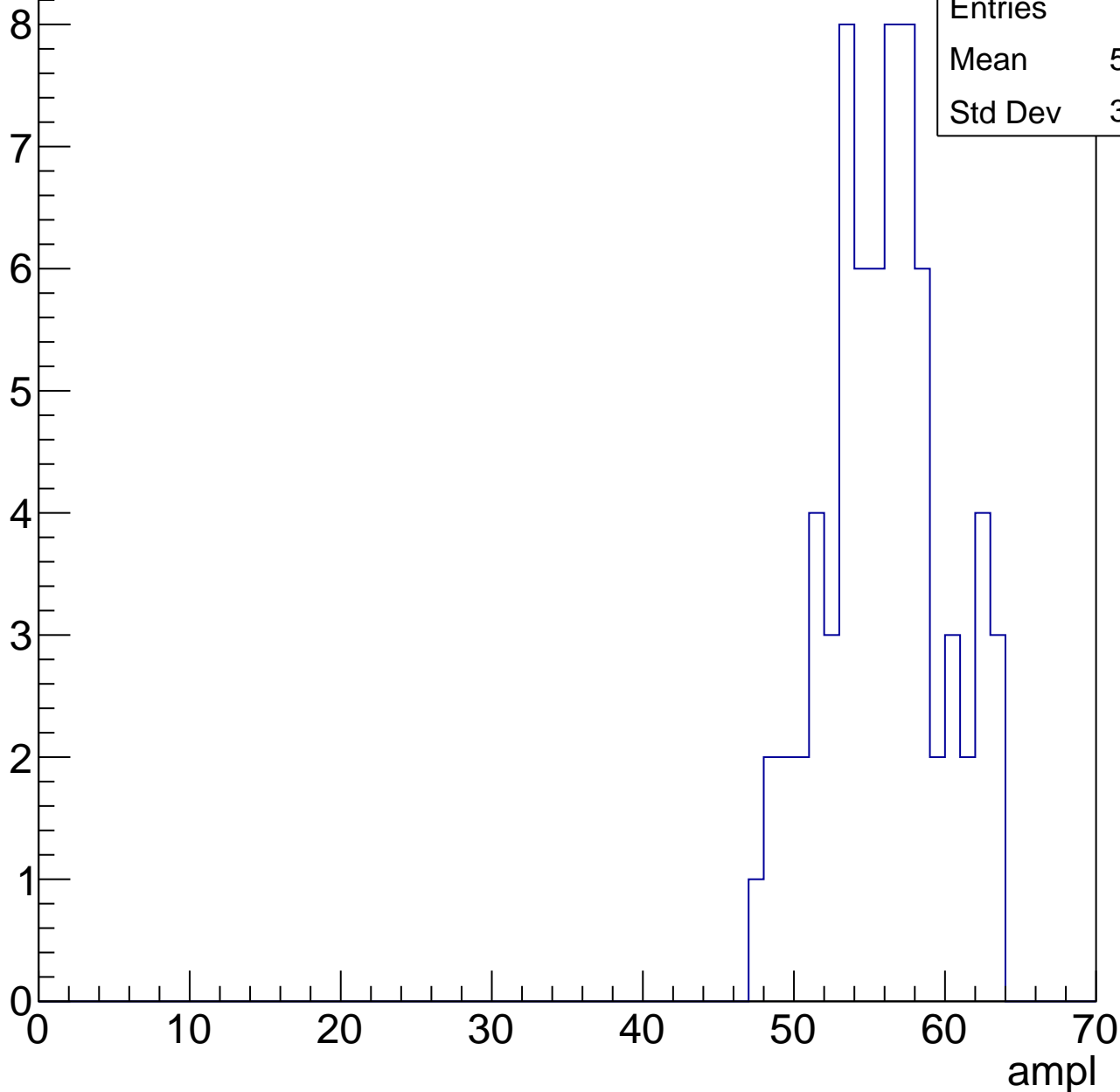


B1L101S, U12-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.54
Std Dev	3.879

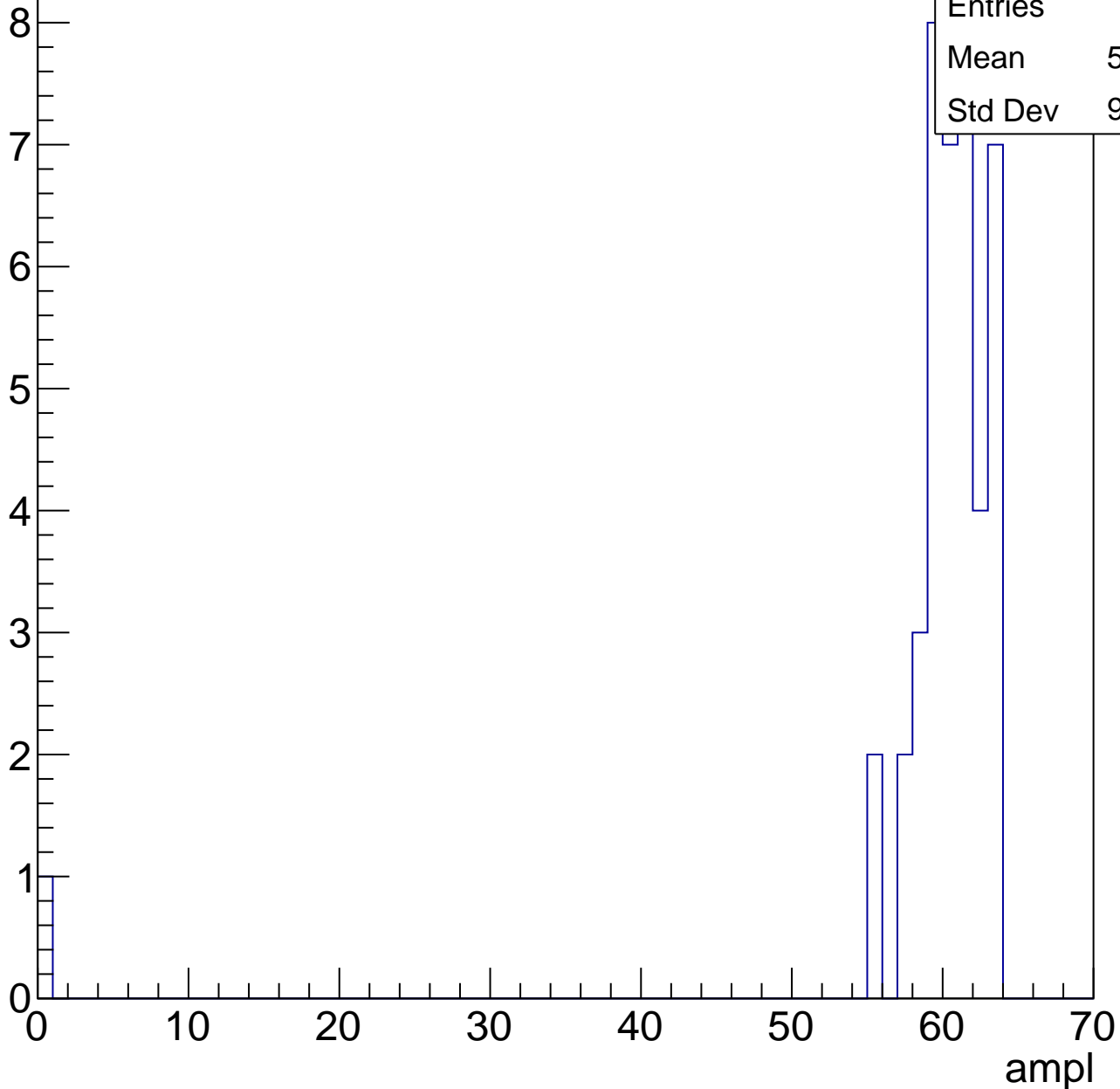


B1L101S, U12-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.74
Std Dev	9.396



B1L101S, U12-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L101S, U12-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch90, adc0

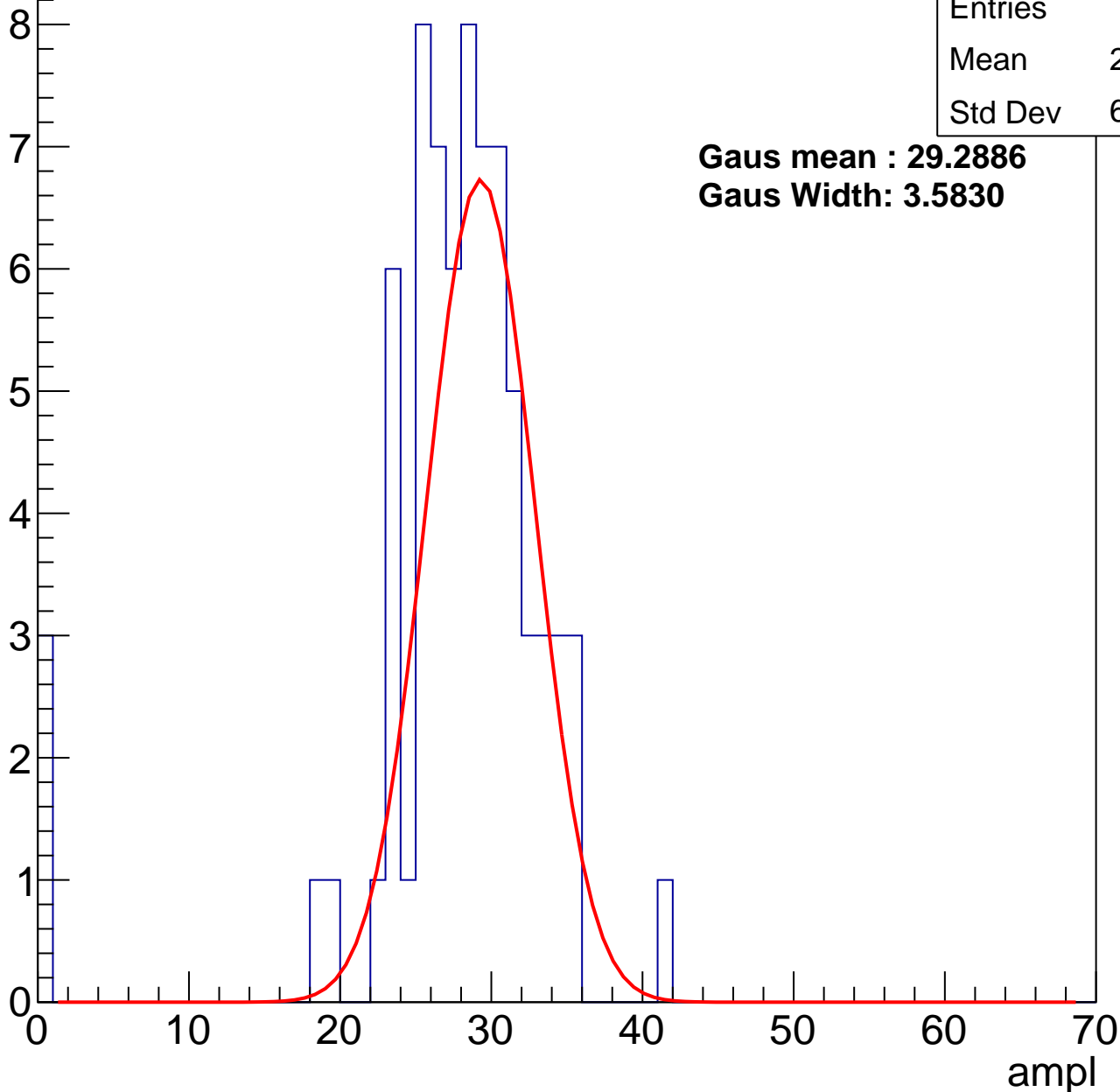
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	27.03
Std Dev	6.776

Gaus mean : 29.2886

Gaus Width: 3.5830



B1L101S, U12-ch90, adc1

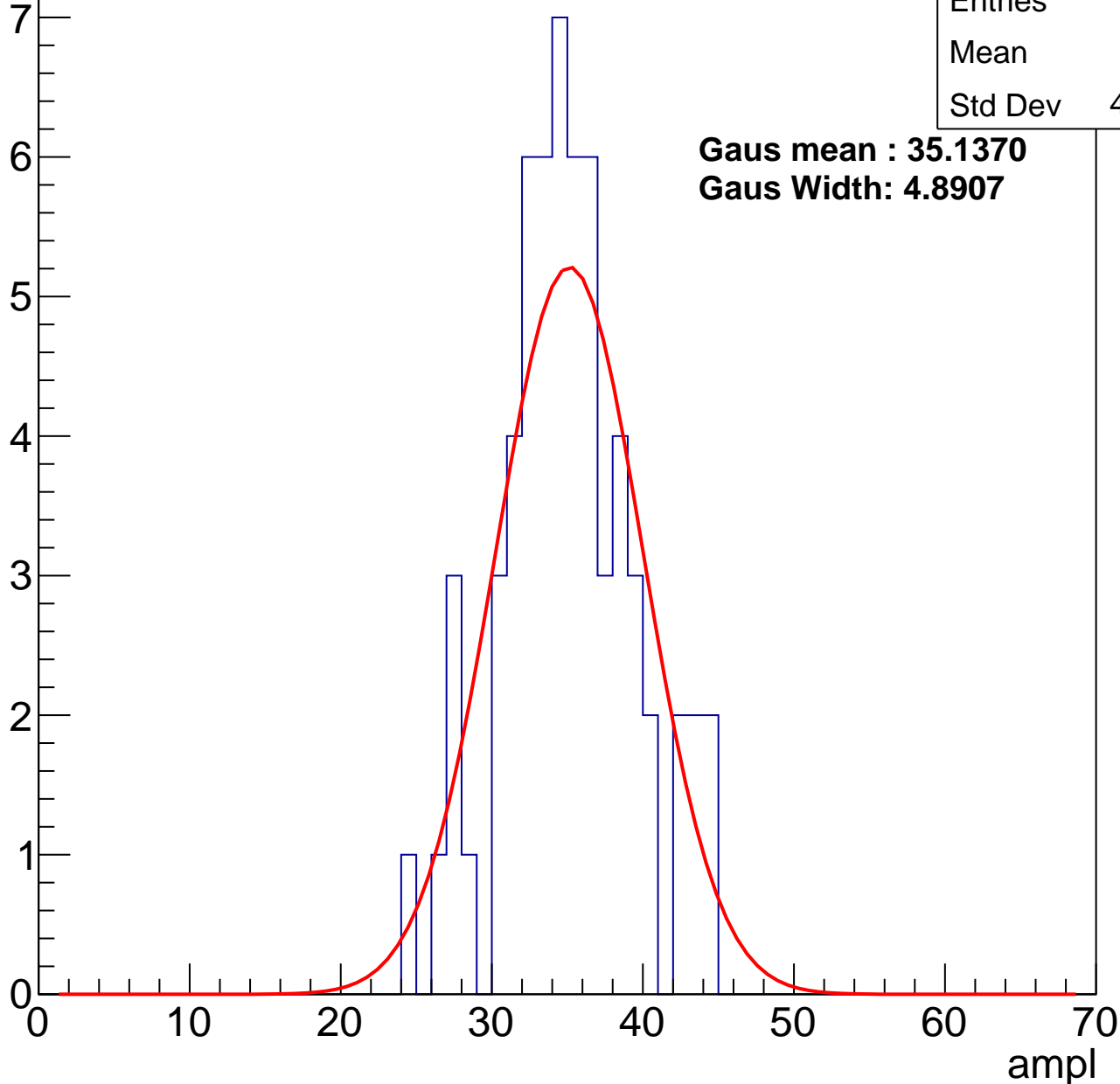
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	34.6
Std Dev	4.394

Gaus mean : 35.1370

Gaus Width: 4.8907



B1L101S, U12-ch90, adc2

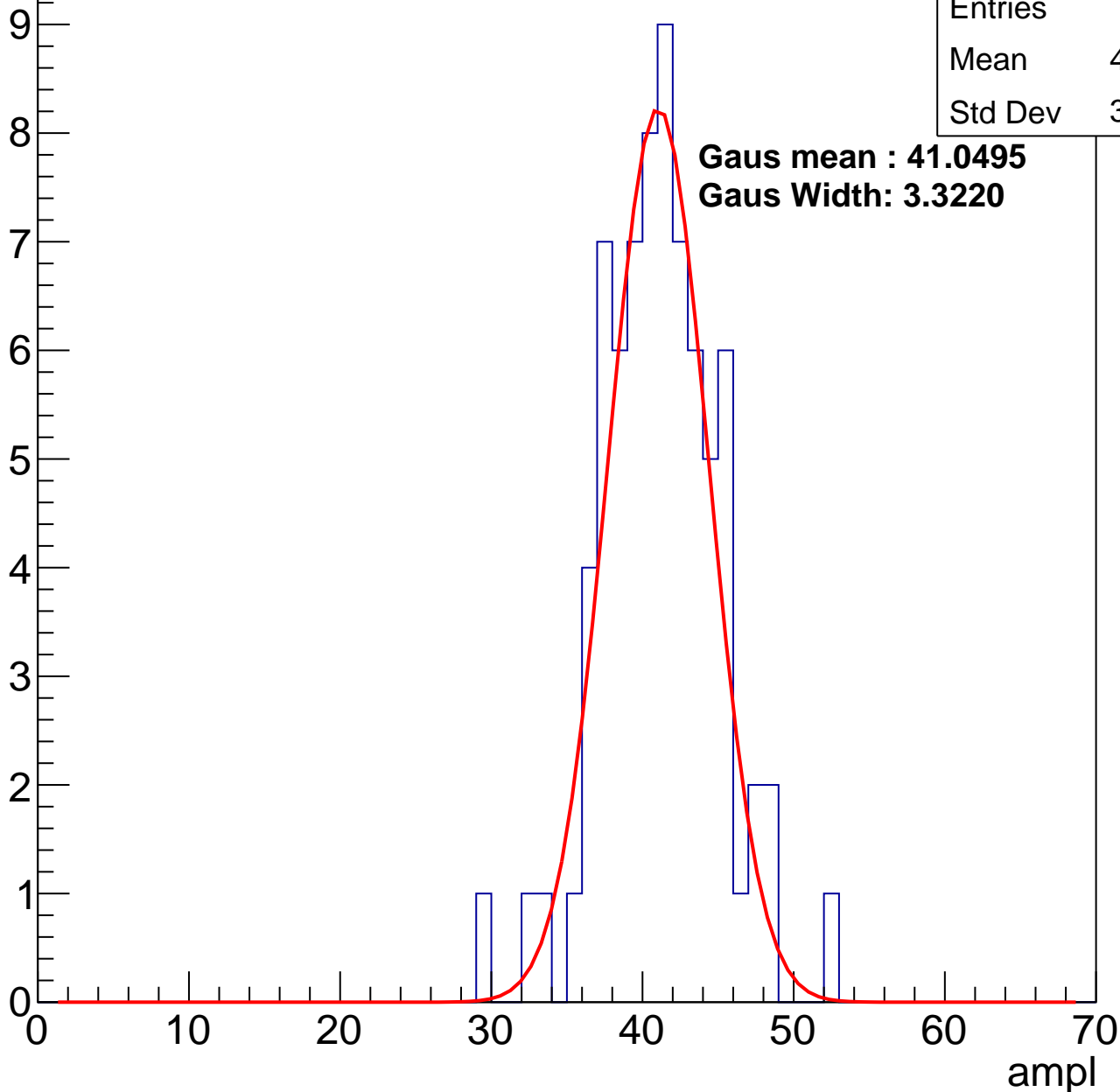
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	40.69
Std Dev	3.844

Gaus mean : 41.0495

Gaus Width: 3.3220



B1L101S, U12-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	49.1
Std Dev	3.469

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

10

20

30

40

50

60

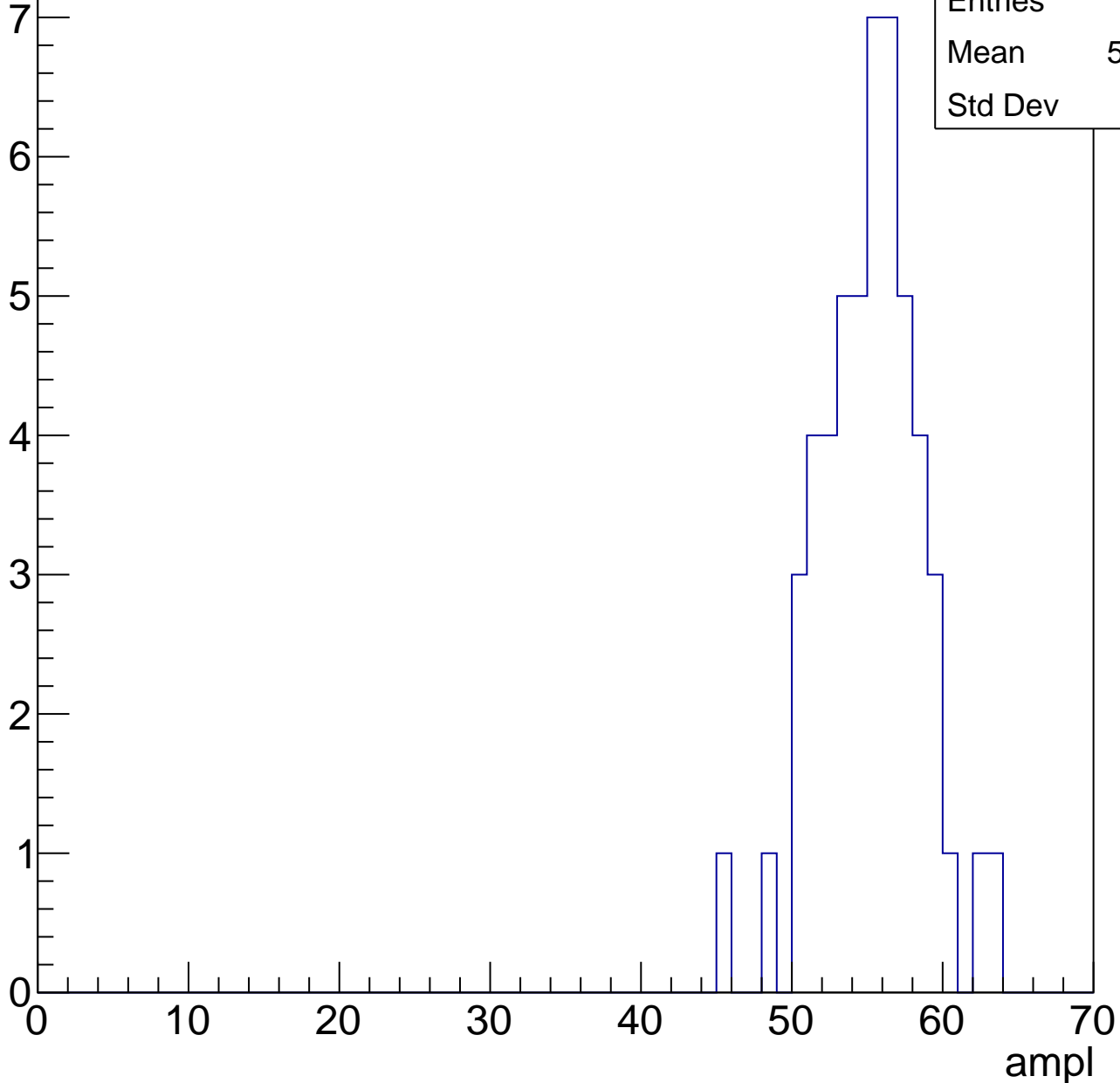
70

B1L101S, U12-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.73
Std Dev	3.38

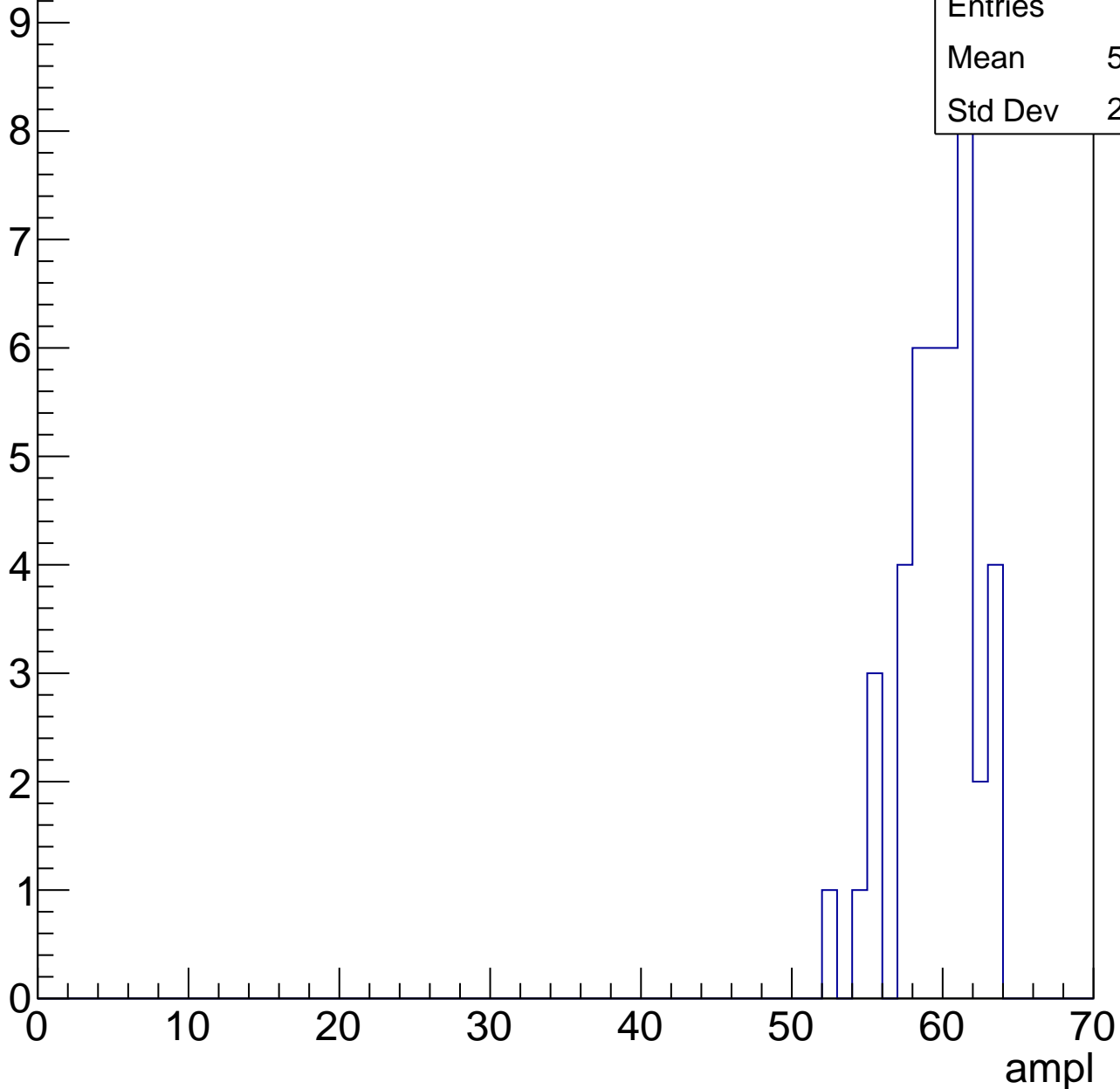


B1L101S, U12-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.19
Std Dev	2.519

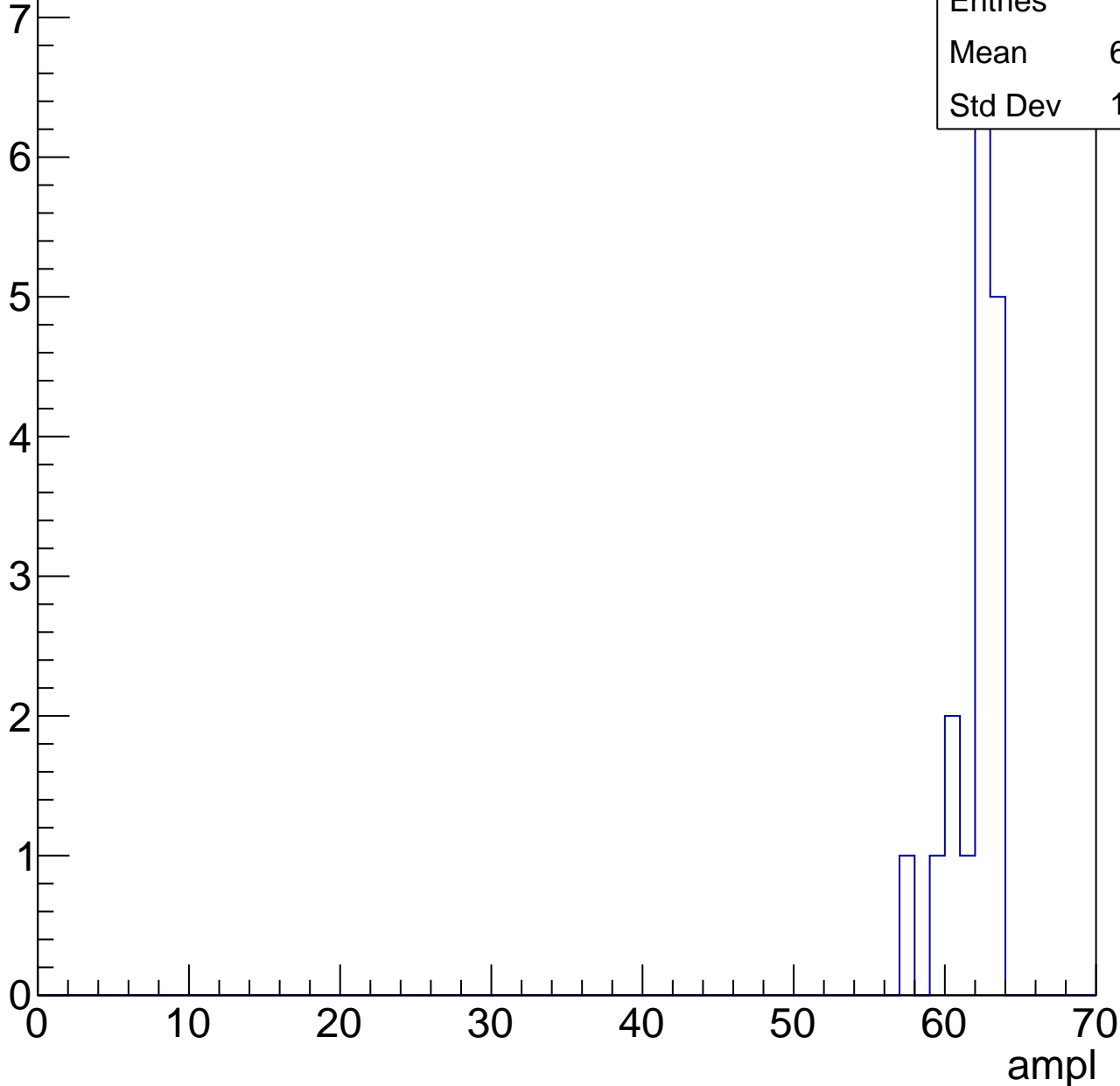


B1L101S, U12-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61.53
Std Dev	1.613



B1L101S, U12-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch91, adc0

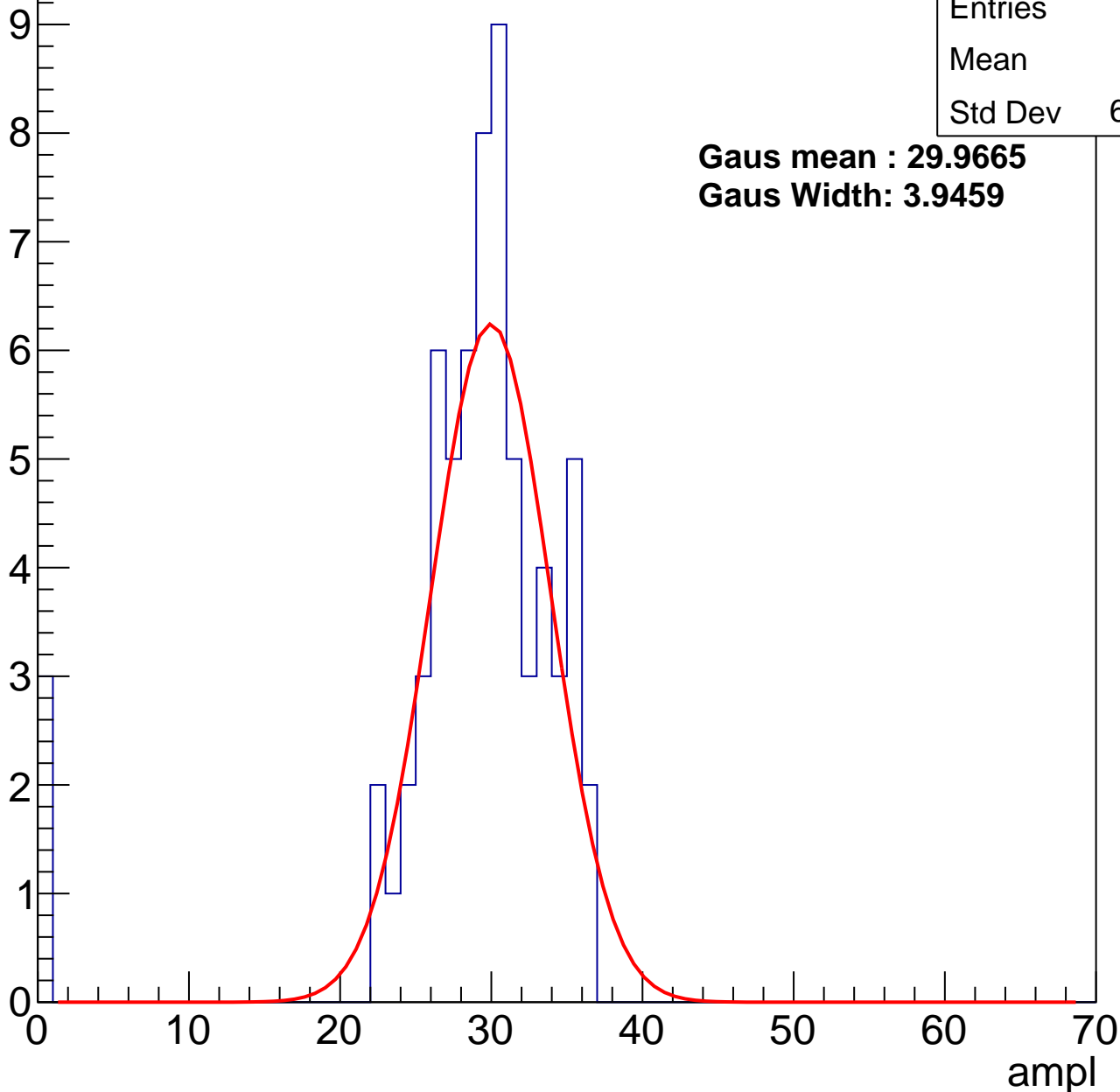
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.1
Std Dev	6.969

Gaus mean : 29.9665

Gaus Width: 3.9459



B1L101S, U12-ch91, adc1

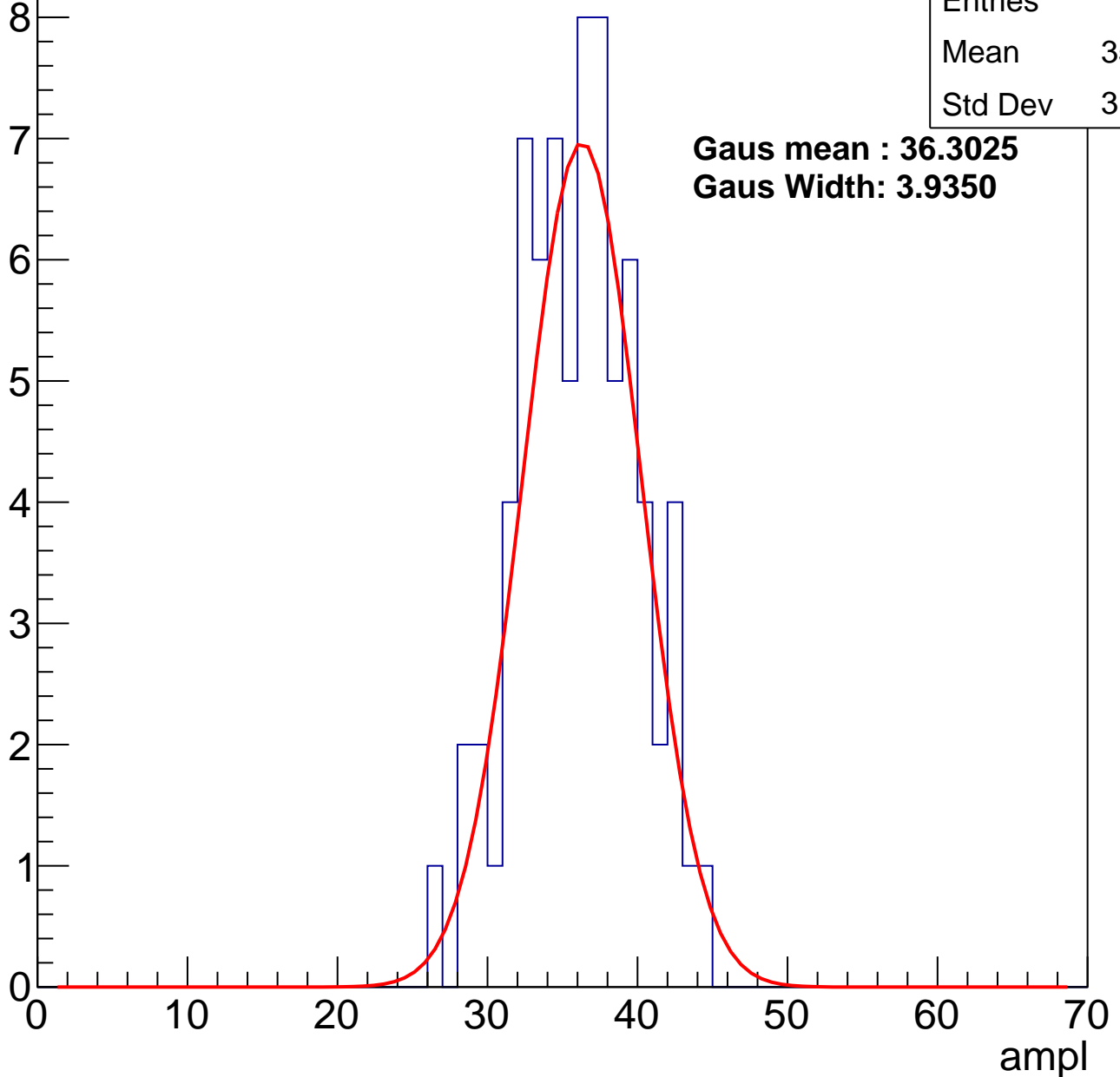
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.59
Std Dev	3.859

Gaus mean : 36.3025

Gaus Width: 3.9350



B1L101S, U12-ch91, adc2

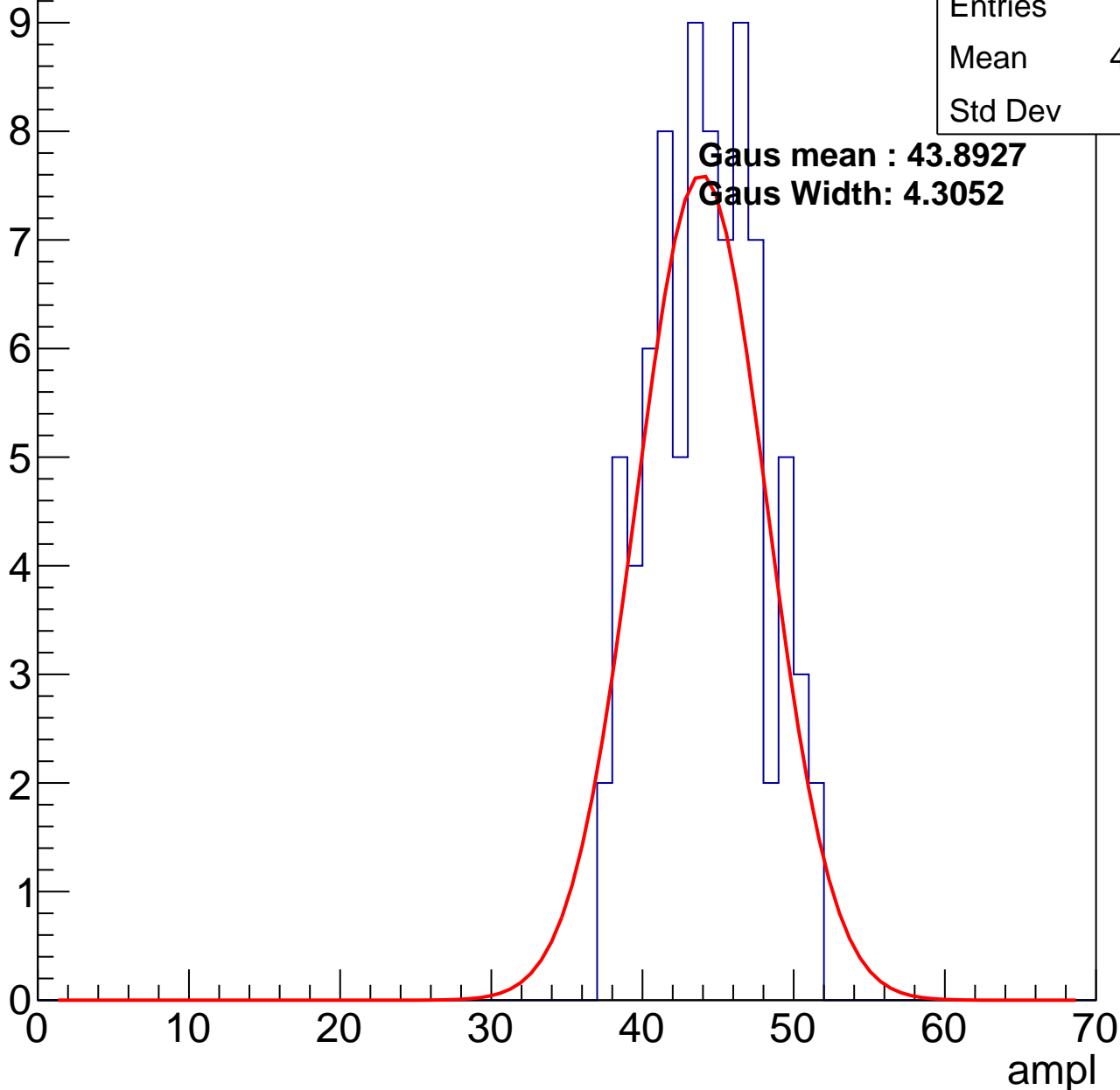
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	43.76
Std Dev	3.56

Gaus mean : 43.8927

Gaus Width: 4.3052

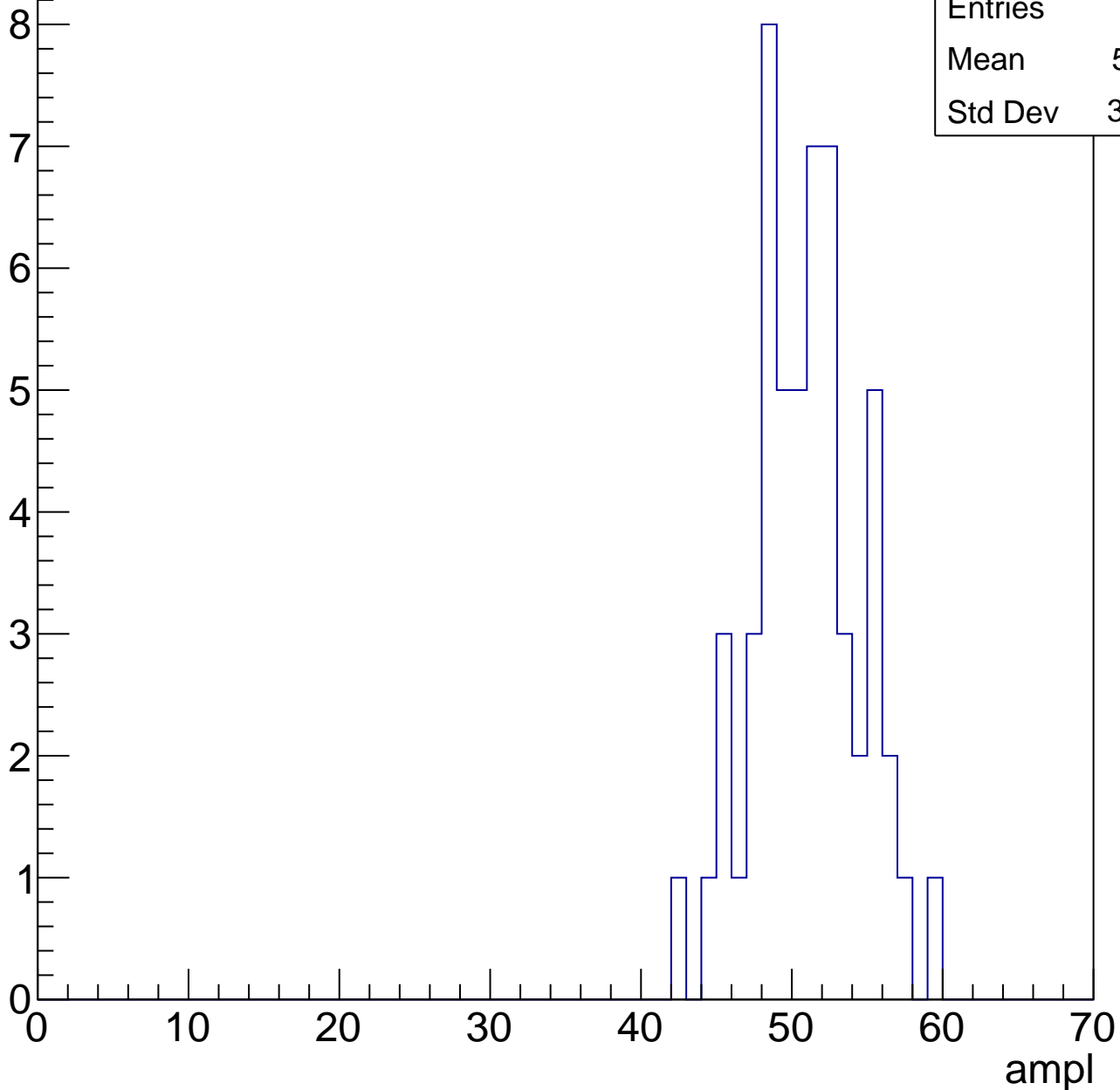


B1L101S, U12-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

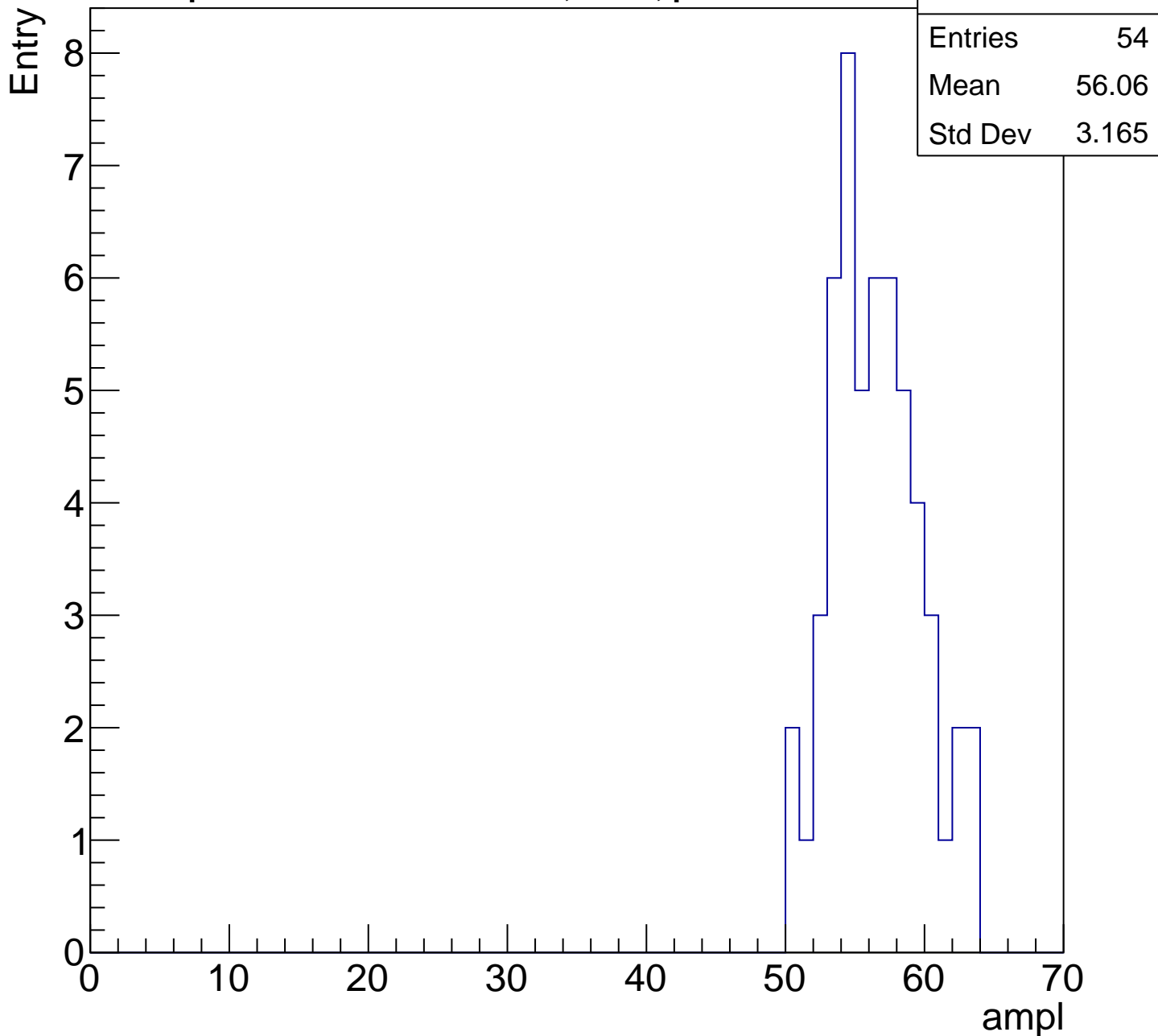
Entry

Entries	55
Mean	50.51
Std Dev	3.469



B1L101S, U12-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch91, adc5

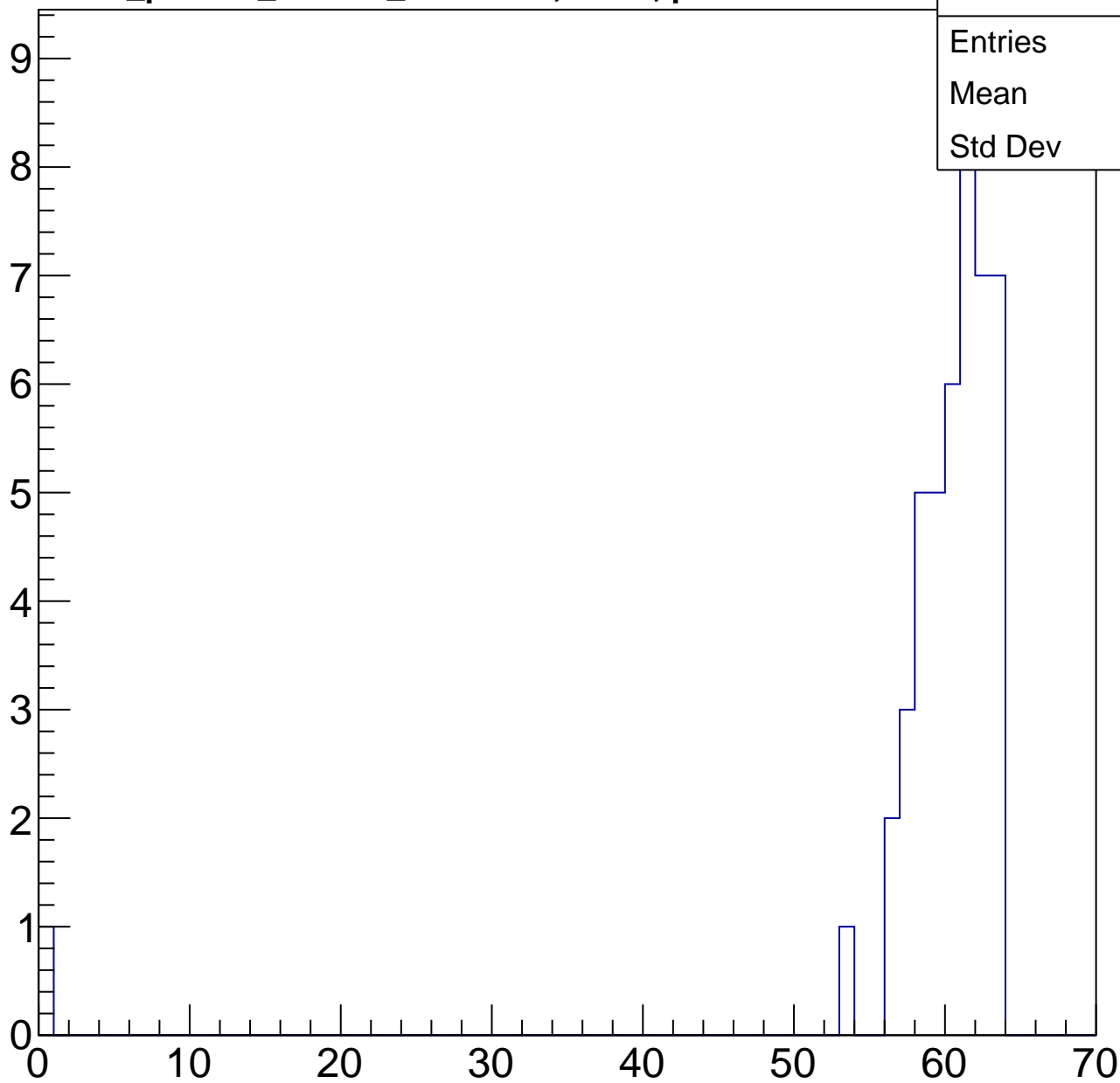
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.8
Std Dev	9.05

ampl



B1L101S, U12-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch92, adc0

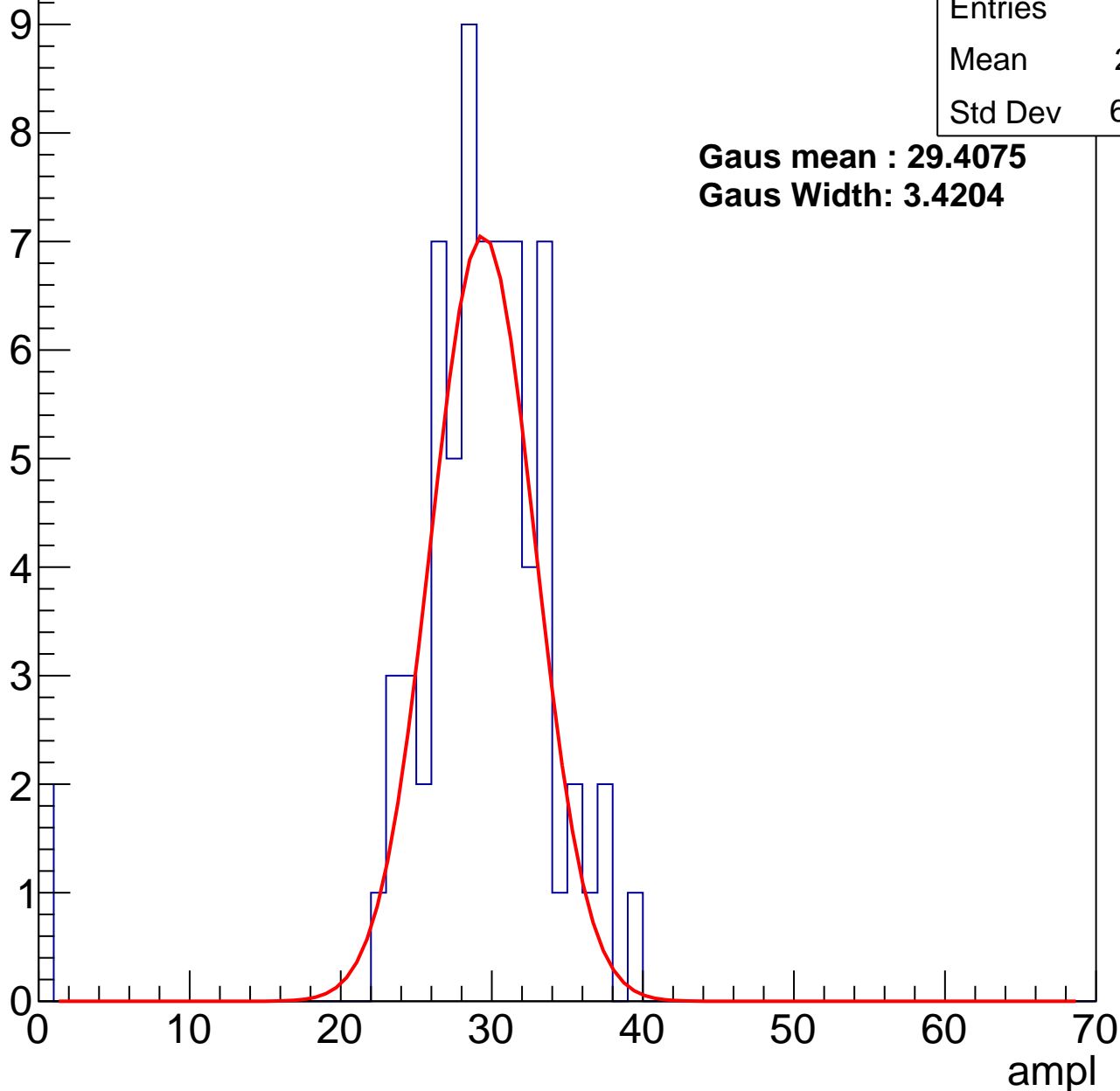
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.51
Std Dev	6.025

Gaus mean : 29.4075

Gaus Width: 3.4204



B1L101S, U12-ch92, adc1

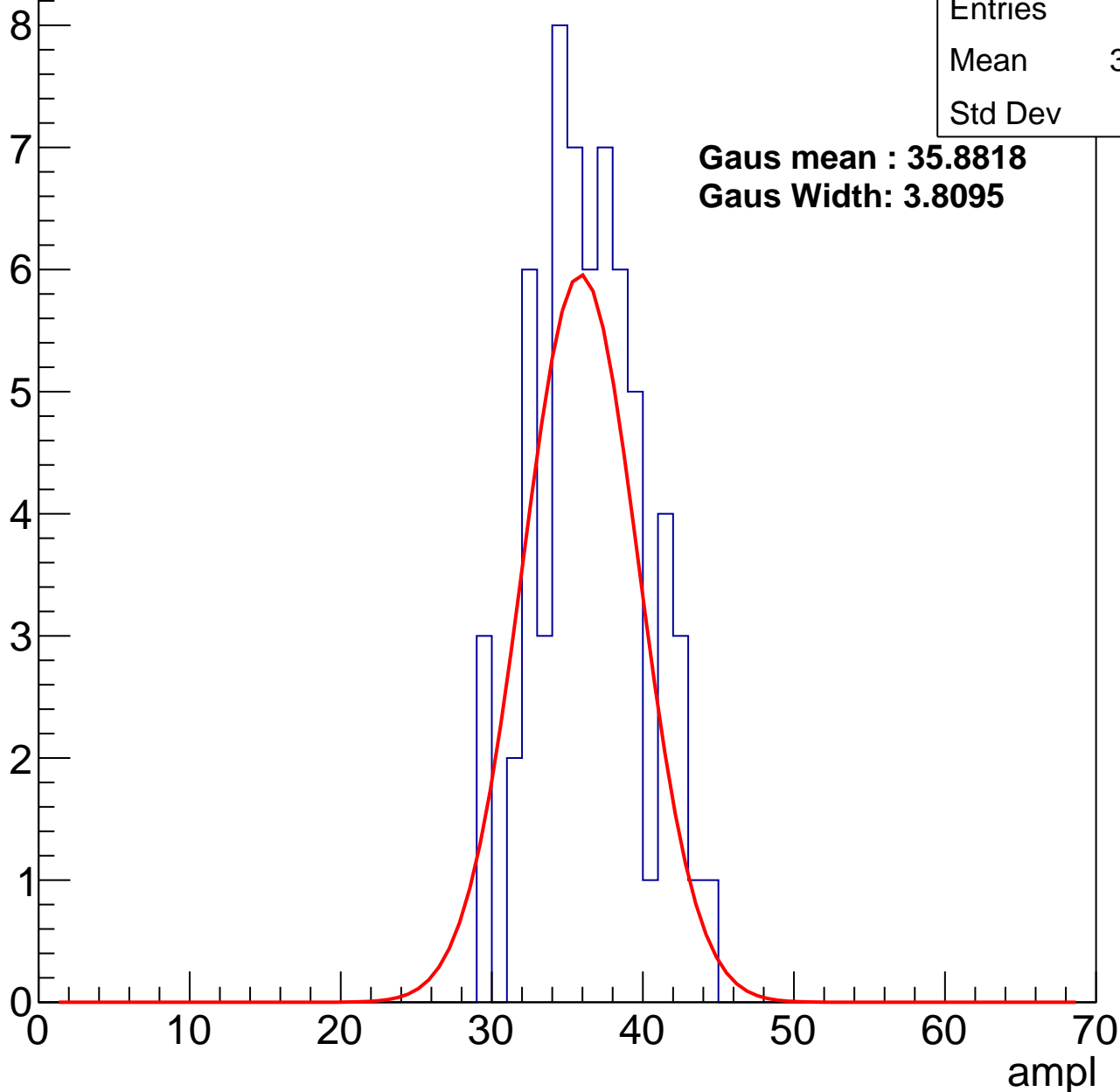
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.06
Std Dev	3.5

Gaus mean : 35.8818

Gaus Width: 3.8095



B1L101S, U12-ch92, adc2

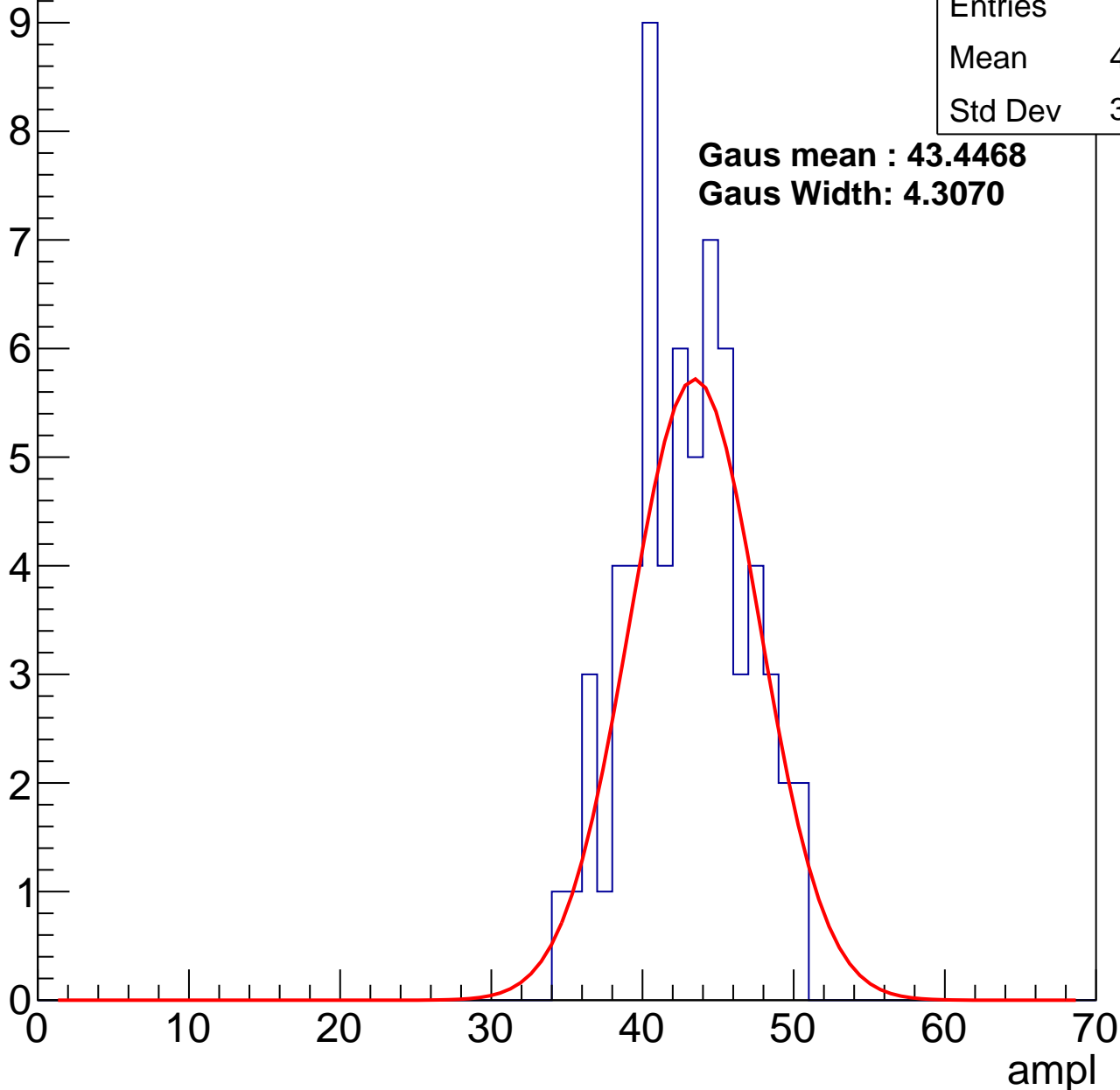
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.45
Std Dev	3.807

Gaus mean : 43.4468

Gaus Width: 4.3070

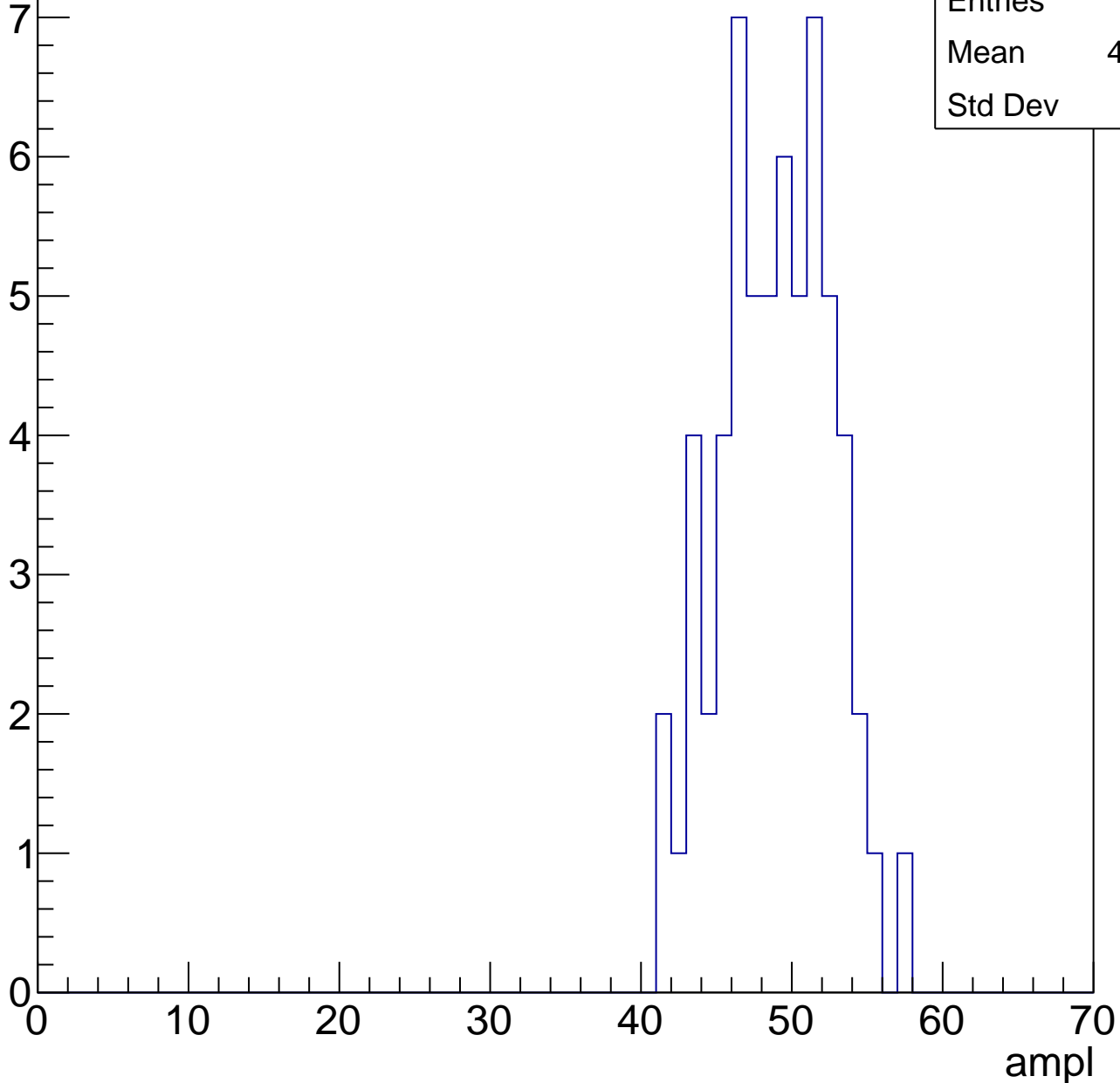


B1L101S, U12-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.43
Std Dev	3.61

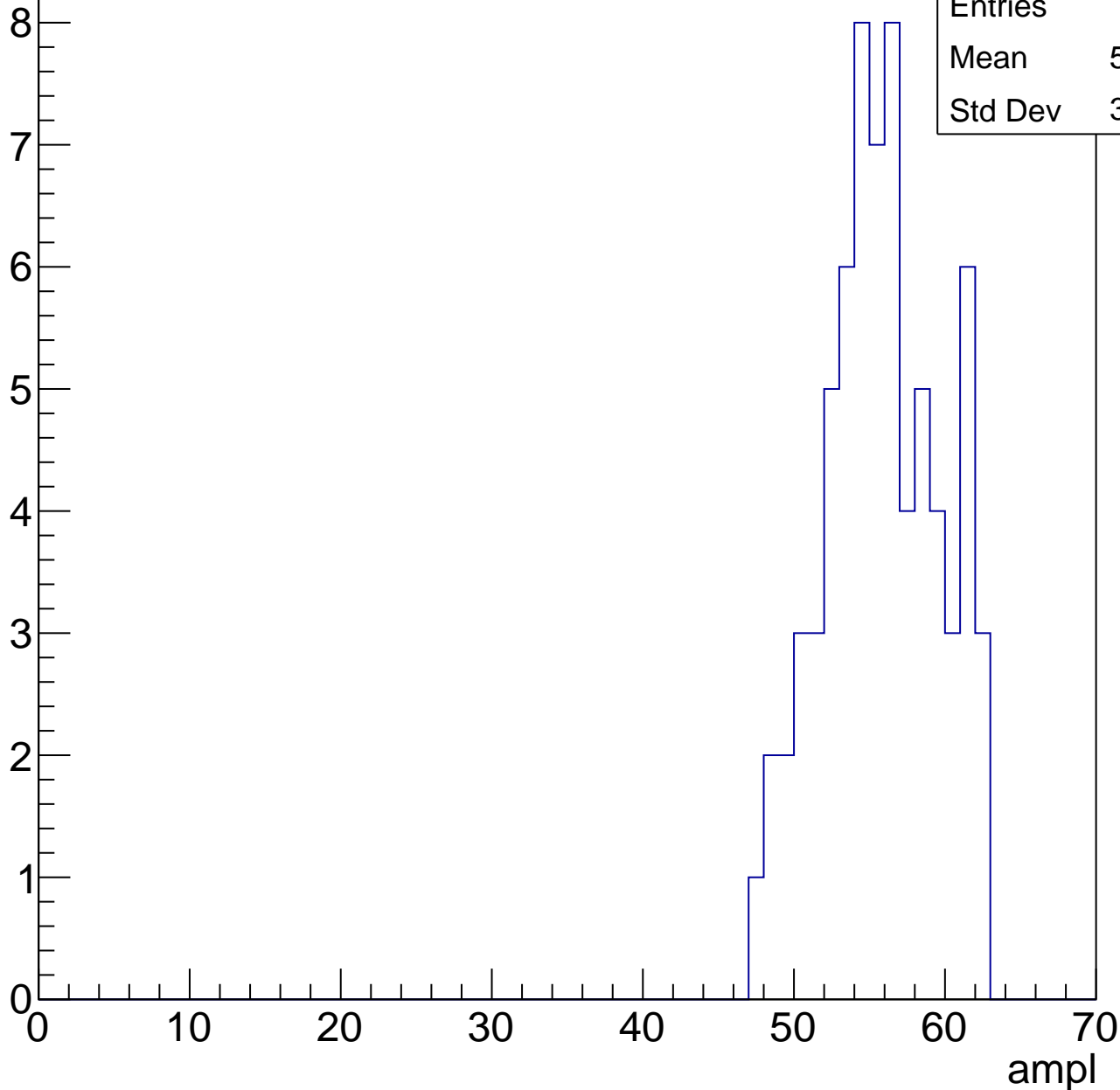


B1L101S, U12-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.33
Std Dev	3.775

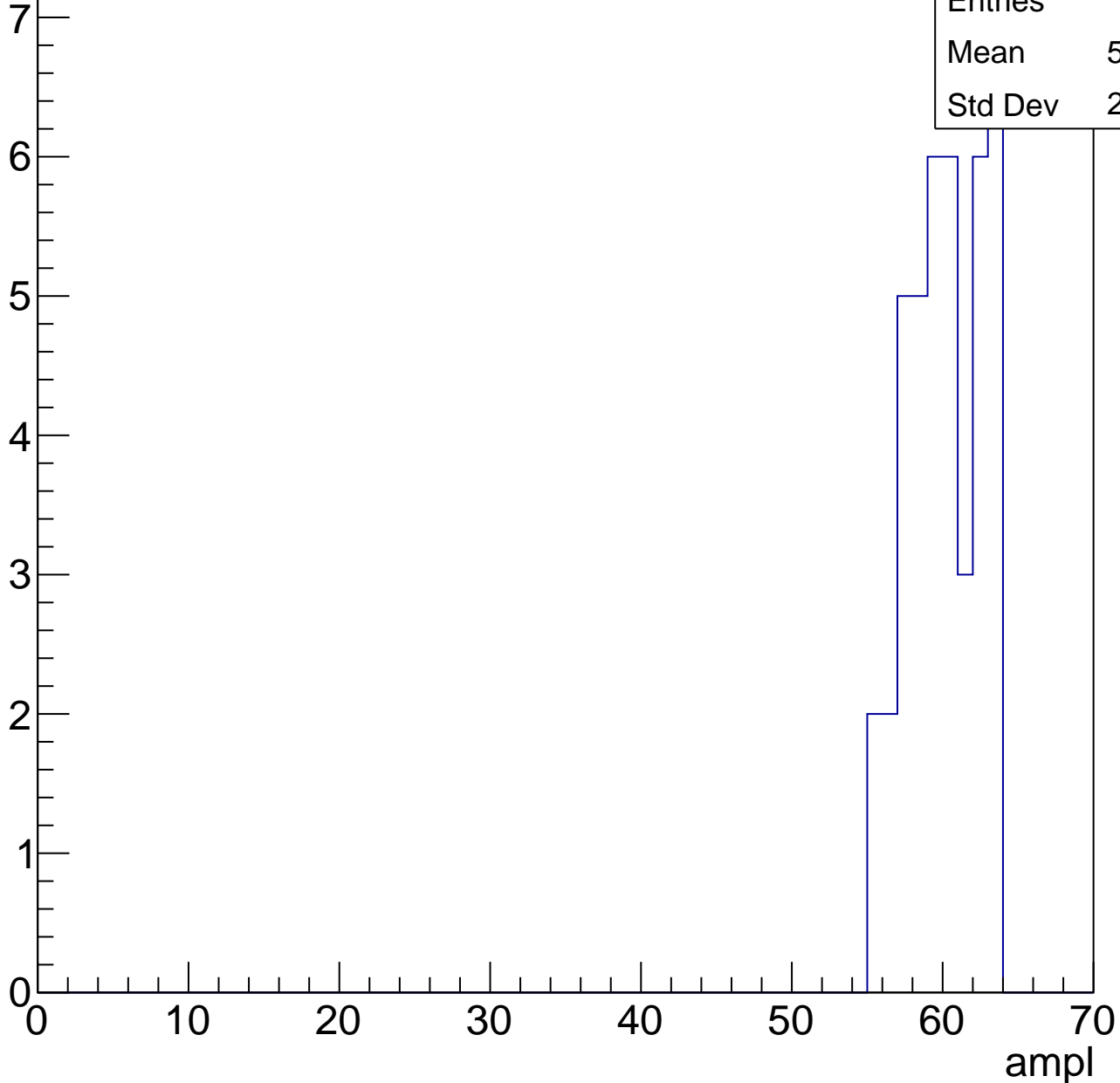


B1L101S, U12-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

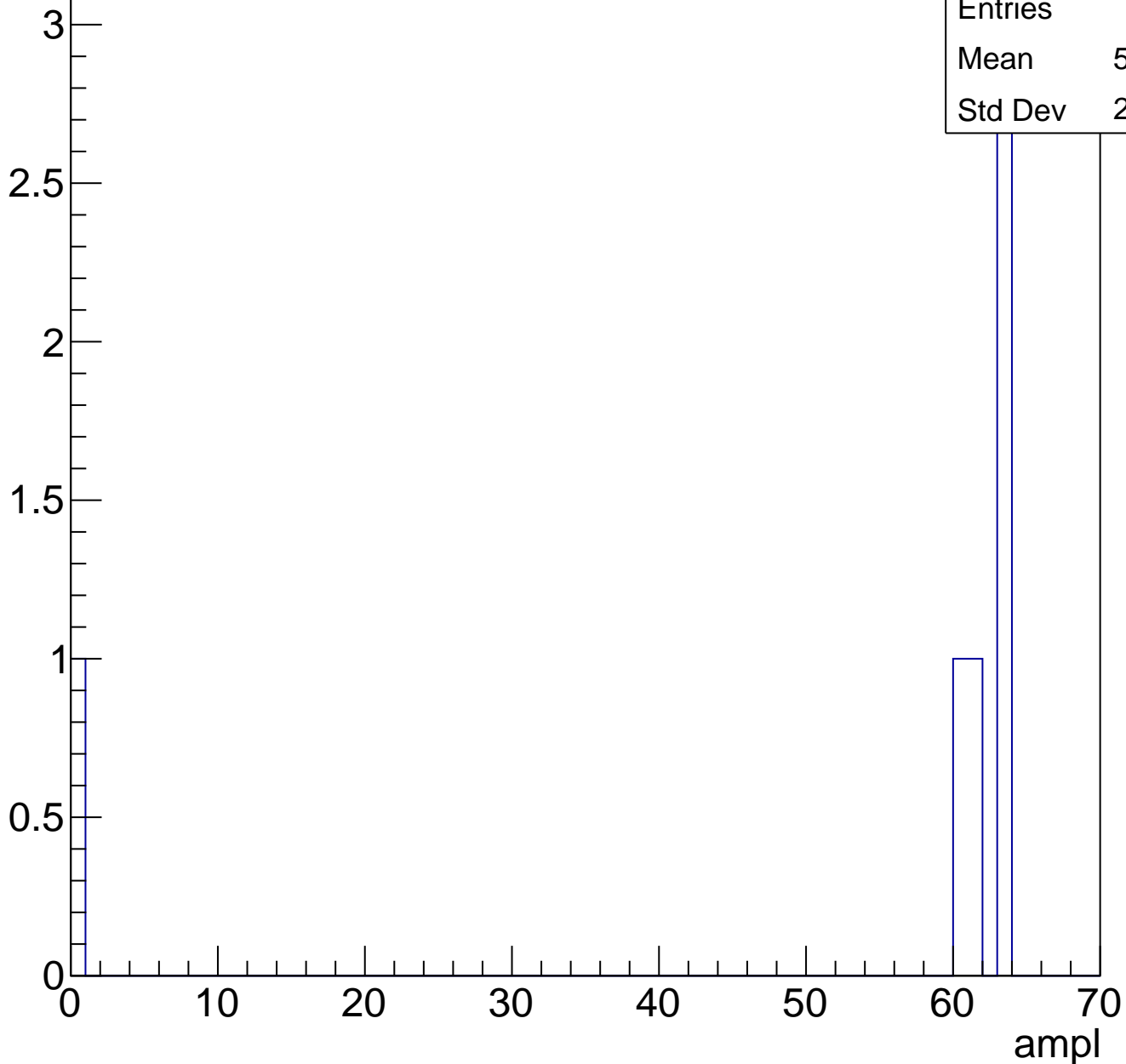
Entries	42
Mean	59.69
Std Dev	2.385



B1L101S, U12-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch93, adc0

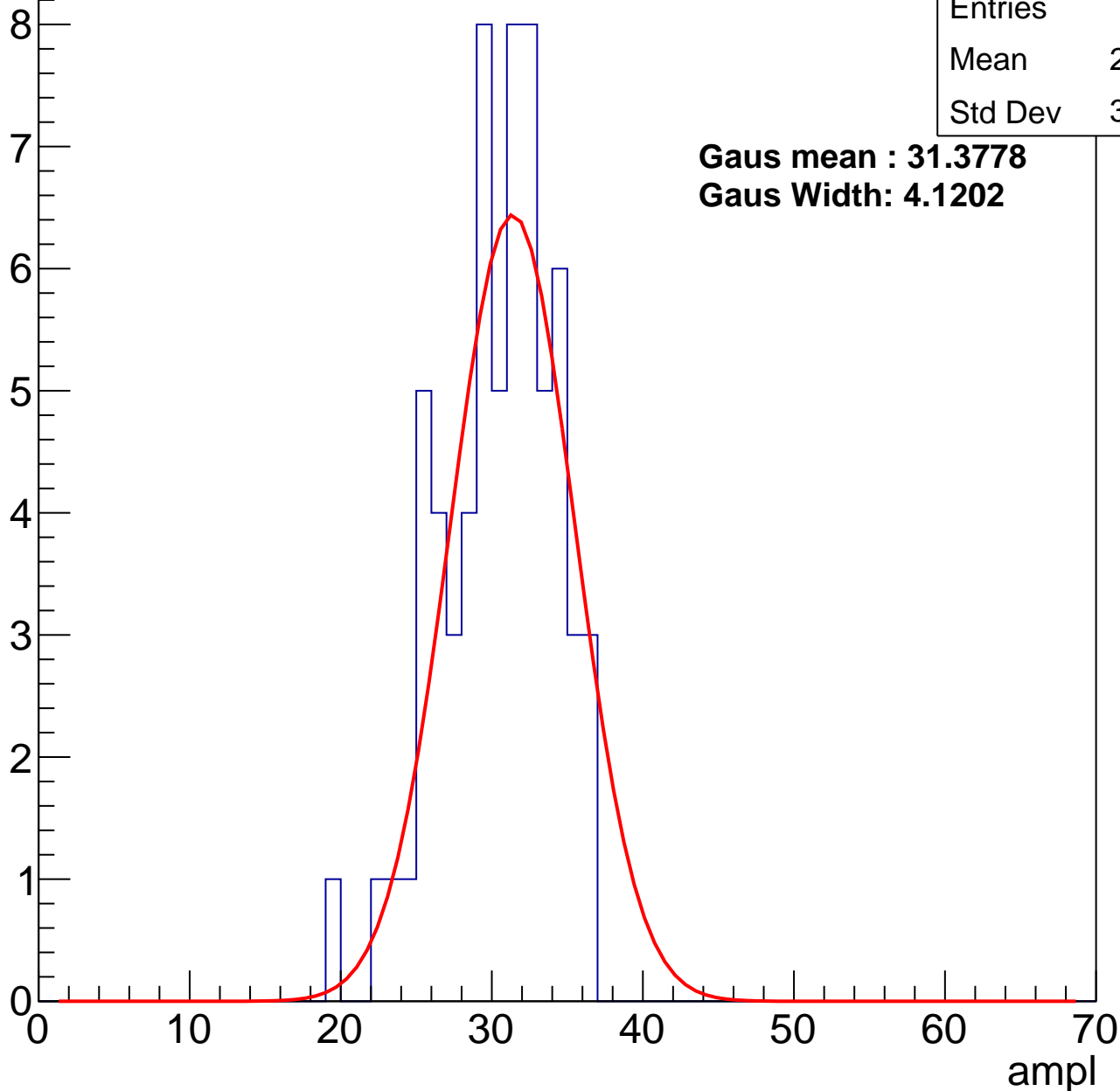
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.97
Std Dev	3.639

Gaus mean : 31.3778

Gaus Width: 4.1202



B1L101S, U12-ch93, adc1

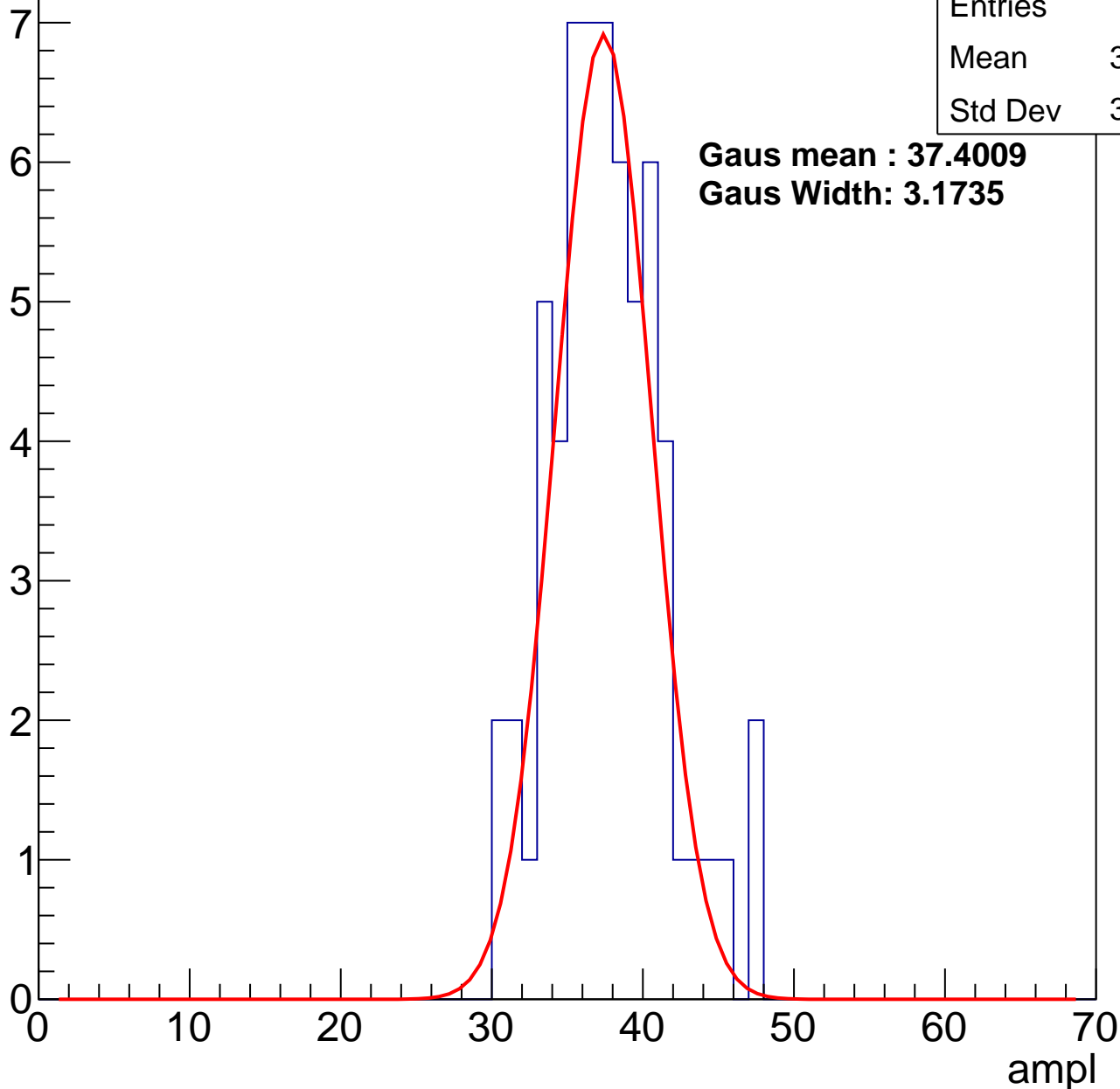
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.19
Std Dev	3.728

Gaus mean : 37.4009

Gaus Width: 3.1735



B1L101S, U12-ch93, adc2

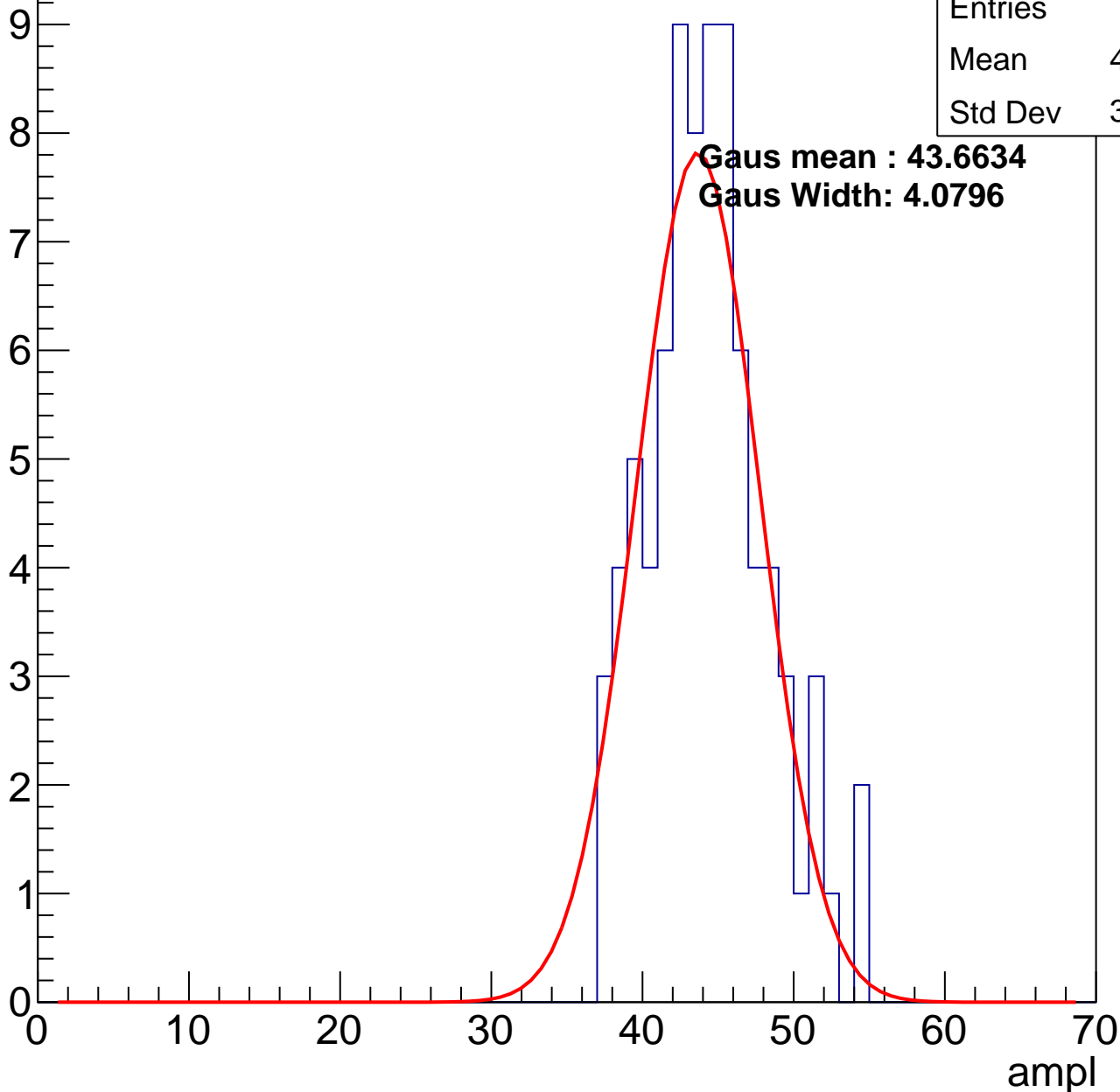
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	43.86
Std Dev	3.899

Gaus mean : 43.6634

Gaus Width: 4.0796

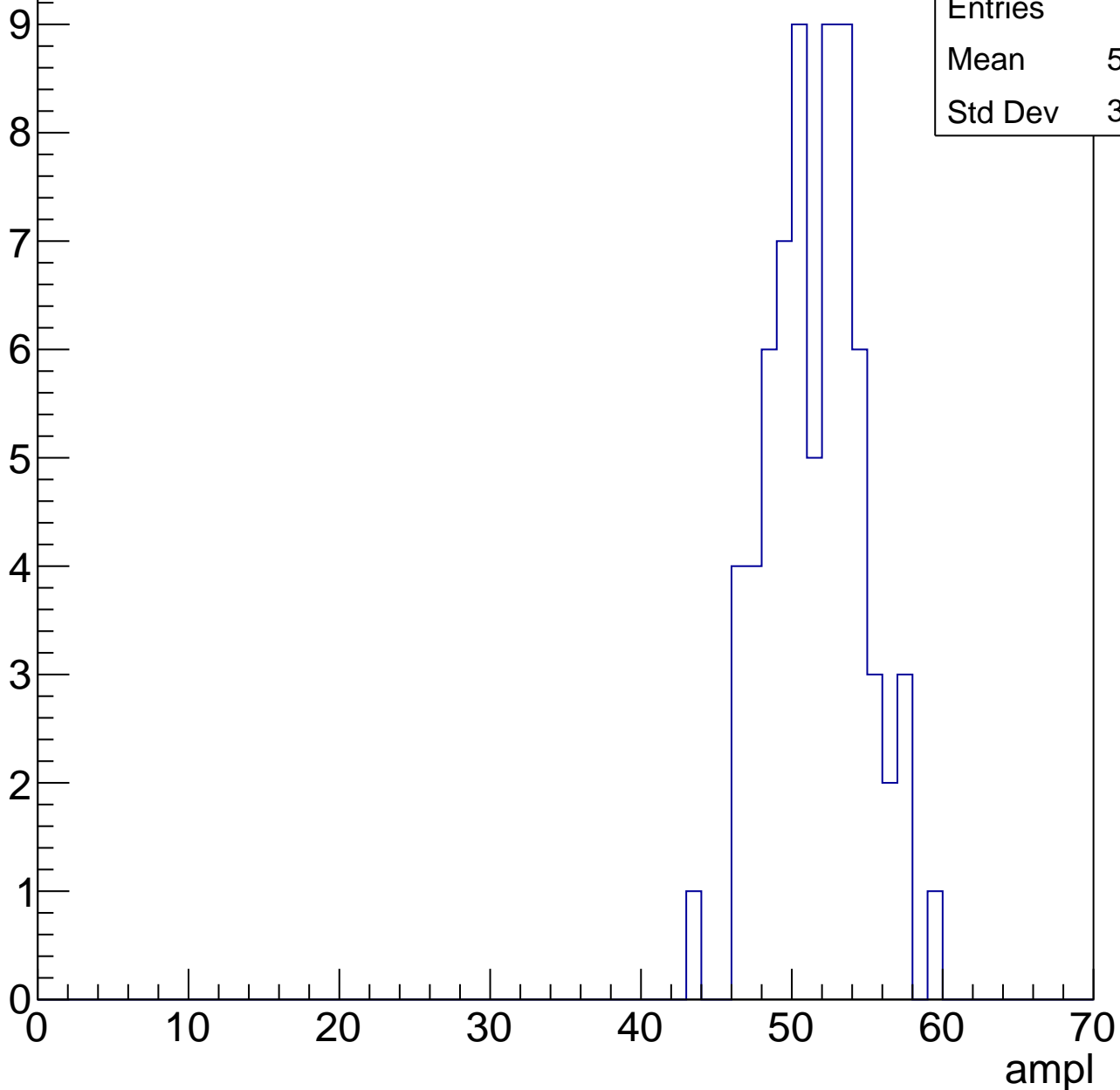


B1L101S, U12-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

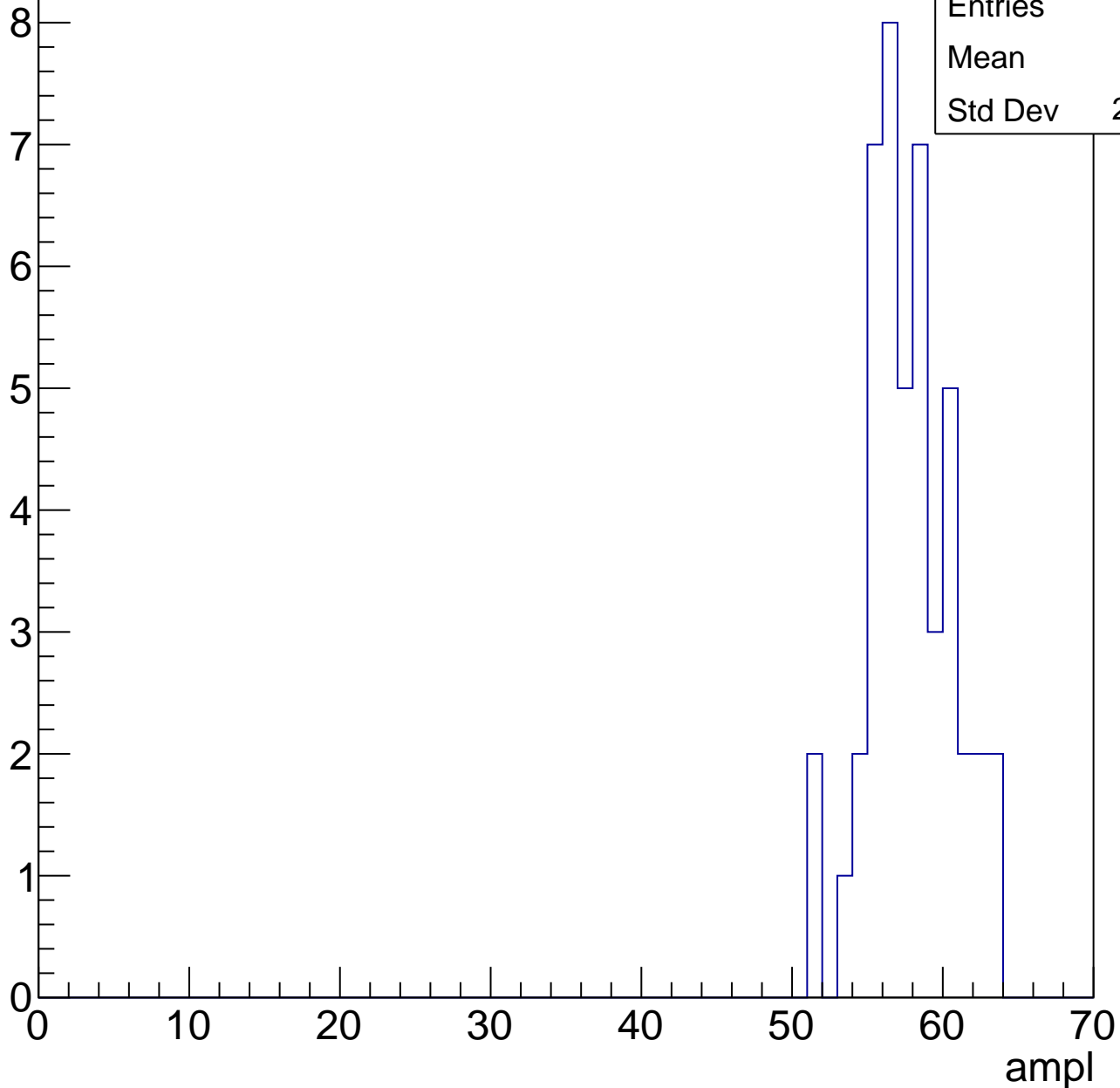
Entries	69
Mean	51.12
Std Dev	3.156



B1L101S, U12-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

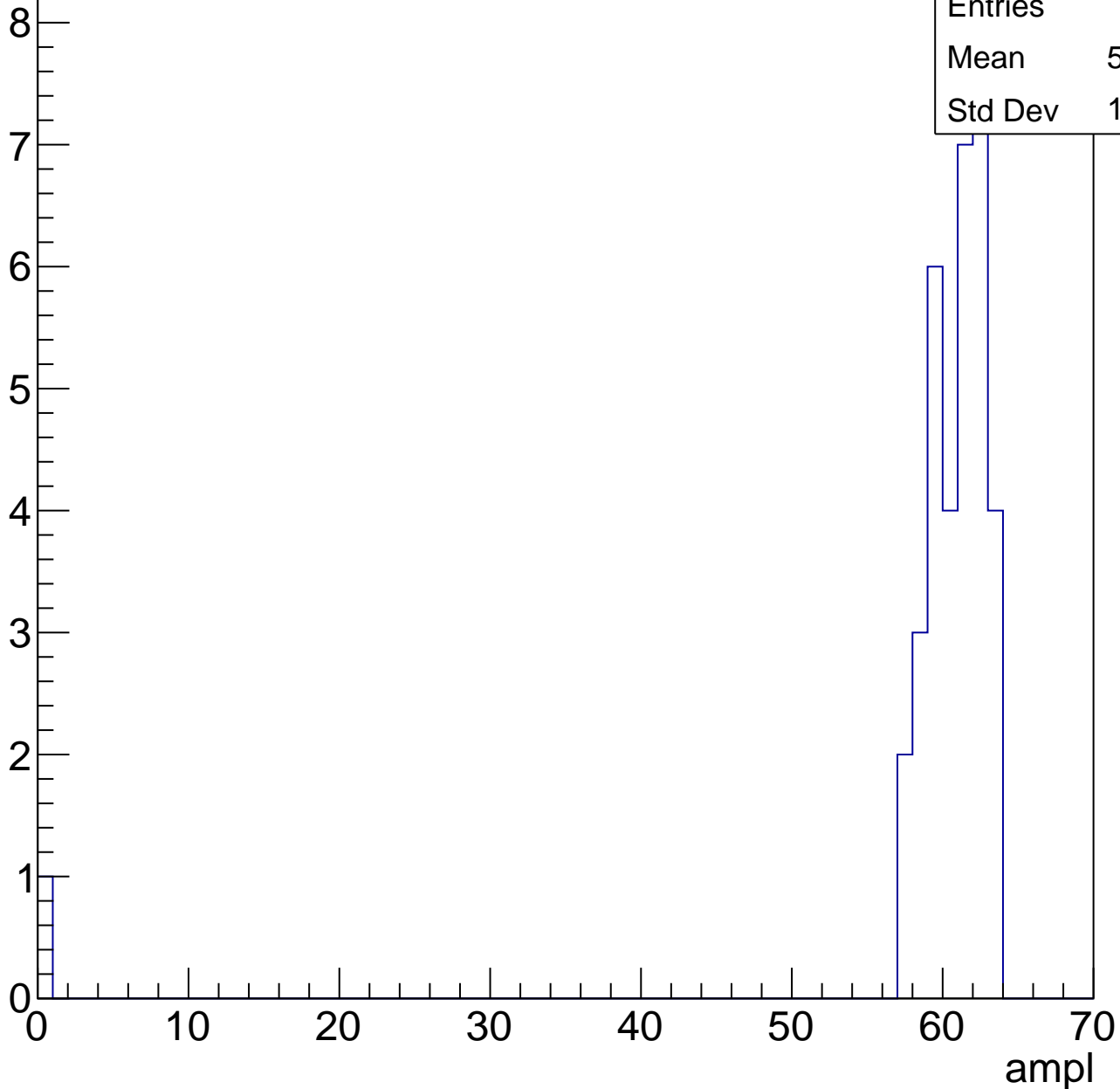


B1L101S, U12-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

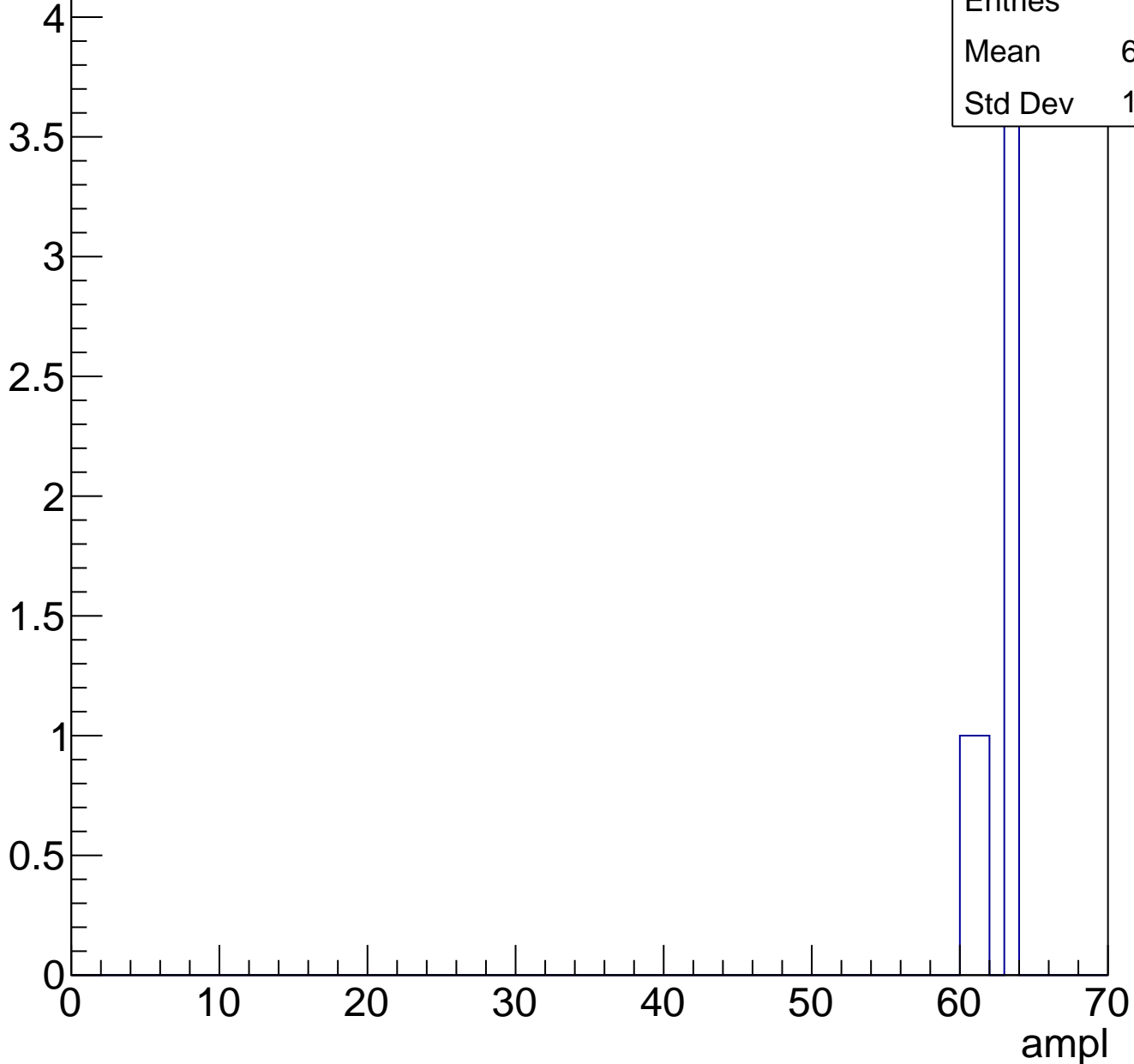
Entries	35
Mean	58.77
Std Dev	10.22



B1L101S, U12-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch94, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	28.62
Std Dev	3.429

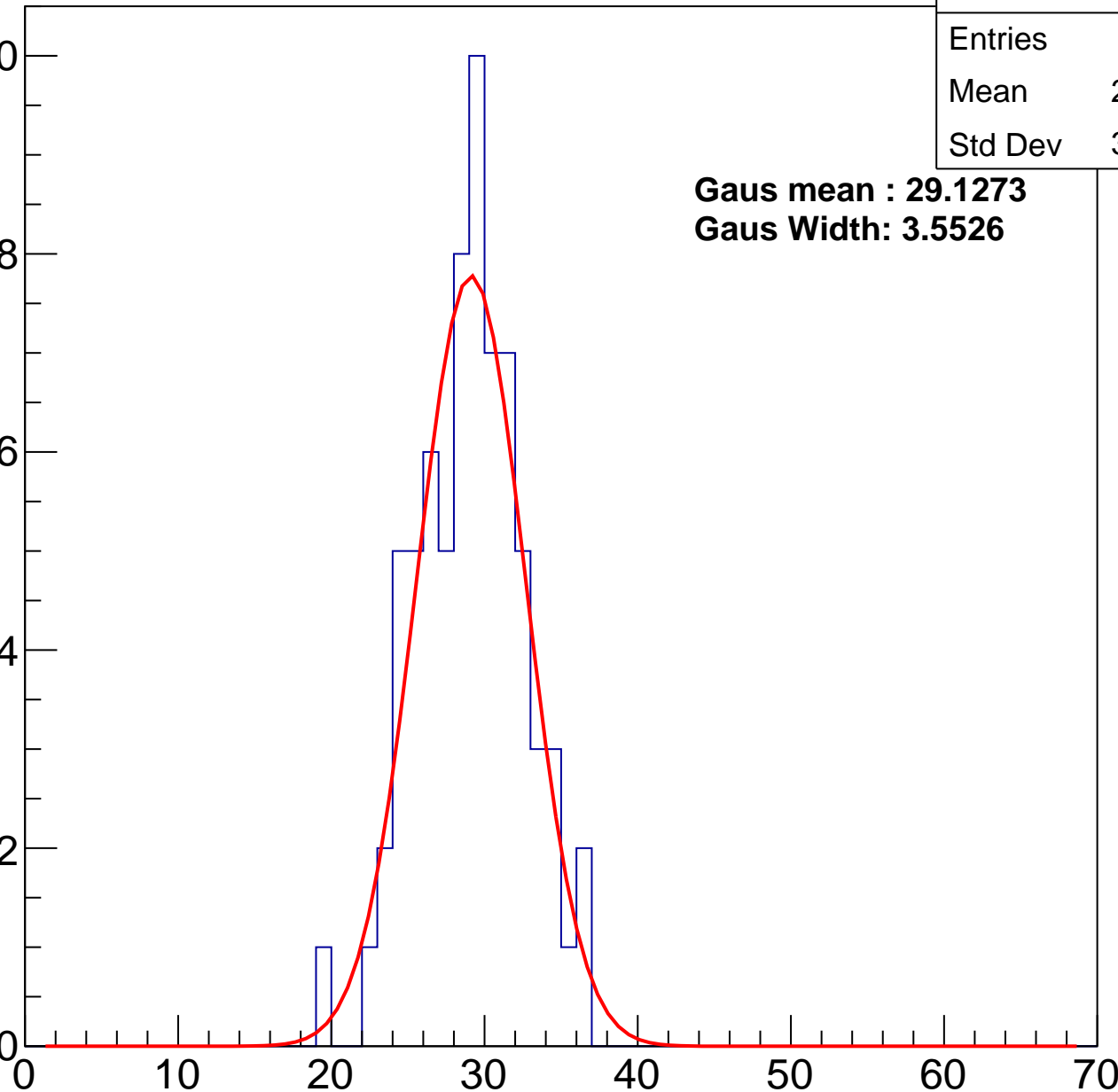
Gaus mean : 29.1273

Gaus Width: 3.5526

Entry

10
8
6
4
2
0

ampl



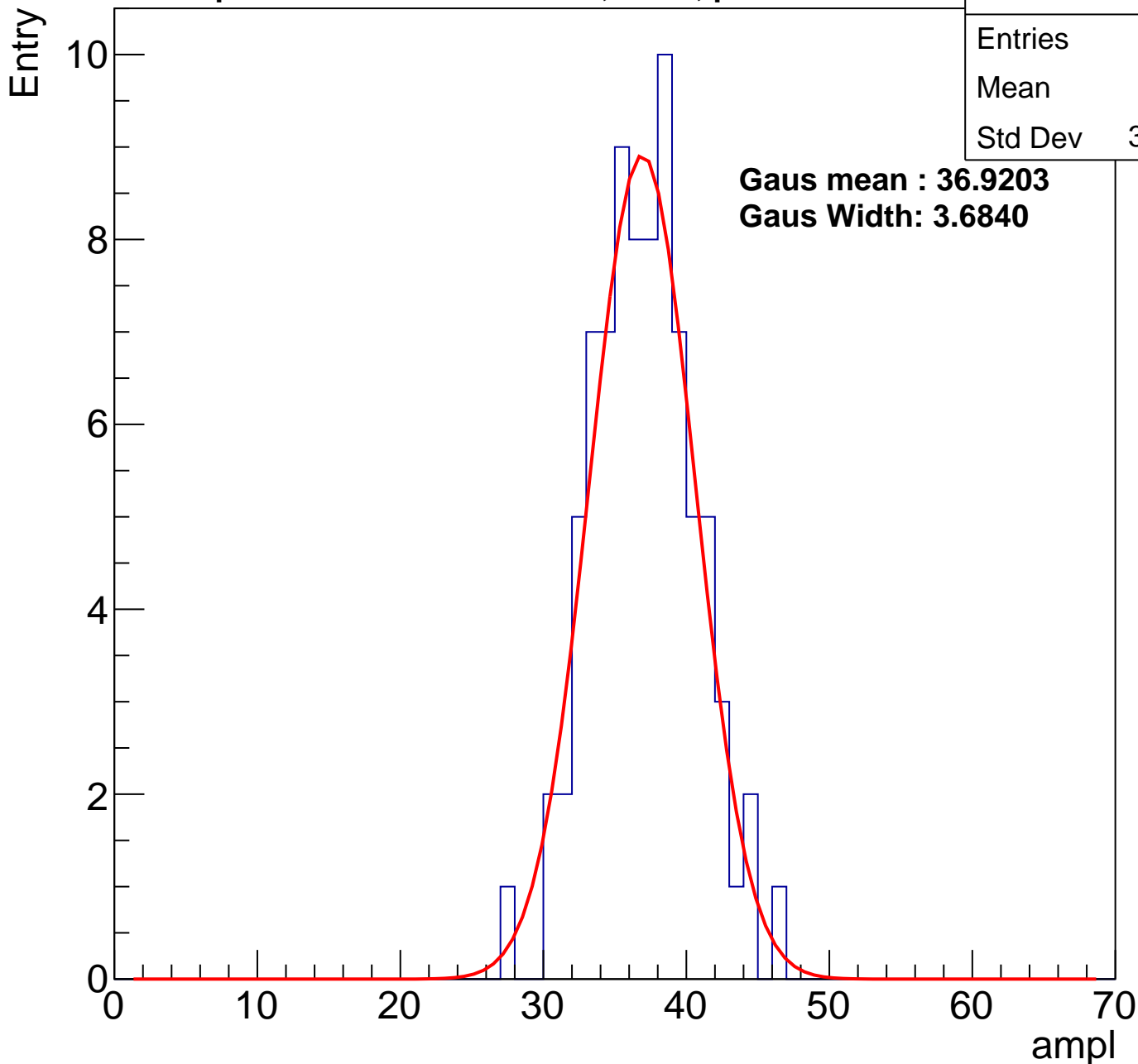
B1L101S, U12-ch94, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	36.6
Std Dev	3.563

Gaus mean : 36.9203

Gaus Width: 3.6840



B1L101S, U12-ch94, adc2

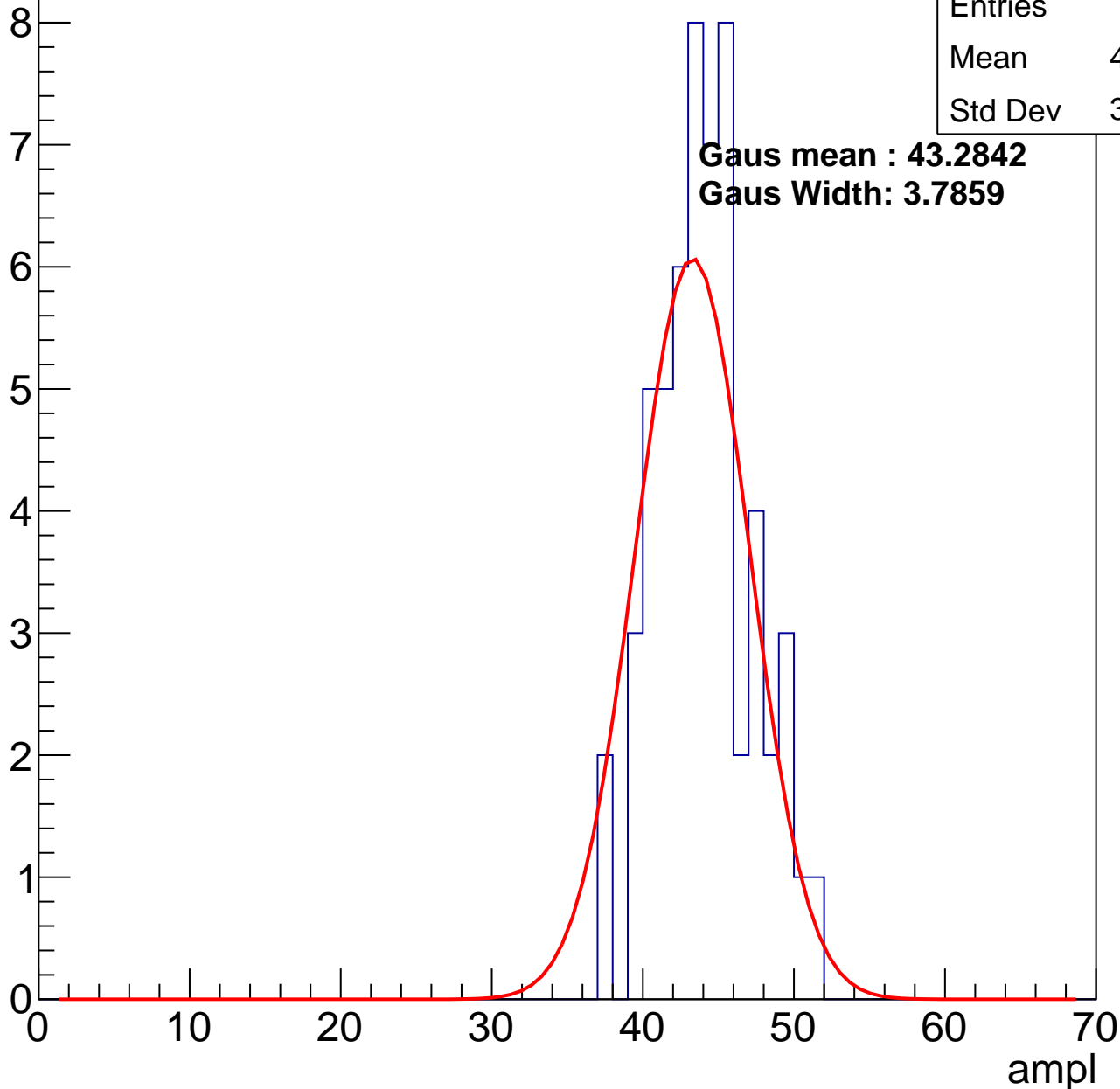
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.58
Std Dev	3.145

Gaus mean : 43.2842

Gaus Width: 3.7859

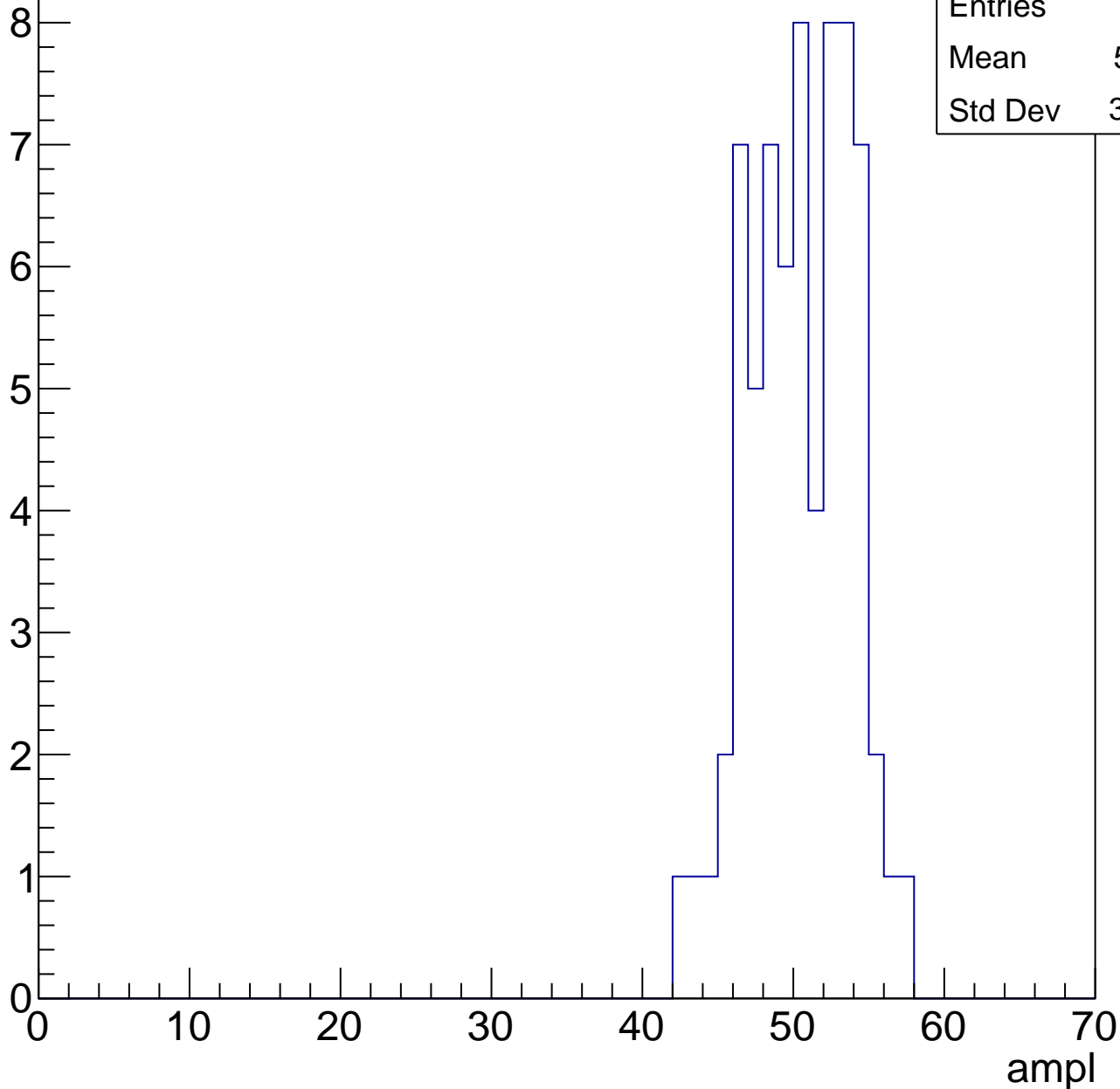


B1L101S, U12-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.01
Std Dev	3.286

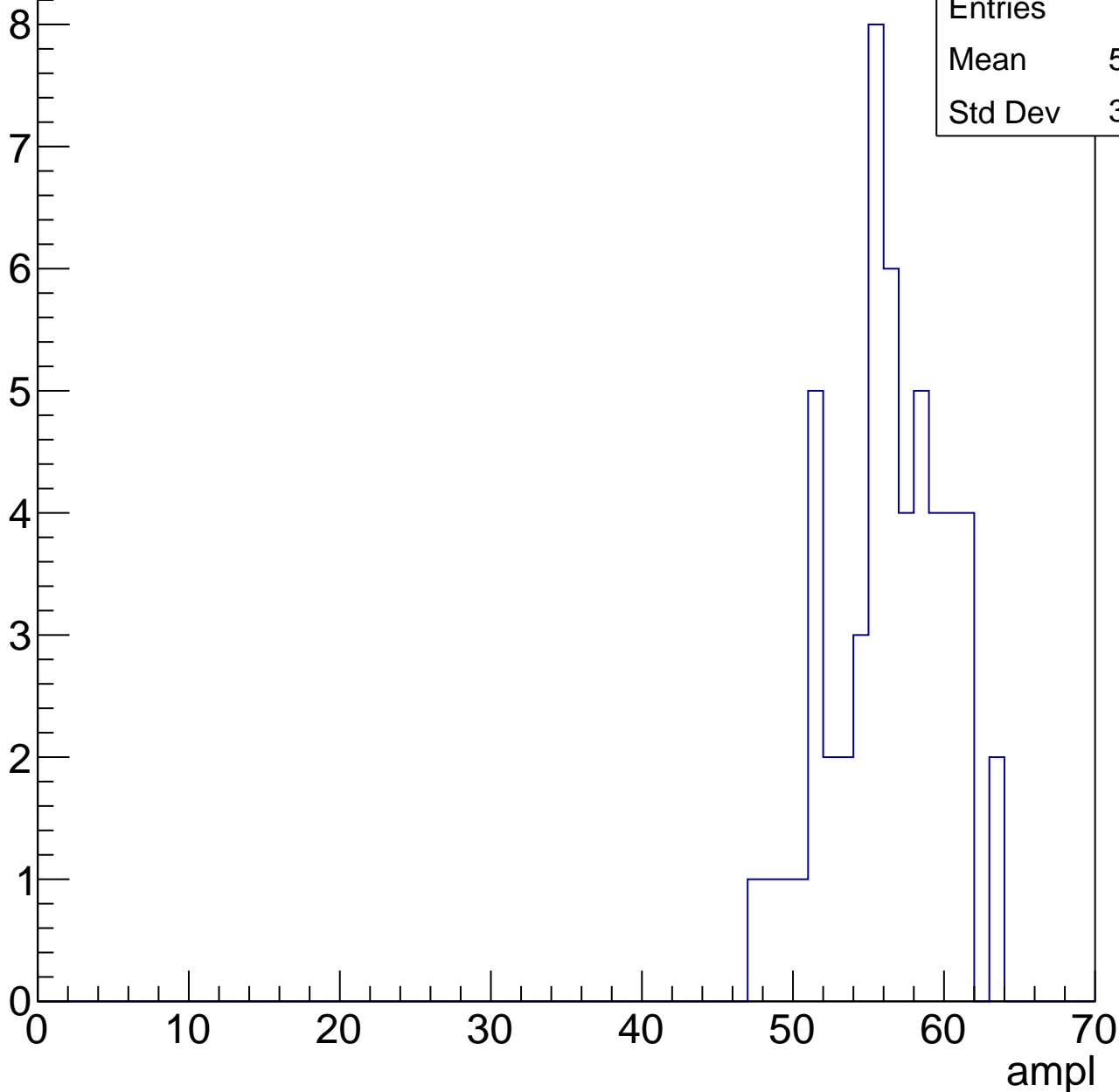


B1L101S, U12-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.87
Std Dev	3.752

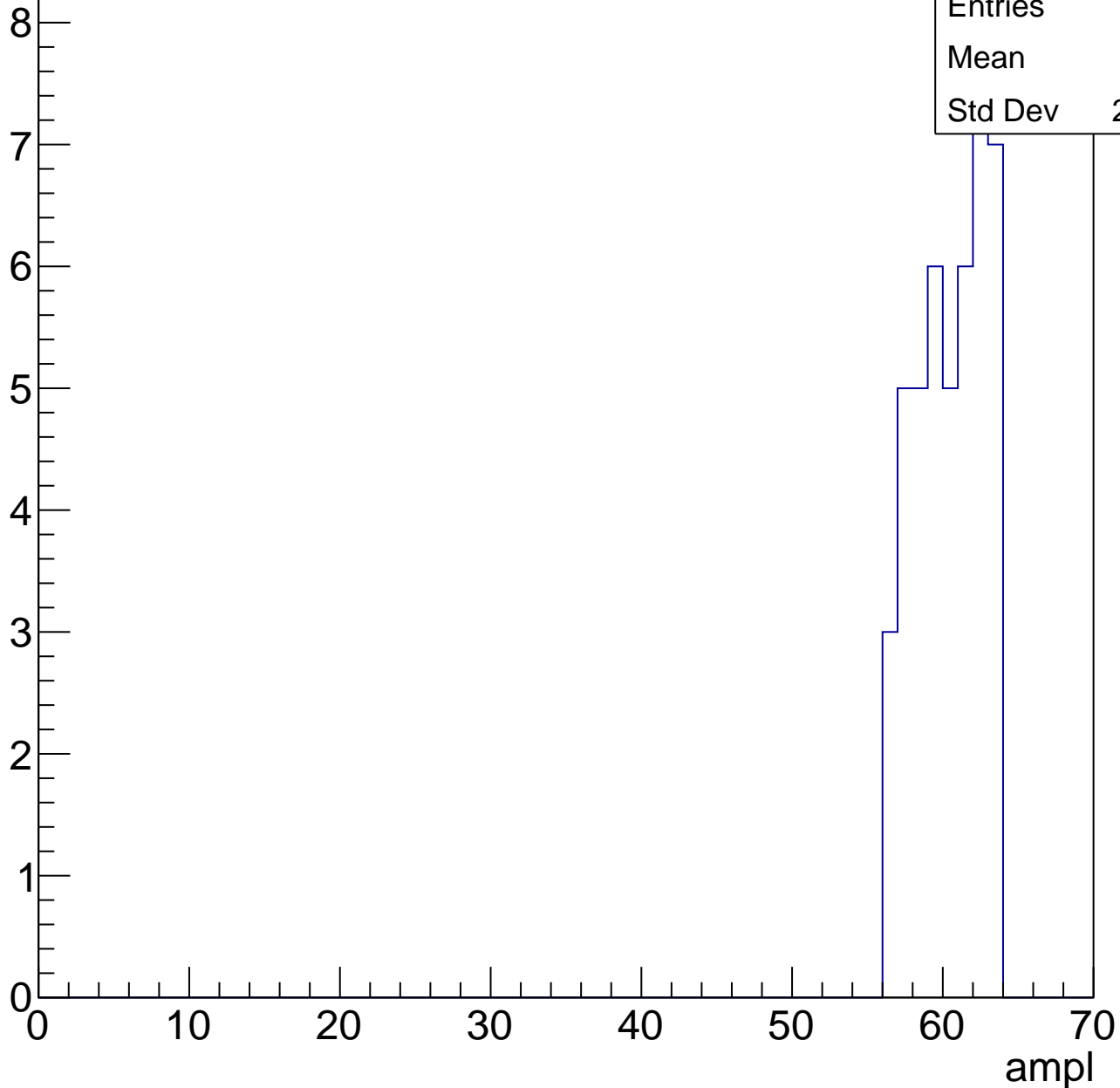


B1L101S, U12-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

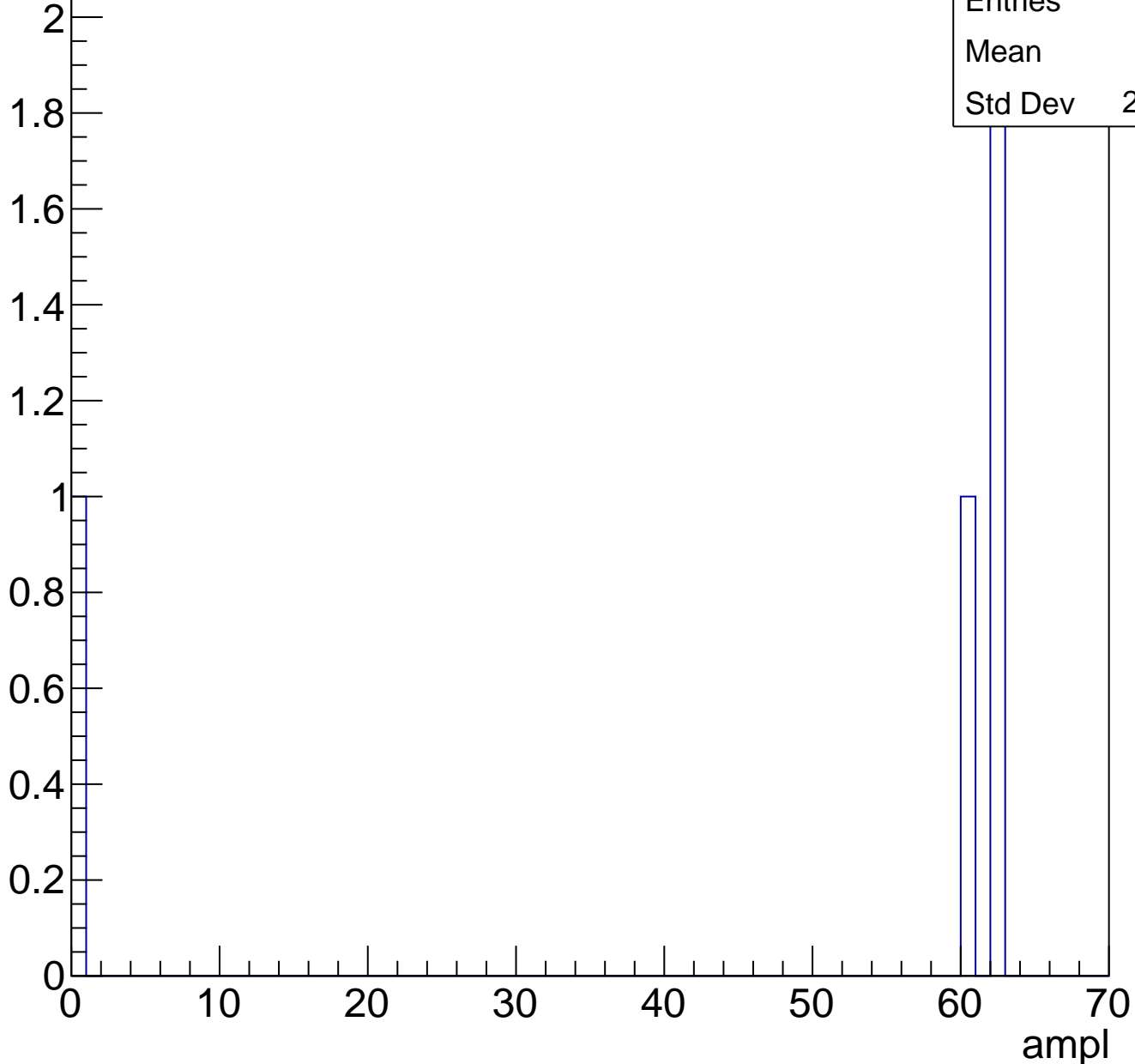
Entries	45
Mean	60
Std Dev	2.211



B1L101S, U12-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U12-ch95, adc0

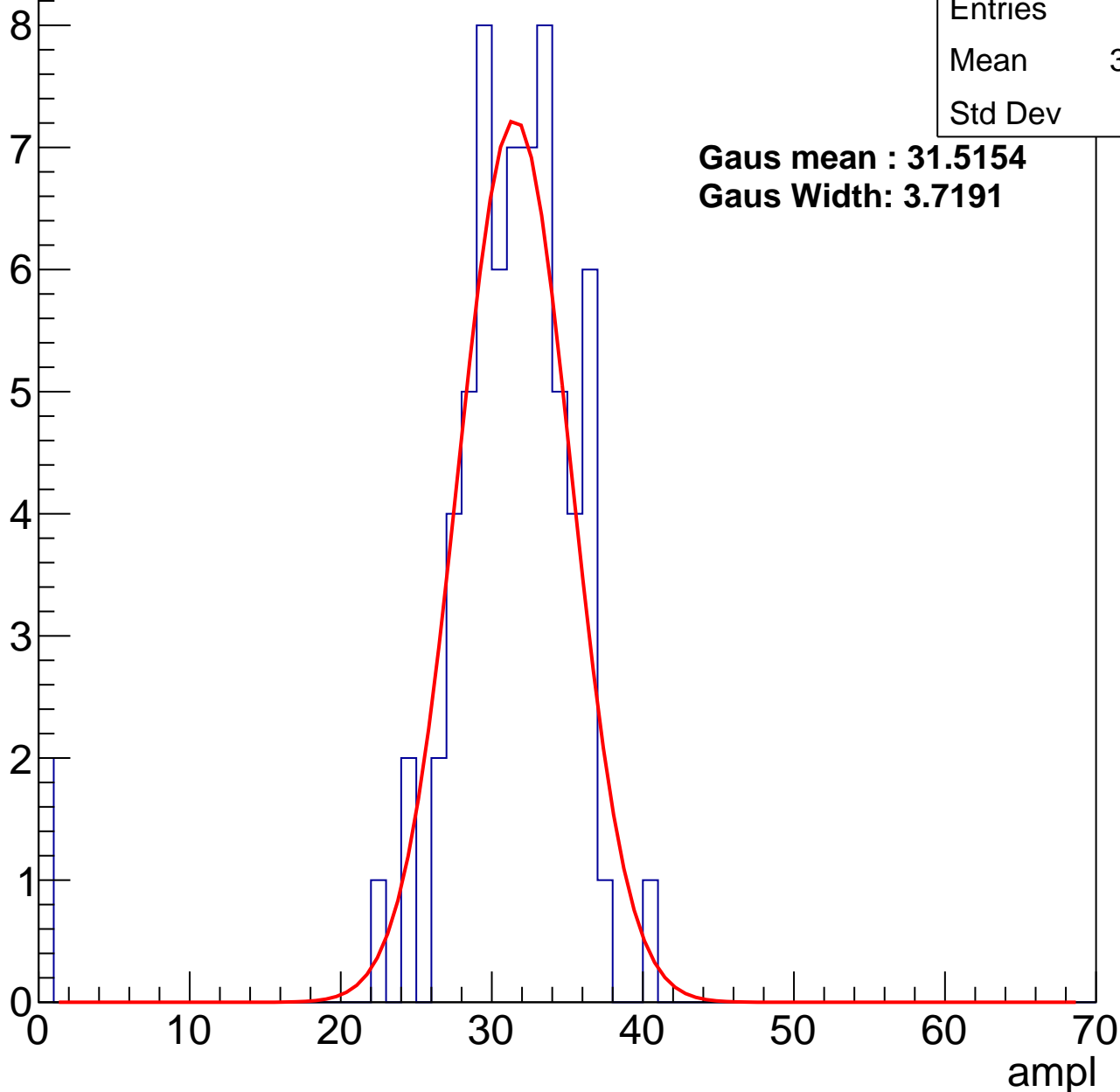
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.29
Std Dev	6.23

Gaus mean : 31.5154

Gaus Width: 3.7191



B1L101S, U12-ch95, adc1

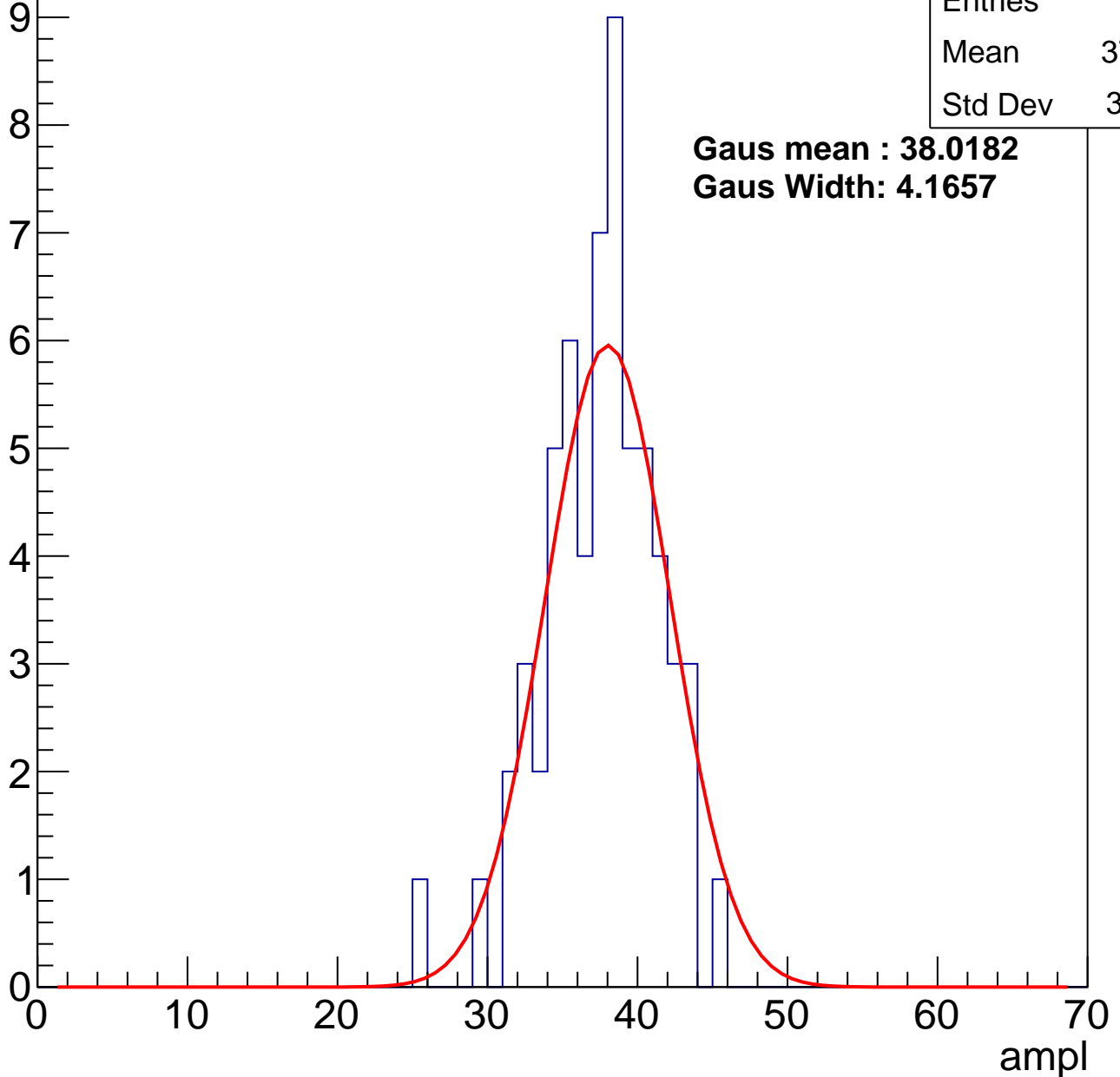
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.08
Std Dev	3.721

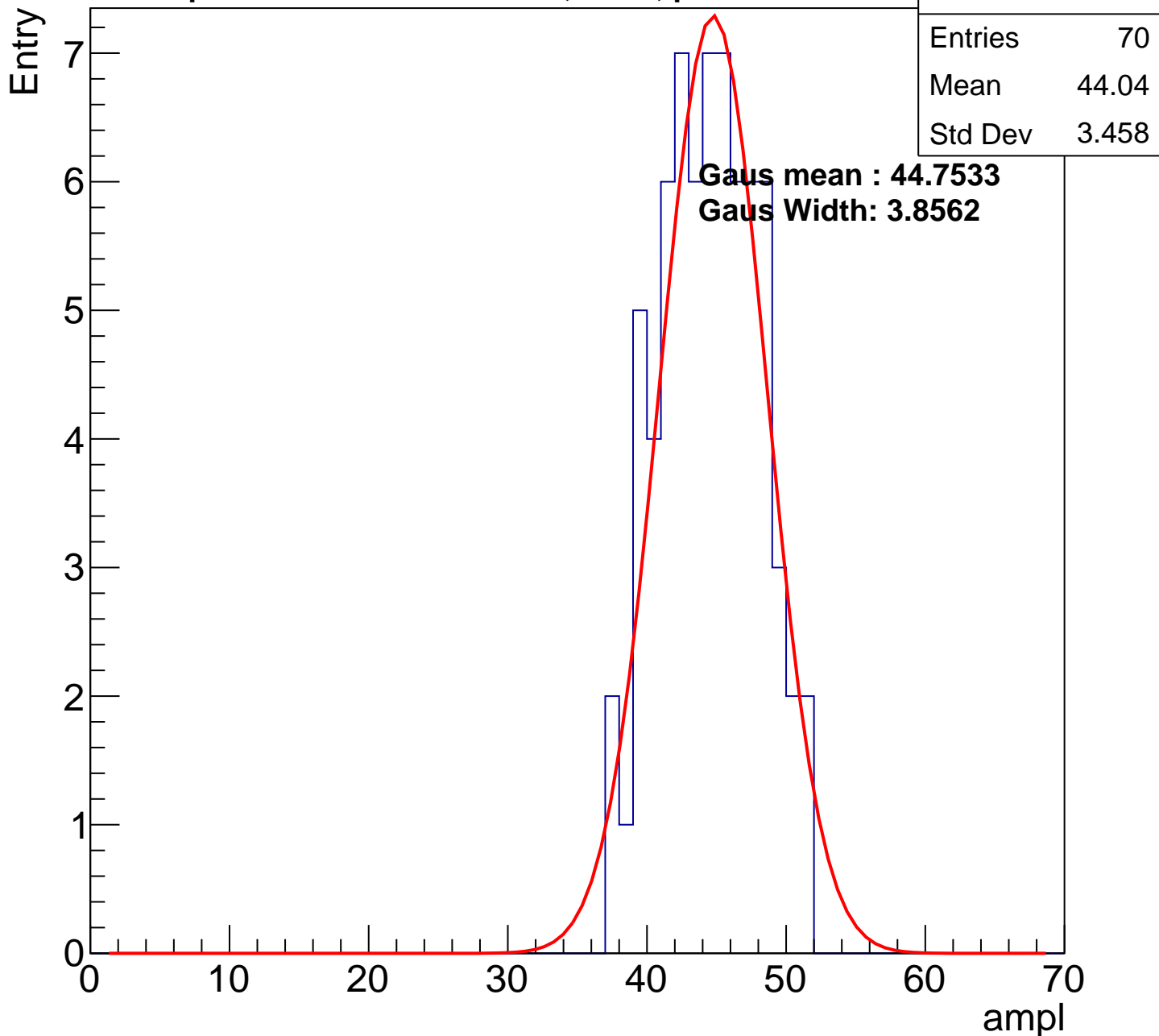
Gaus mean : 38.0182

Gaus Width: 4.1657



B1L101S, U12-ch95, adc2

calib_packv5_042523_0143.root, FC#0, port D2

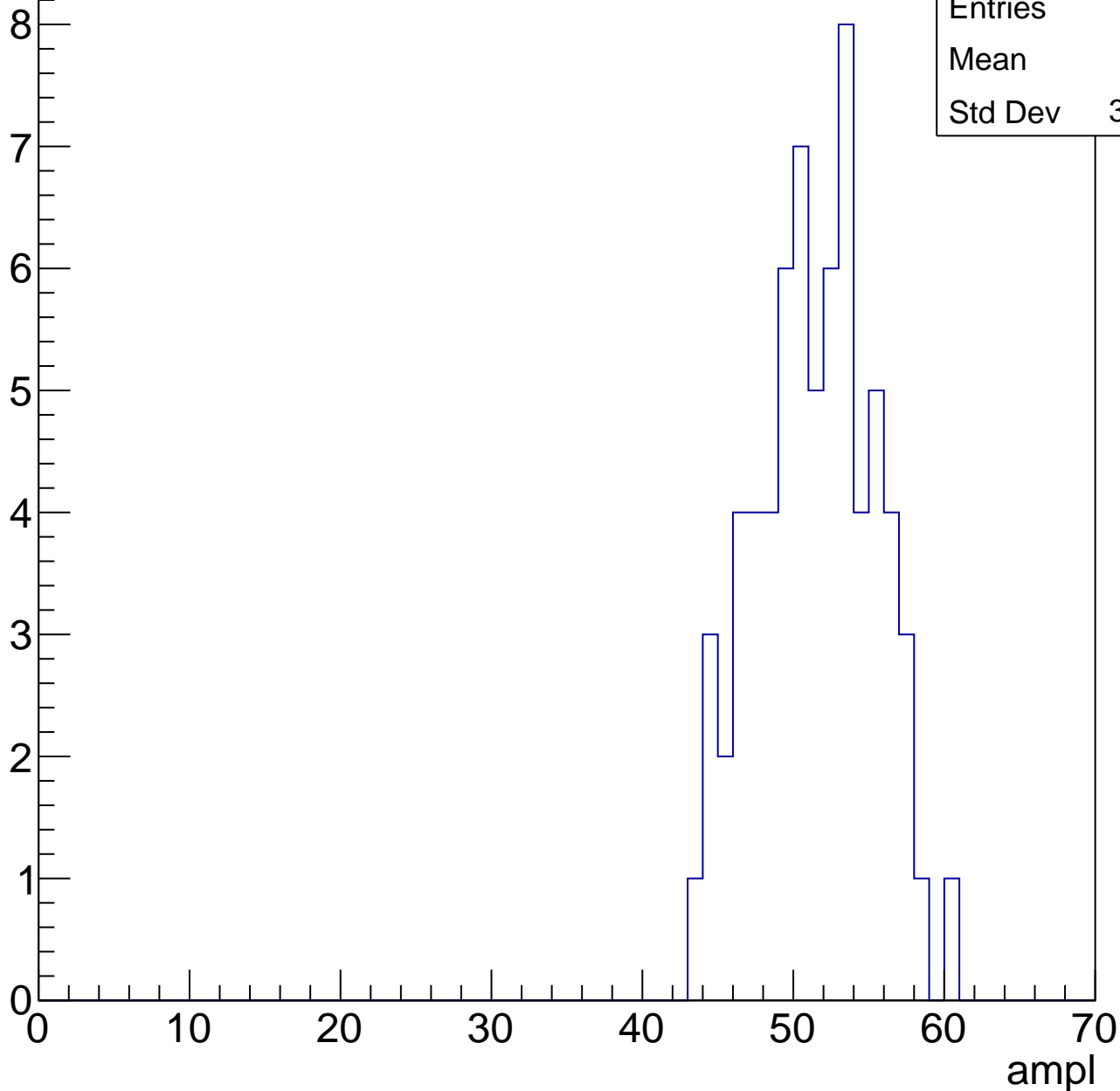


B1L101S, U12-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51
Std Dev	3.846

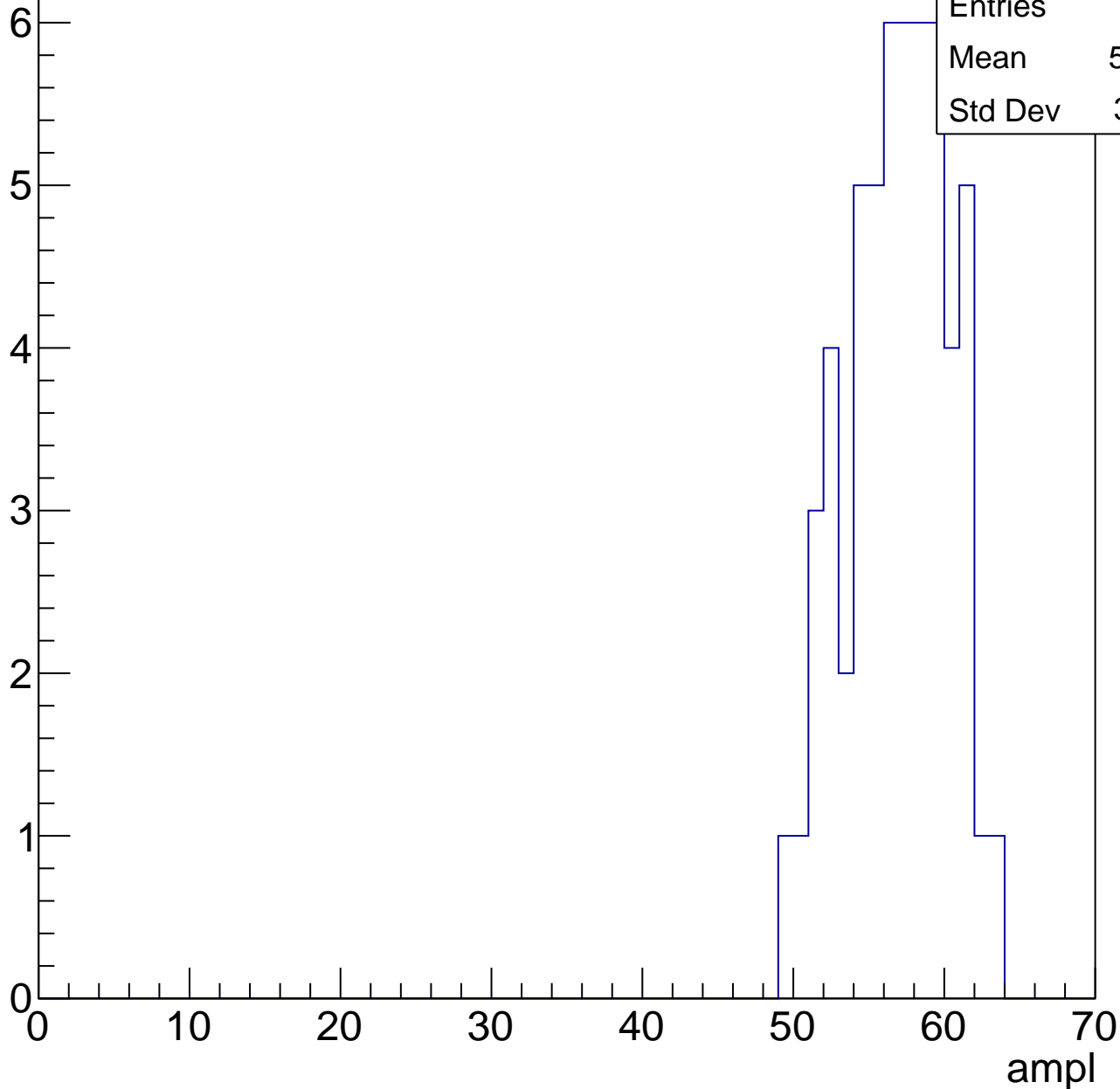


B1L101S, U12-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.45
Std Dev	3.311

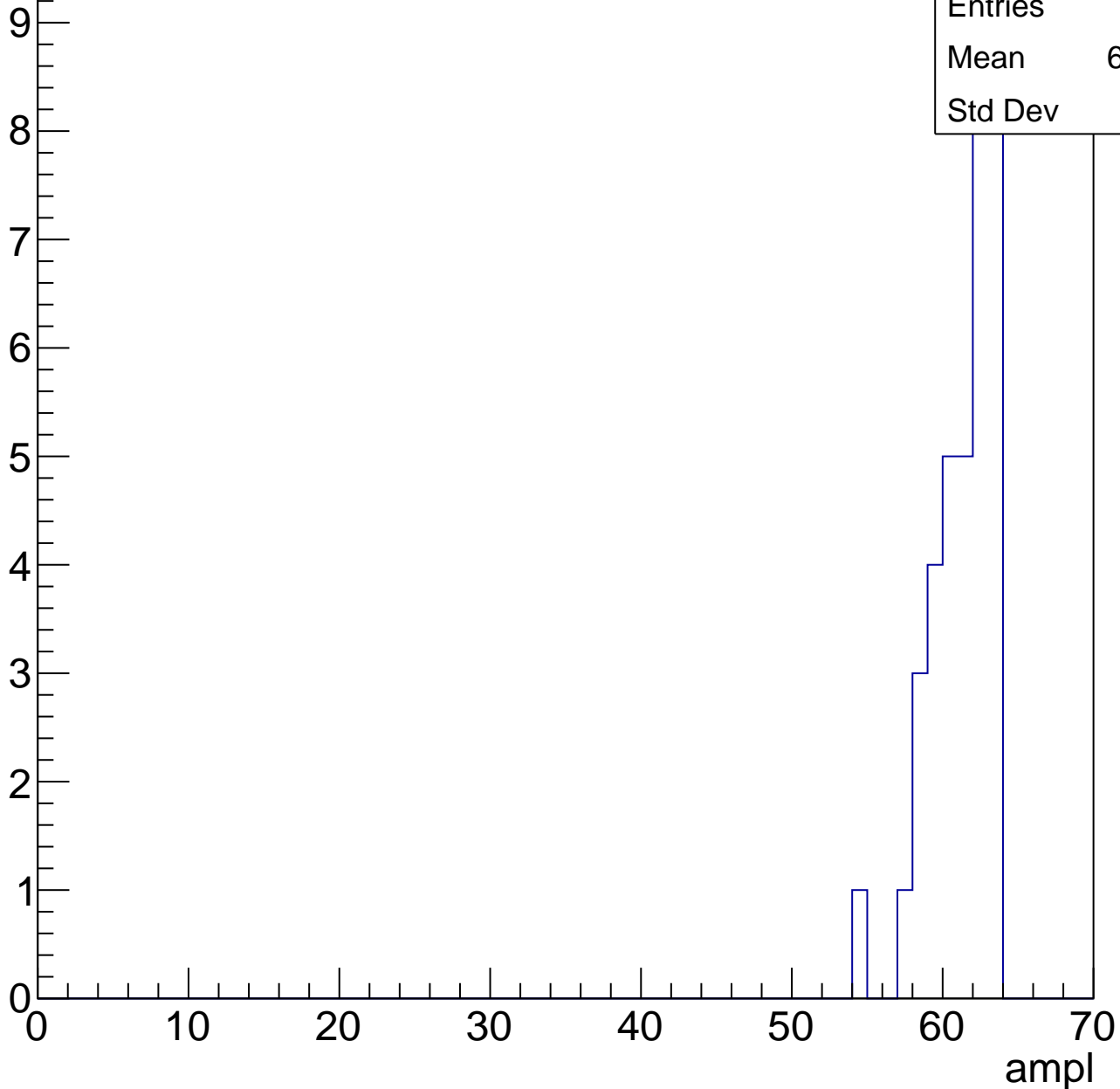


B1L101S, U12-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	60.84
Std Dev	2.06



B1L101S, U12-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch96, adc0

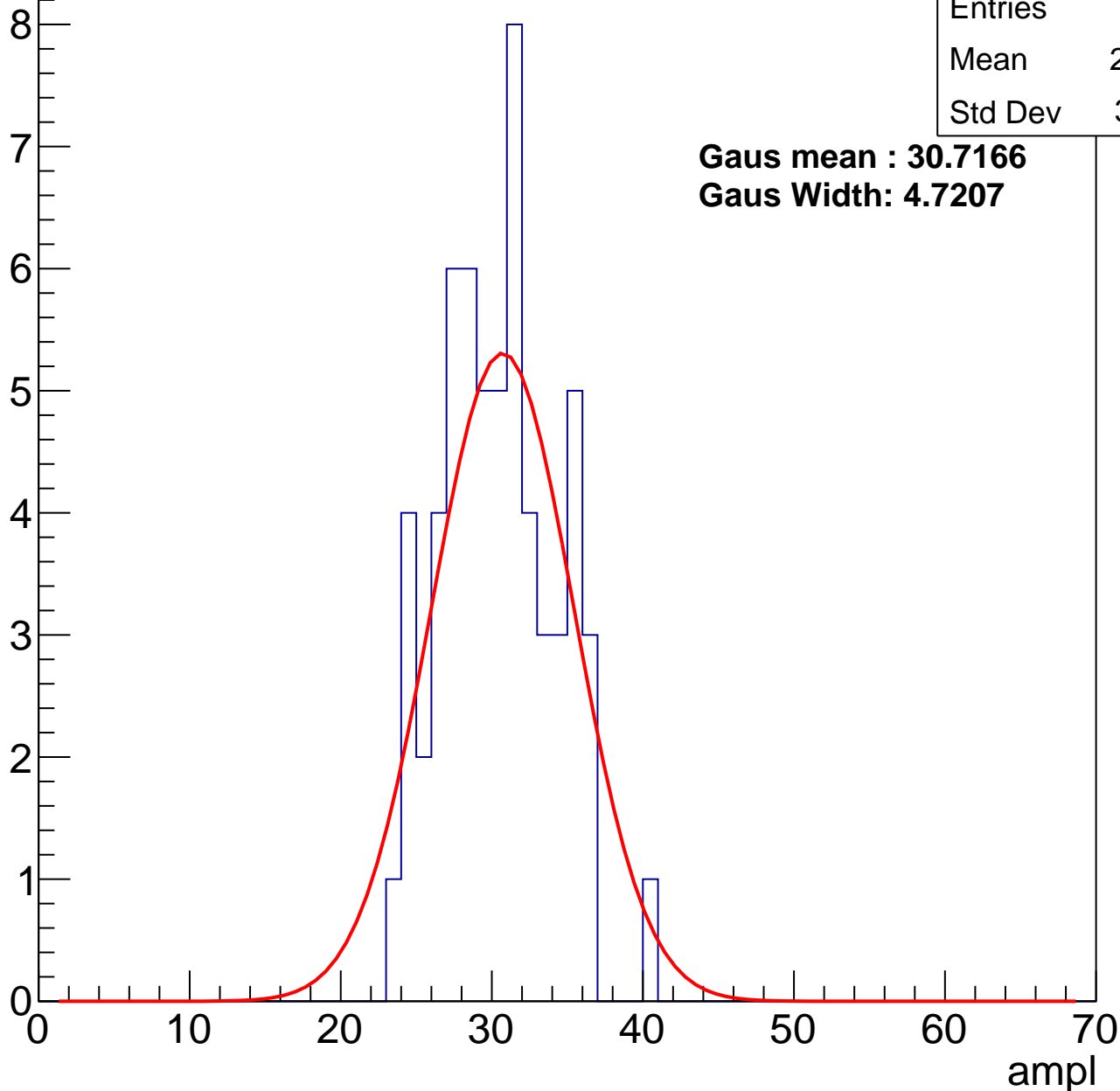
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.97
Std Dev	3.701

Gaus mean : 30.7166

Gaus Width: 4.7207



B1L101S, U12-ch96, adc1

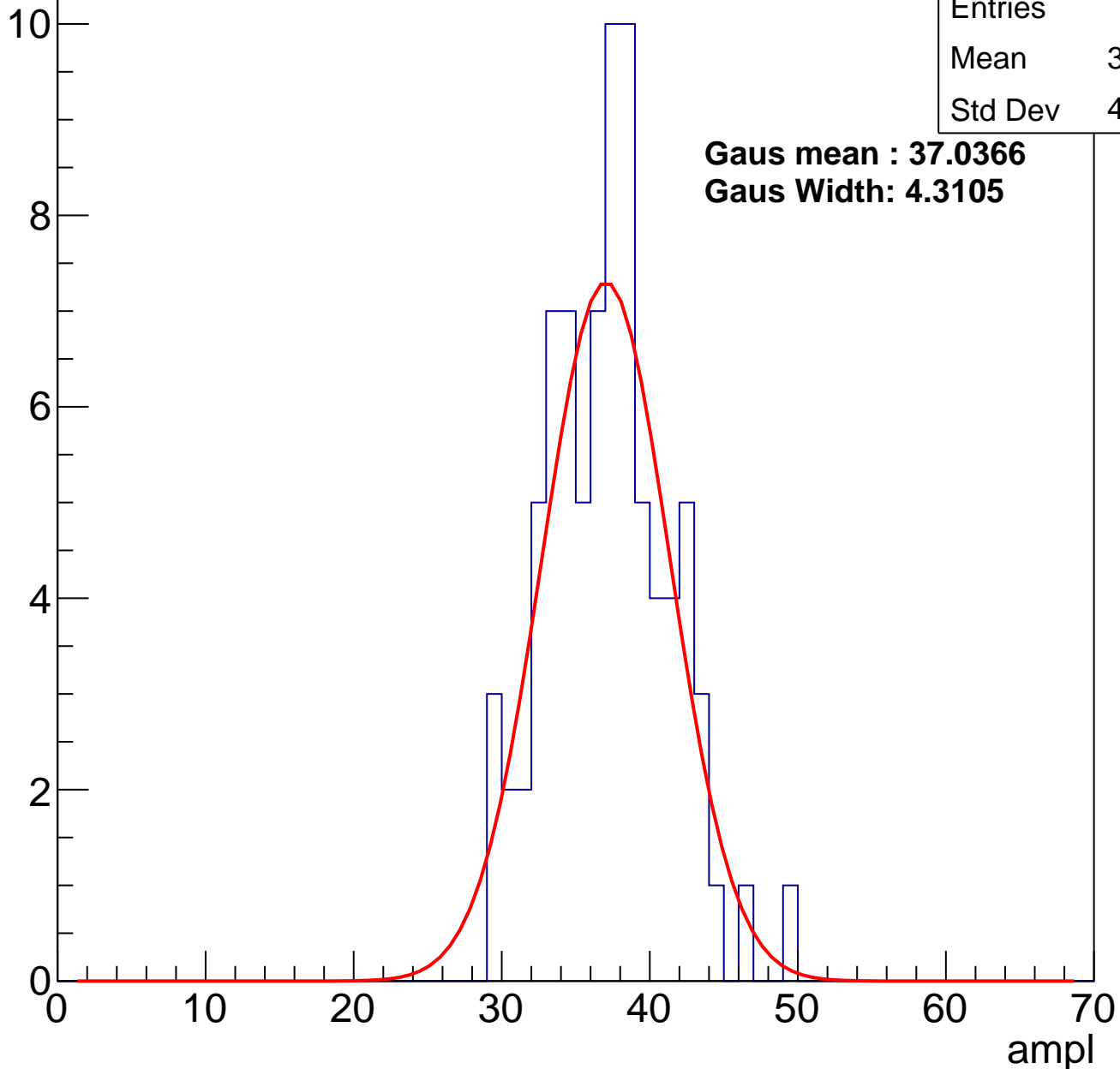
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	36.73
Std Dev	4.012

Gaus mean : 37.0366

Gaus Width: 4.3105

Entry



B1L101S, U12-ch96, adc2

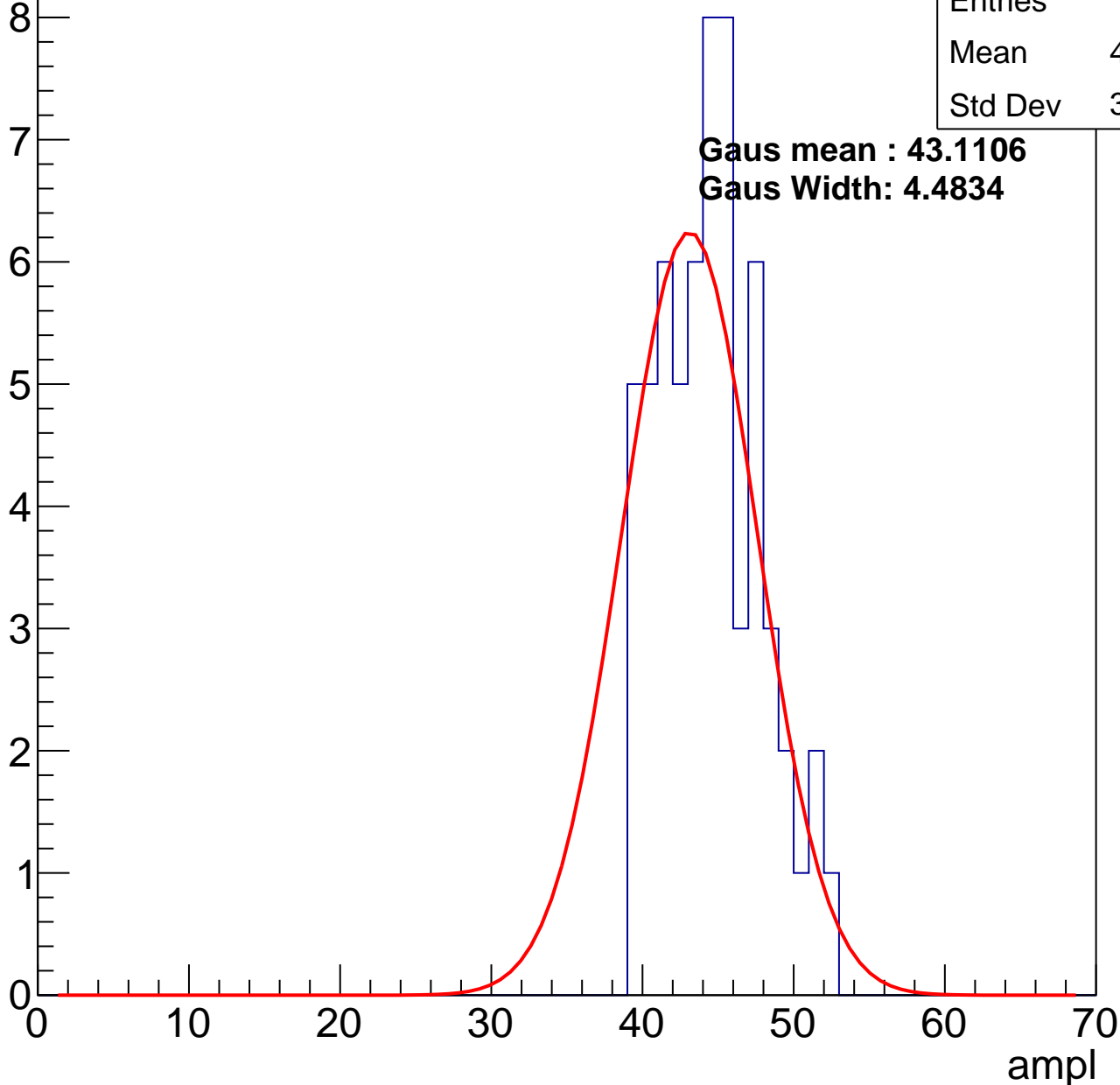
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	44.05
Std Dev	3.276

Gaus mean : 43.1106

Gaus Width: 4.4834

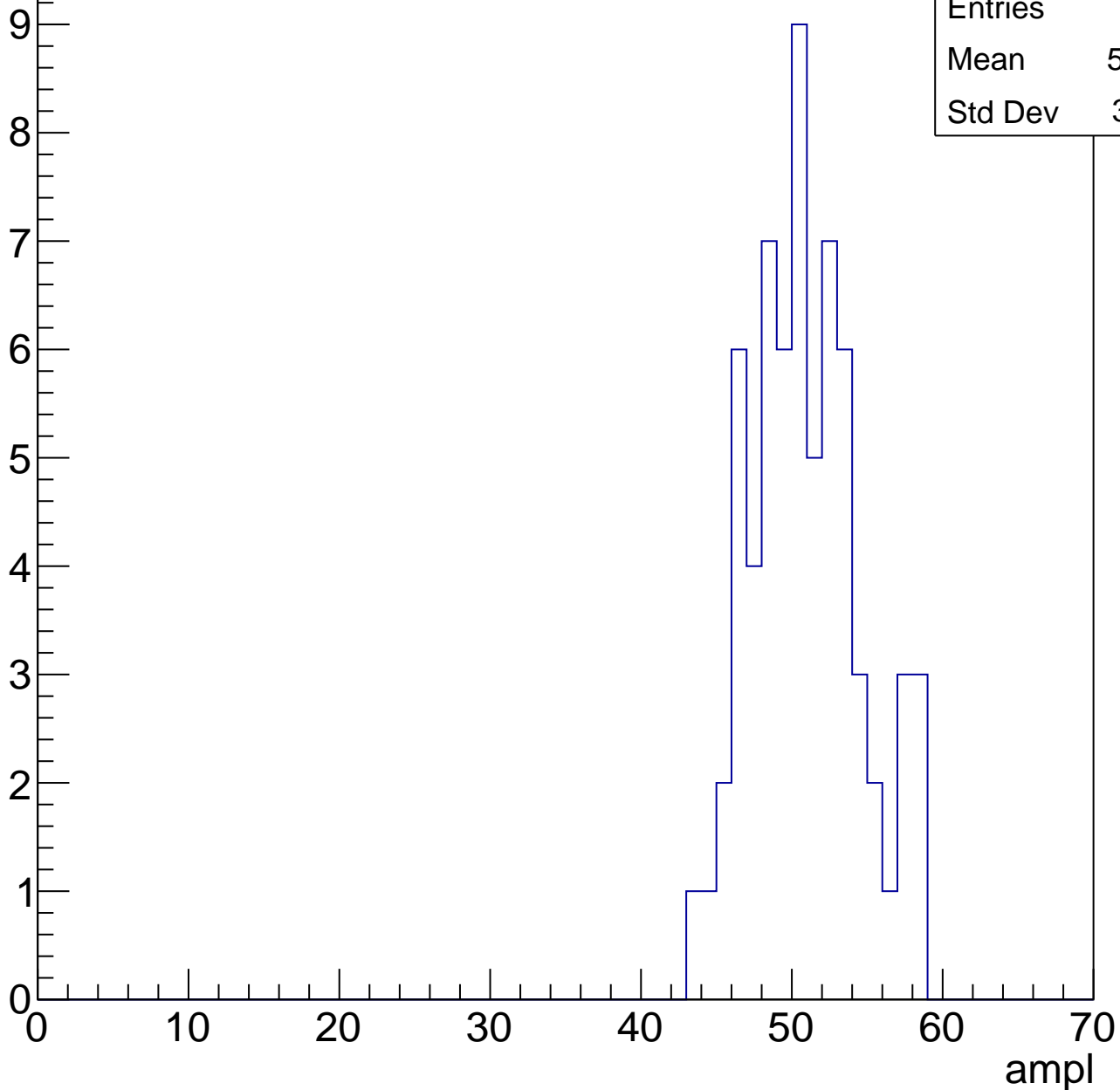


B1L101S, U12-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.47
Std Dev	3.581

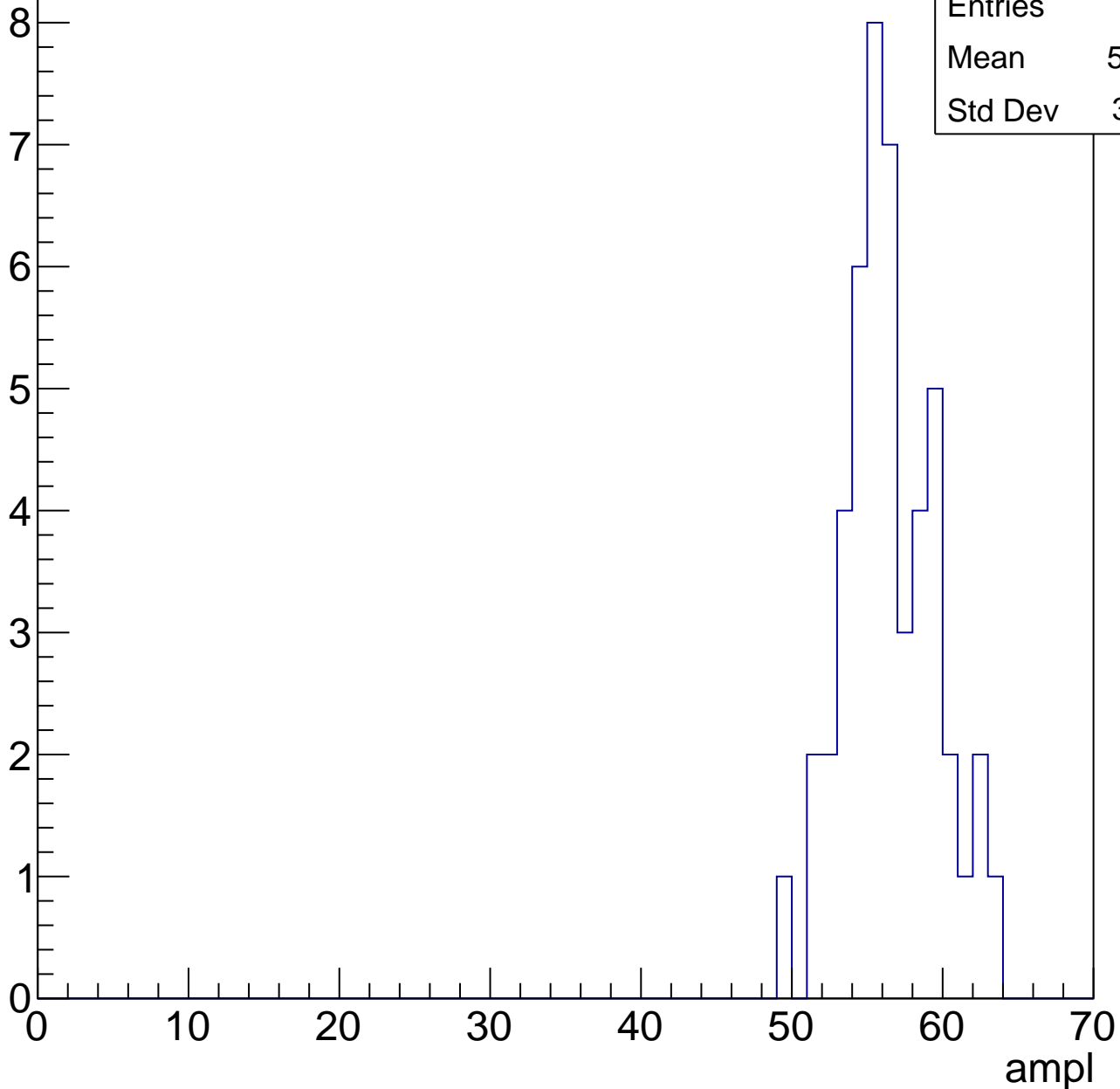


B1L101S, U12-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	56.02
Std Dev	3.031

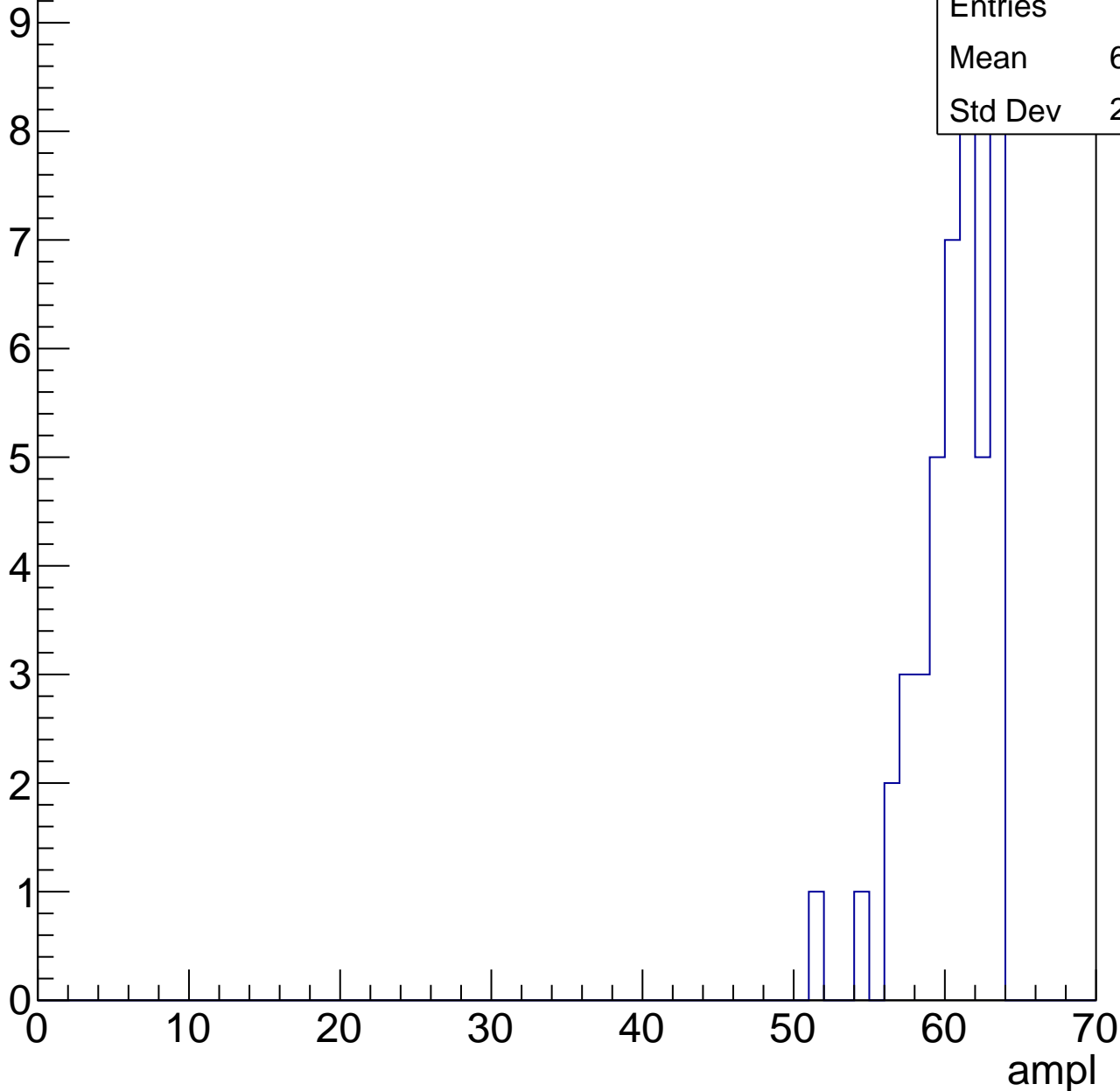


B1L101S, U12-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

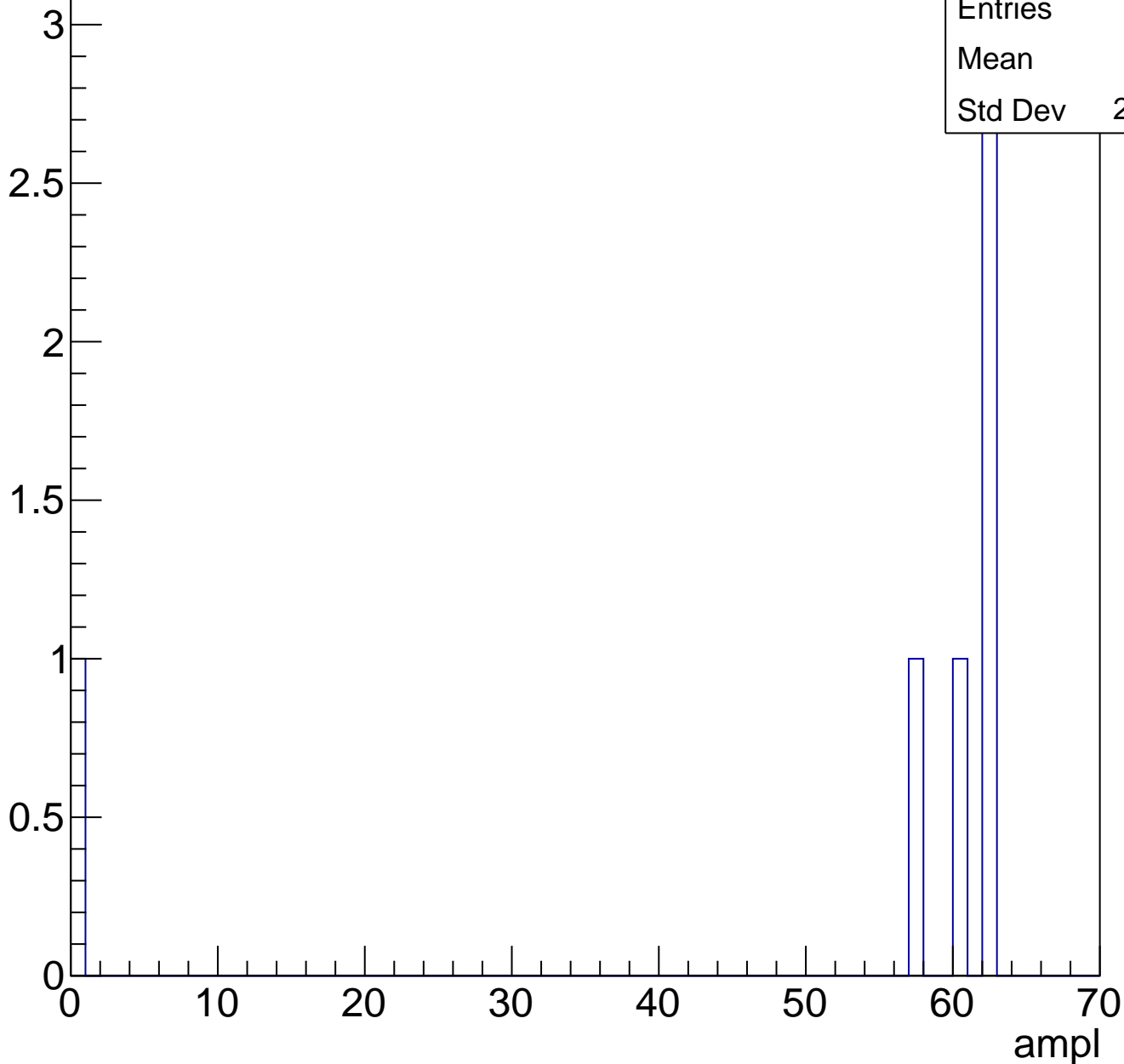
Entries	45
Mean	60.07
Std Dev	2.594



B1L101S, U12-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch97, adc0

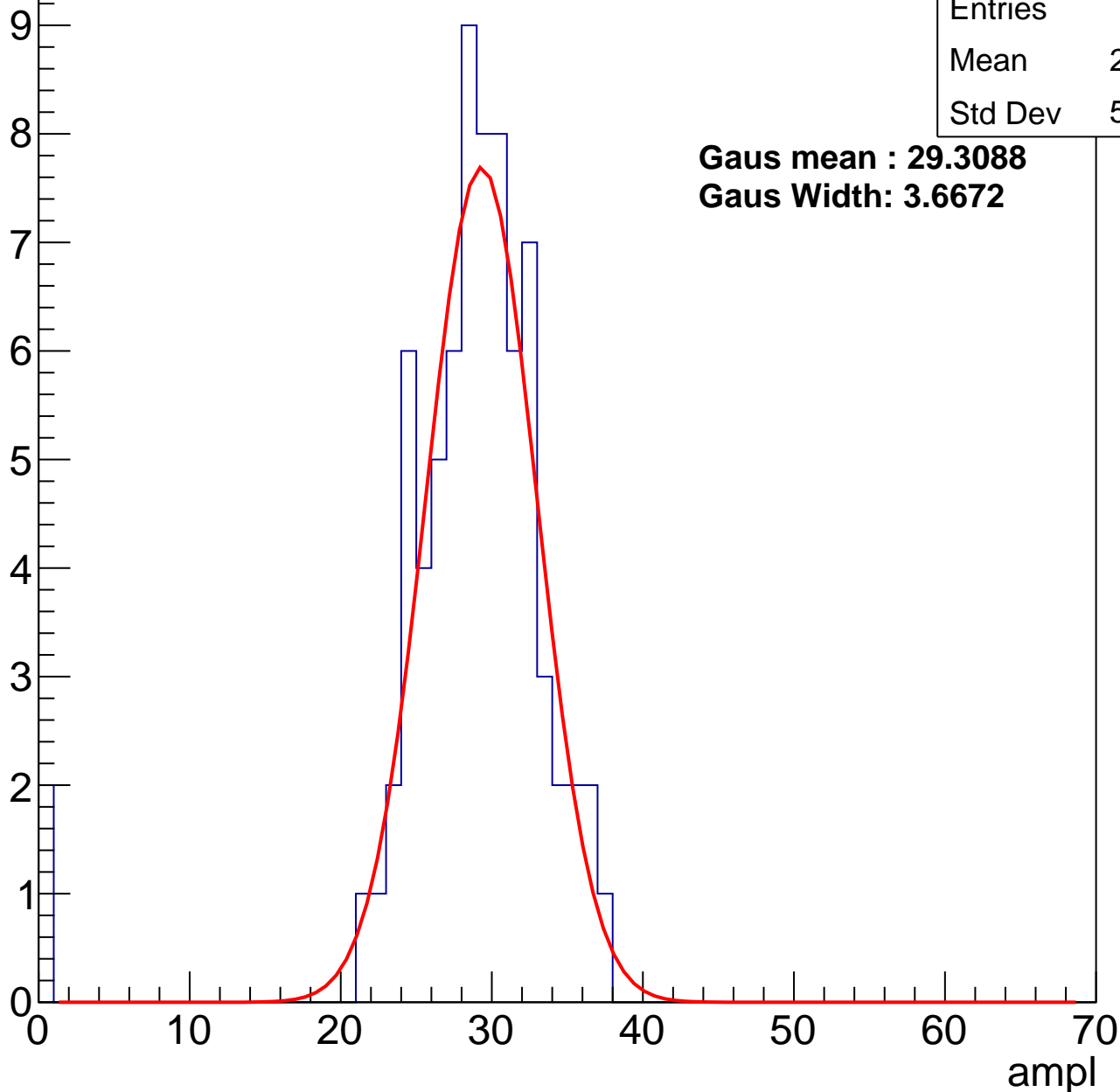
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	28.07
Std Dev	5.797

Gaus mean : 29.3088

Gaus Width: 3.6672



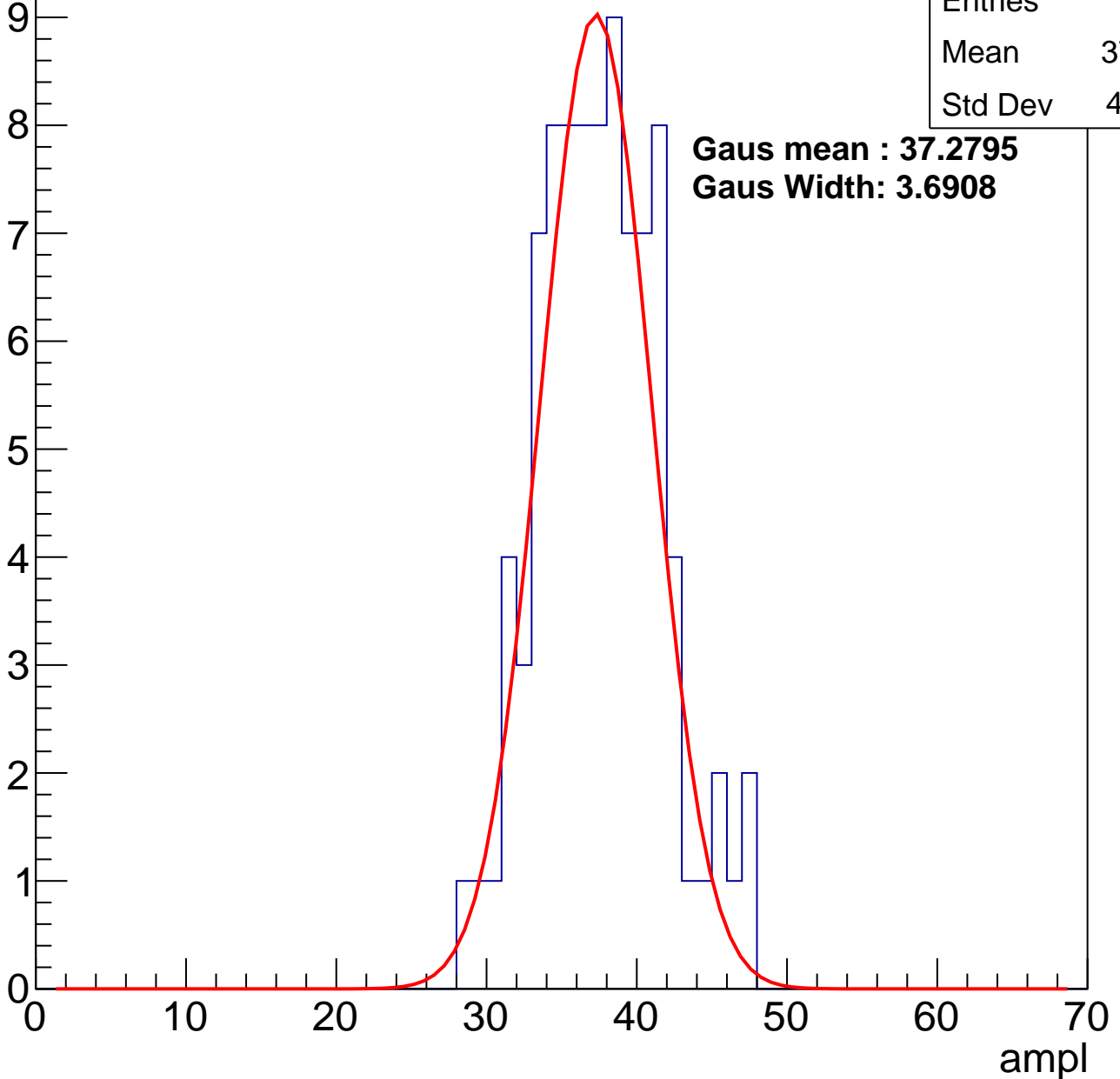
B1L101S, U12-ch97, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	91
Mean	37.16
Std Dev	4.001

Gaus mean : 37.2795
Gaus Width: 3.6908



B1L101S, U12-ch97, adc2

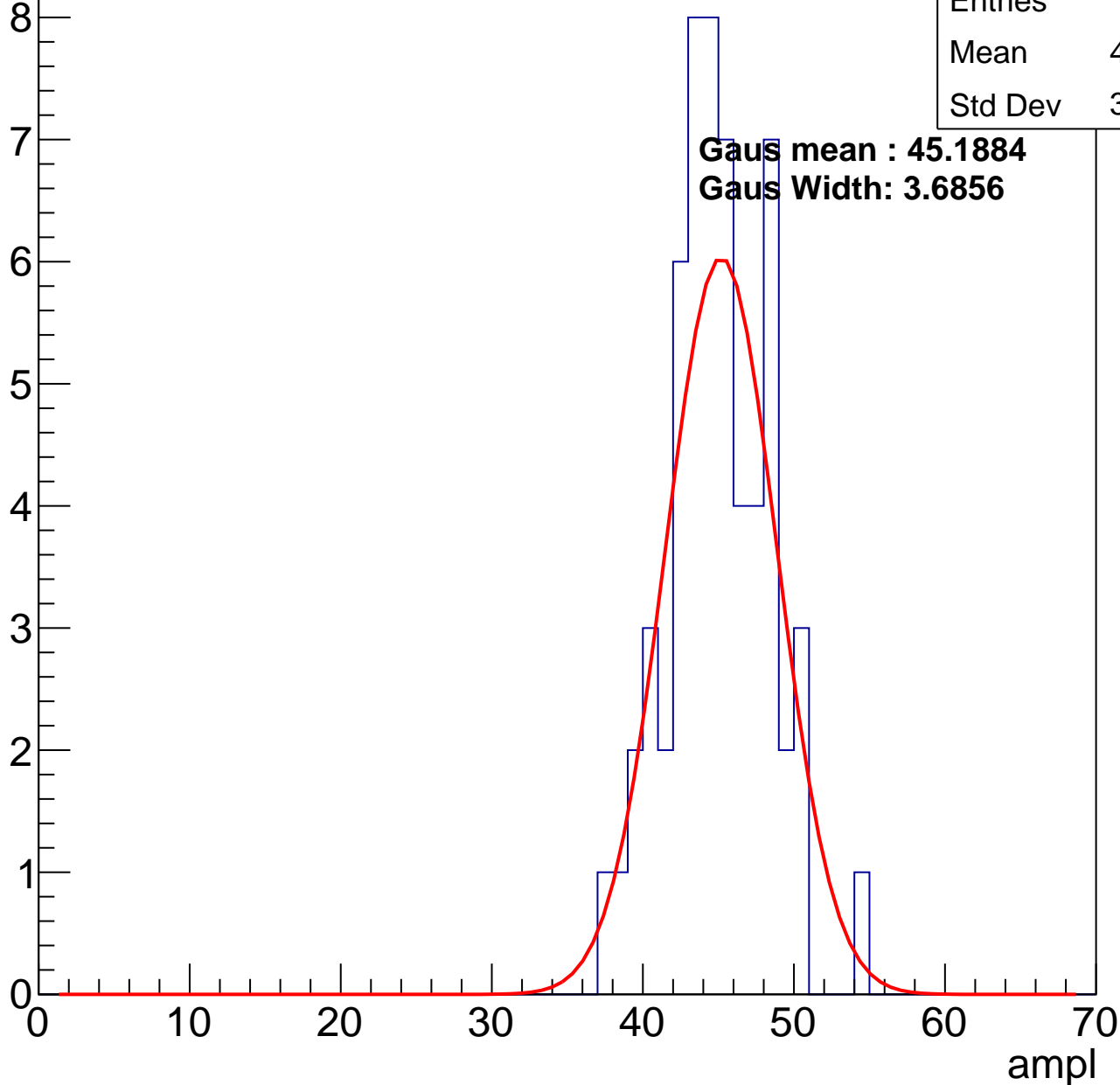
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.54
Std Dev	3.305

Gaus mean : 45.1884

Gaus Width: 3.6856

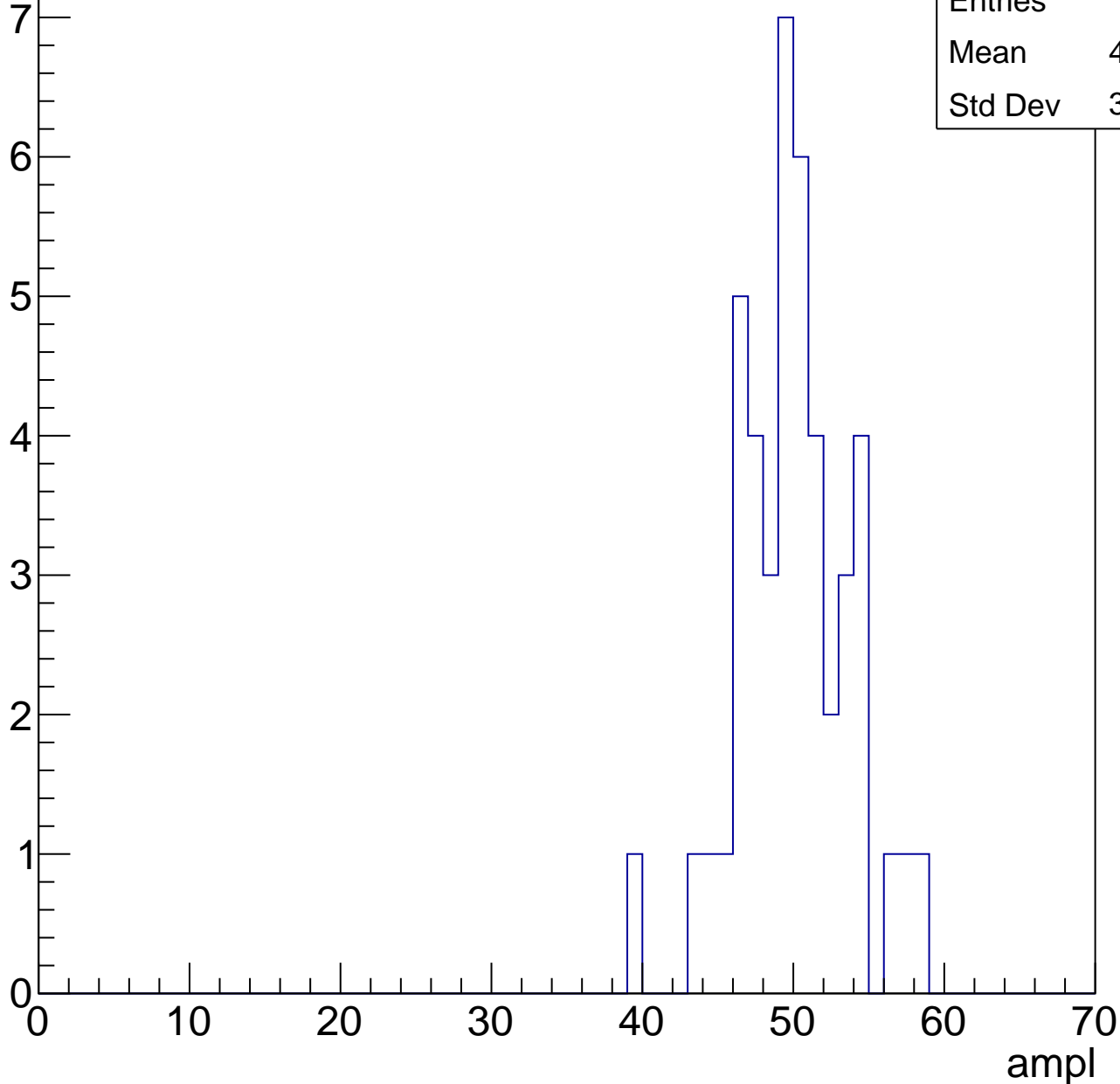


B1L101S, U12-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	49.56
Std Dev	3.679



B1L101S, U12-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	55.72
Std Dev	3.319

Entry

10

8

6

4

2

0

0

10

20

30

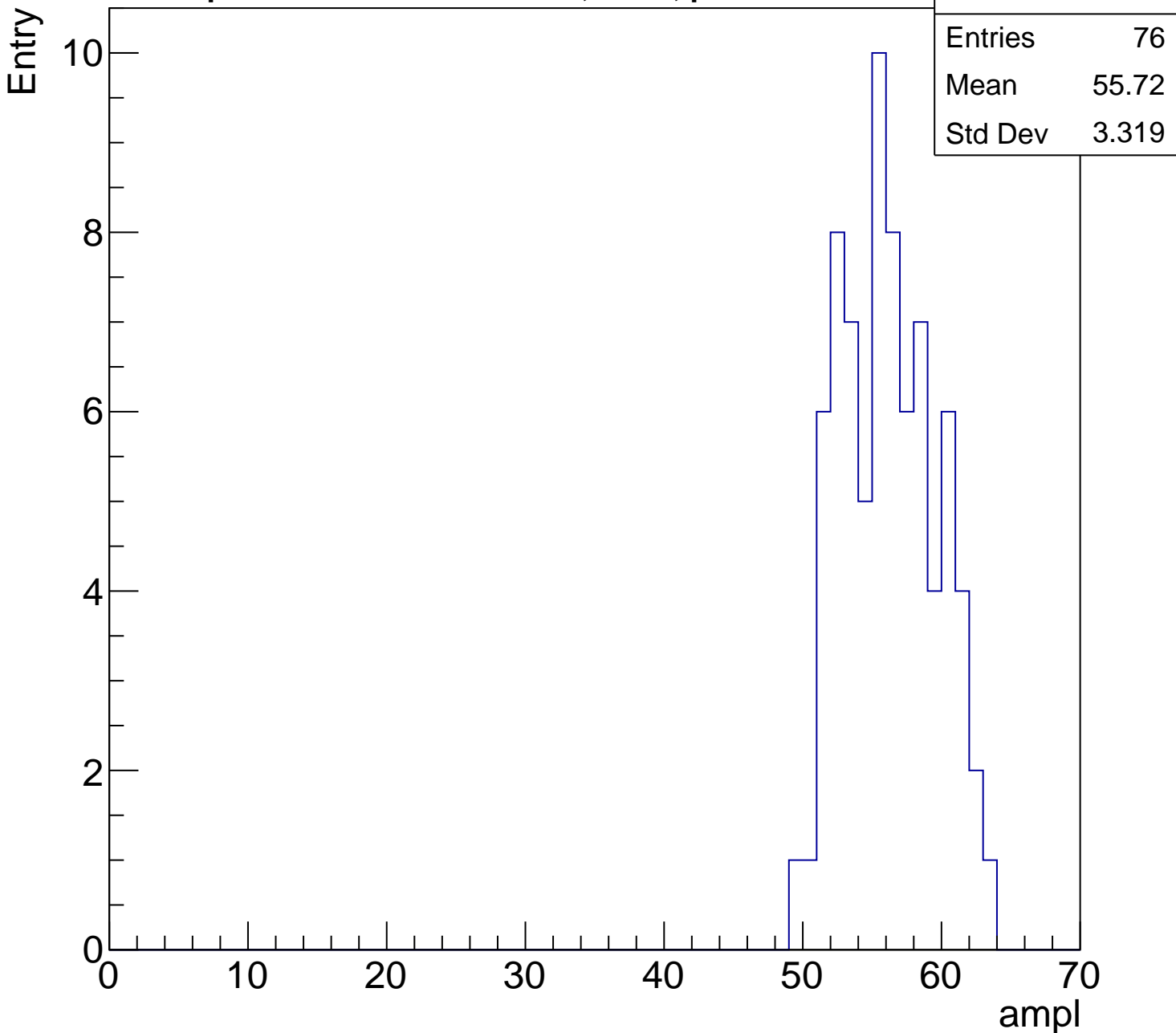
40

50

60

ampl

70



B1L101S, U12-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries

32

Mean

60.22

Std Dev

1.948

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

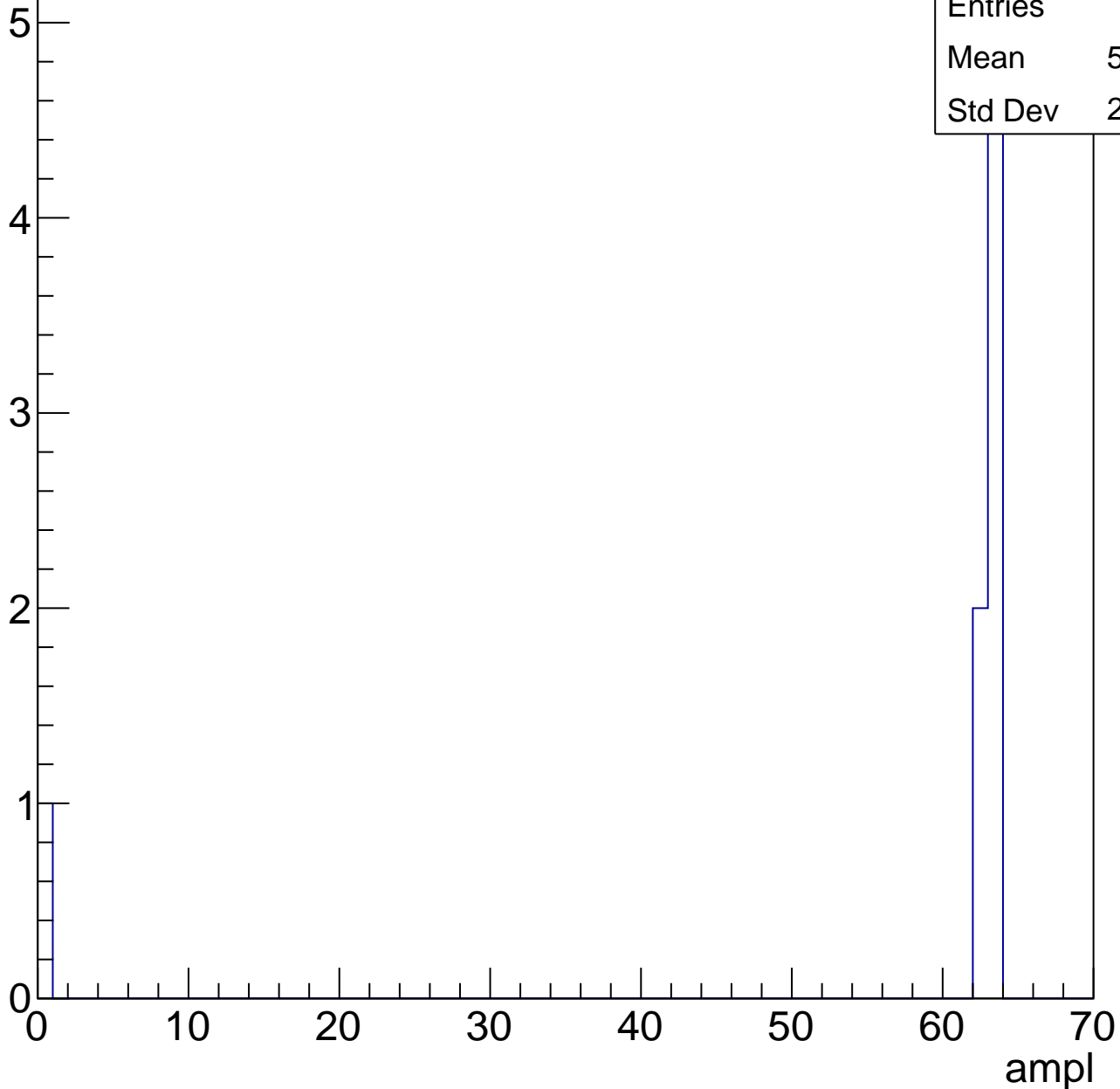
ampl

B1L101S, U12-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B1L101S, U12-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U12-ch98, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	29.21
Std Dev	5.922

Gaus mean : 30.4456

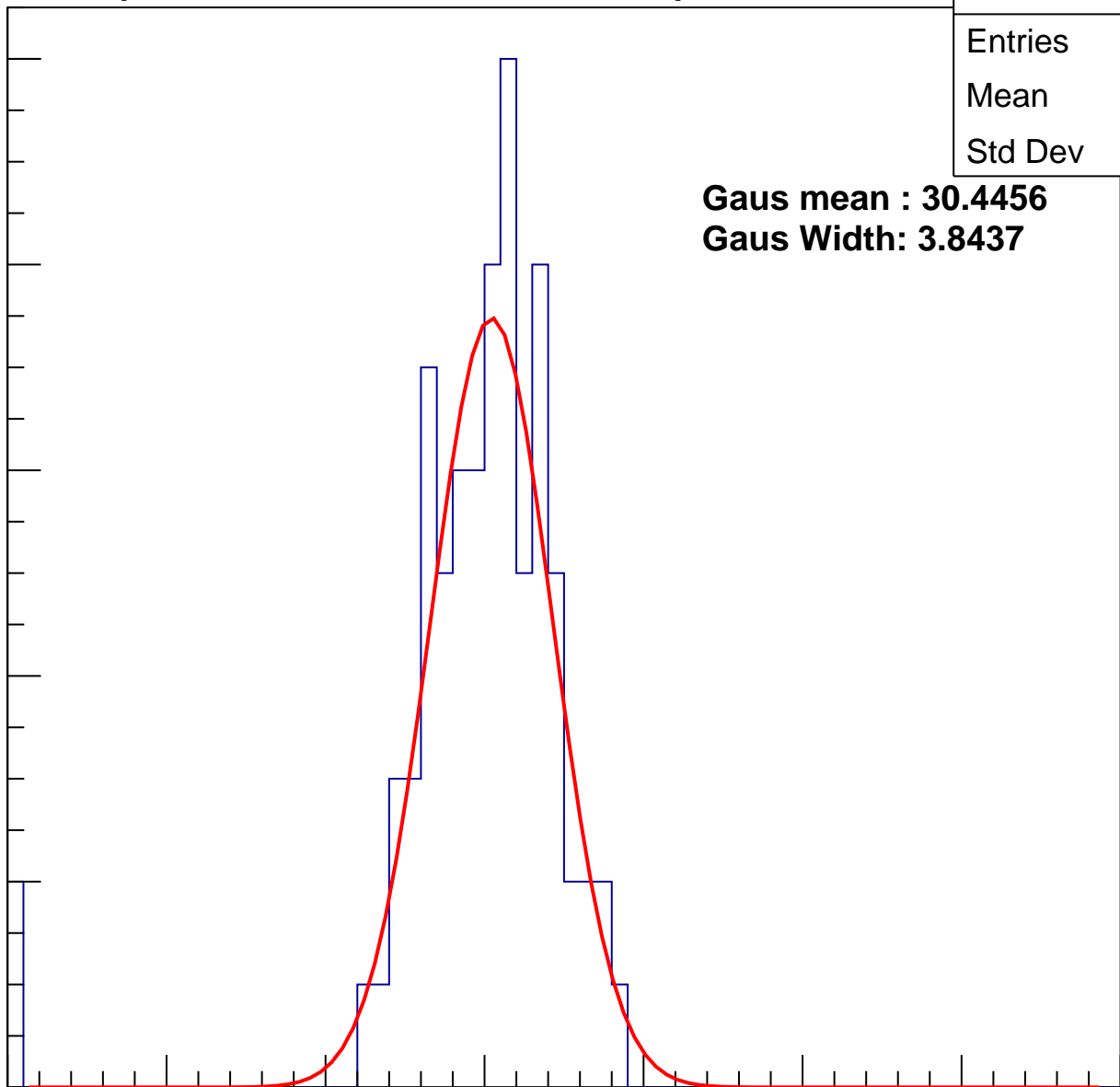
Gaus Width: 3.8437

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch98, adc1

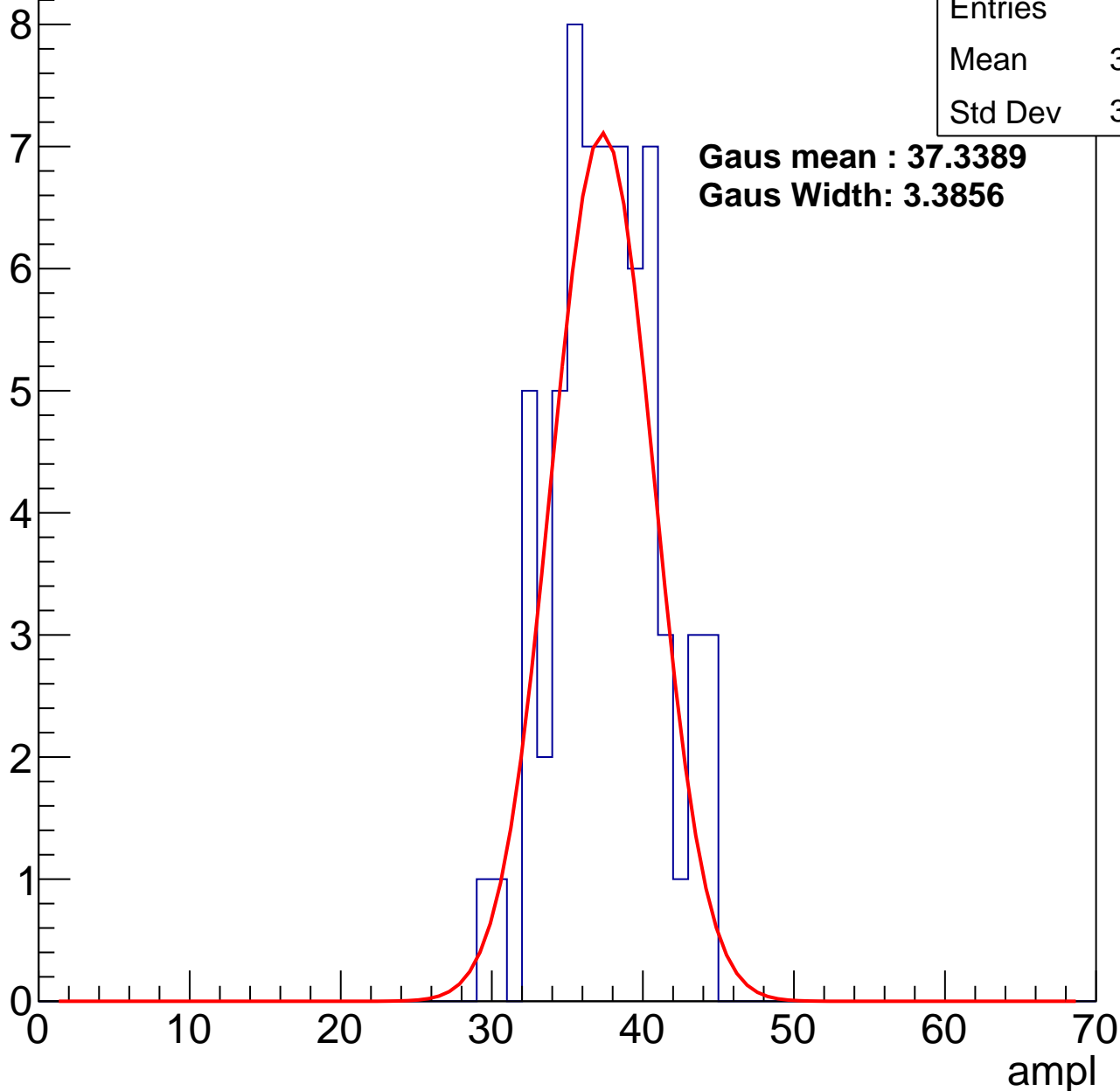
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.15
Std Dev	3.426

Gaus mean : 37.3389

Gaus Width: 3.3856



B1L101S, U12-ch98, adc2

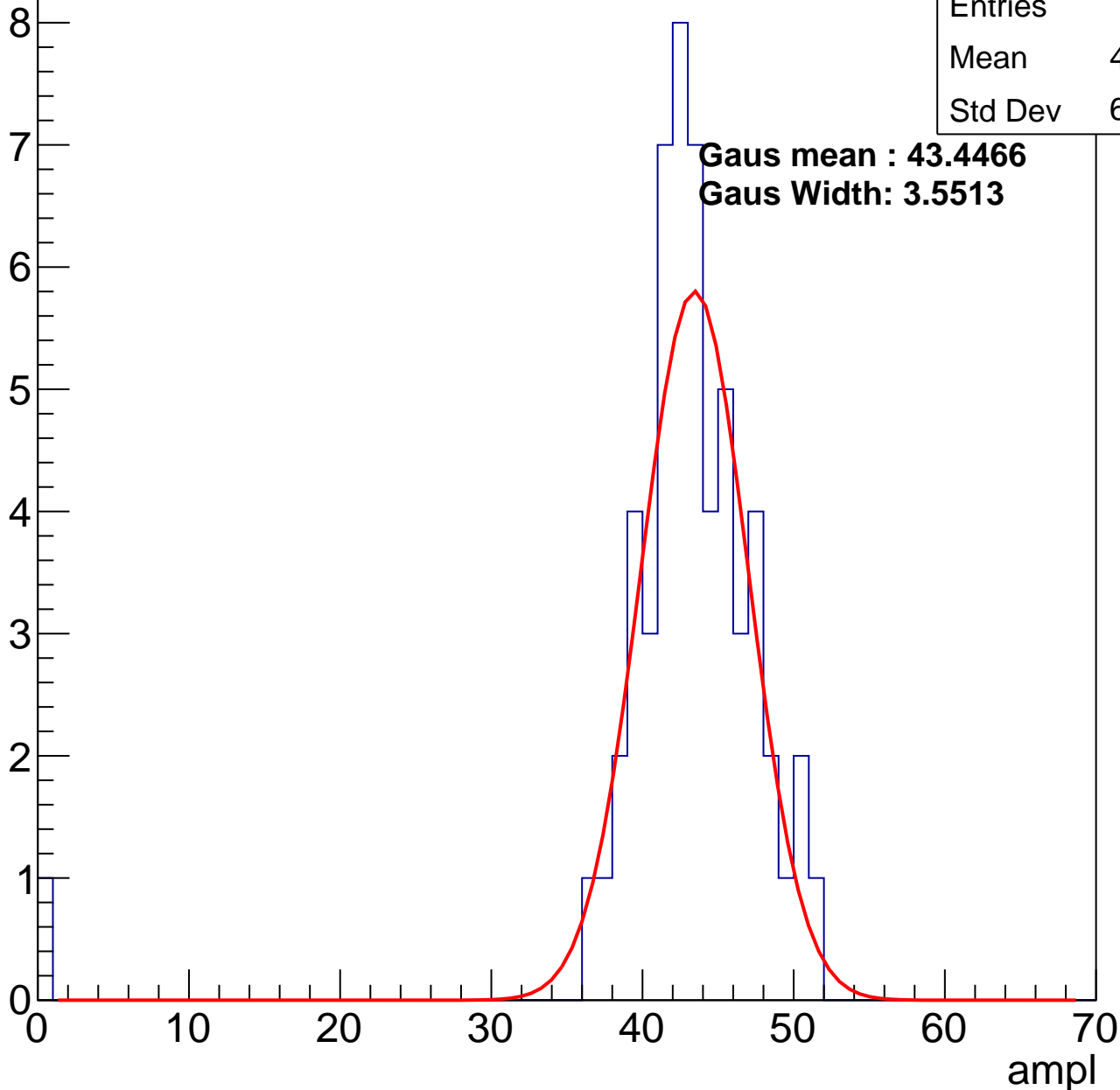
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.36
Std Dev	6.616

Gaus mean : 43.4466

Gaus Width: 3.5513

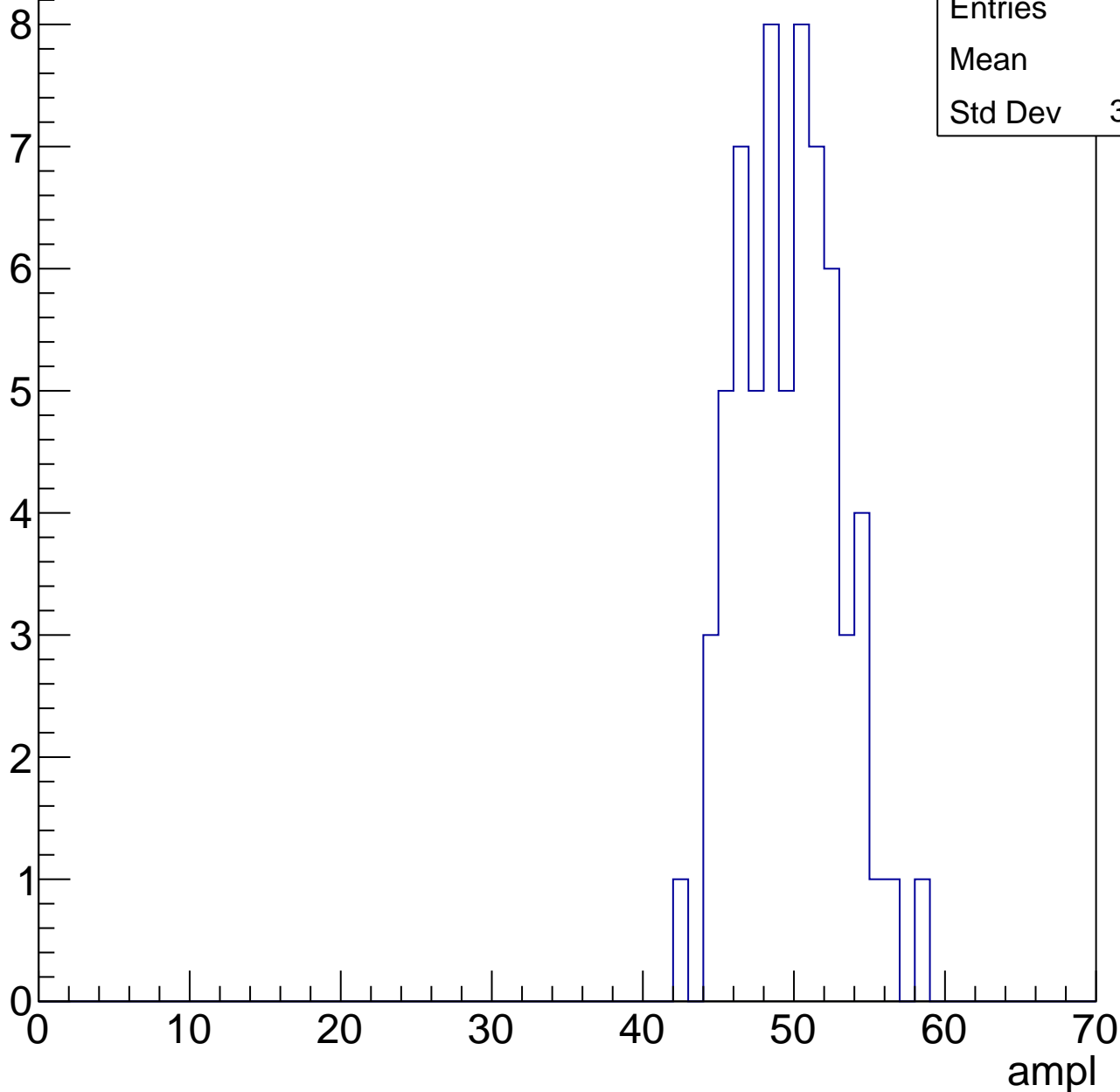


B1L101S, U12-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.2
Std Dev	3.273

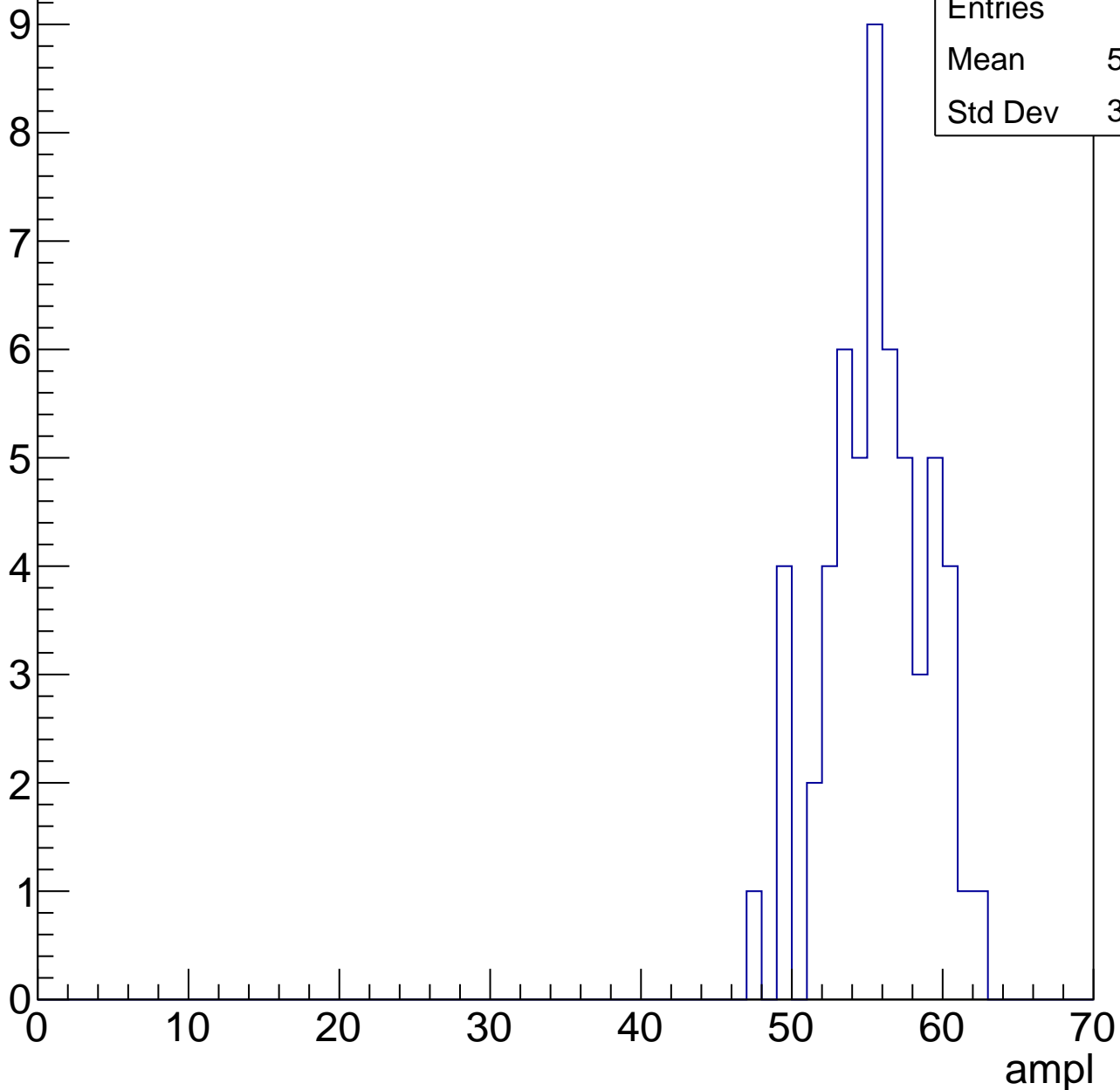


B1L101S, U12-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.16
Std Dev	3.332

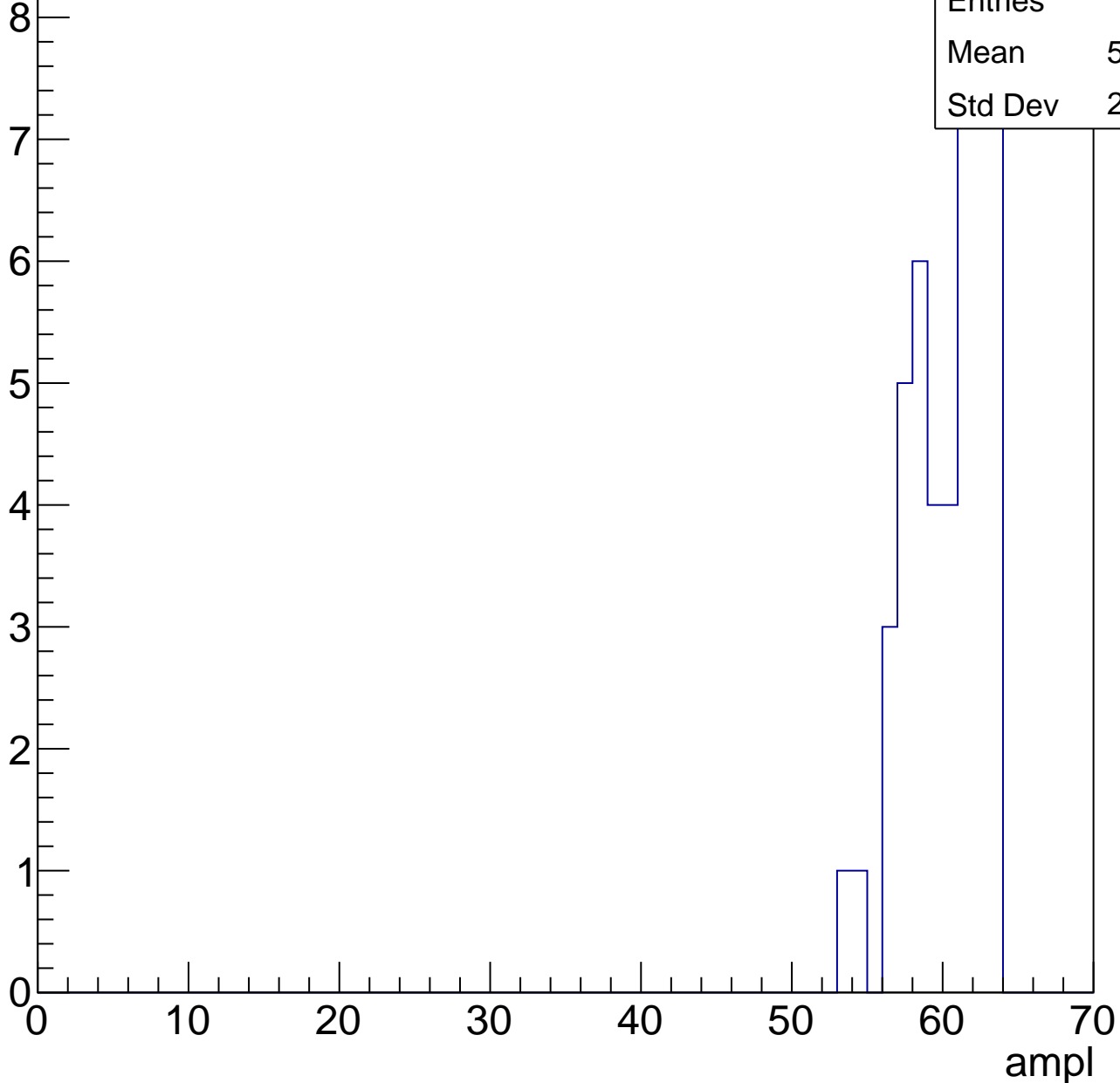


B1L101S, U12-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	59.83
Std Dev	2.569



B1L101S, U12-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.5
Std Dev	20.27

ampl

0 10 20 30 40 50 60 70

B1L101S, U12-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch99, adc0

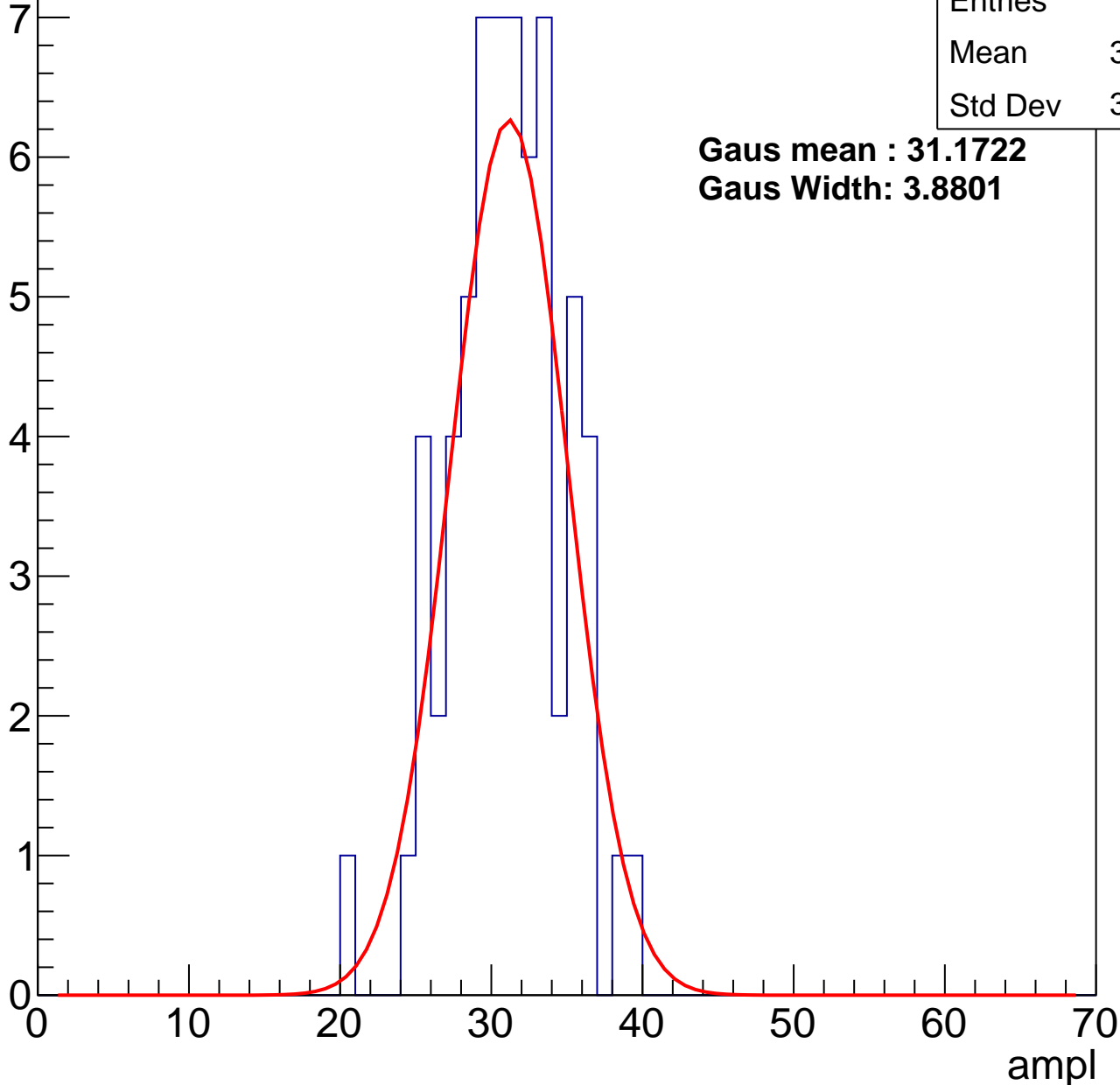
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.64
Std Dev	3.633

Gaus mean : 31.1722

Gaus Width: 3.8801



B1L101S, U12-ch99, adc1

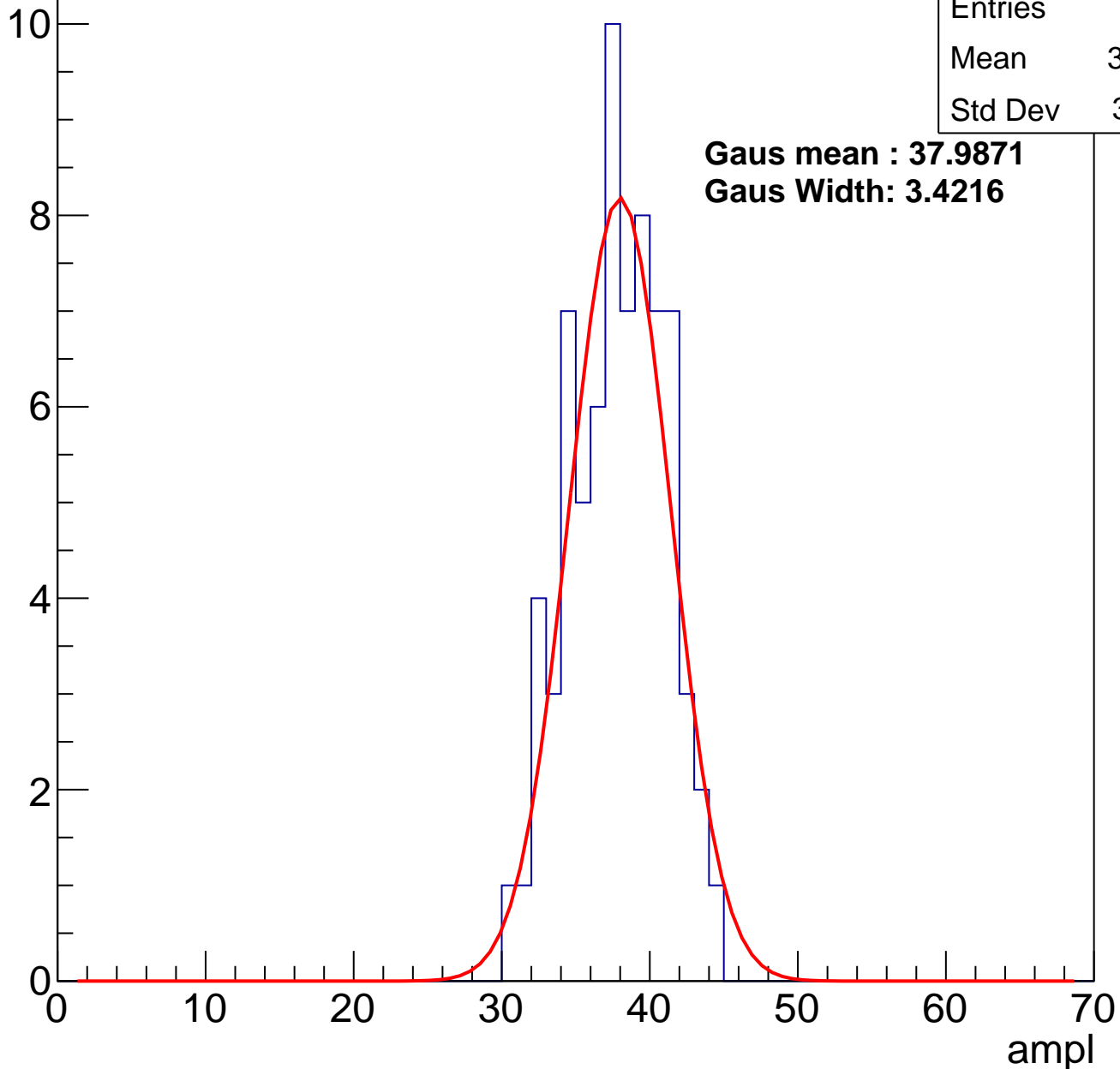
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	37.33
Std Dev	3.171

Gaus mean : 37.9871

Gaus Width: 3.4216

Entry



B1L101S, U12-ch99, adc2

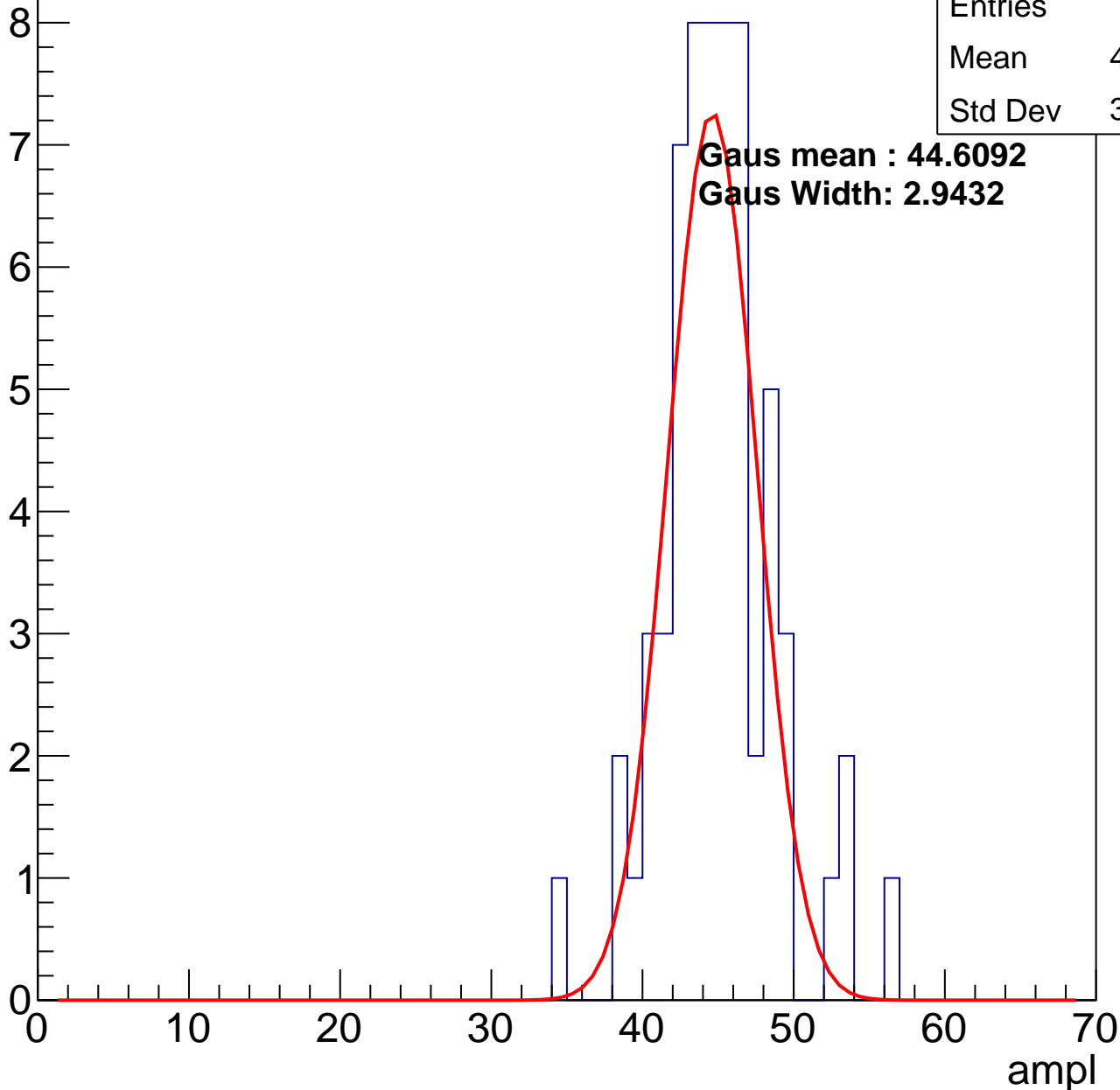
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.52
Std Dev	3.724

Gaus mean : 44.6092

Gaus Width: 2.9432



B1L101S, U12-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	51.06
Std Dev	3.265

Entry

10

8

6

4

2

0

0

10

20

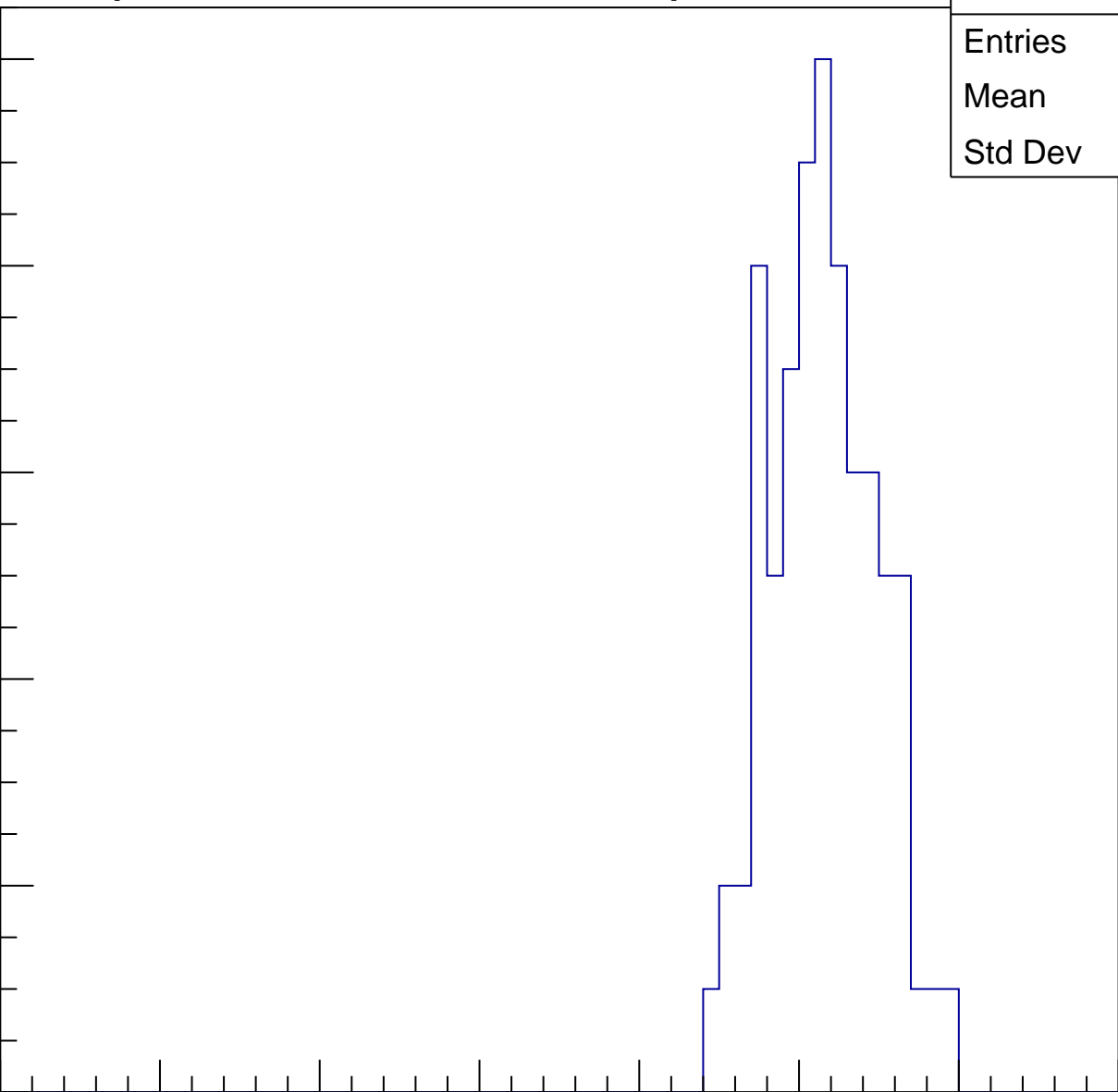
30

40

50

60

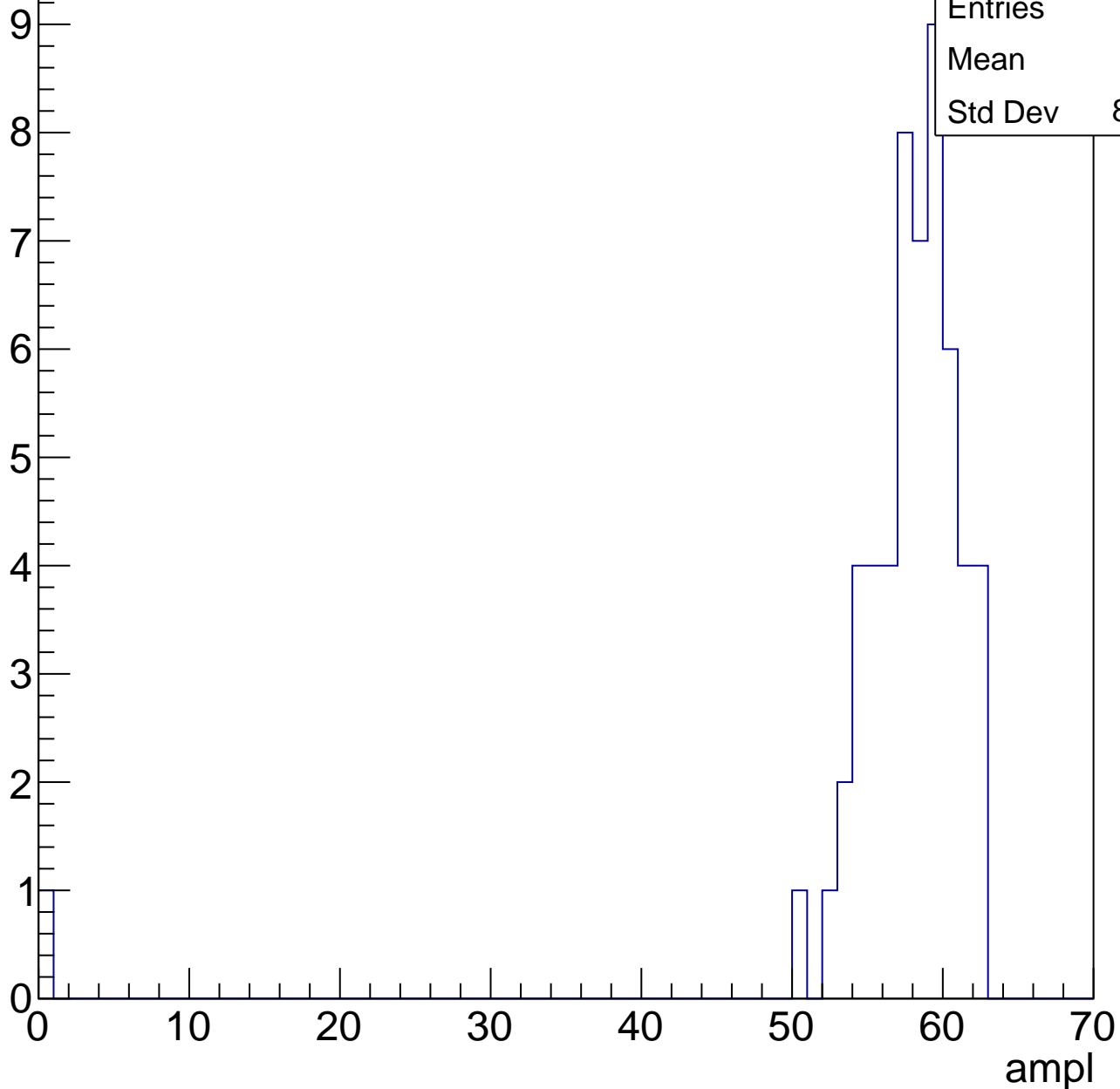
ampl



B1L101S, U12-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

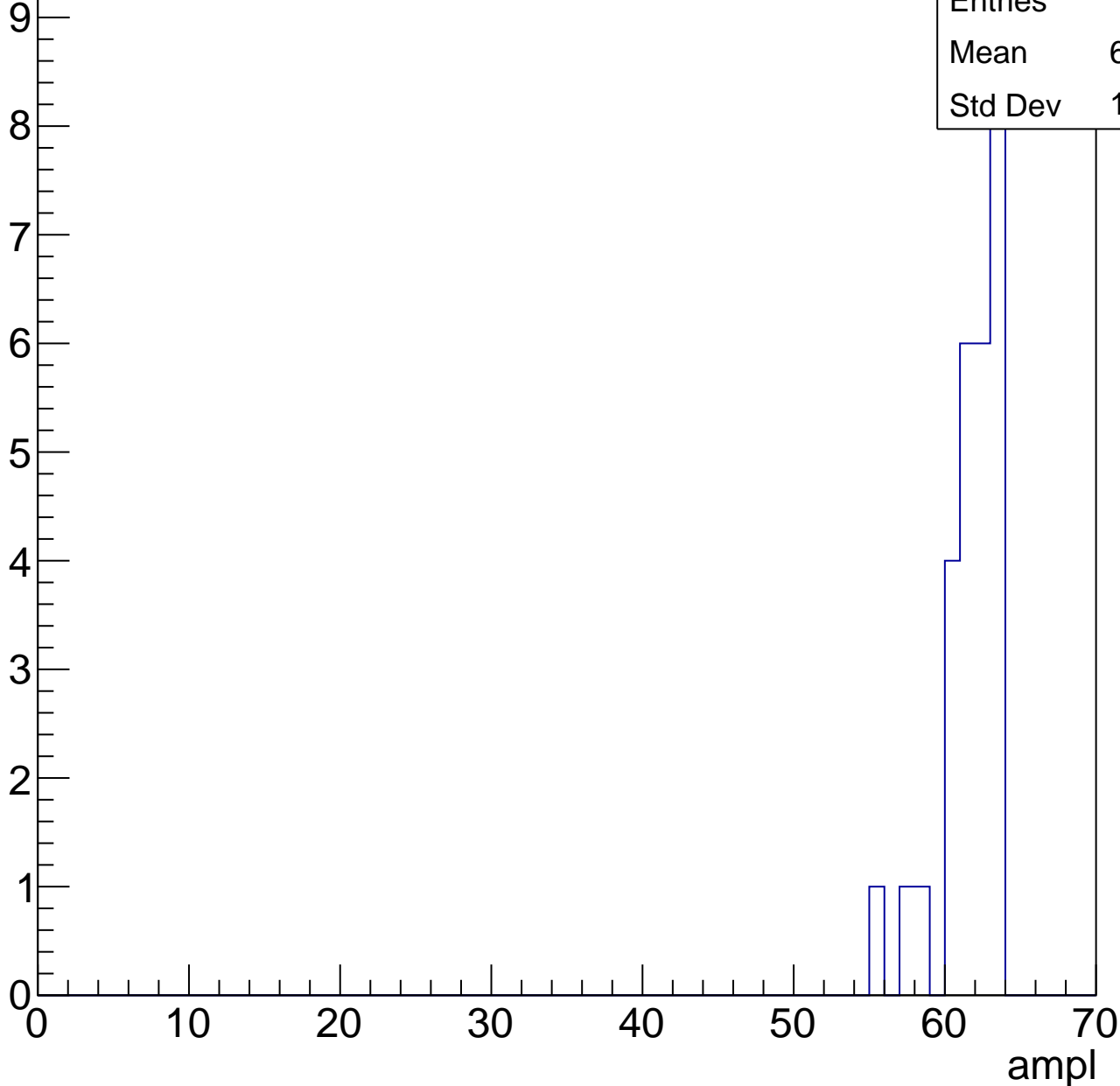


B1L101S, U12-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	61.25
Std Dev	1.939



B1L101S, U12-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	2
Mean	60.5
Std Dev	2.5

B1L101S, U12-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch100, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	29.17
Std Dev	3.44

Gaus mean : 29.9545

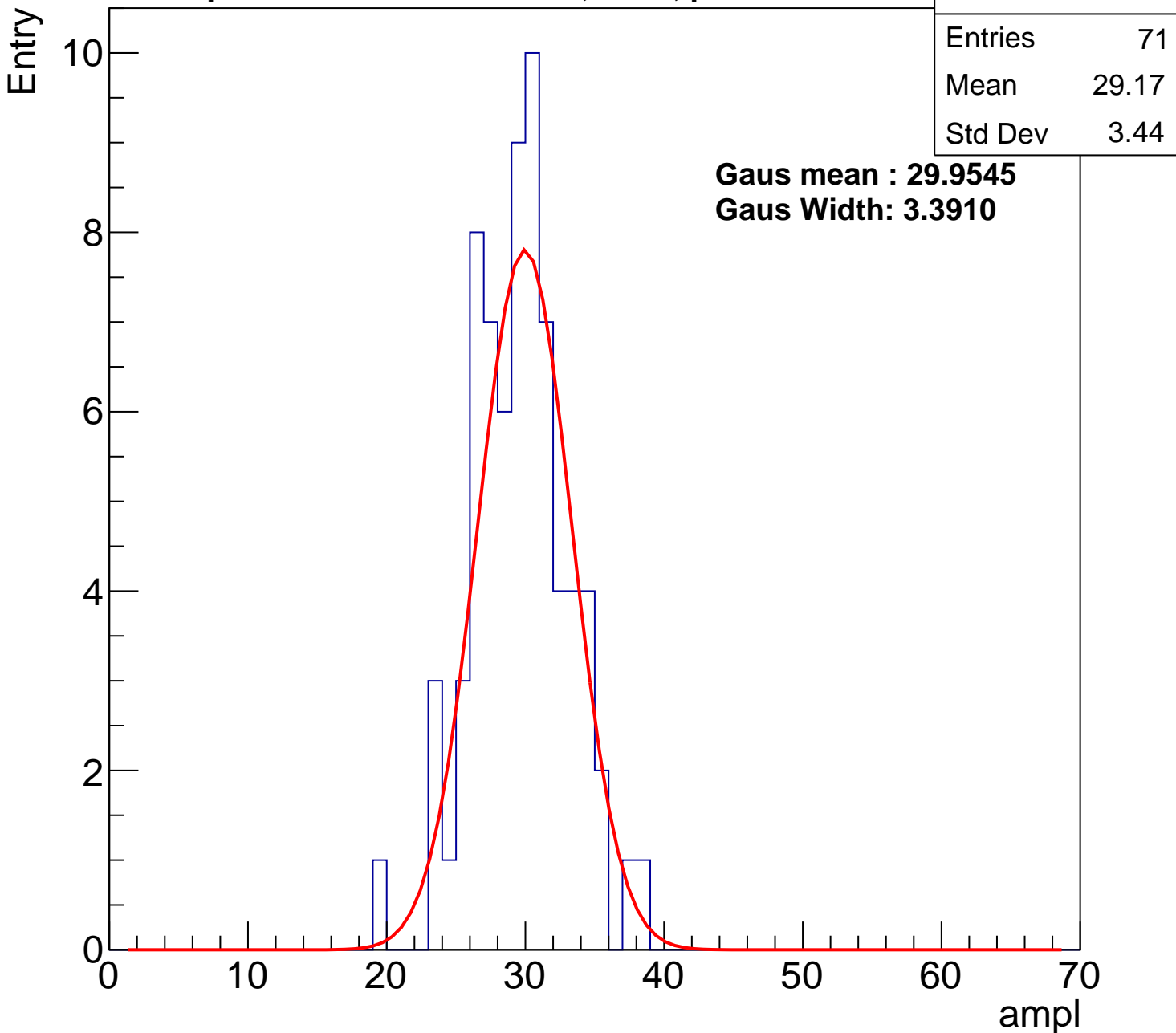
Gaus Width: 3.3910

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch100, adc1

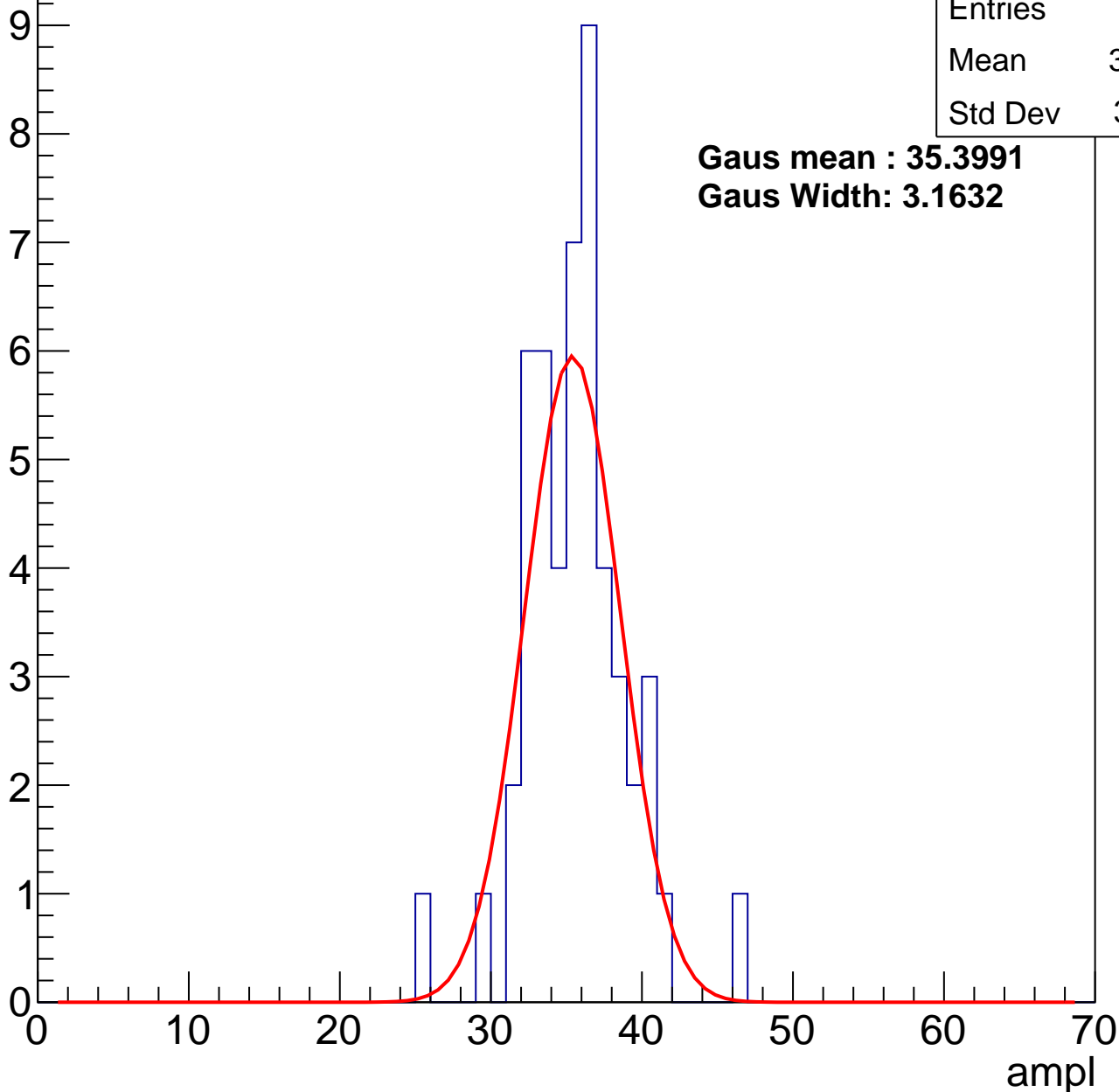
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	35.16
Std Dev	3.361

Gaus mean : 35.3991

Gaus Width: 3.1632



B1L101S, U12-ch100, adc2

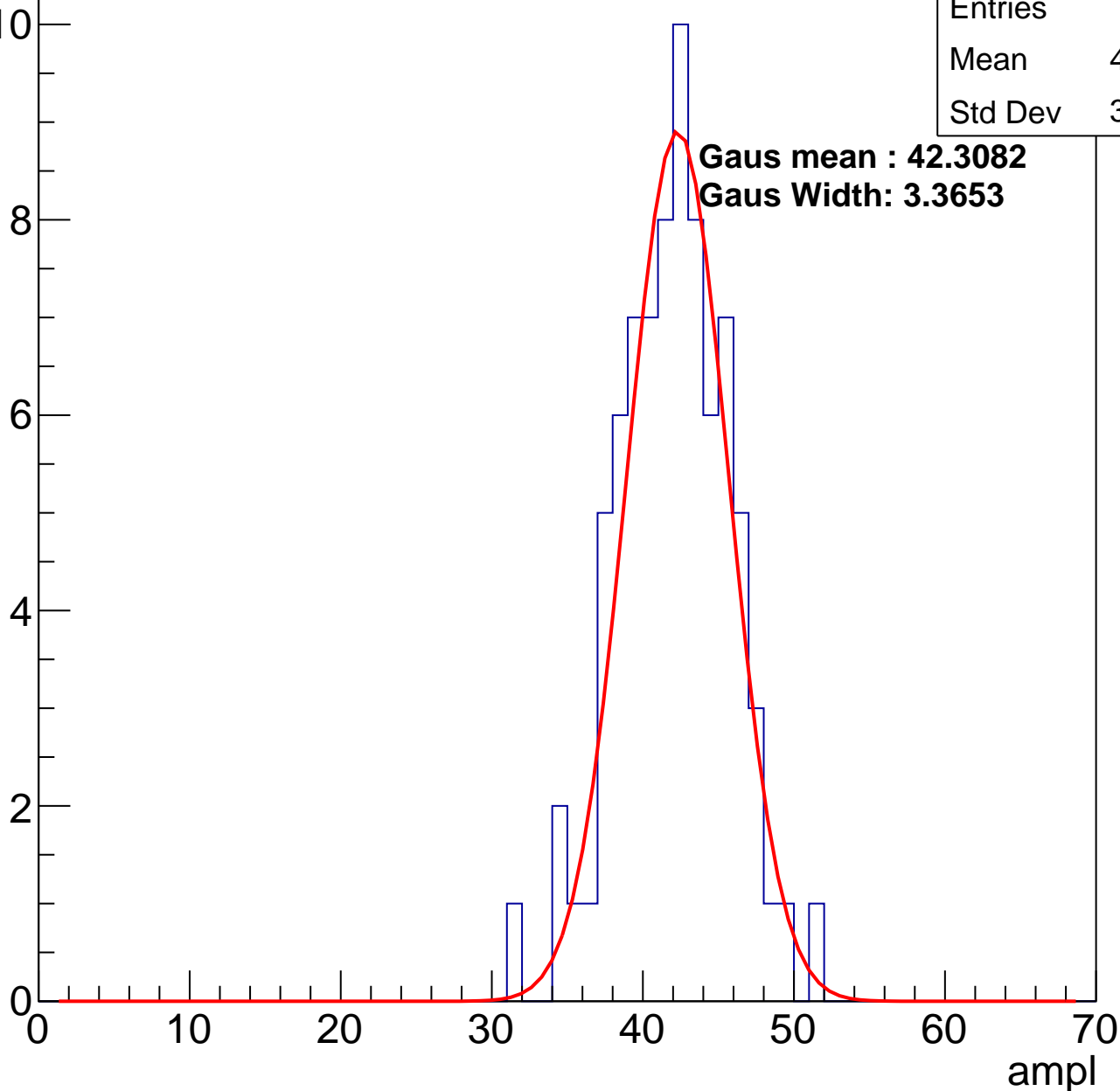
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	41.58
Std Dev	3.632

Gaus mean : 42.3082

Gaus Width: 3.3653

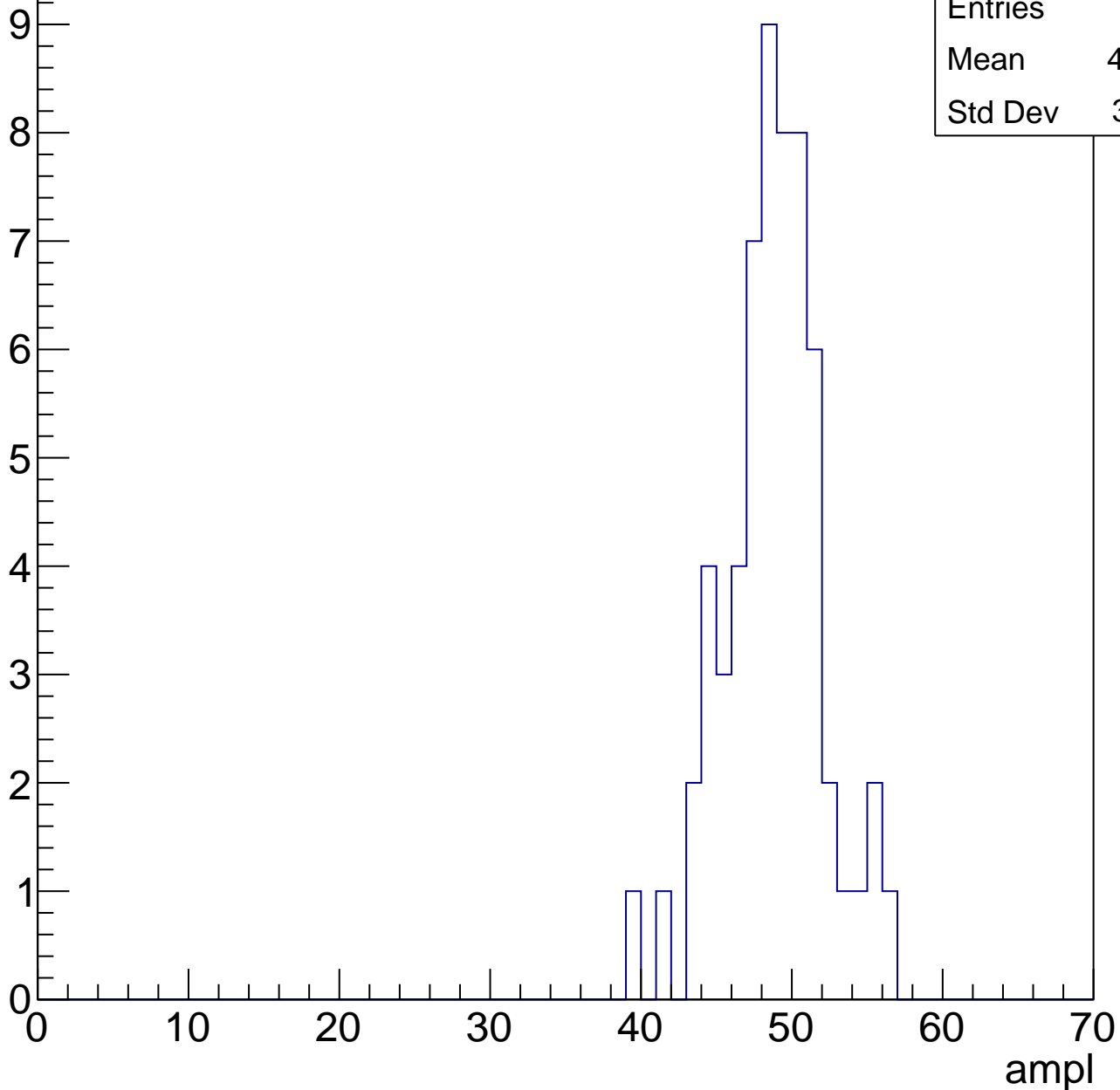


B1L101S, U12-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.28
Std Dev	3.261

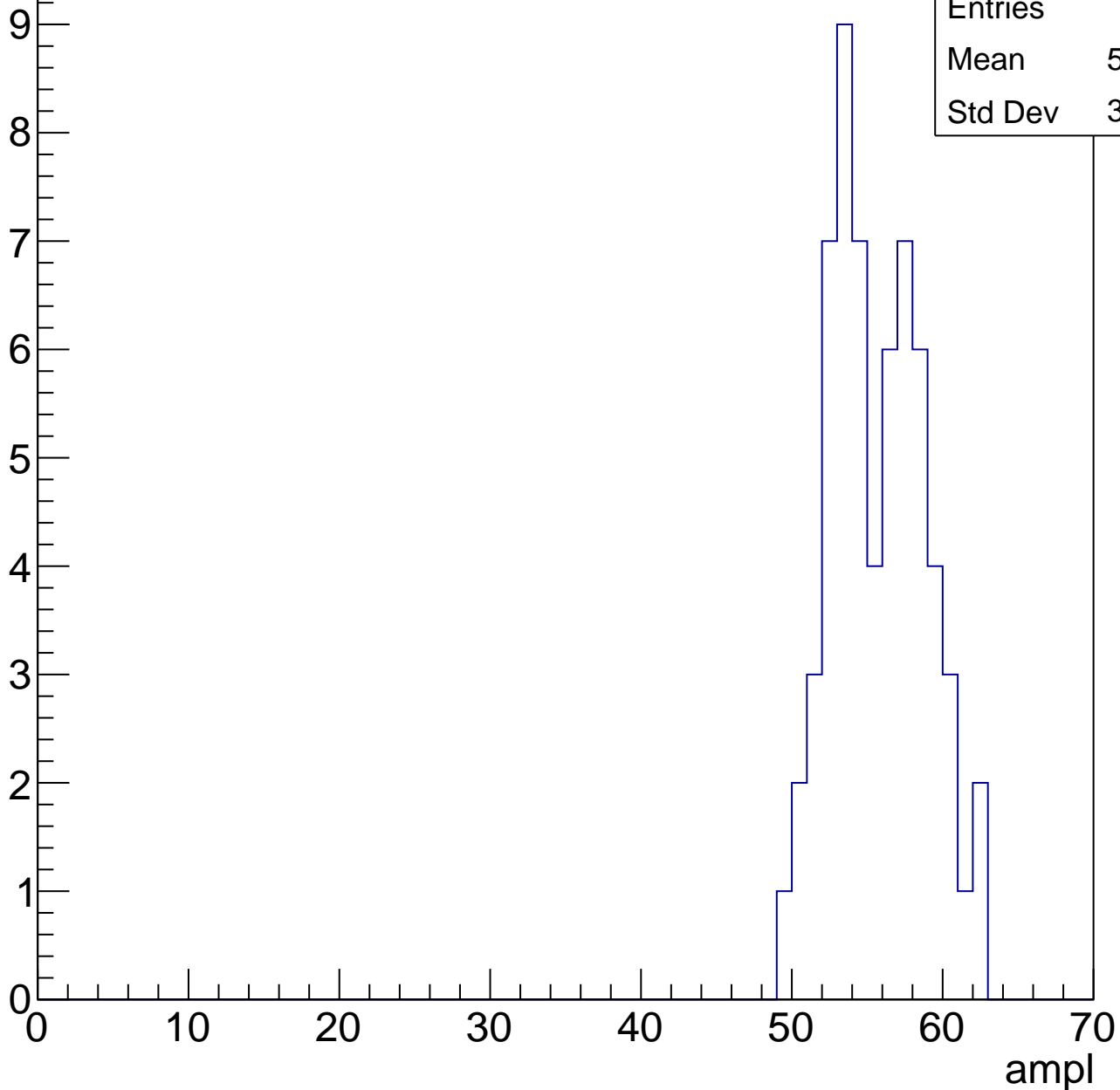


B1L101S, U12-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.24
Std Dev	3.104

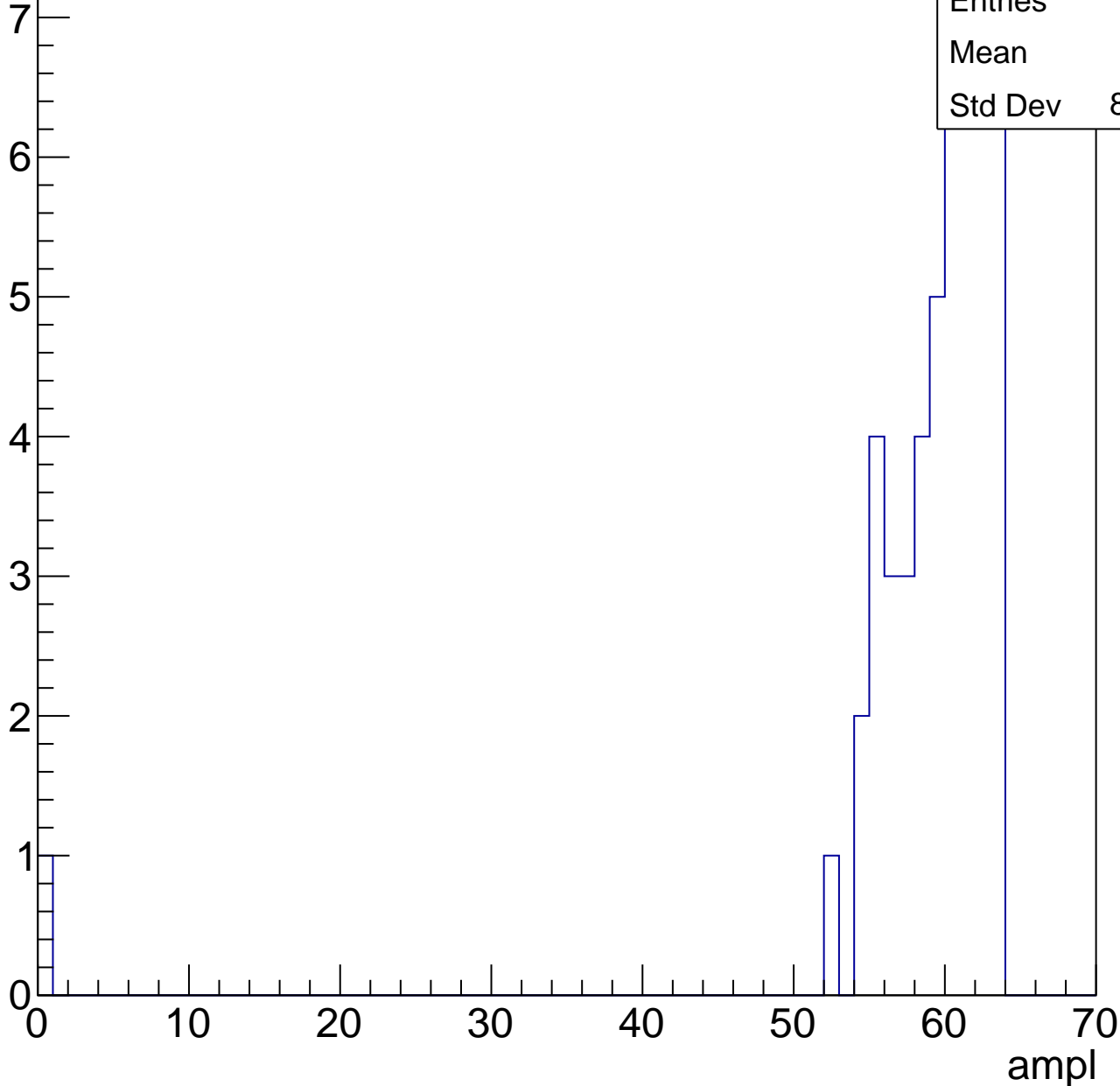


B1L101S, U12-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

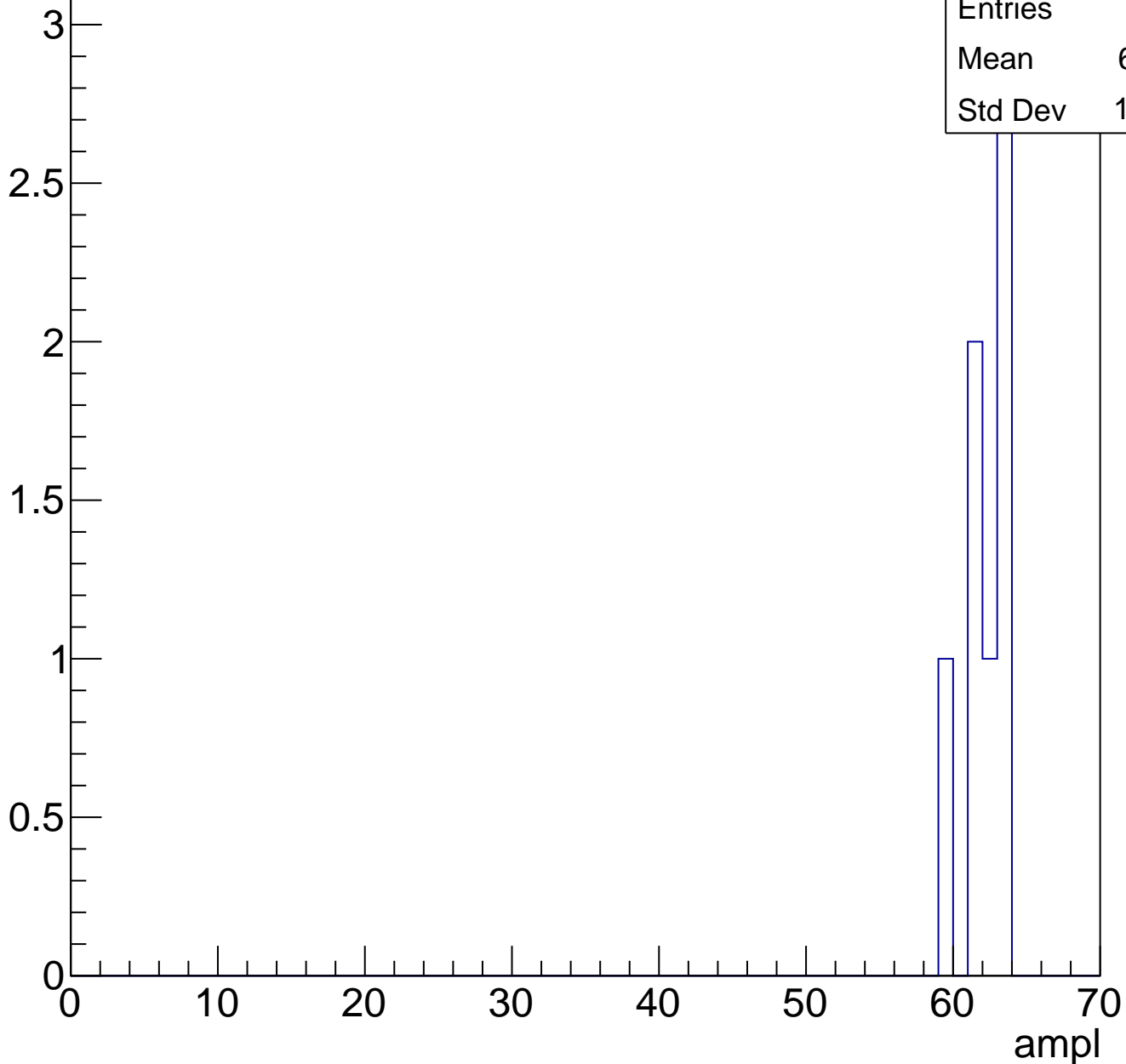
Entries	51
Mean	58.2
Std Dev	8.704



B1L101S, U12-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch101, adc0

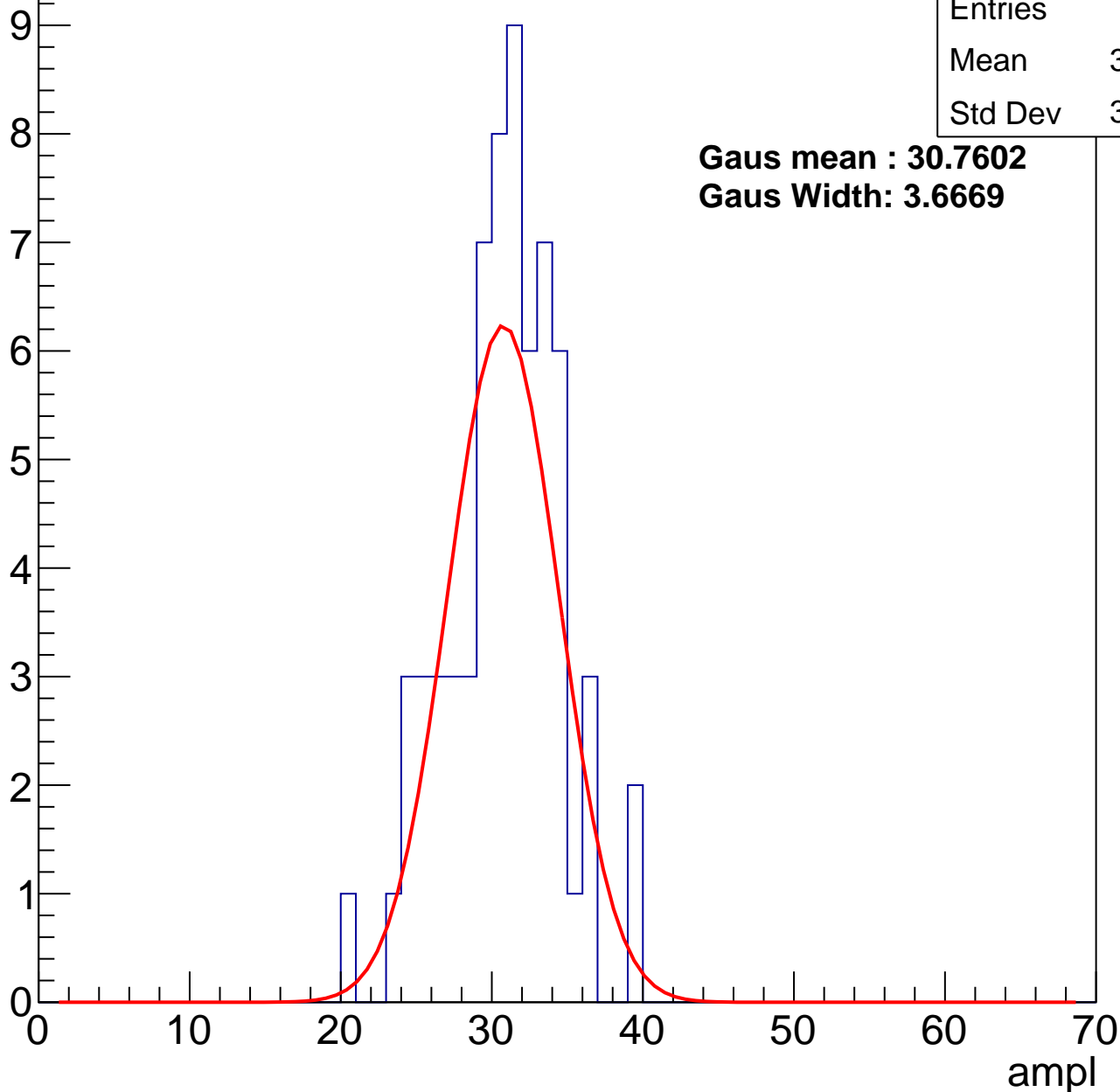
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.35
Std Dev	3.703

Gaus mean : 30.7602

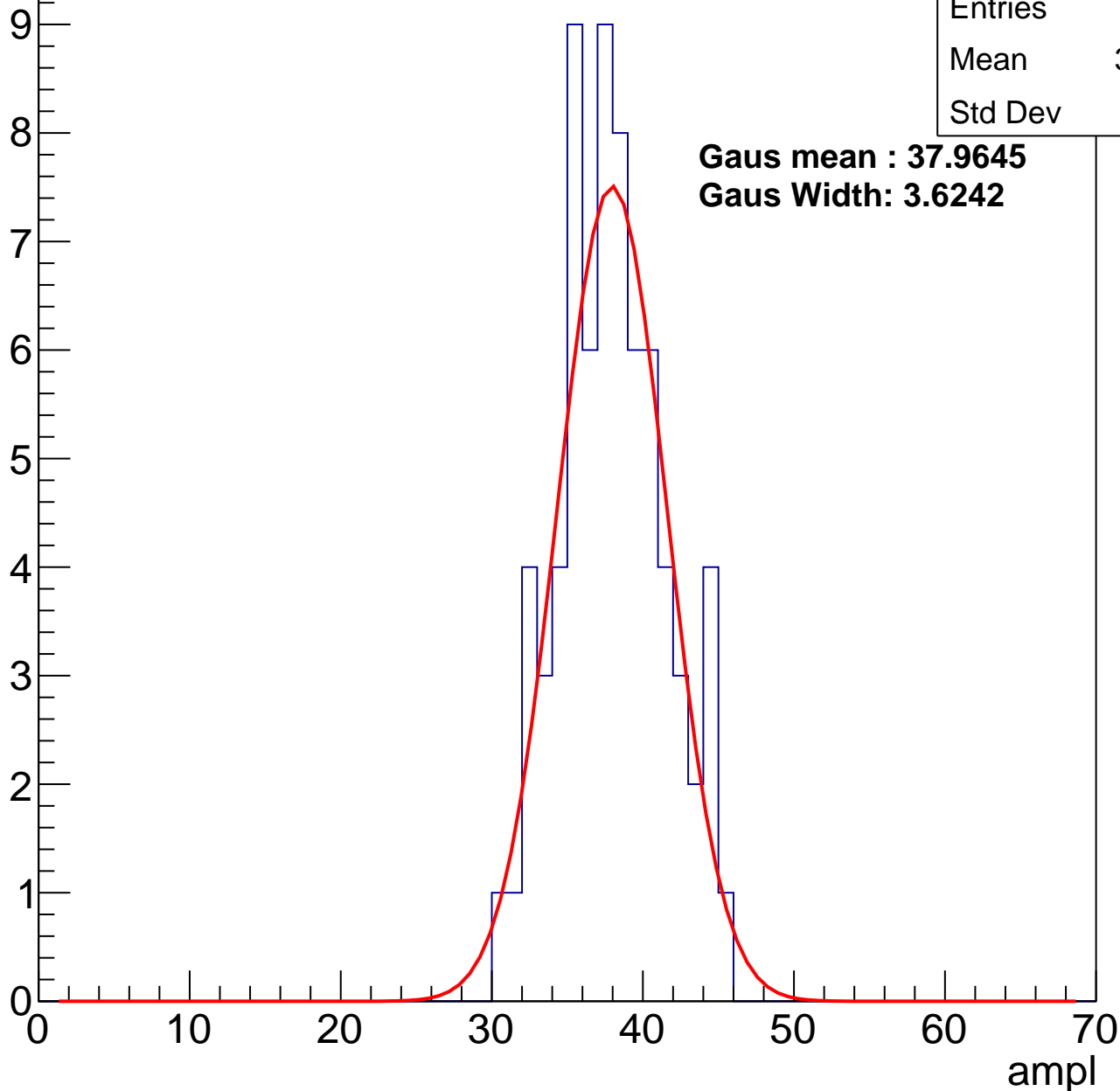
Gaus Width: 3.6669



B1L101S, U12-ch101, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch101, adc2

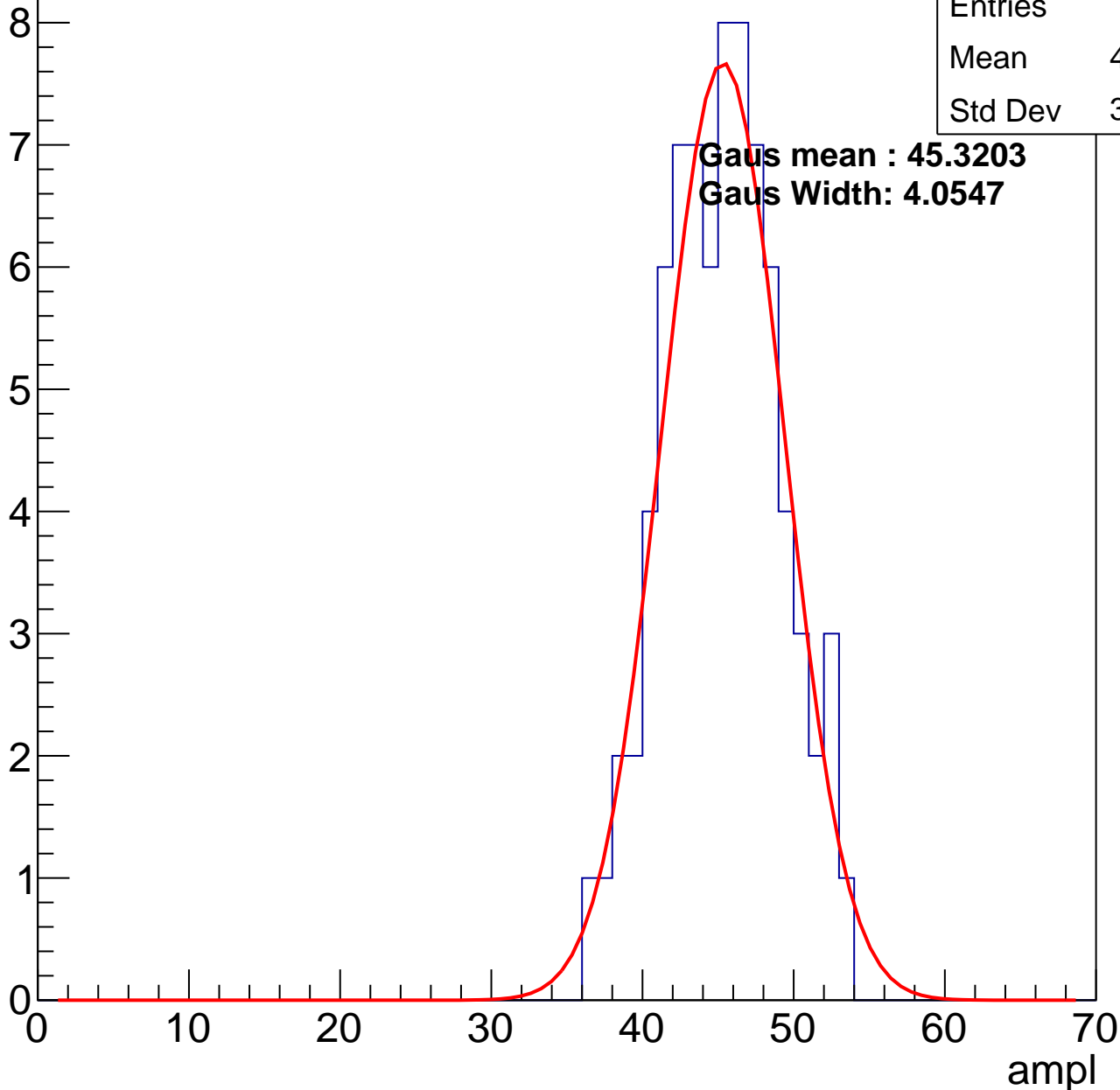
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.79
Std Dev	3.784

Gaus mean : 45.3203

Gaus Width: 4.0547

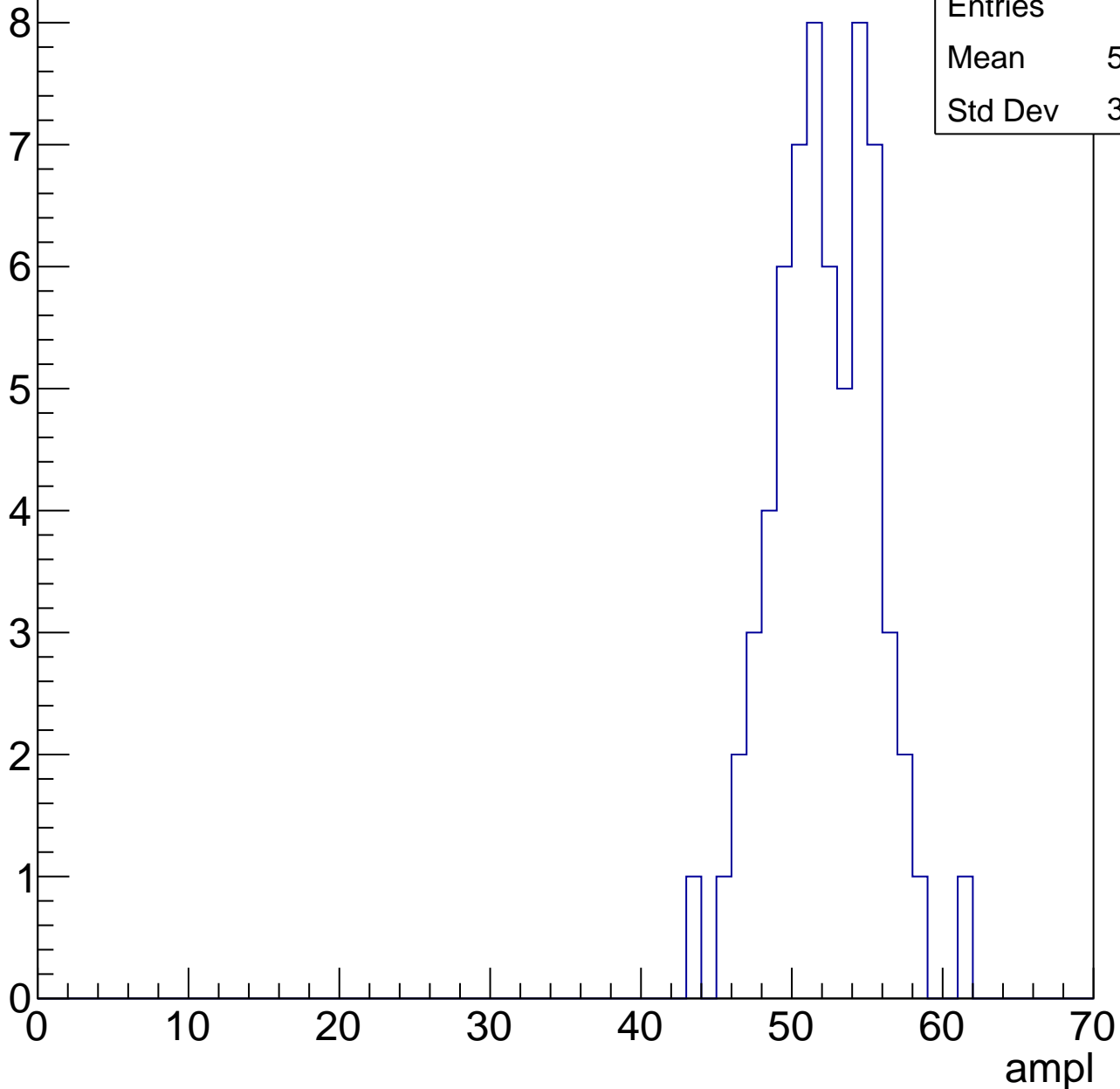


B1L101S, U12-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	51.69
Std Dev	3.374

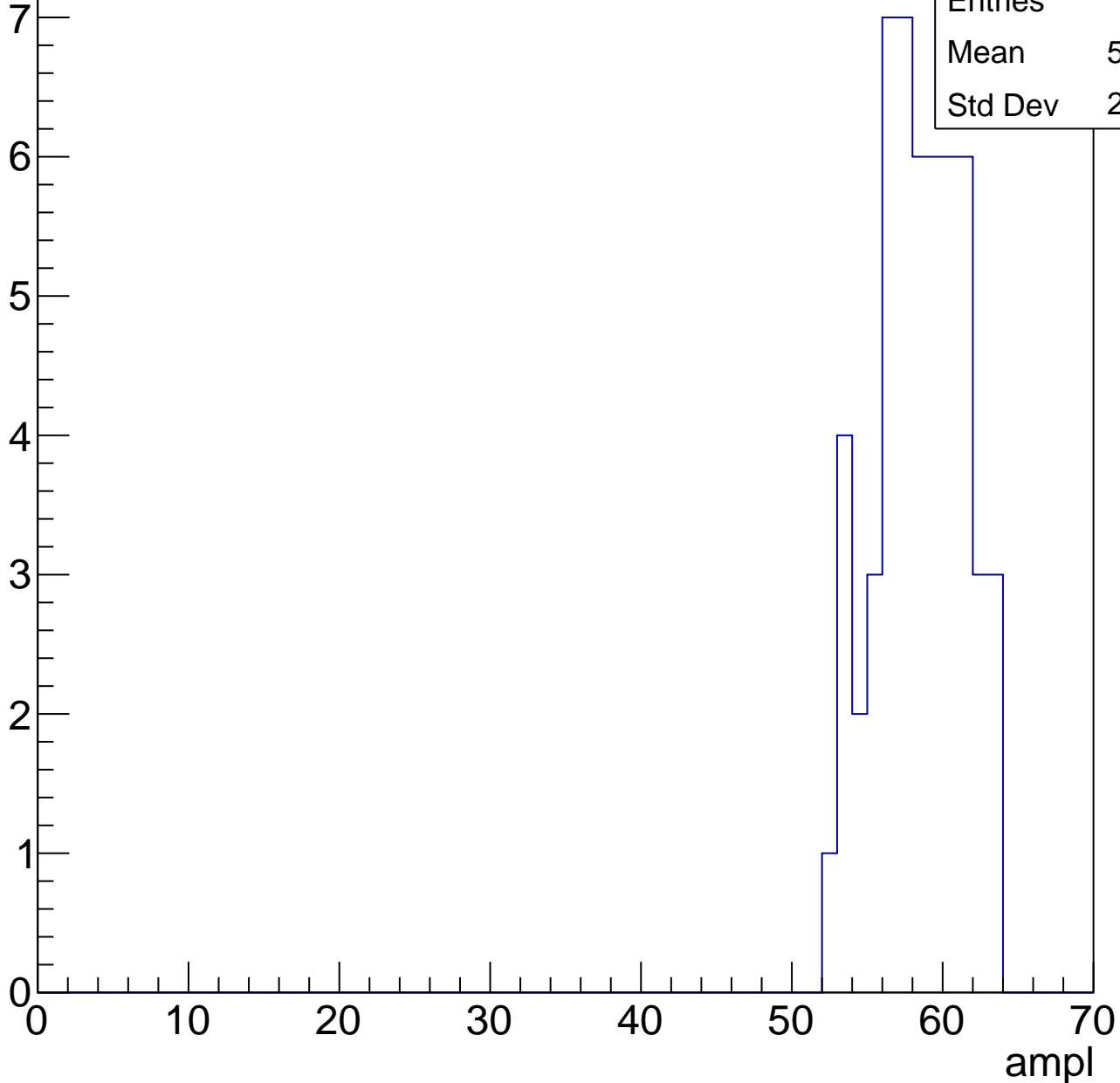


B1L101S, U12-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.98
Std Dev	2.845

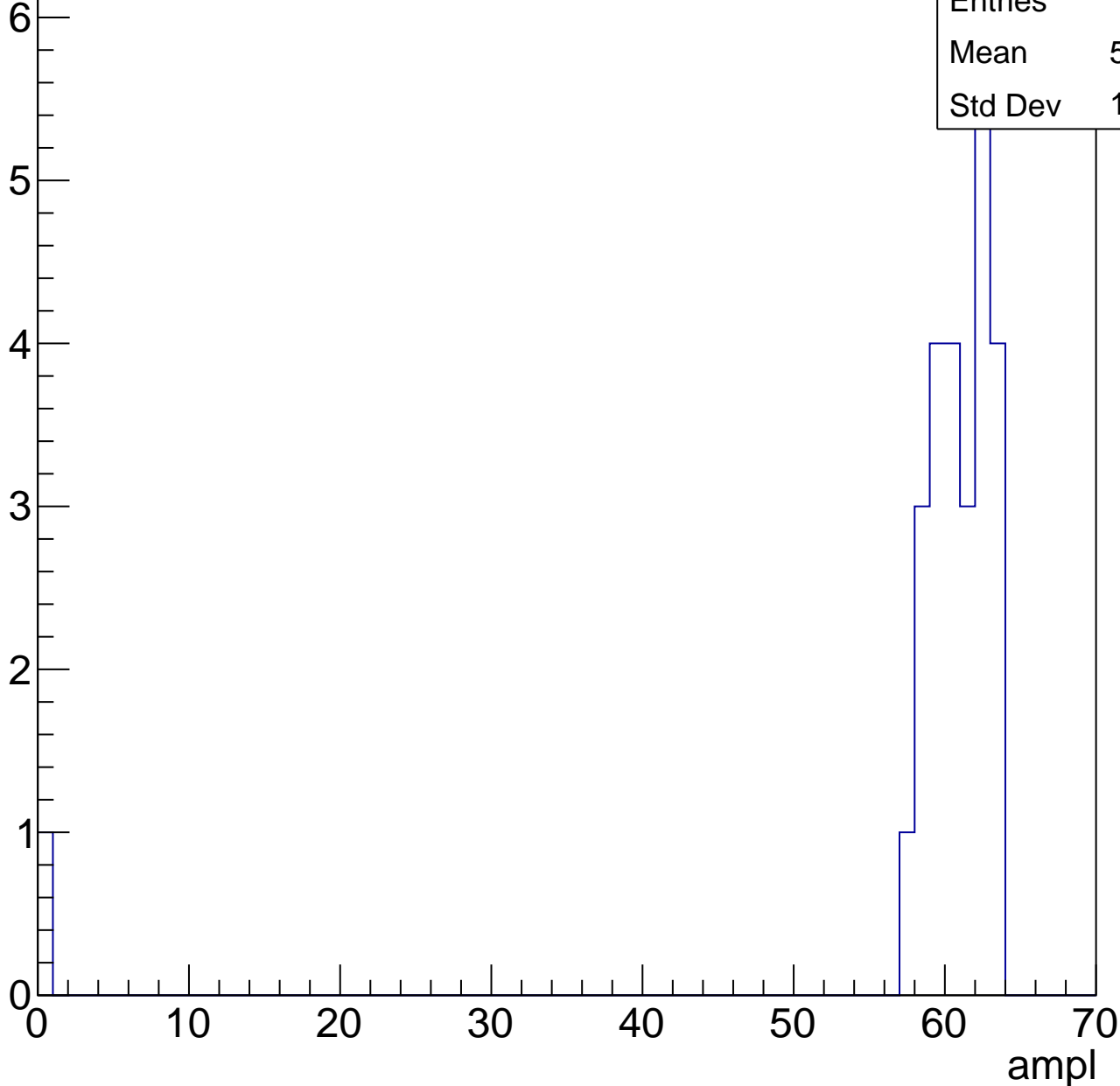


B1L101S, U12-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	58.23
Std Dev	11.78



B1L101S, U12-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B1L101S, U12-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch102, adc0

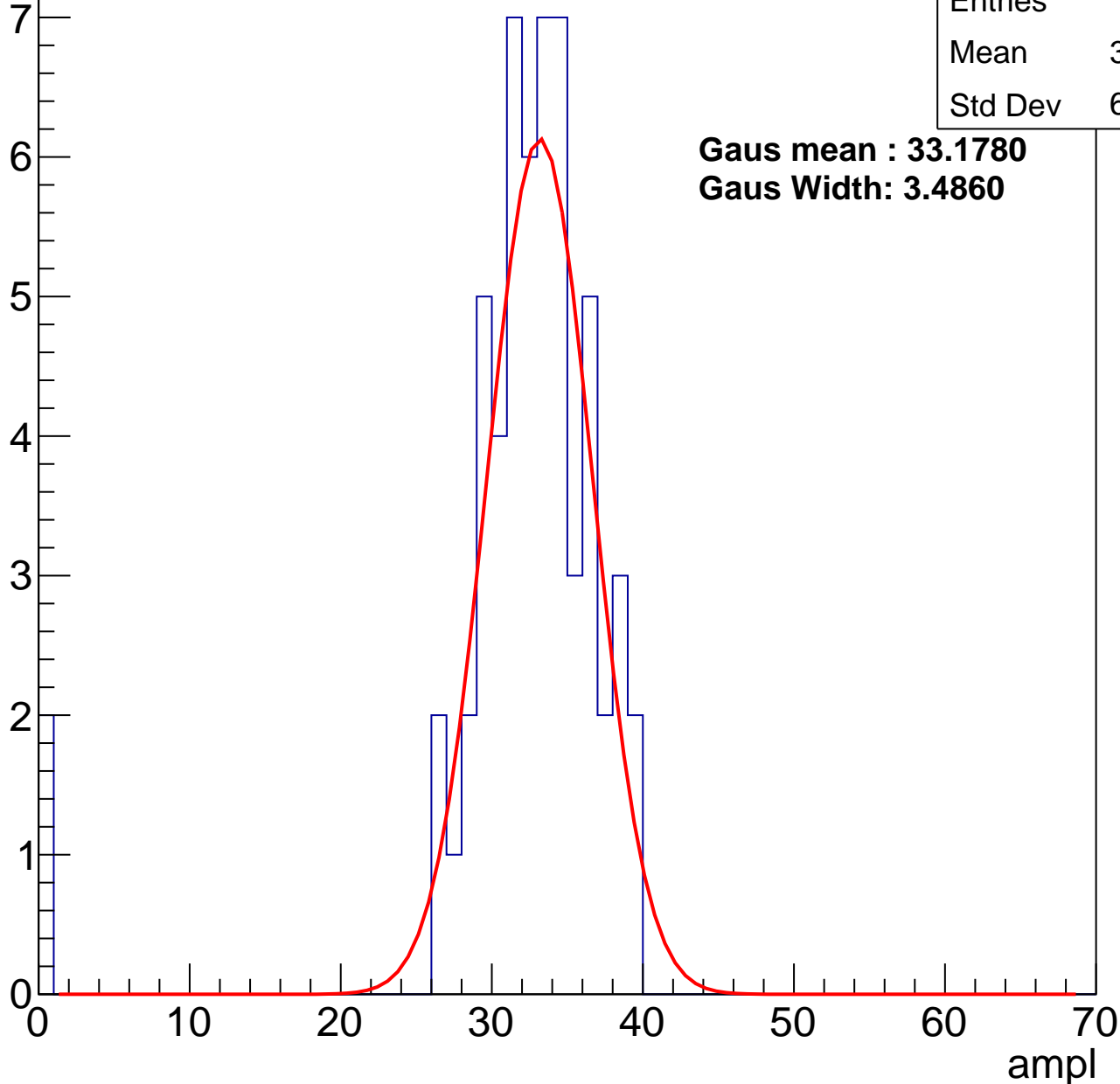
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	31.53
Std Dev	6.734

Gaus mean : 33.1780

Gaus Width: 3.4860



B1L101S, U12-ch102, adc1

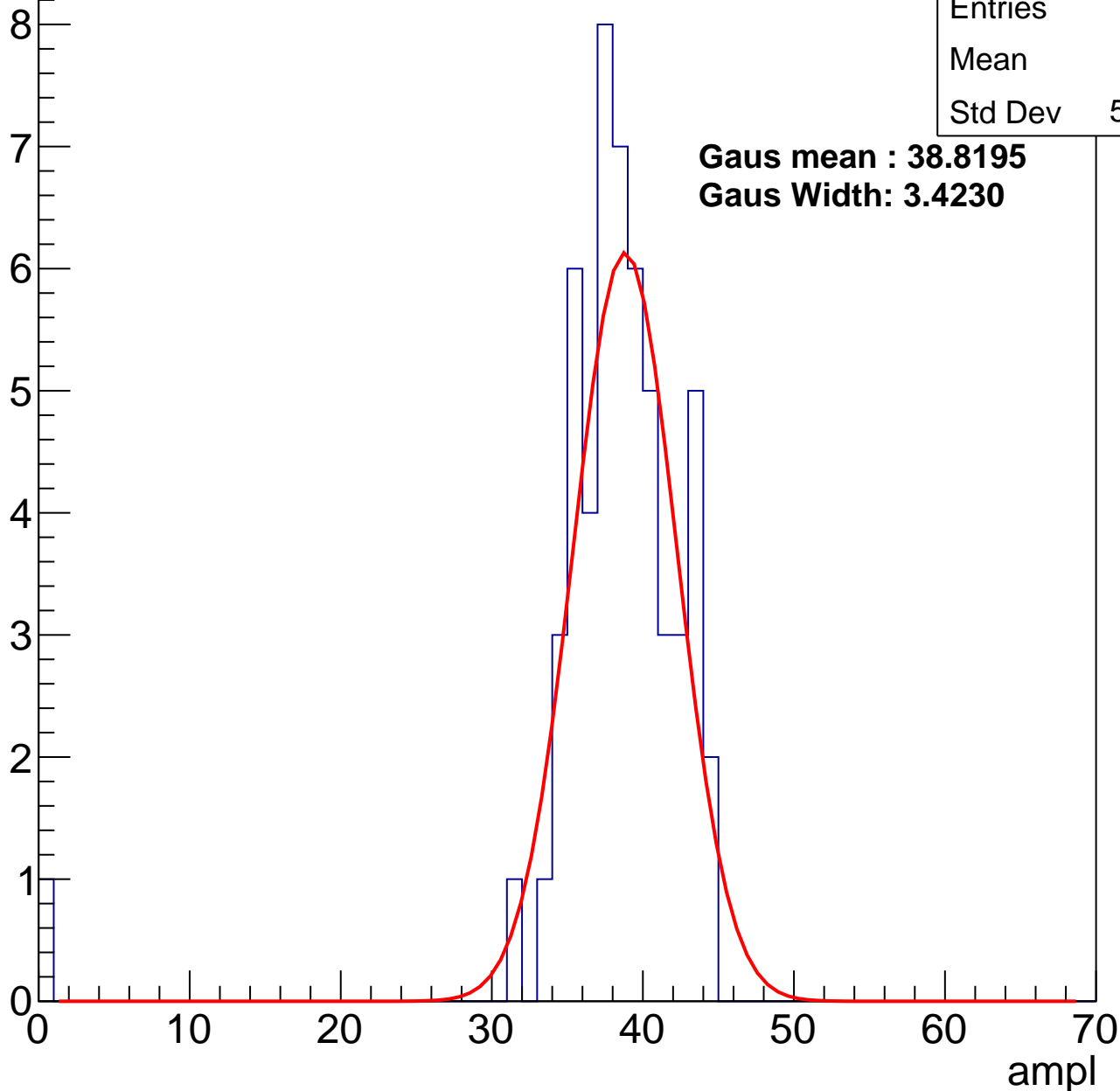
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.6
Std Dev	5.929

Gaus mean : 38.8195

Gaus Width: 3.4230



B1L101S, U12-ch102, adc2

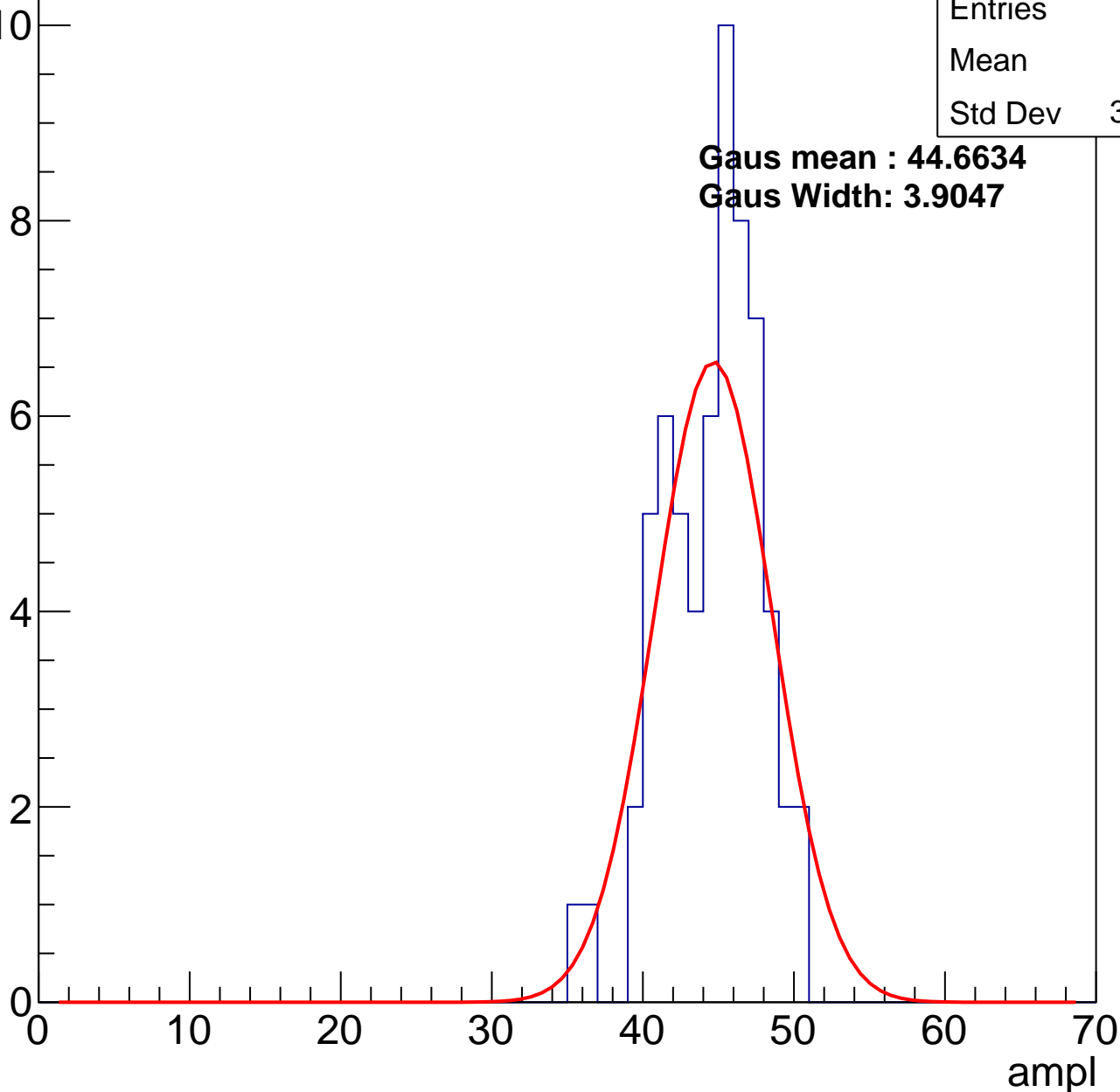
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.1
Std Dev	3.196

Gaus mean : 44.6634

Gaus Width: 3.9047

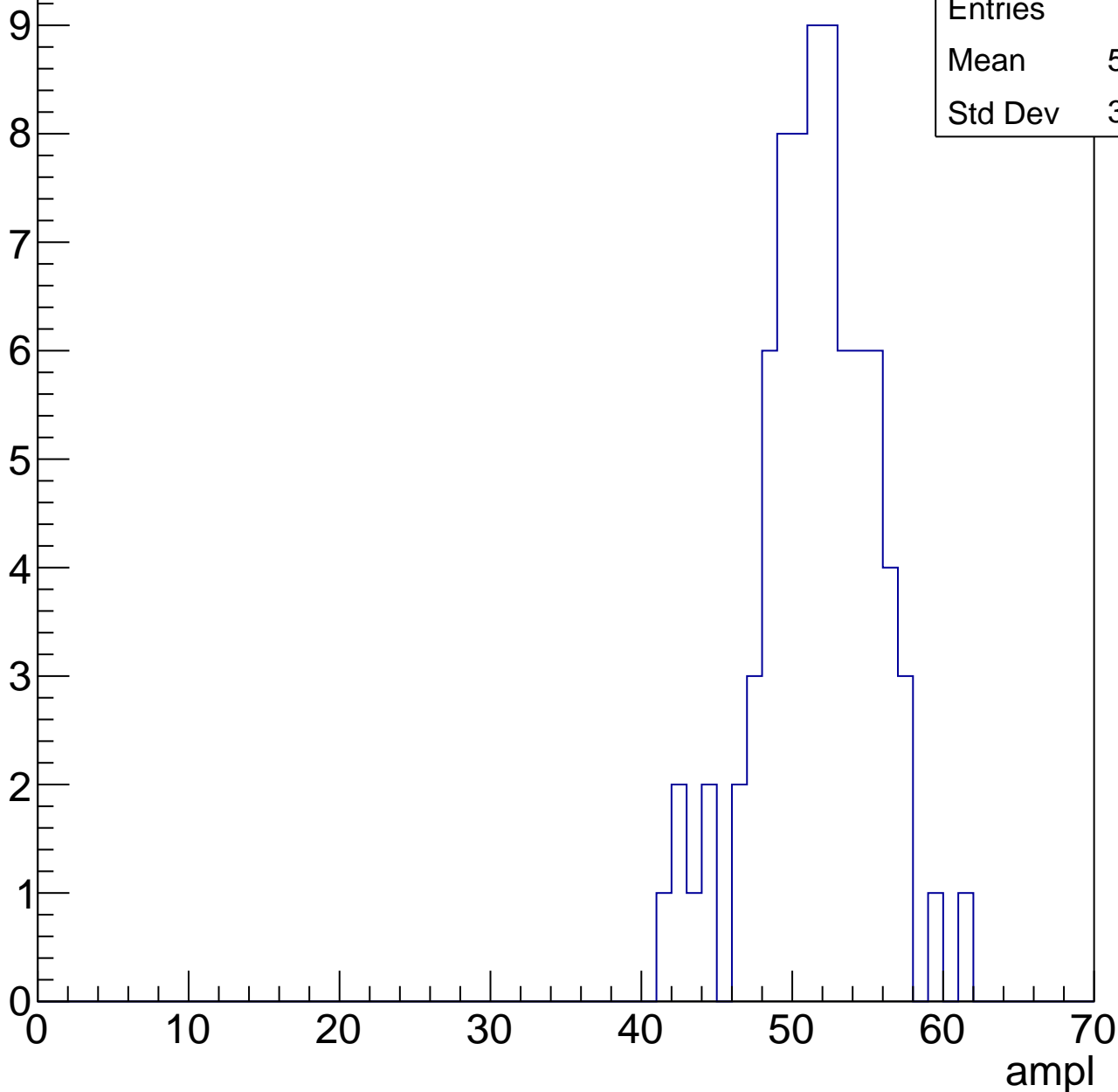


B1L101S, U12-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	51.06
Std Dev	3.874

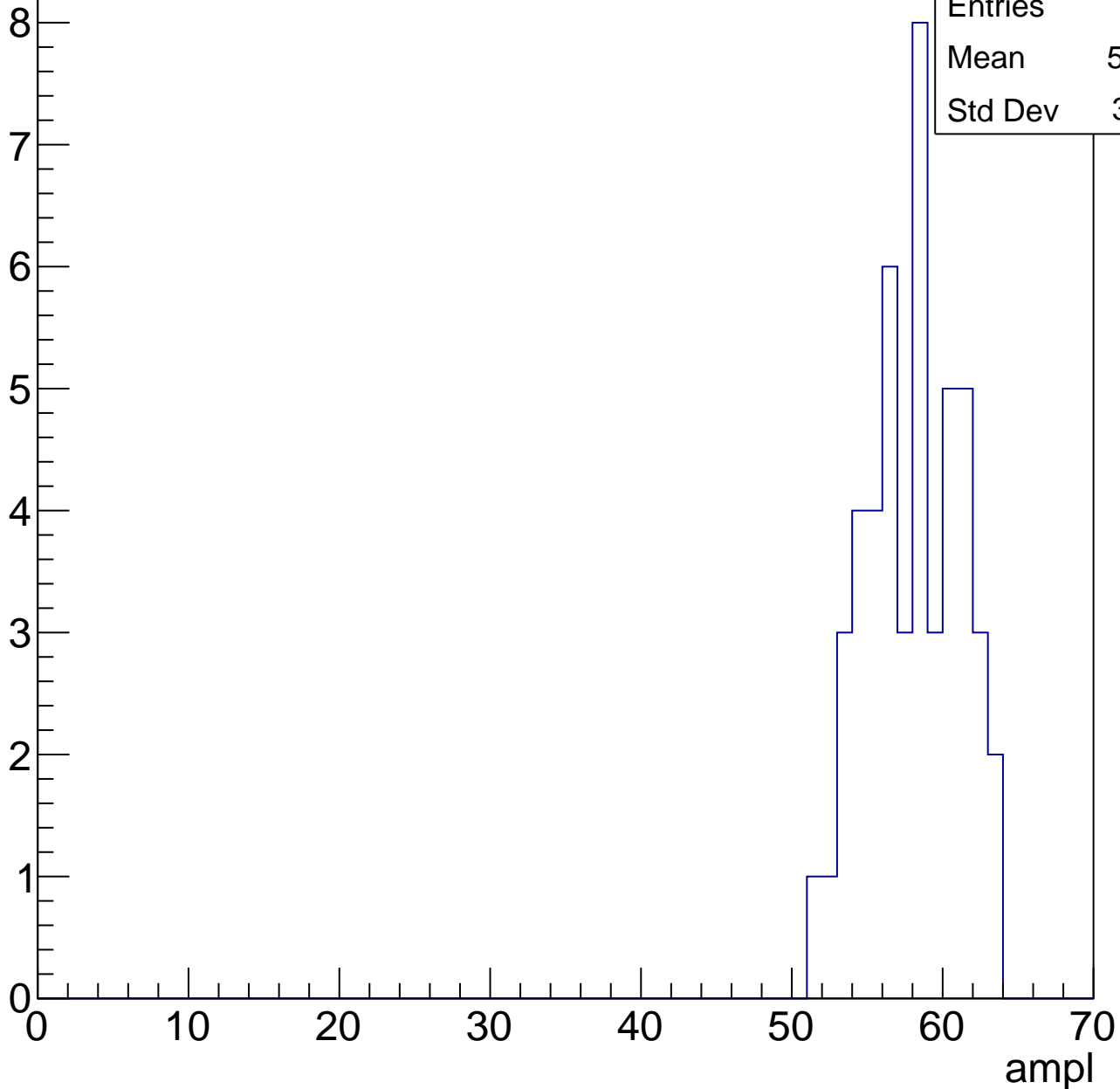


B1L101S, U12-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.56
Std Dev	3.041

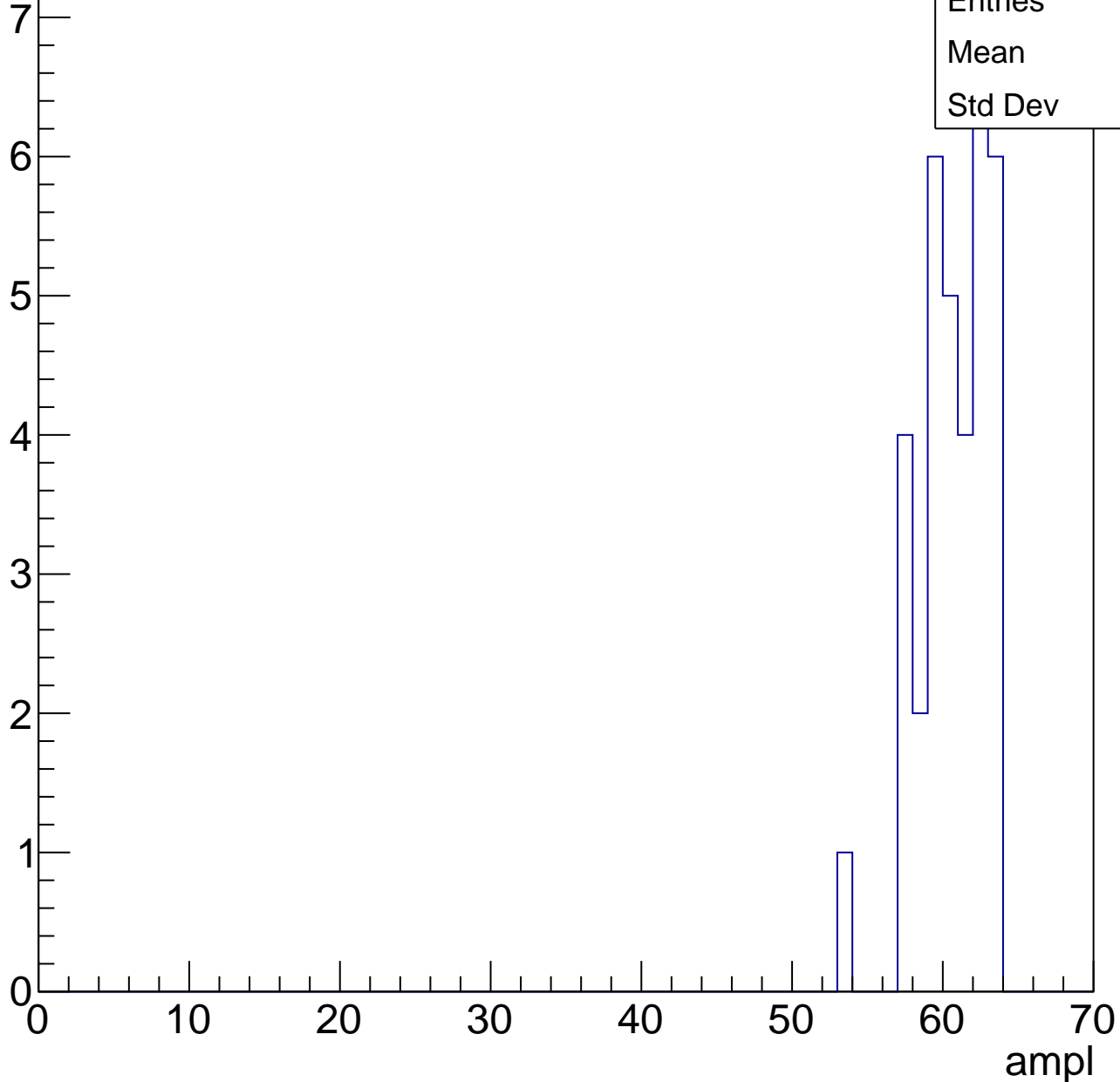


B1L101S, U12-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.2
Std Dev	2.29



B1L101S, U12-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L101S, U12-ch103, adc0

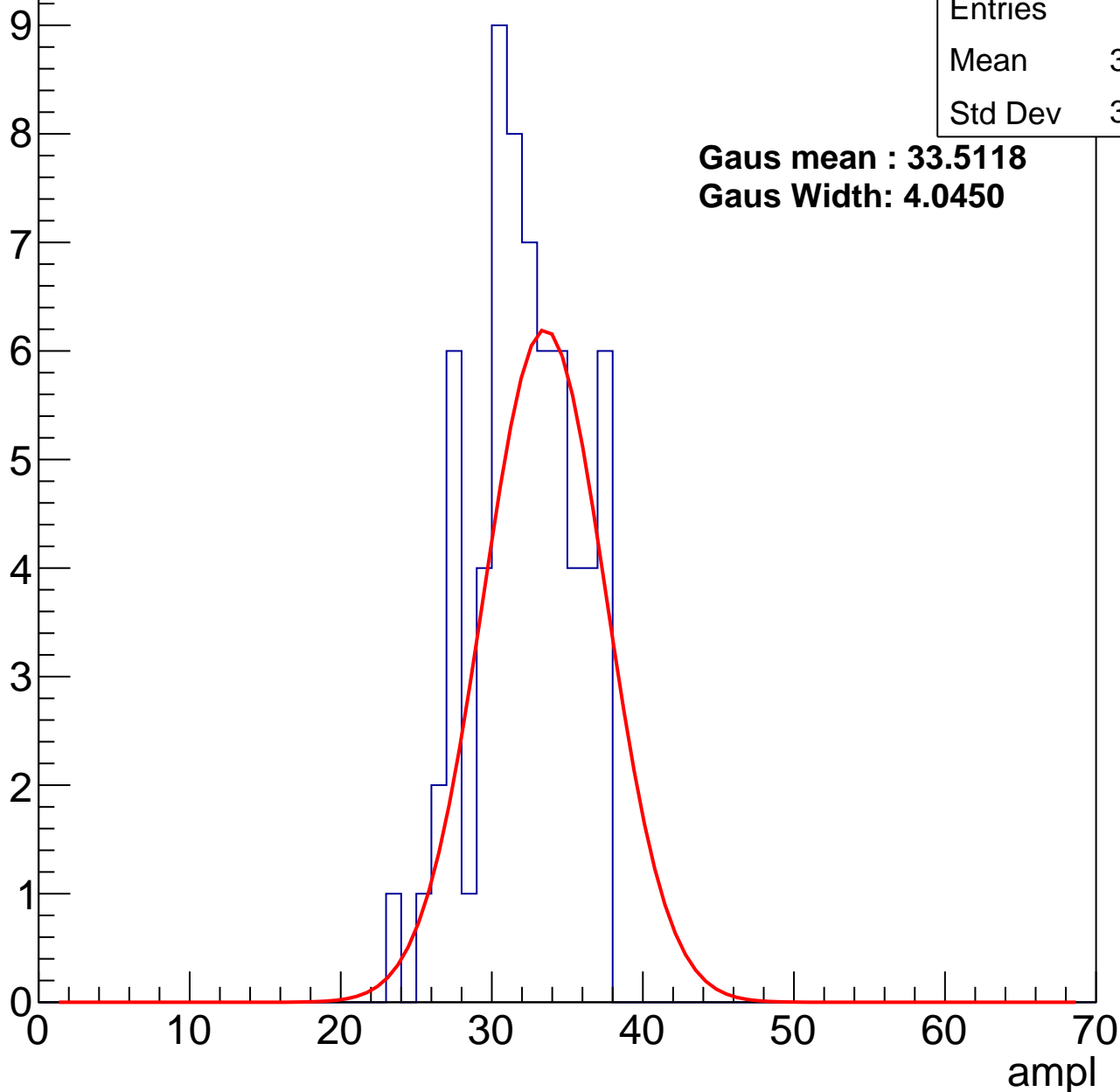
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	31.63
Std Dev	3.326

Gaus mean : 33.5118

Gaus Width: 4.0450



B1L101S, U12-ch103, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	54
Mean	37.69
Std Dev	3.349

Gaus mean : 38.6834

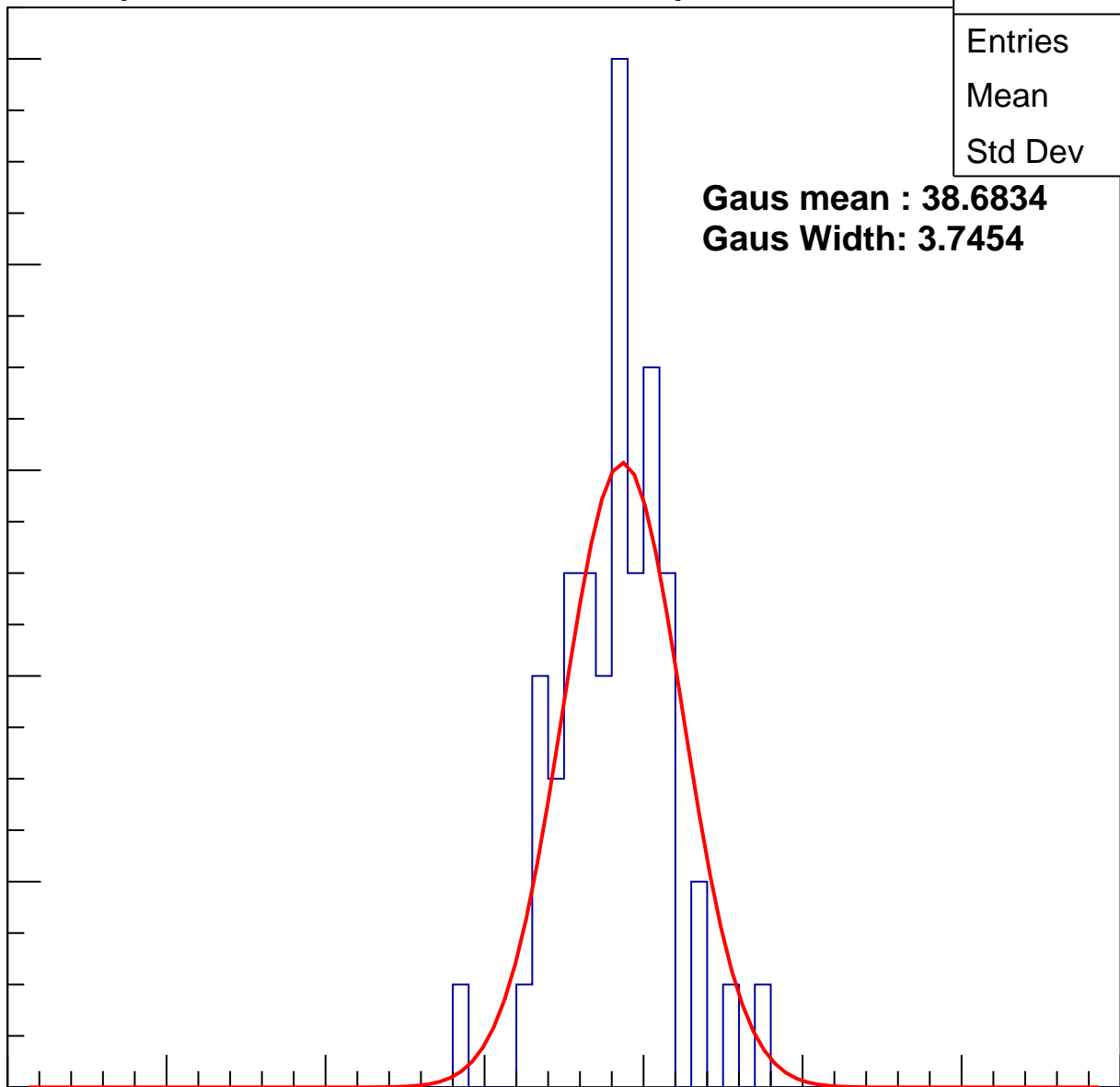
Gaus Width: 3.7454

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch103, adc2

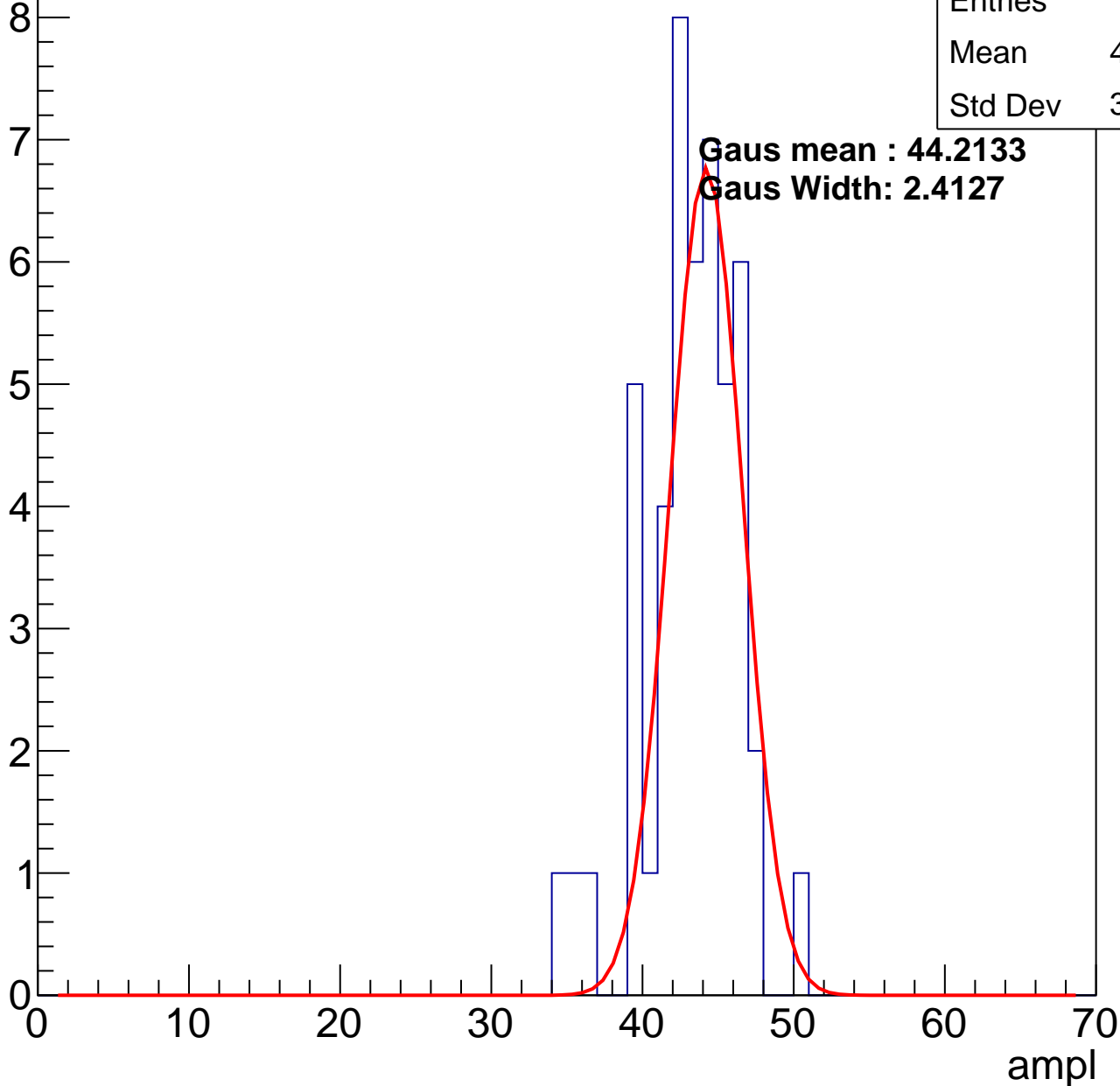
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	42.73
Std Dev	3.114

Gaus mean : 44.2133

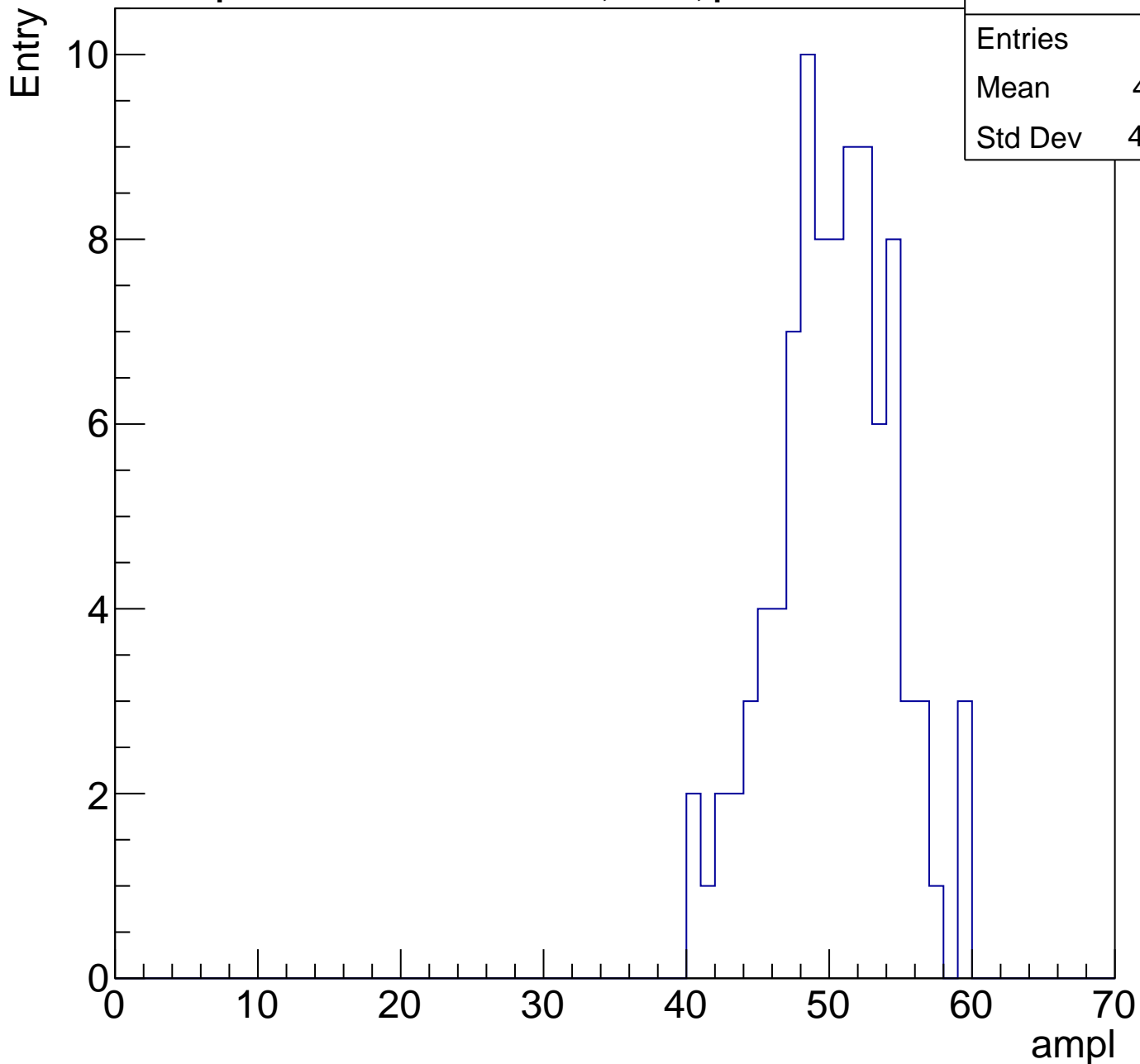
Gaus Width: 2.4127



B1L101S, U12-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

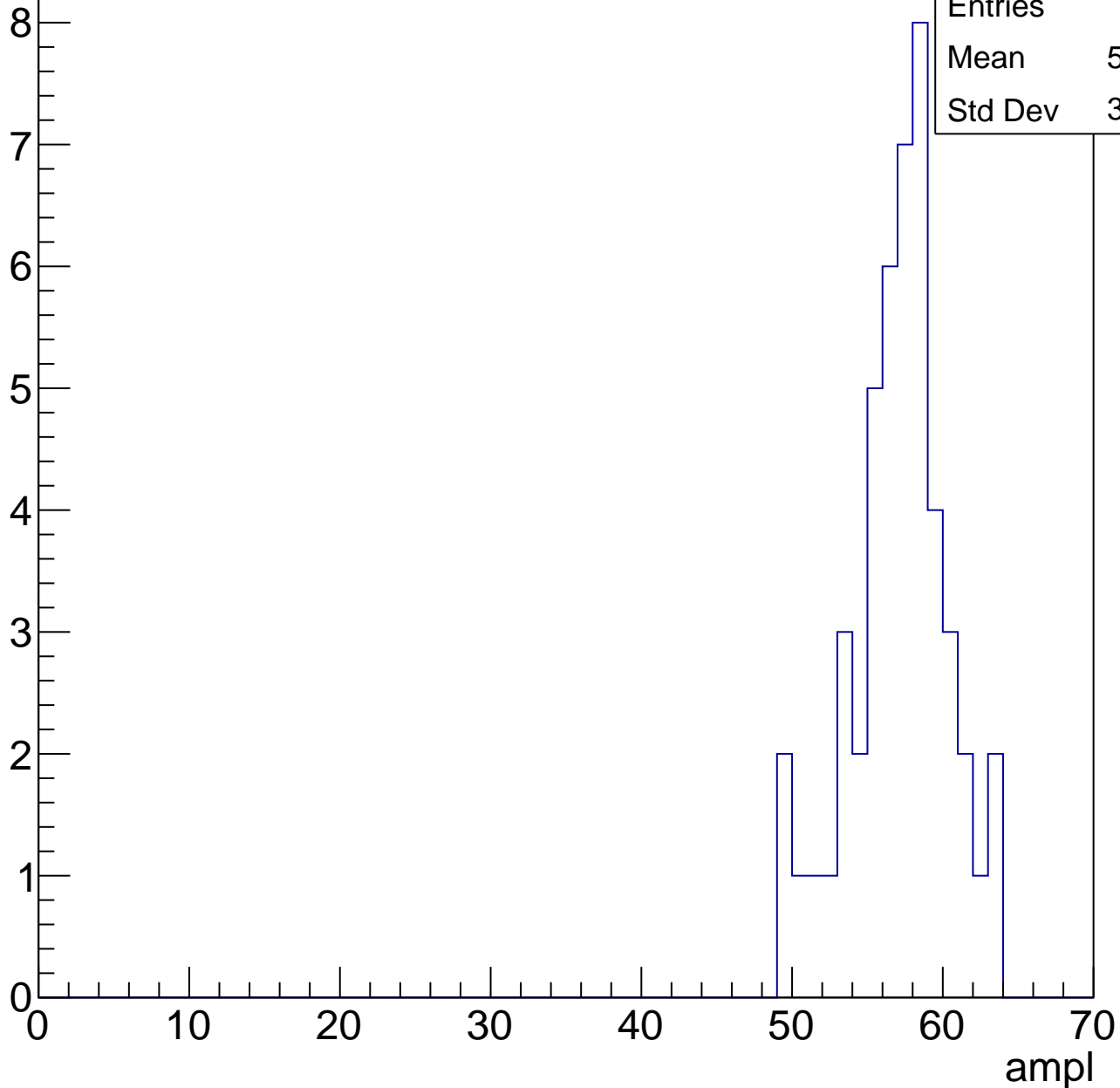
Entries	93
Mean	49.81
Std Dev	4.133



B1L101S, U12-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

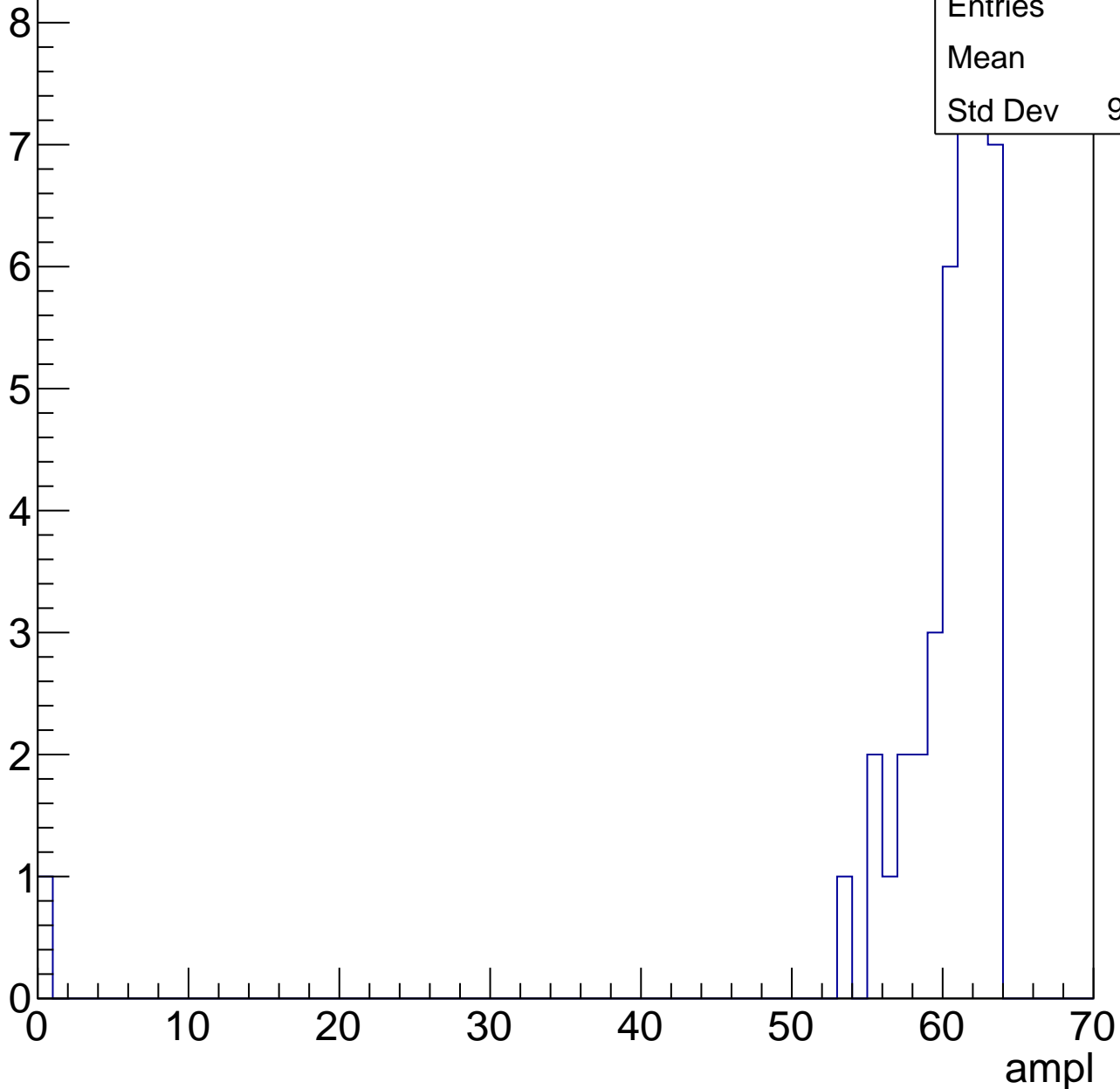


B1L101S, U12-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.8
Std Dev	9.613



B1L101S, U12-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch104, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	99
Mean	28.16
Std Dev	4.76

Gaus mean : 29.4238

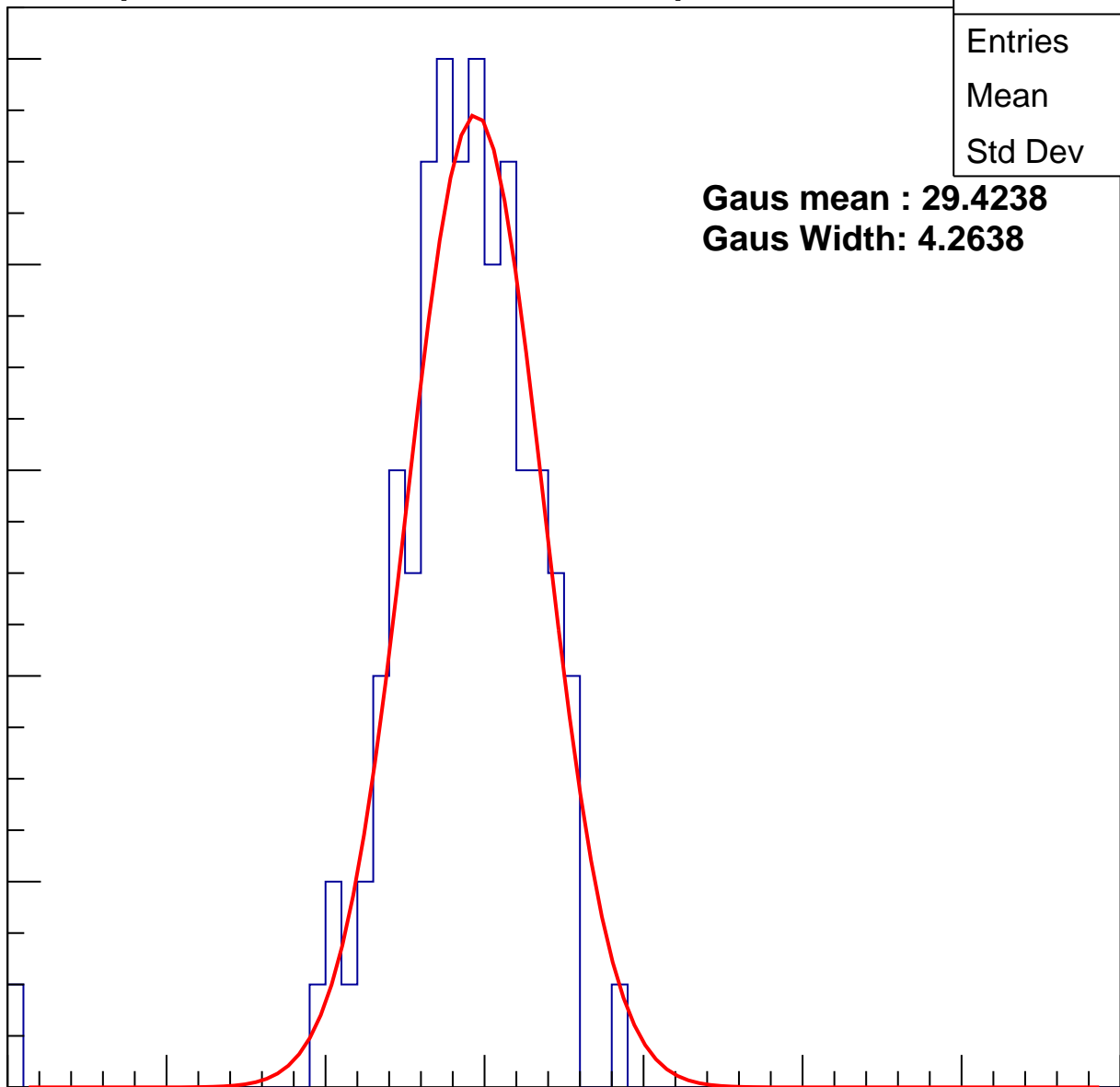
Gaus Width: 4.2638

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch104, adc1

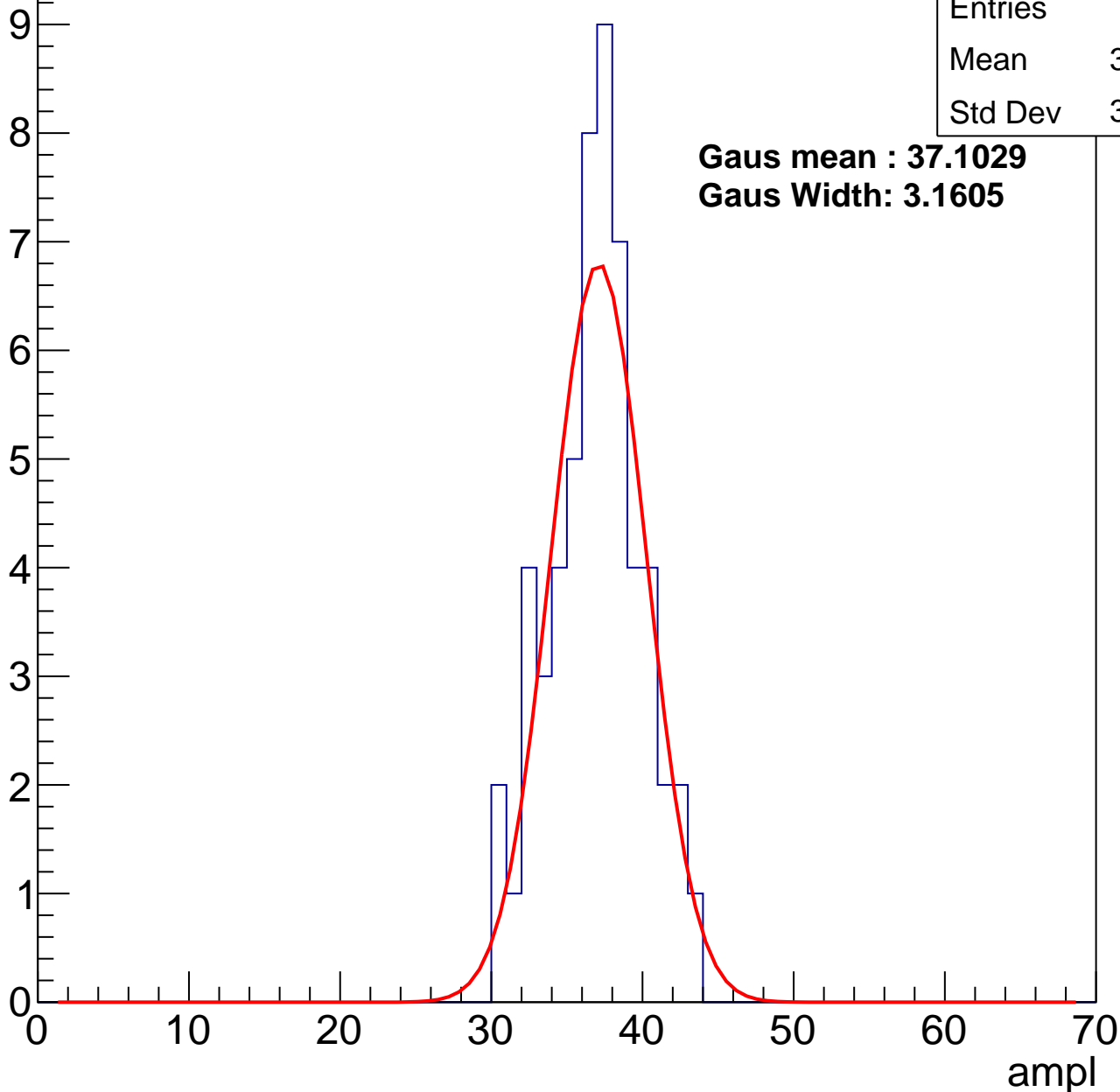
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	36.45
Std Dev	3.005

Gaus mean : 37.1029

Gaus Width: 3.1605



B1L101S, U12-ch104, adc2

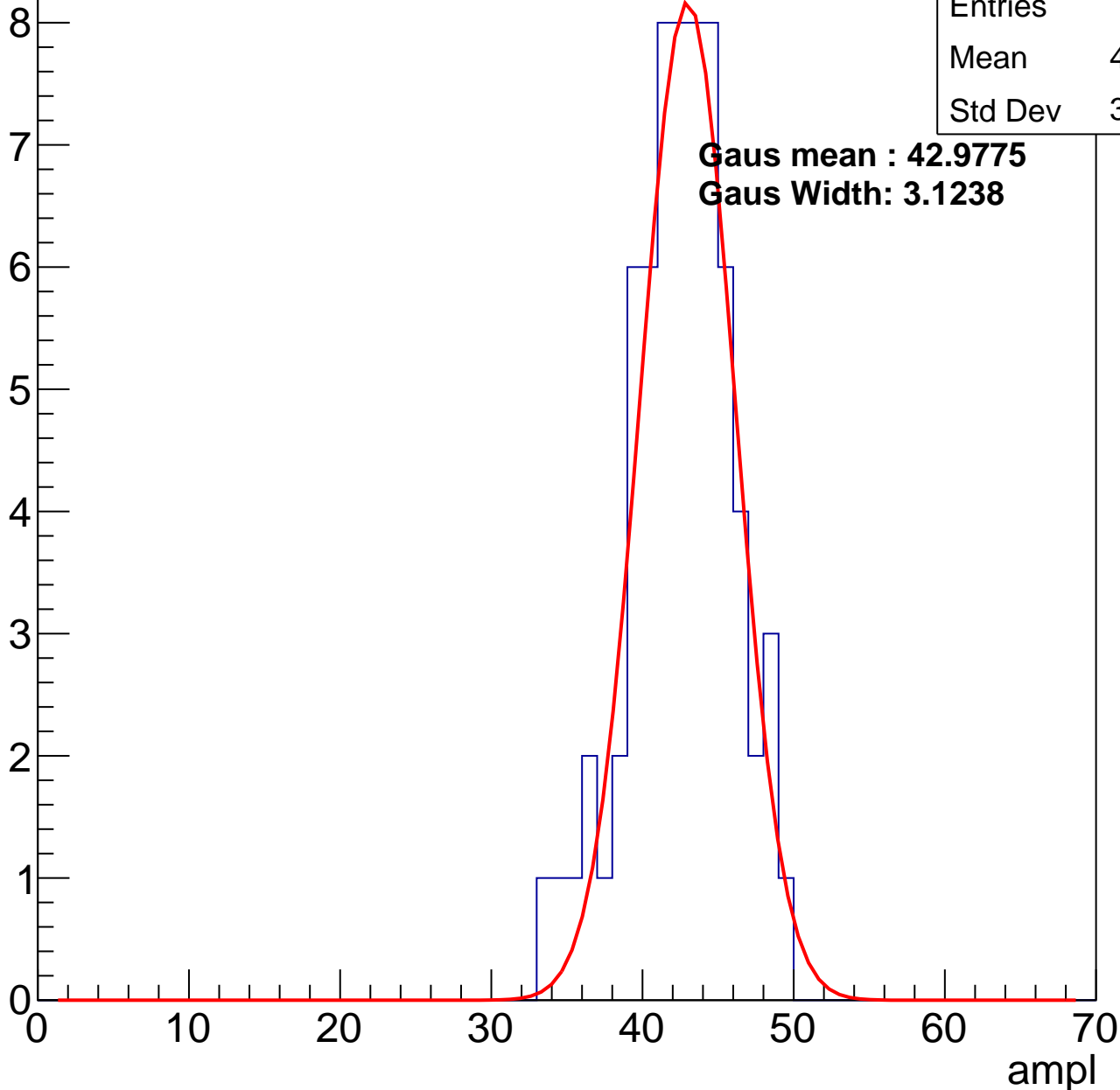
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	42.09
Std Dev	3.373

Gaus mean : 42.9775

Gaus Width: 3.1238

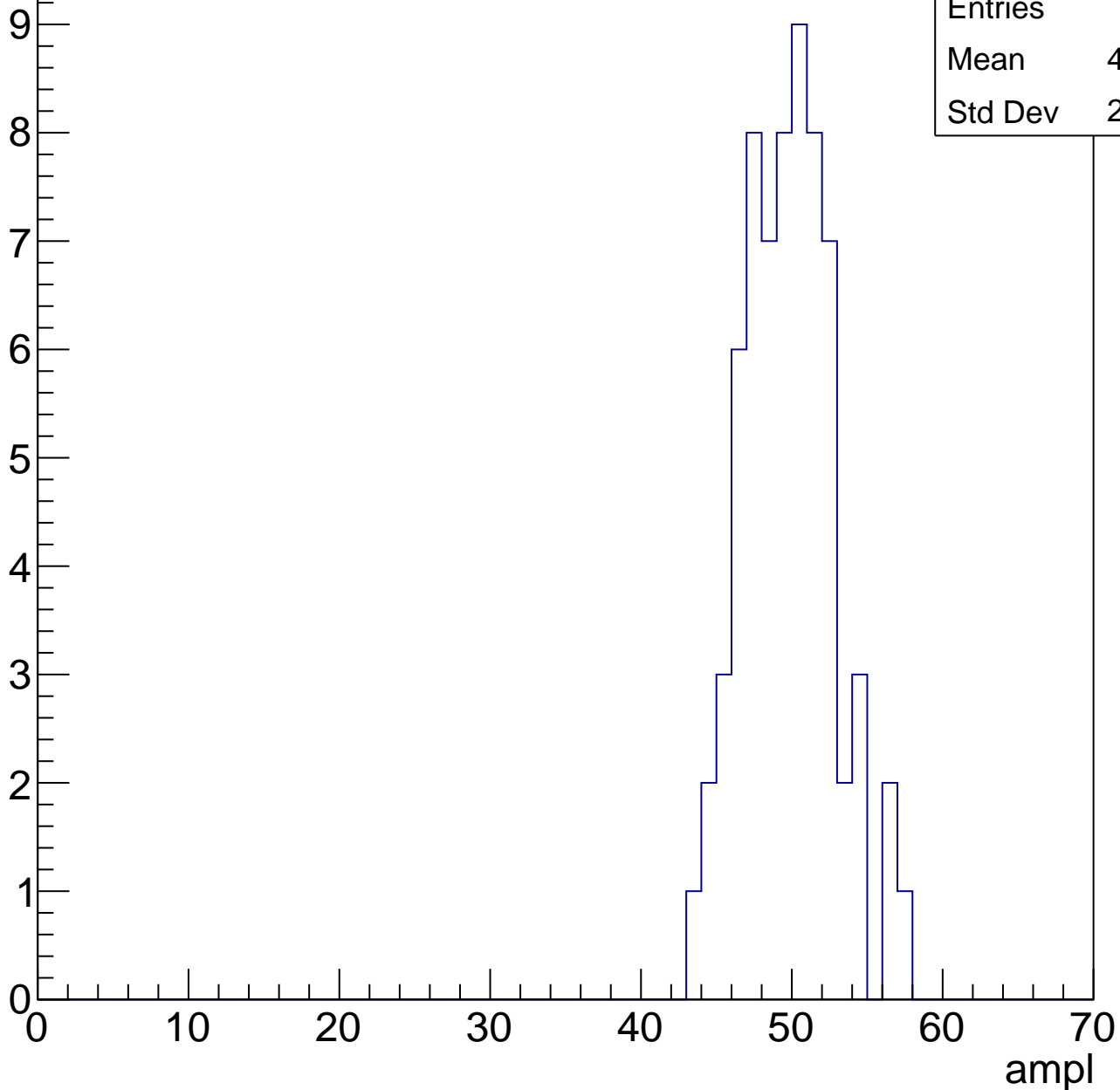


B1L101S, U12-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.33
Std Dev	2.974

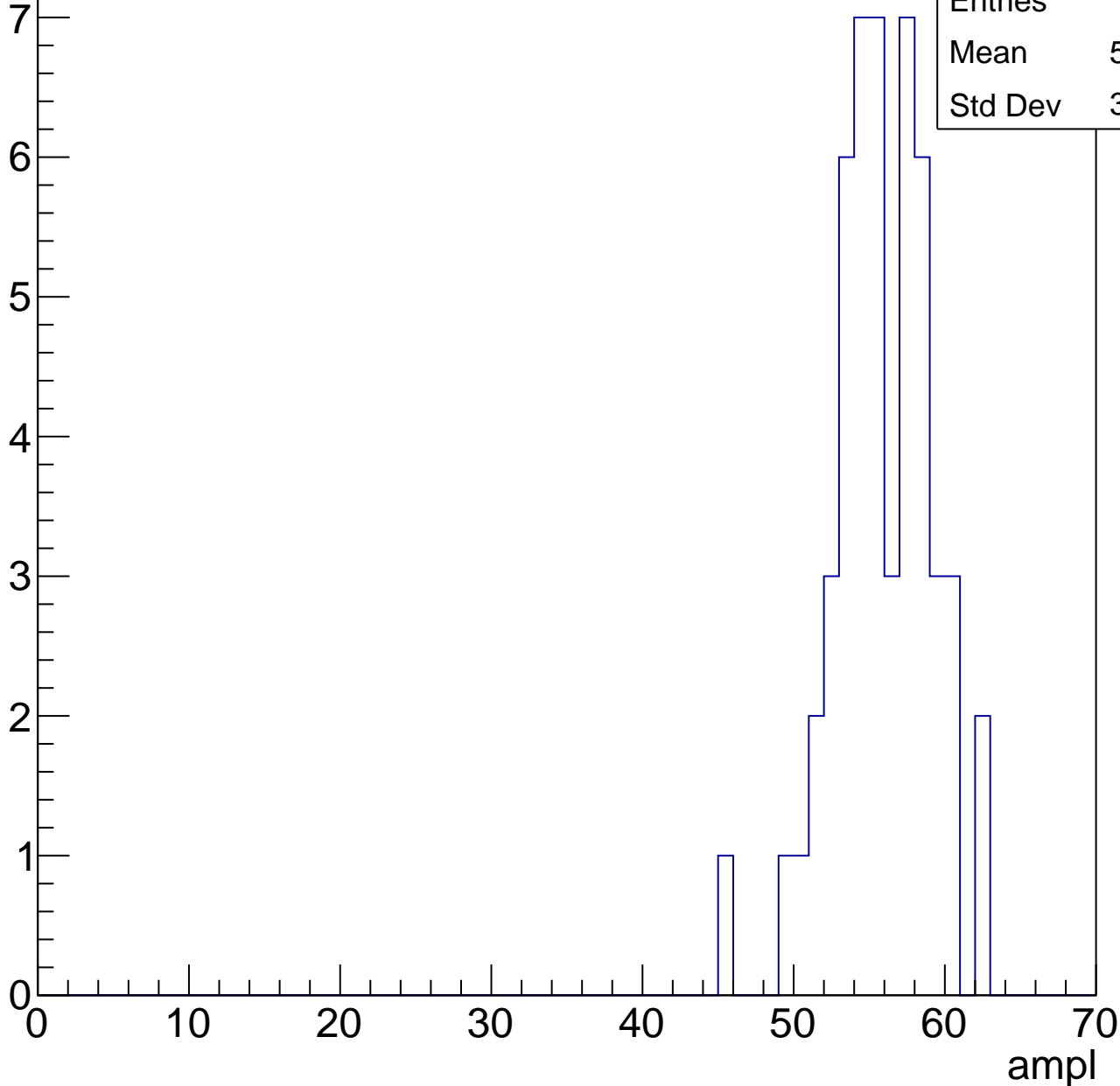


B1L101S, U12-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	55.37
Std Dev	3.252

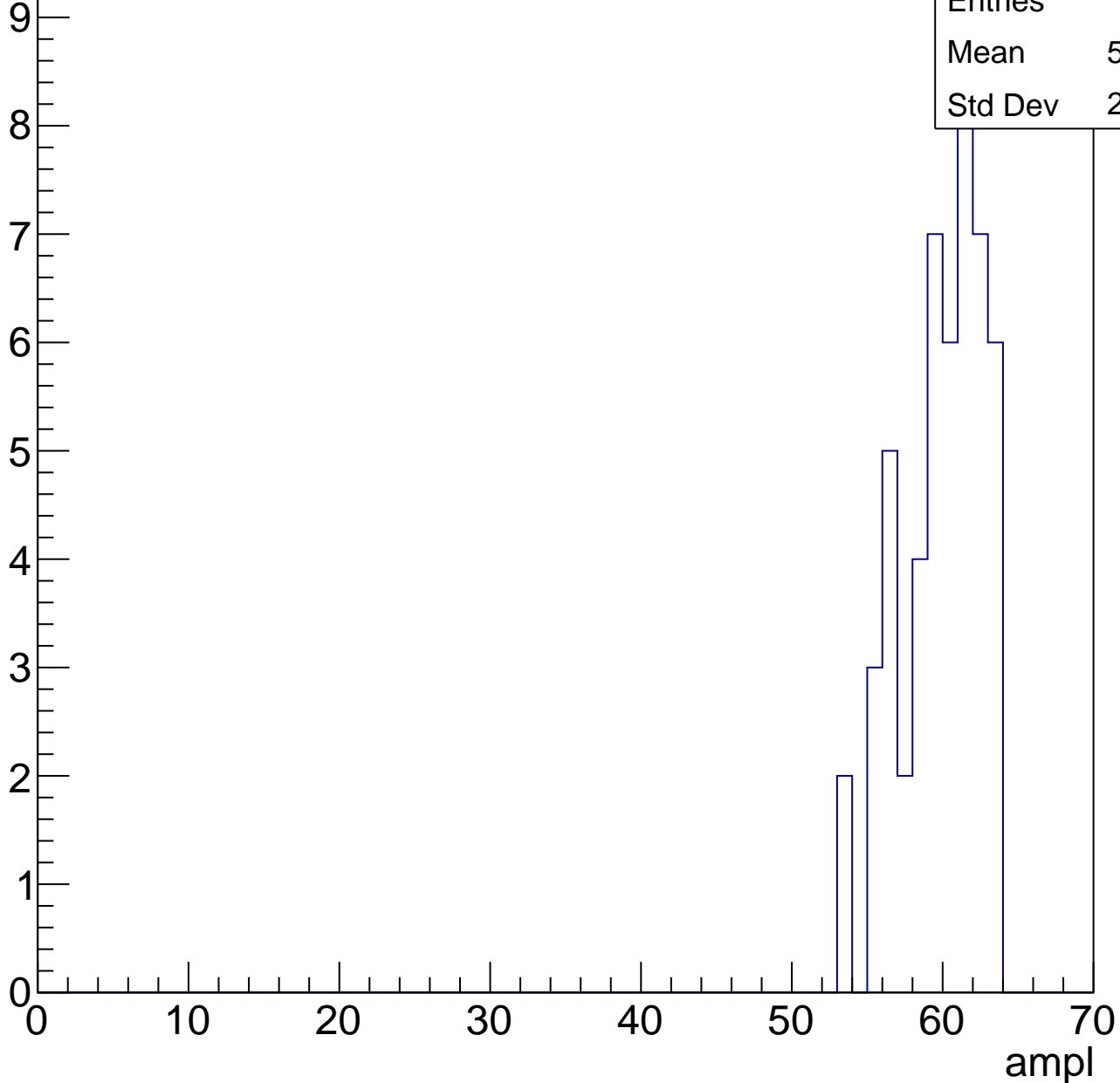


B1L101S, U12-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

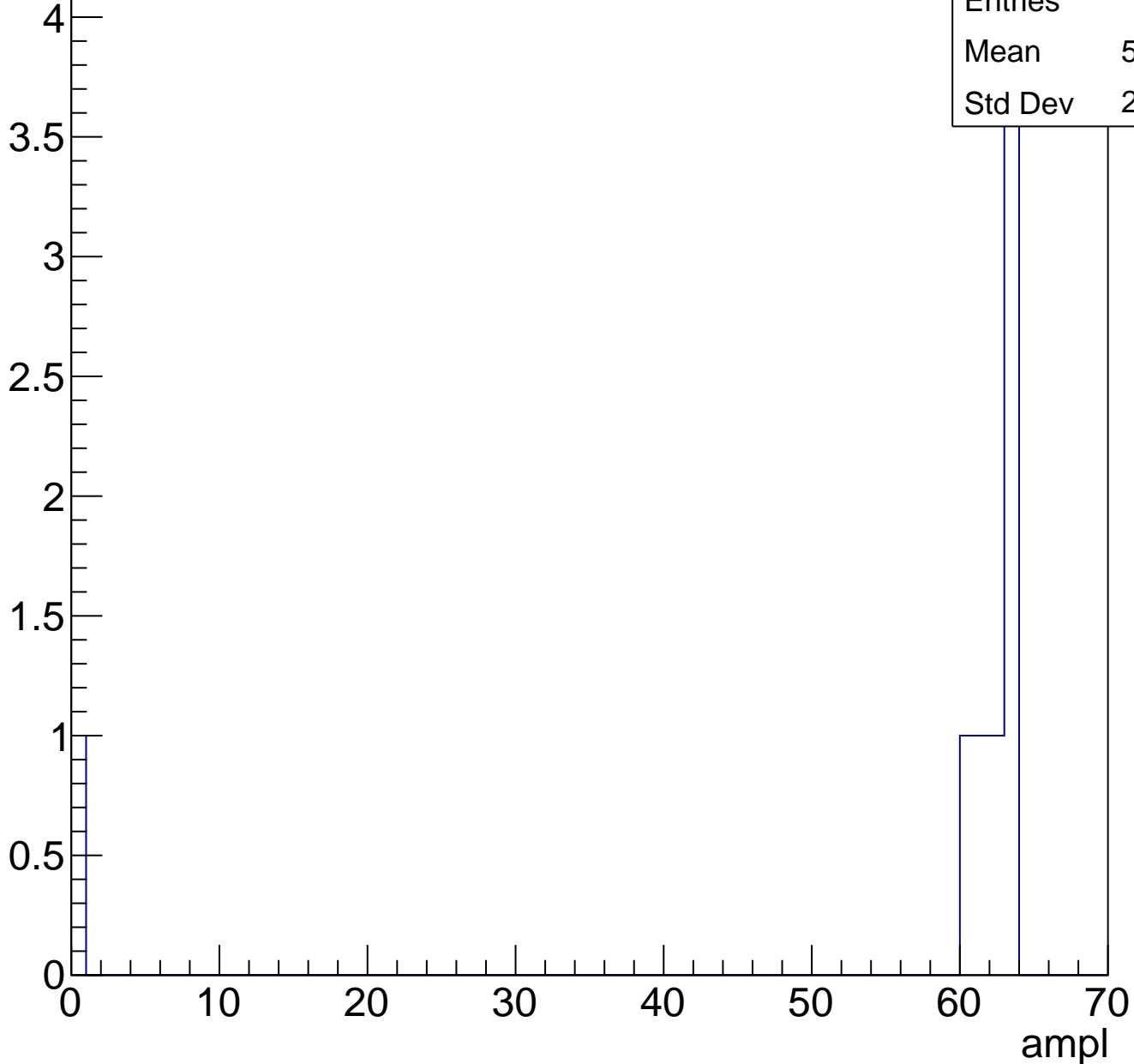
Entries	51
Mean	59.43
Std Dev	2.688



B1L101S, U12-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch105, adc0

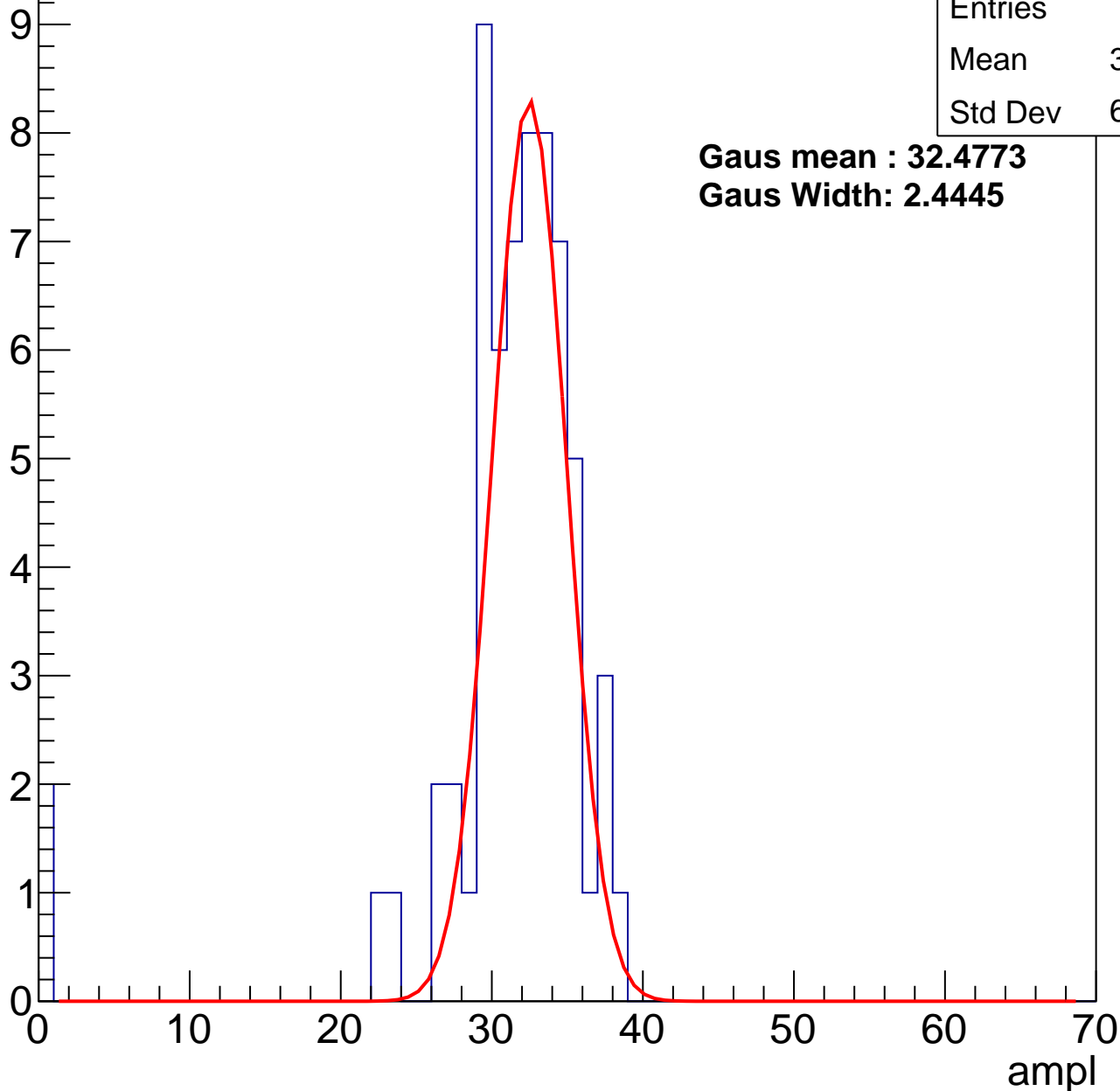
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.55
Std Dev	6.314

Gaus mean : 32.4773

Gaus Width: 2.4445



B1L101S, U12-ch105, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	38.37
Std Dev	5.593

Gaus mean : 39.4920

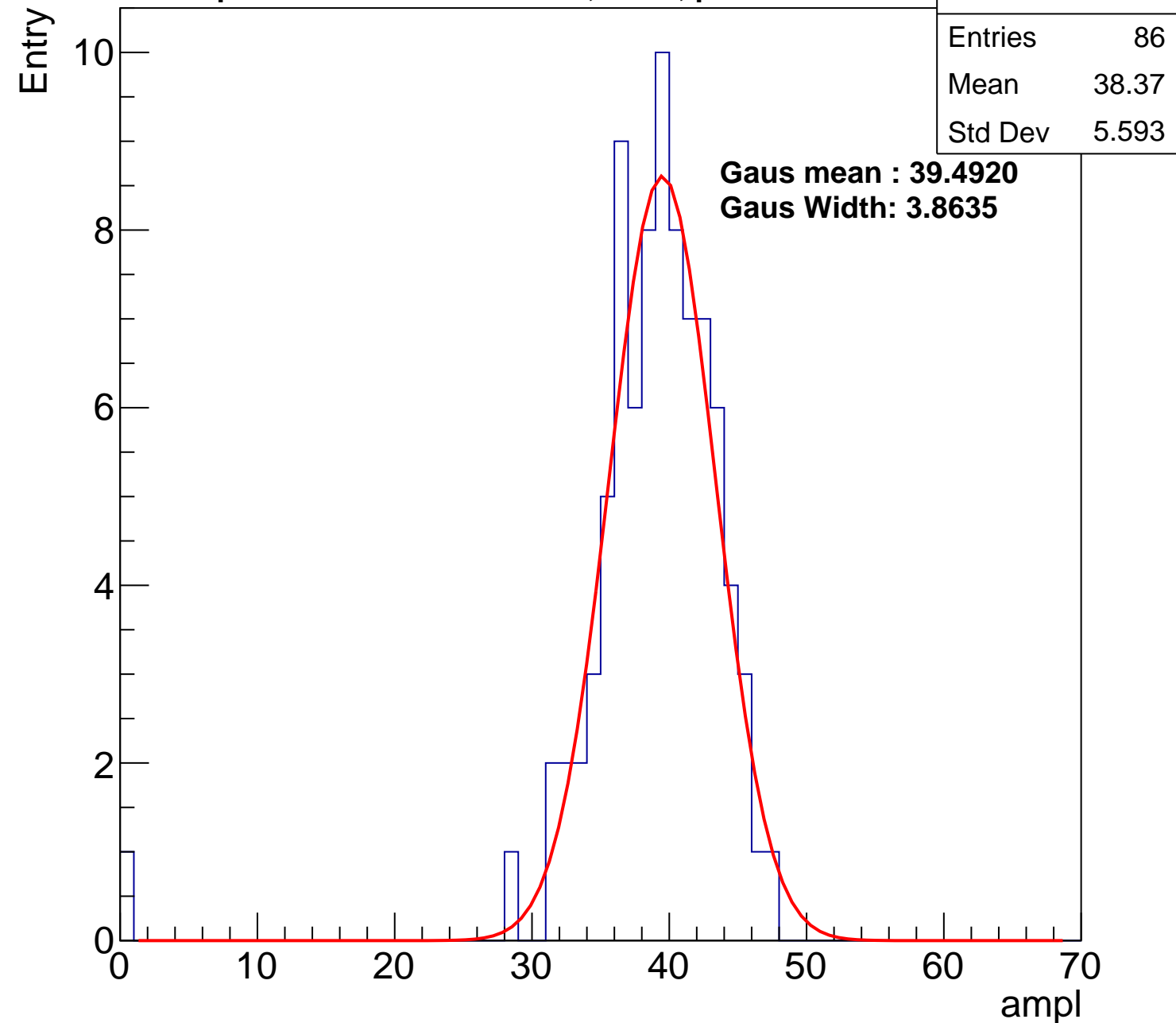
Gaus Width: 3.8635

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch105, adc2

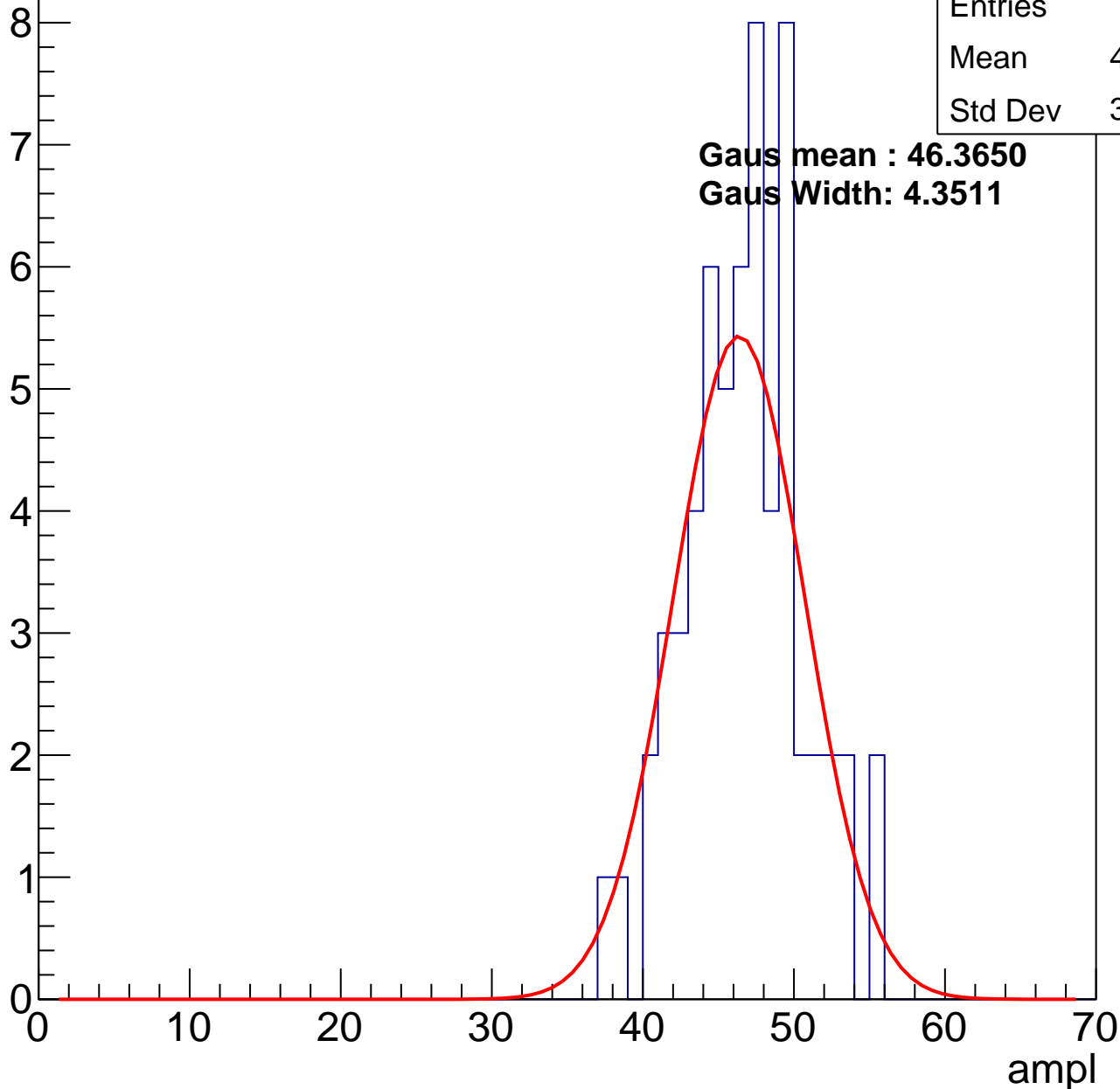
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	46.28
Std Dev	3.863

Gaus mean : 46.3650

Gaus Width: 4.3511

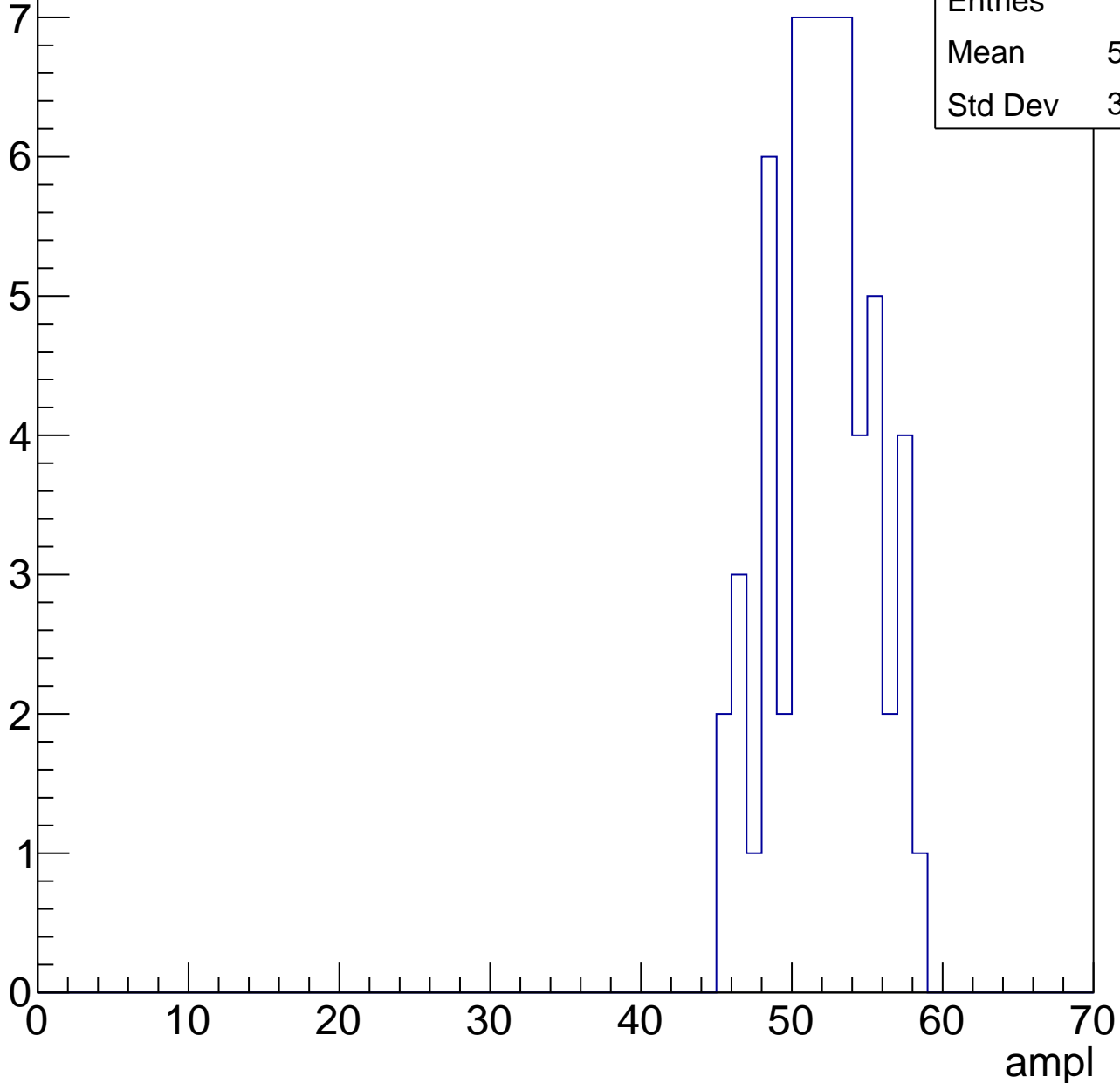


B1L101S, U12-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	51.59
Std Dev	3.233

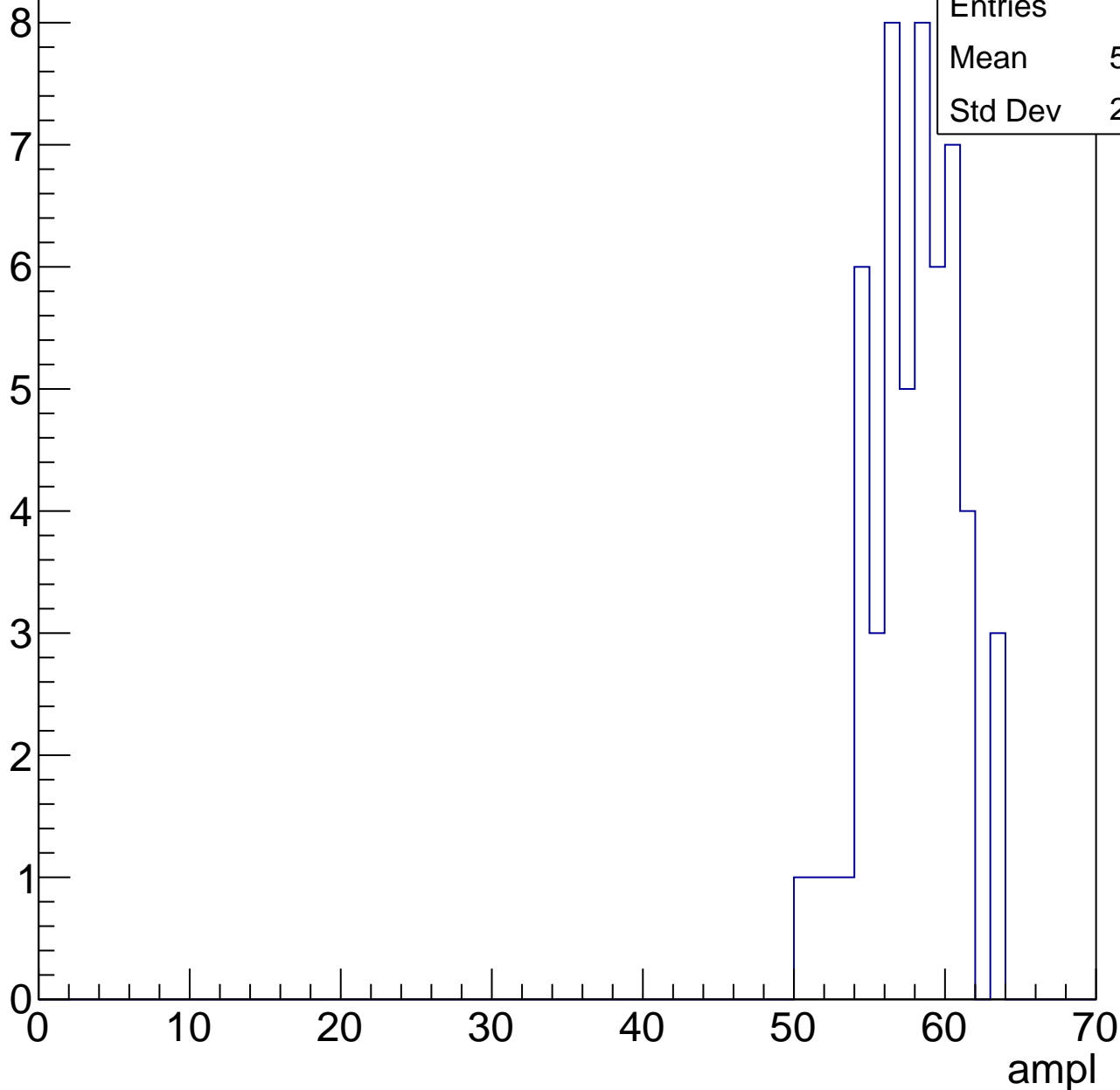


B1L101S, U12-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.39
Std Dev	2.915

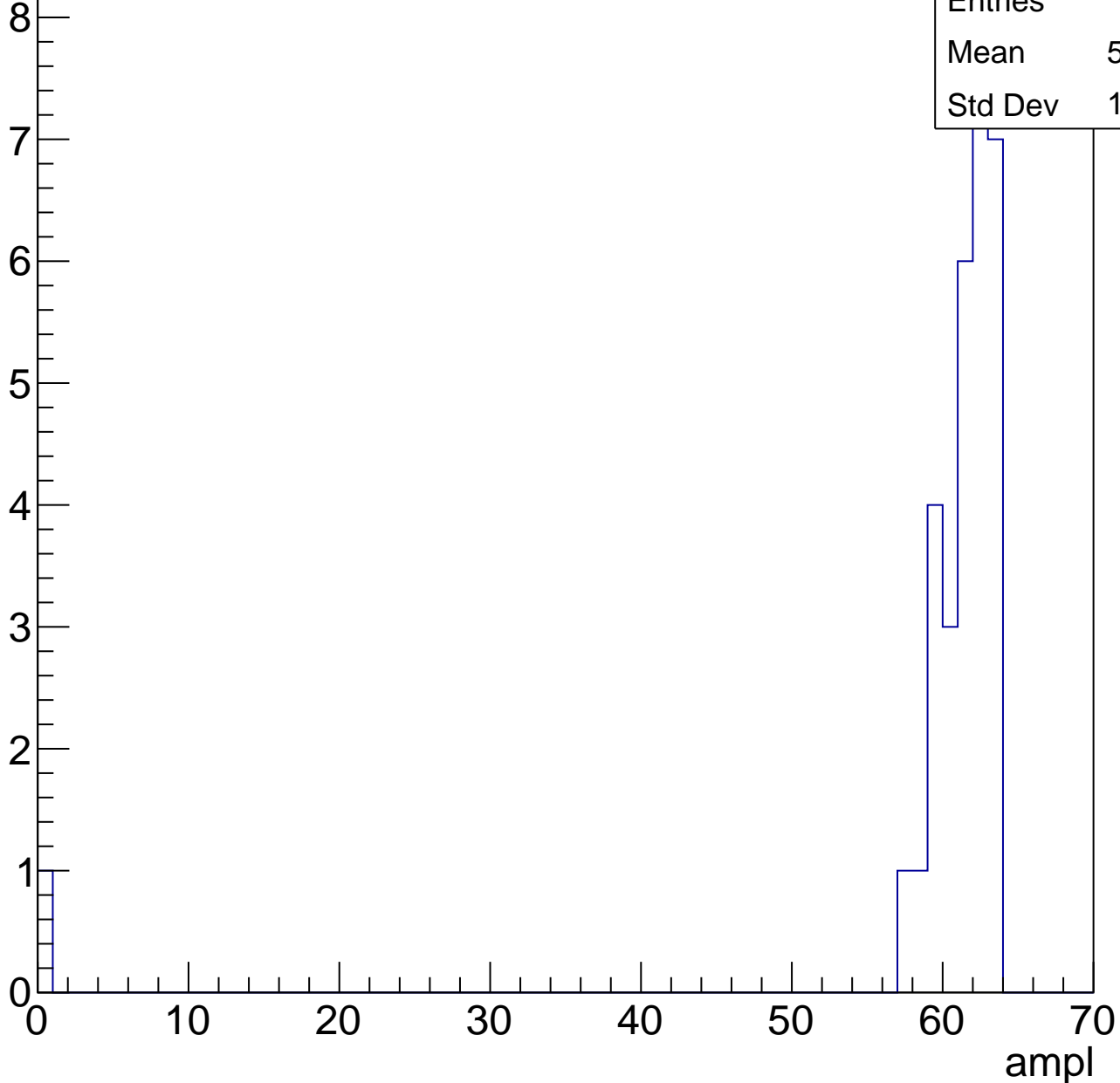


B1L101S, U12-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	59.16
Std Dev	10.92



B1L101S, U12-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B1L101S, U12-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch106, adc0

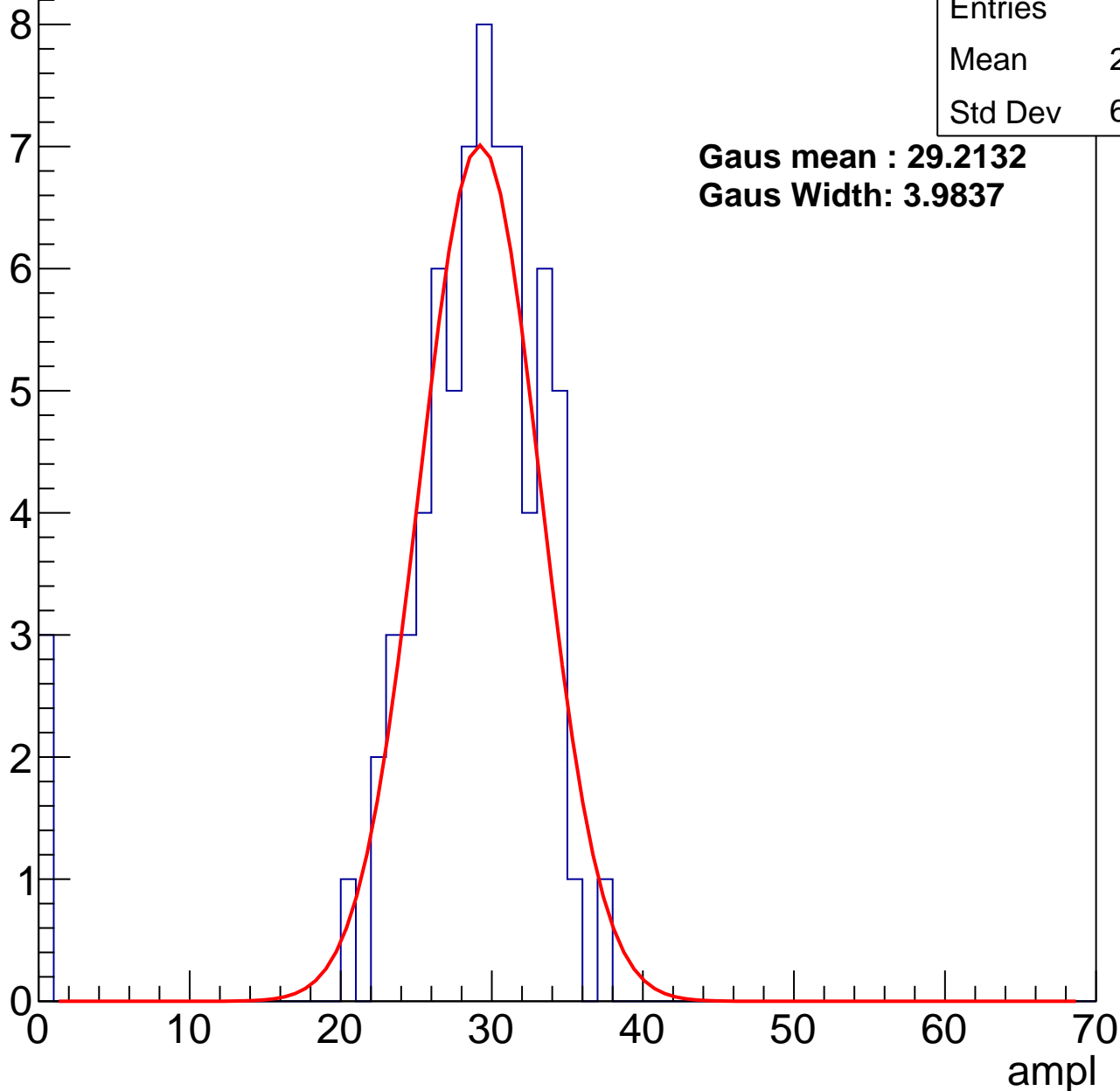
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.66
Std Dev	6.714

Gaus mean : 29.2132

Gaus Width: 3.9837



B1L101S, U12-ch106, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	35.67
Std Dev	3.231

Gaus mean : 36.2965

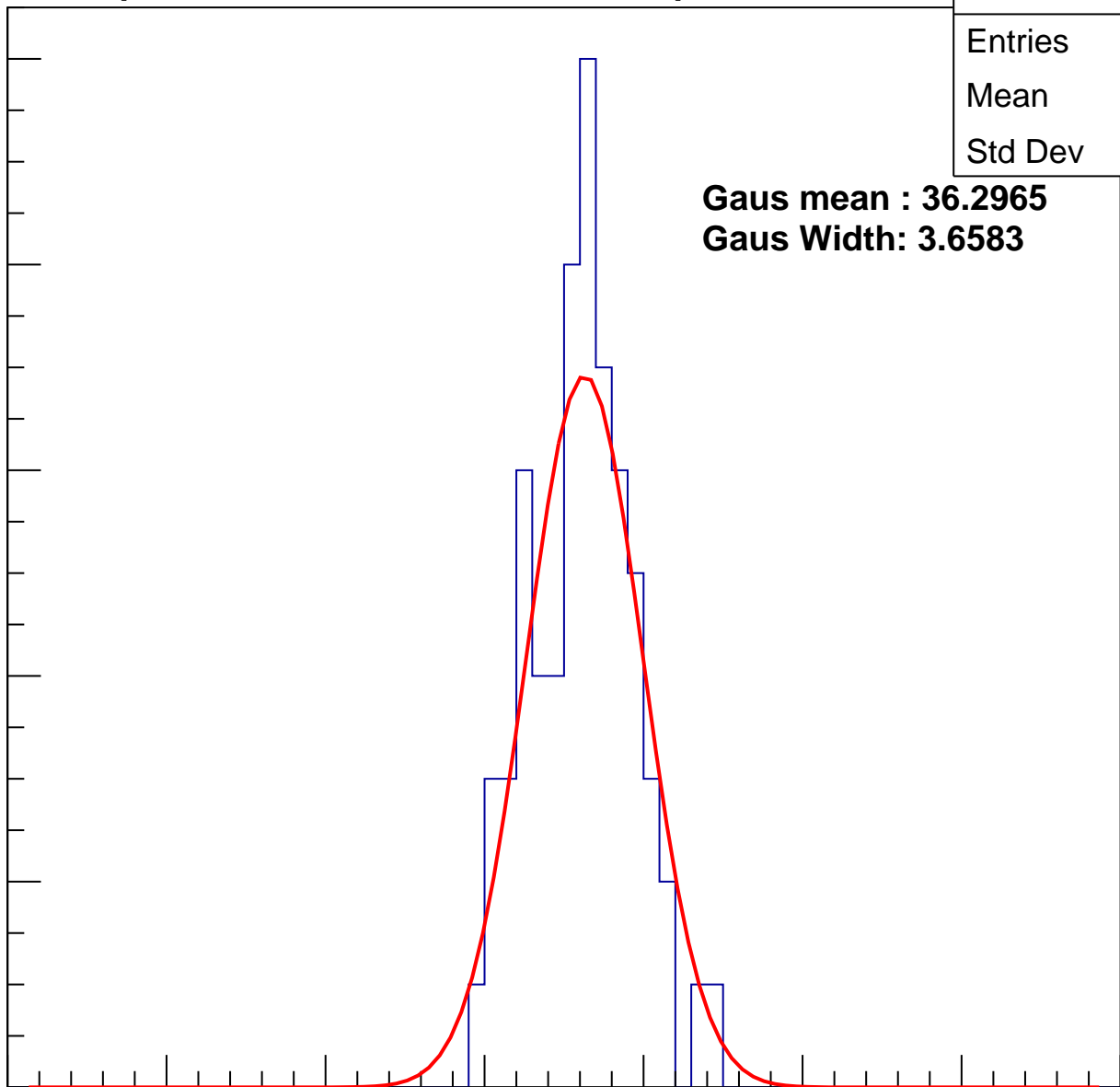
Gaus Width: 3.6583

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch106, adc2

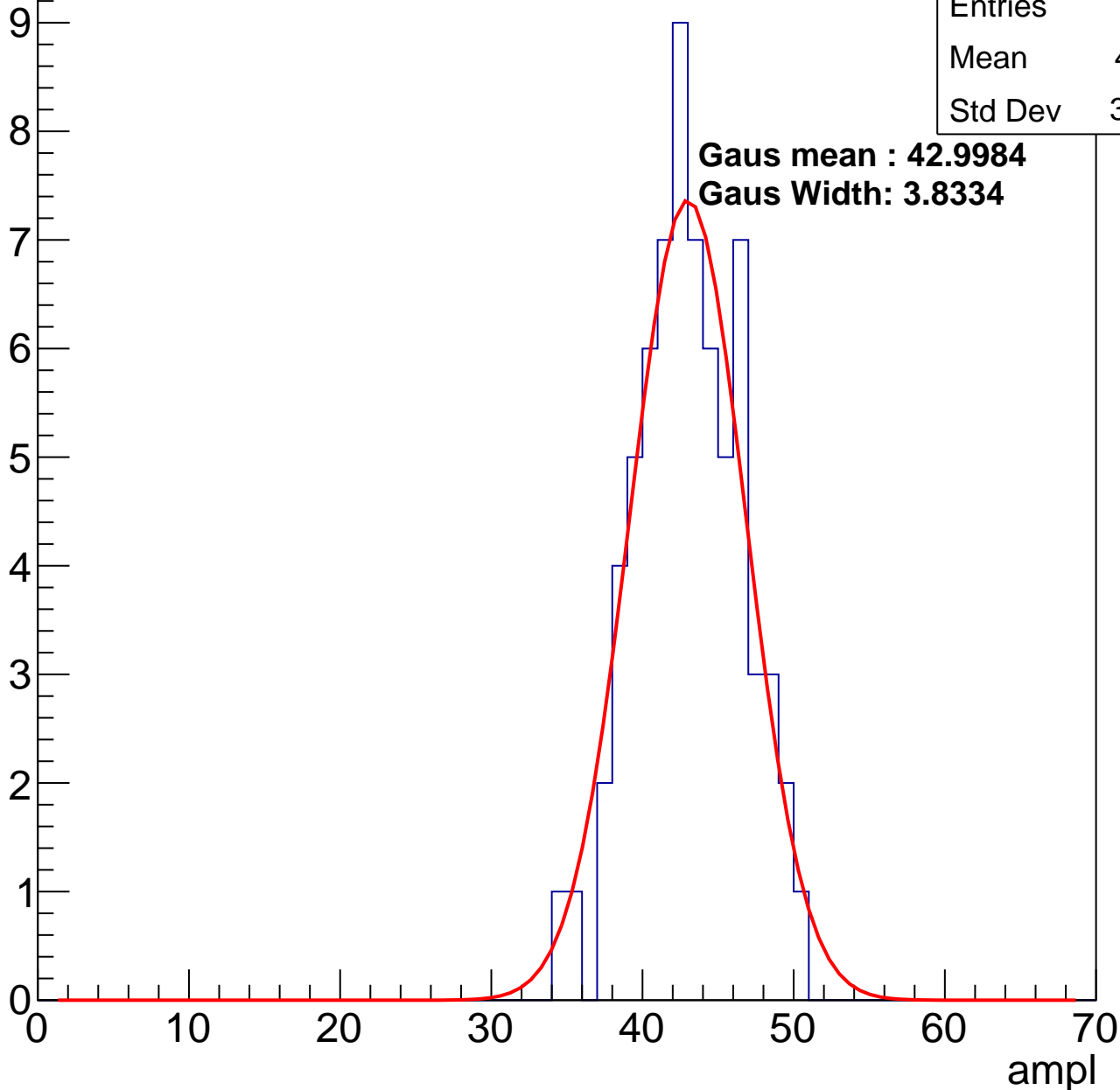
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.61
Std Dev	3.427

Gaus mean : 42.9984

Gaus Width: 3.8334

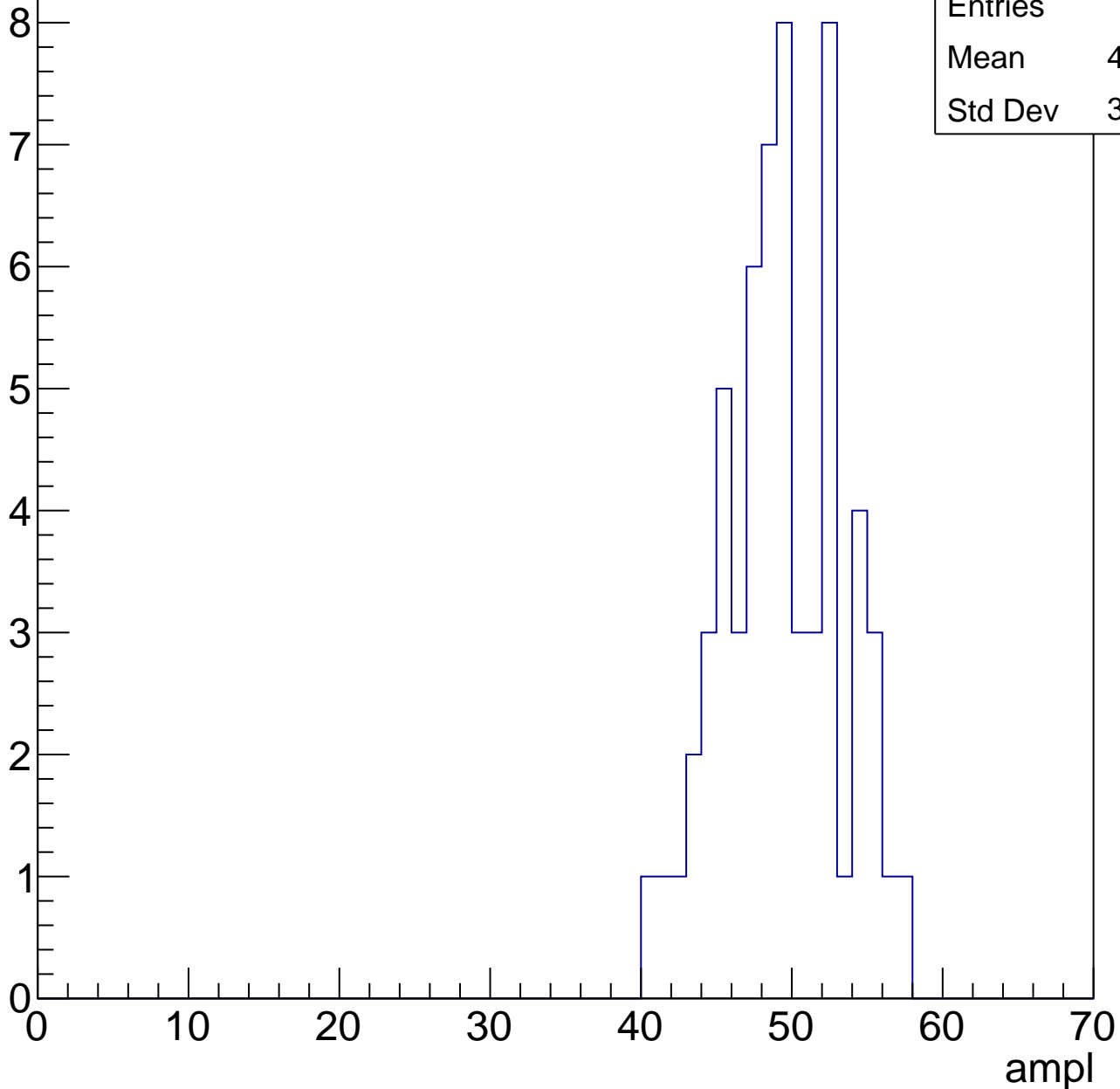


B1L101S, U12-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.85
Std Dev	3.845

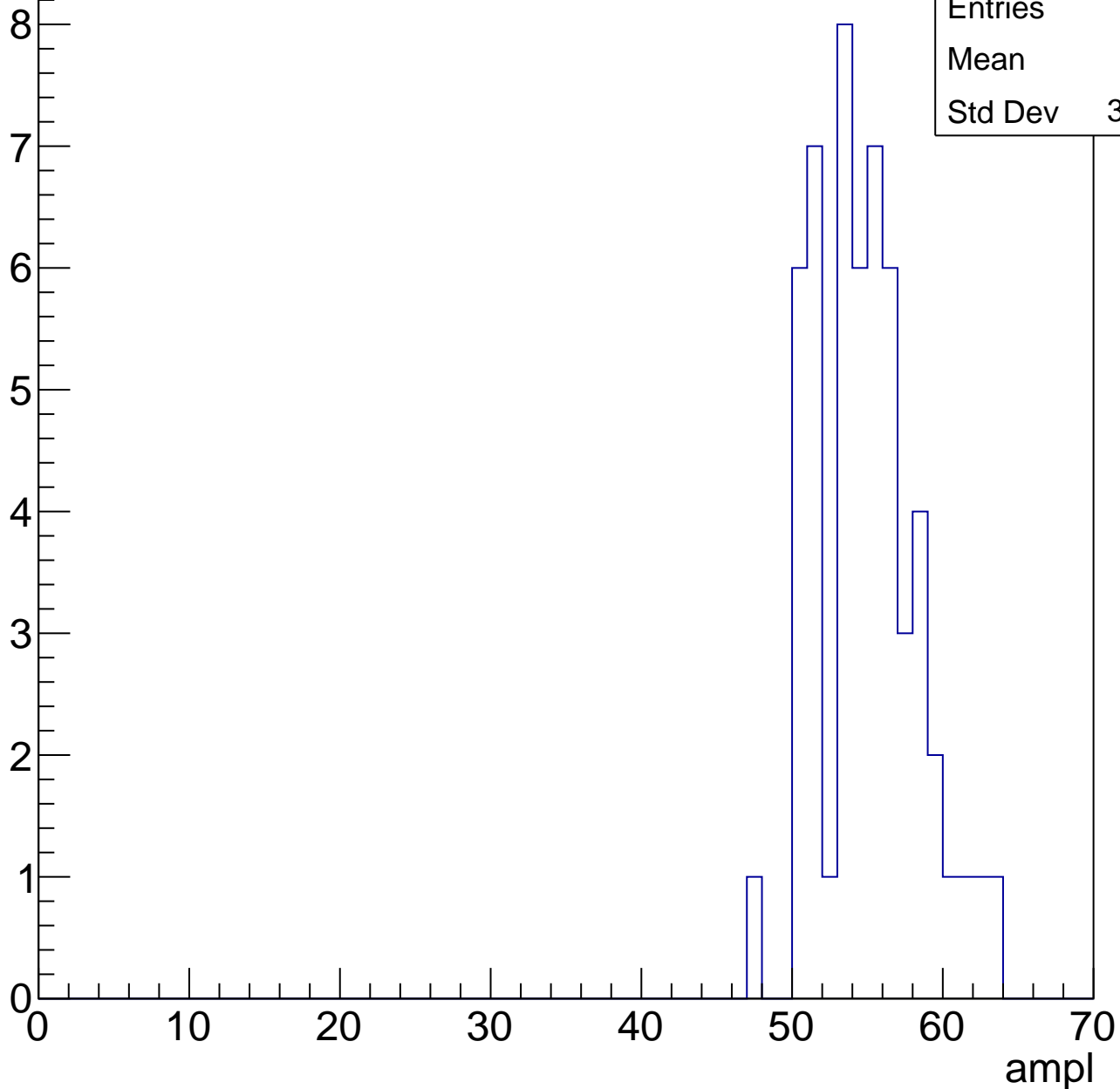


B1L101S, U12-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.4
Std Dev	3.339

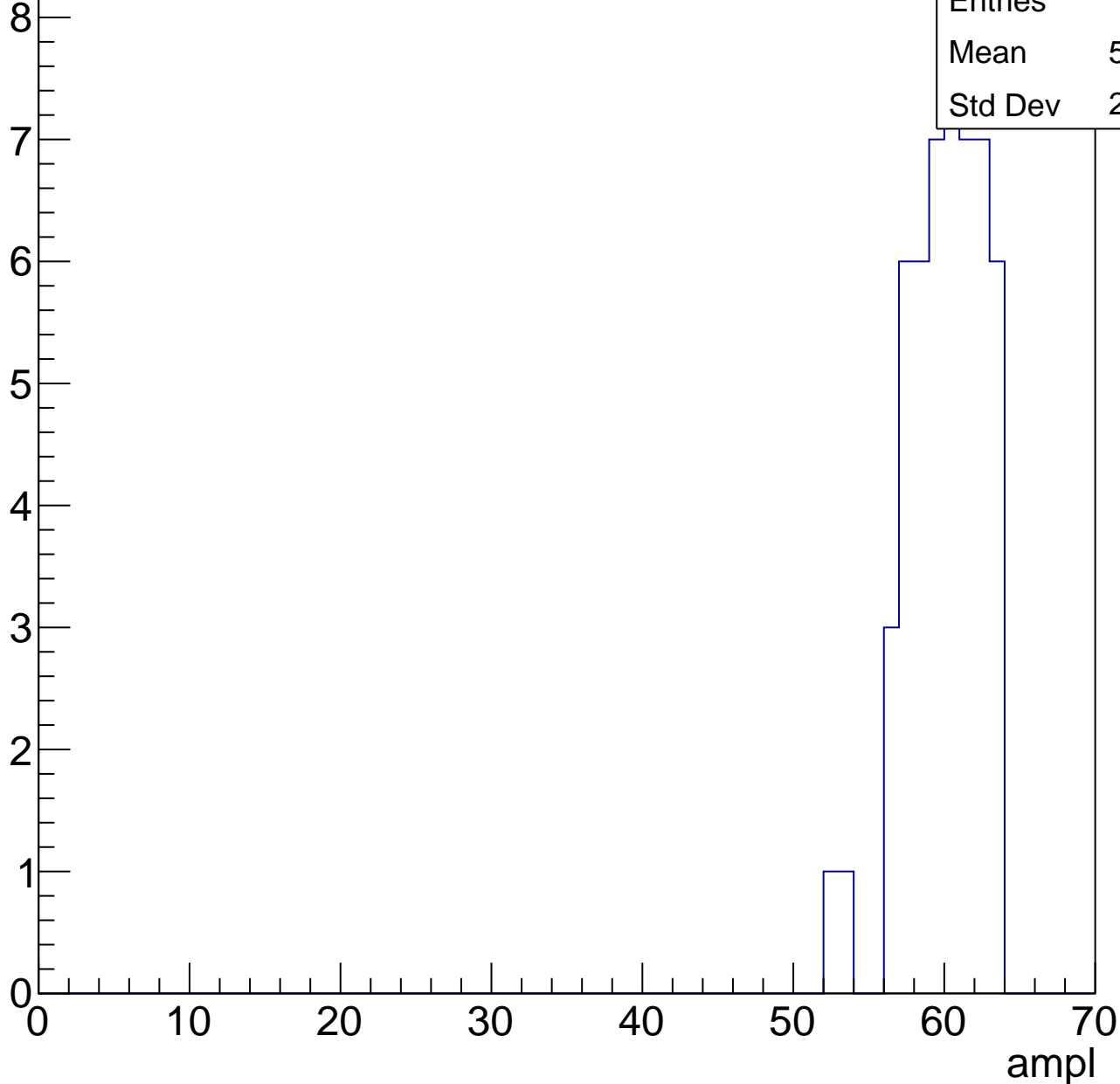


B1L101S, U12-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	59.52
Std Dev	2.492



B1L101S, U12-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

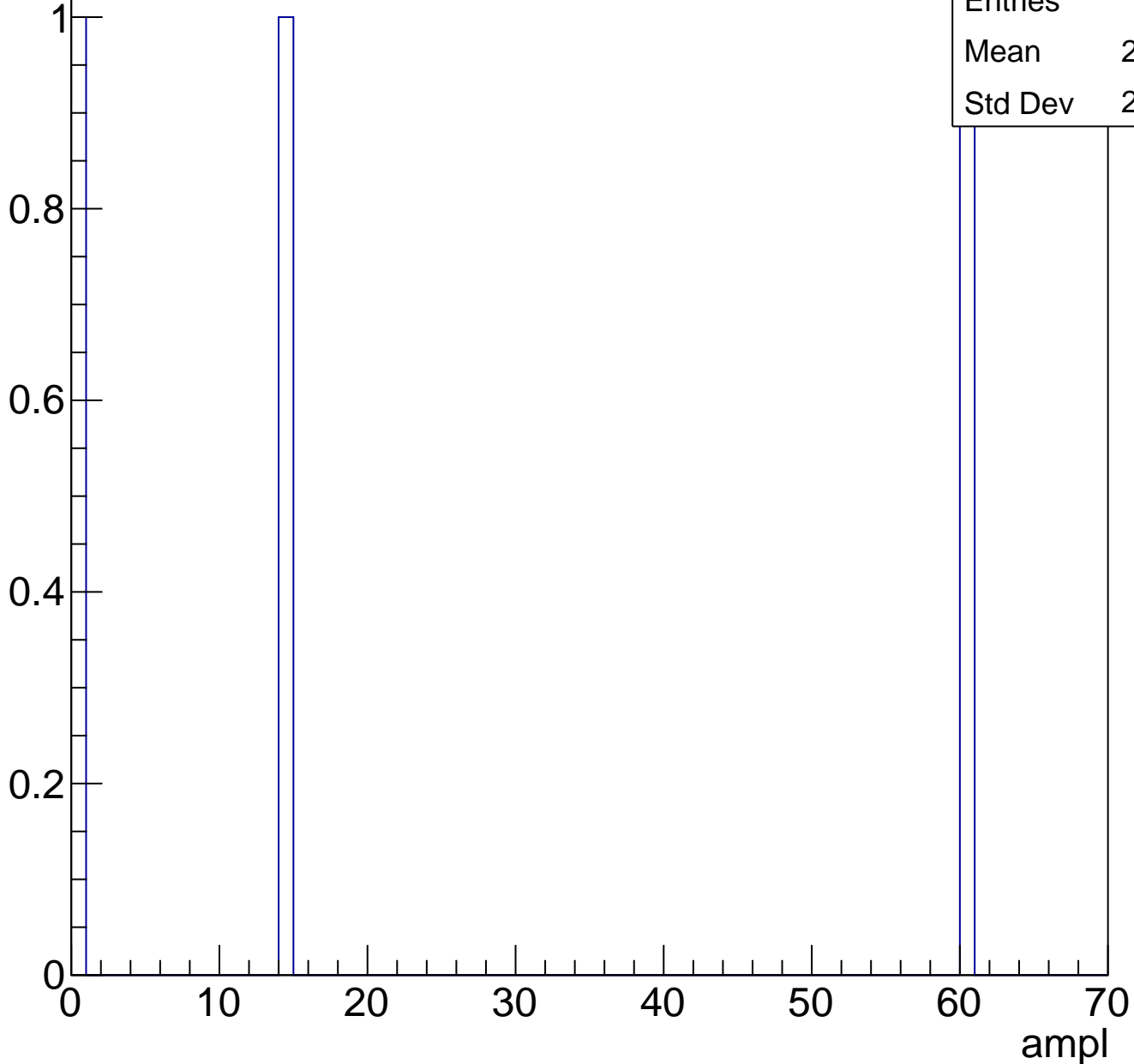
ampl

Entries	9
Mean	54.89
Std Dev	19.44

B1L101S, U12-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	24.67
Std Dev	25.63

B1L101S, U12-ch107, adc0

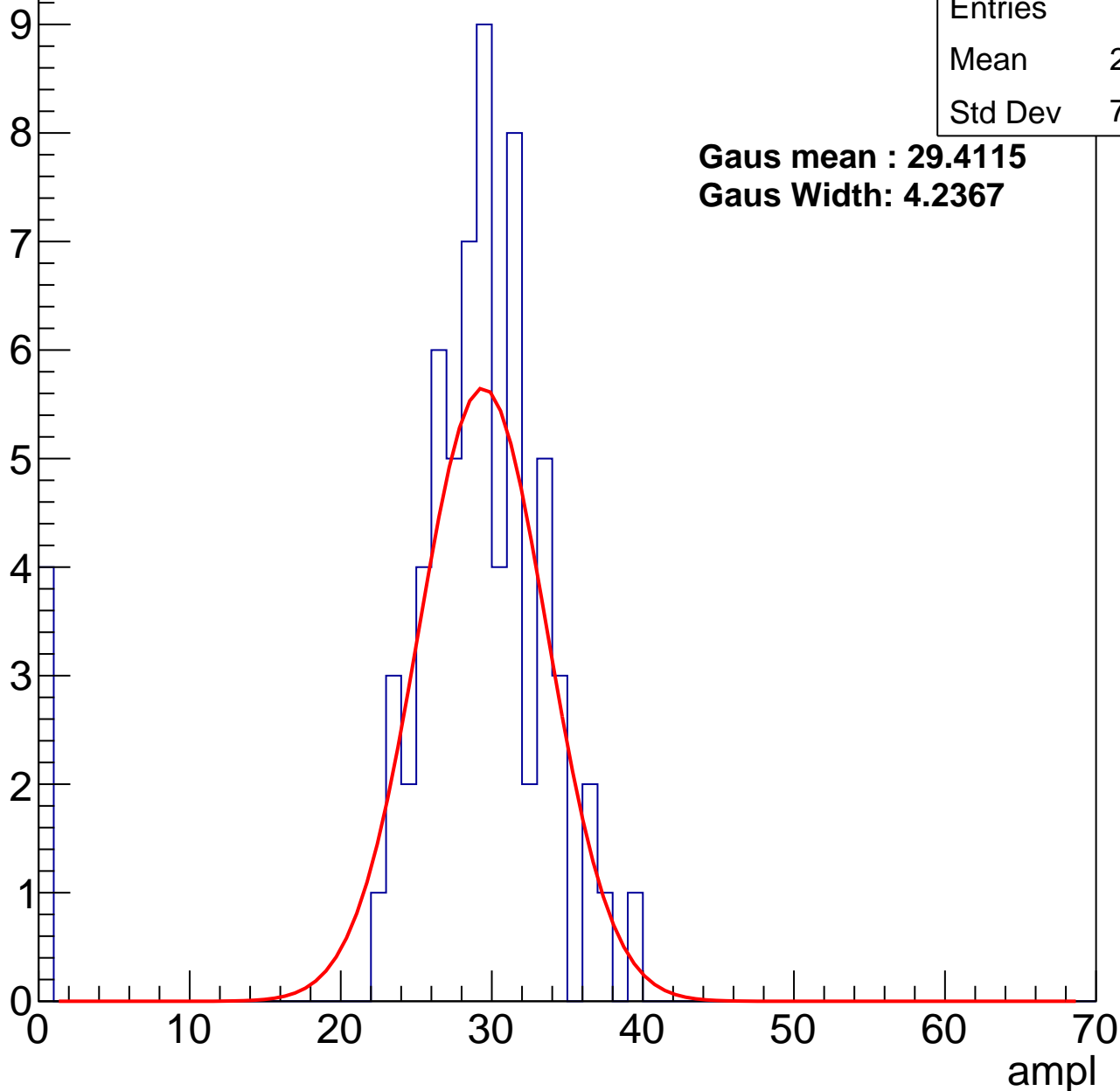
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	27.37
Std Dev	7.736

Gaus mean : 29.4115

Gaus Width: 4.2367



B1L101S, U12-ch107, adc1

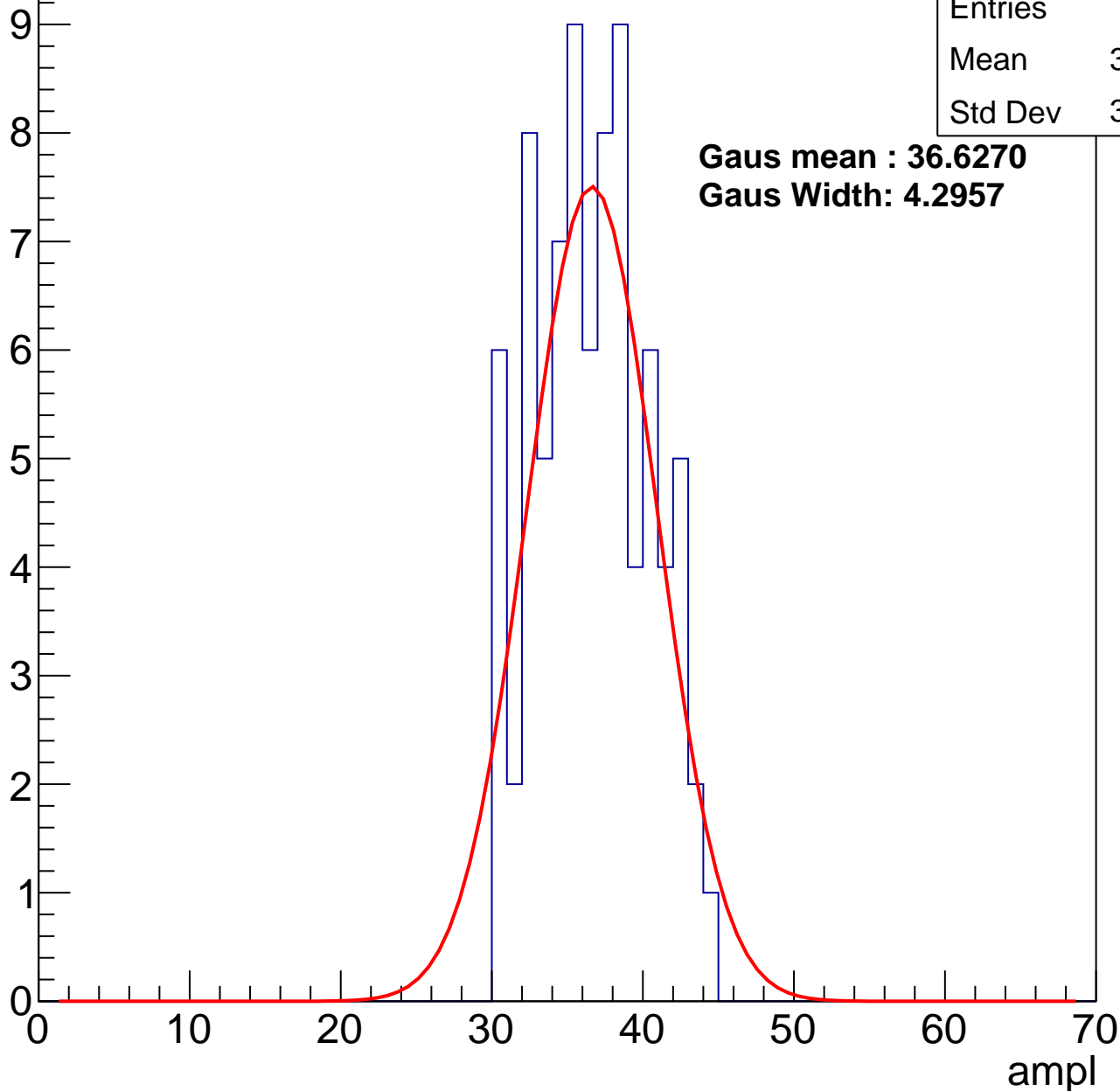
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.22
Std Dev	3.646

Gaus mean : 36.6270

Gaus Width: 4.2957



B1L101S, U12-ch107, adc2

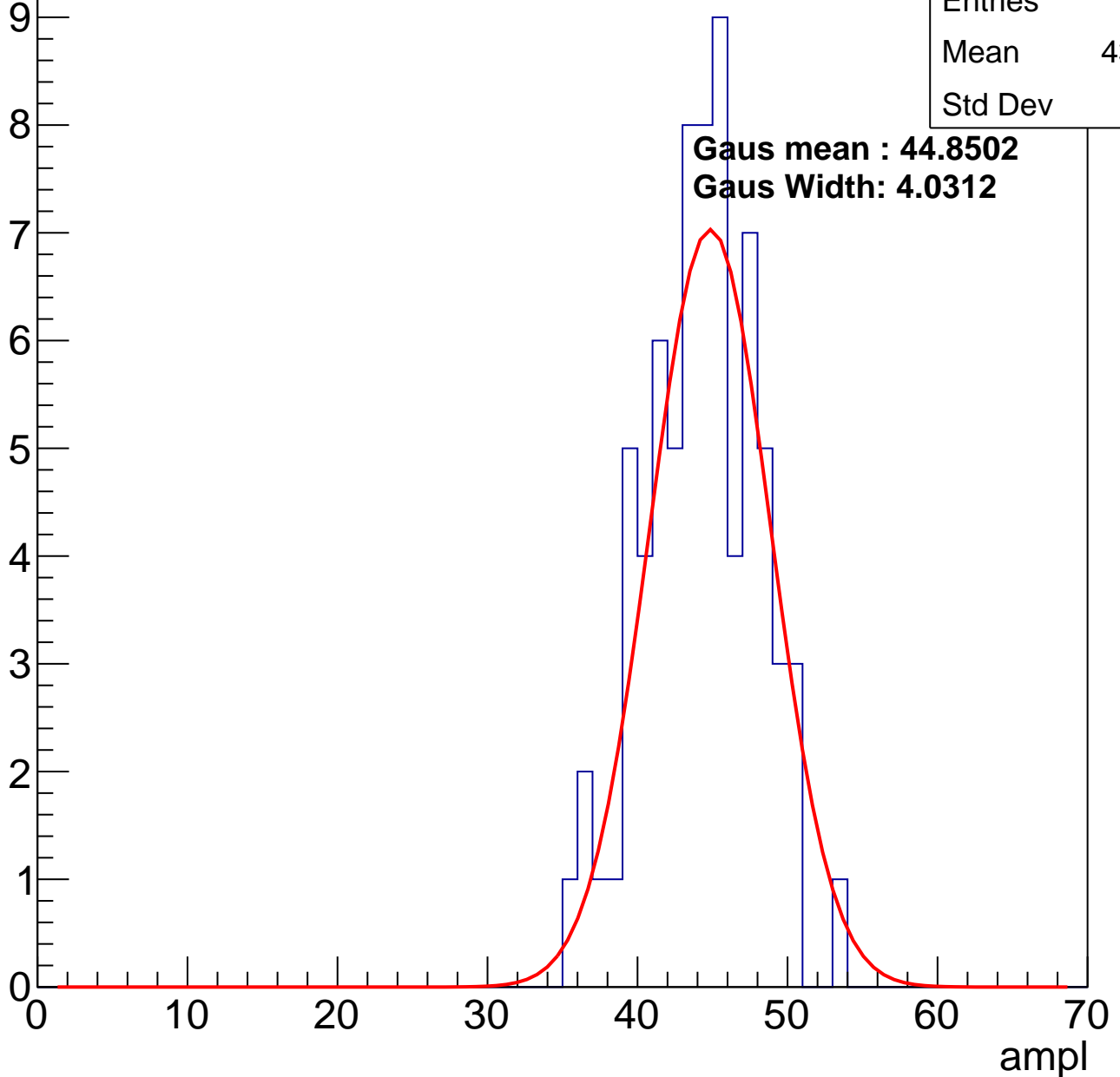
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.79
Std Dev	3.69

Gaus mean : 44.8502

Gaus Width: 4.0312

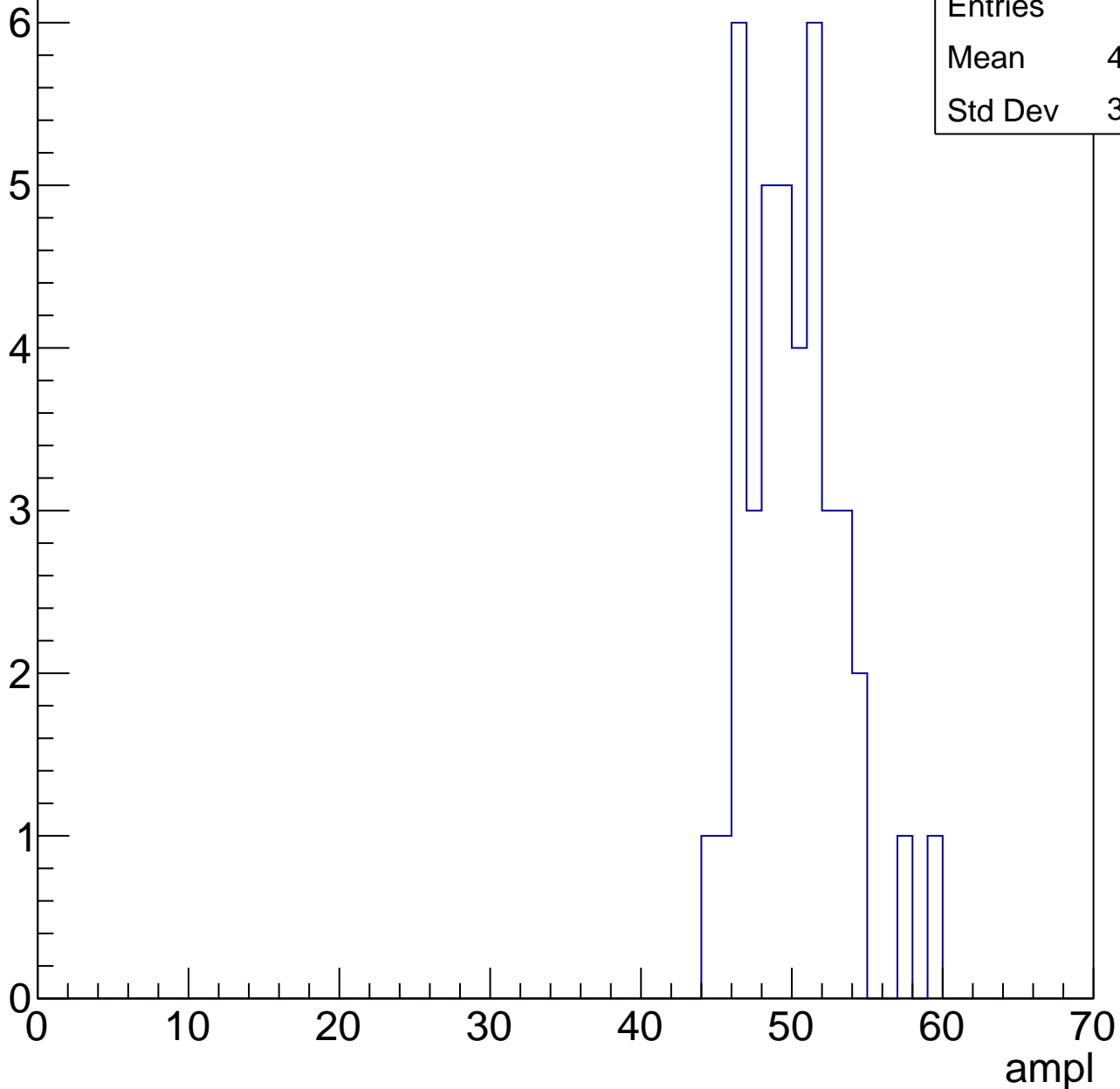


B1L101S, U12-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	49.66
Std Dev	3.167



B1L101S, U12-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

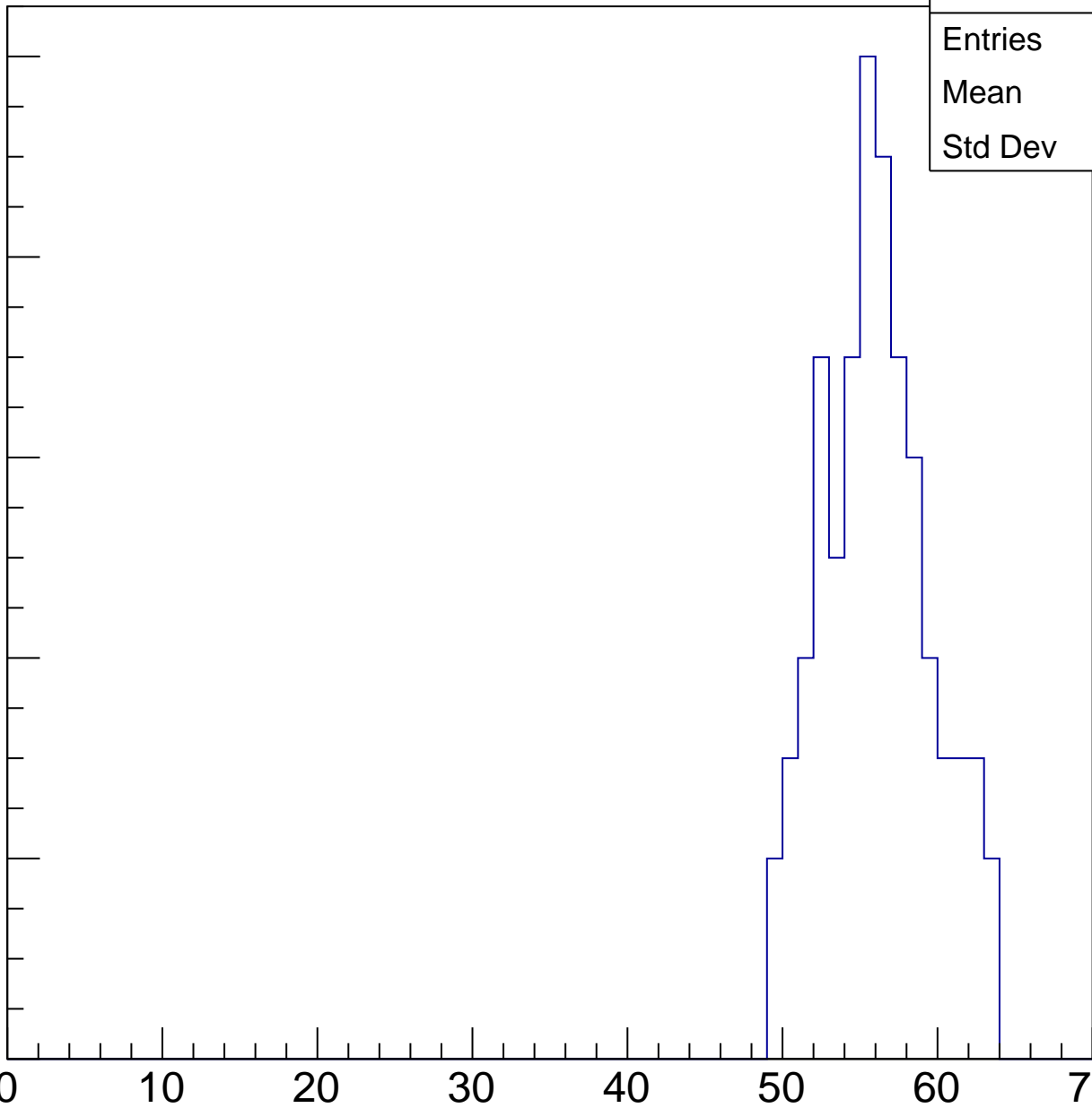
Entries	75
Mean	55.61
Std Dev	3.437

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

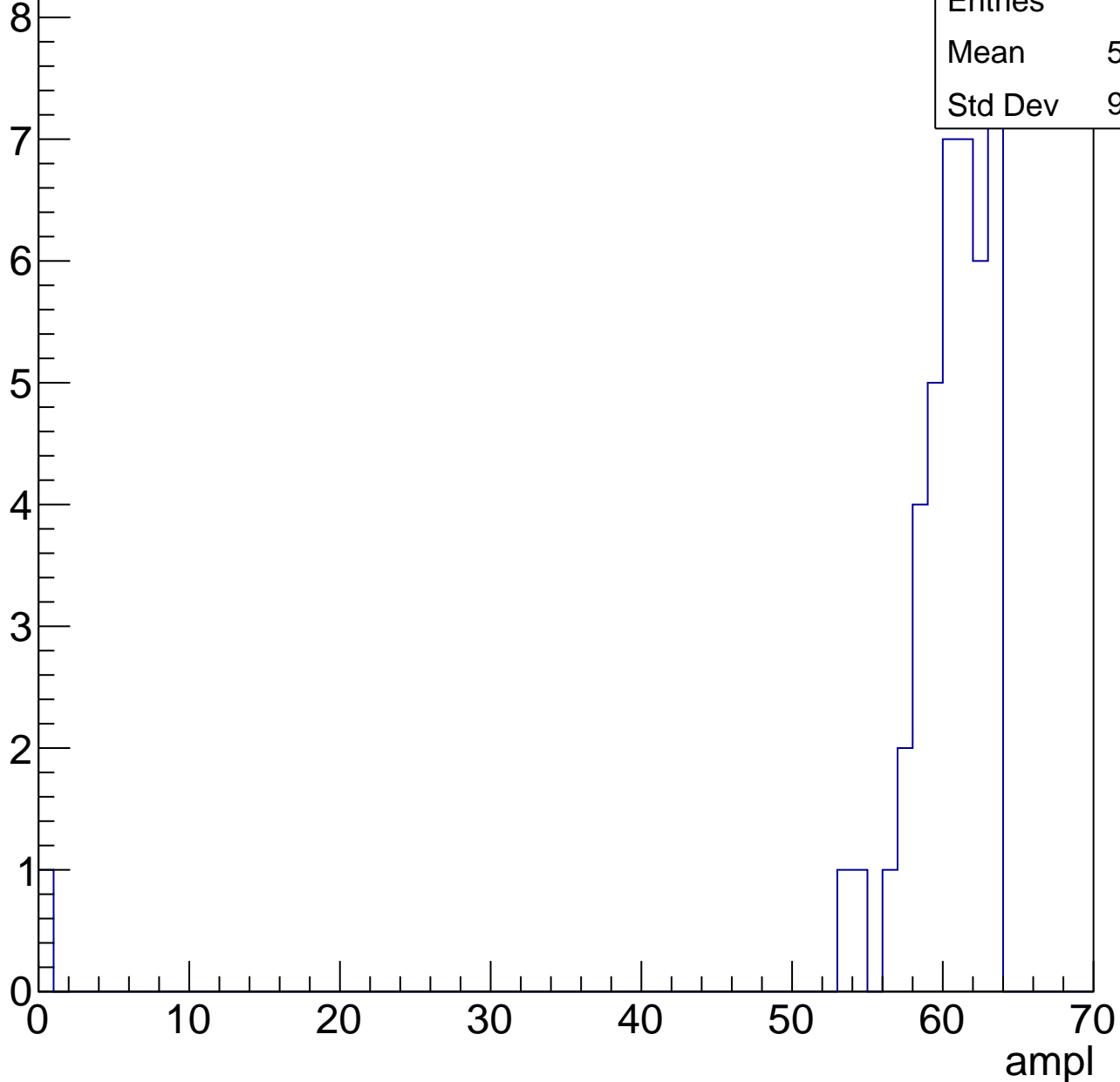


B1L101S, U12-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.77
Std Dev	9.373



B1L101S, U12-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch108, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	29.51
Std Dev	3.701

Gaus mean : 30.1050

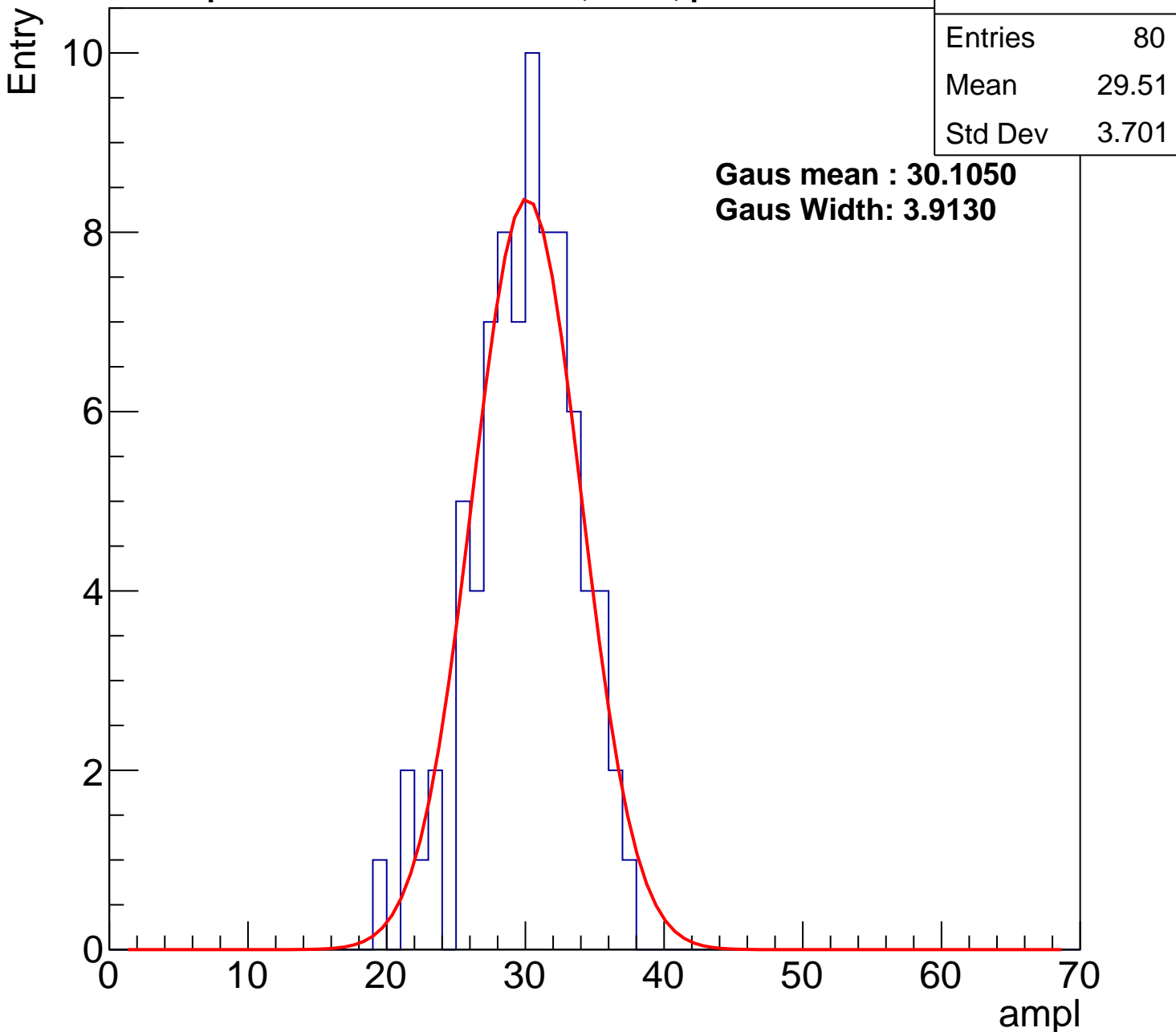
Gaus Width: 3.9130

Entry

10
8
6
4
2
0

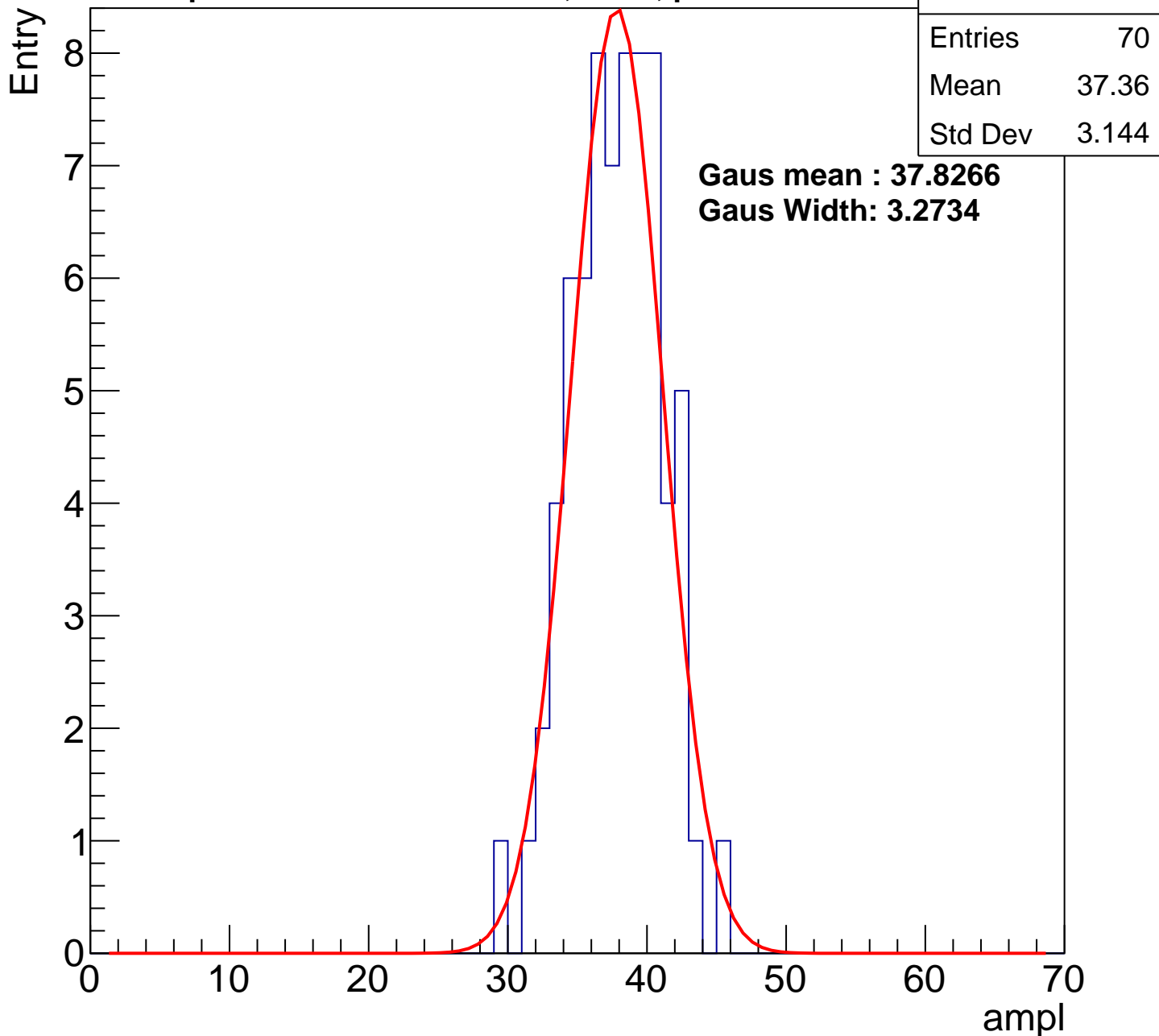
ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch108, adc1

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U12-ch108, adc2

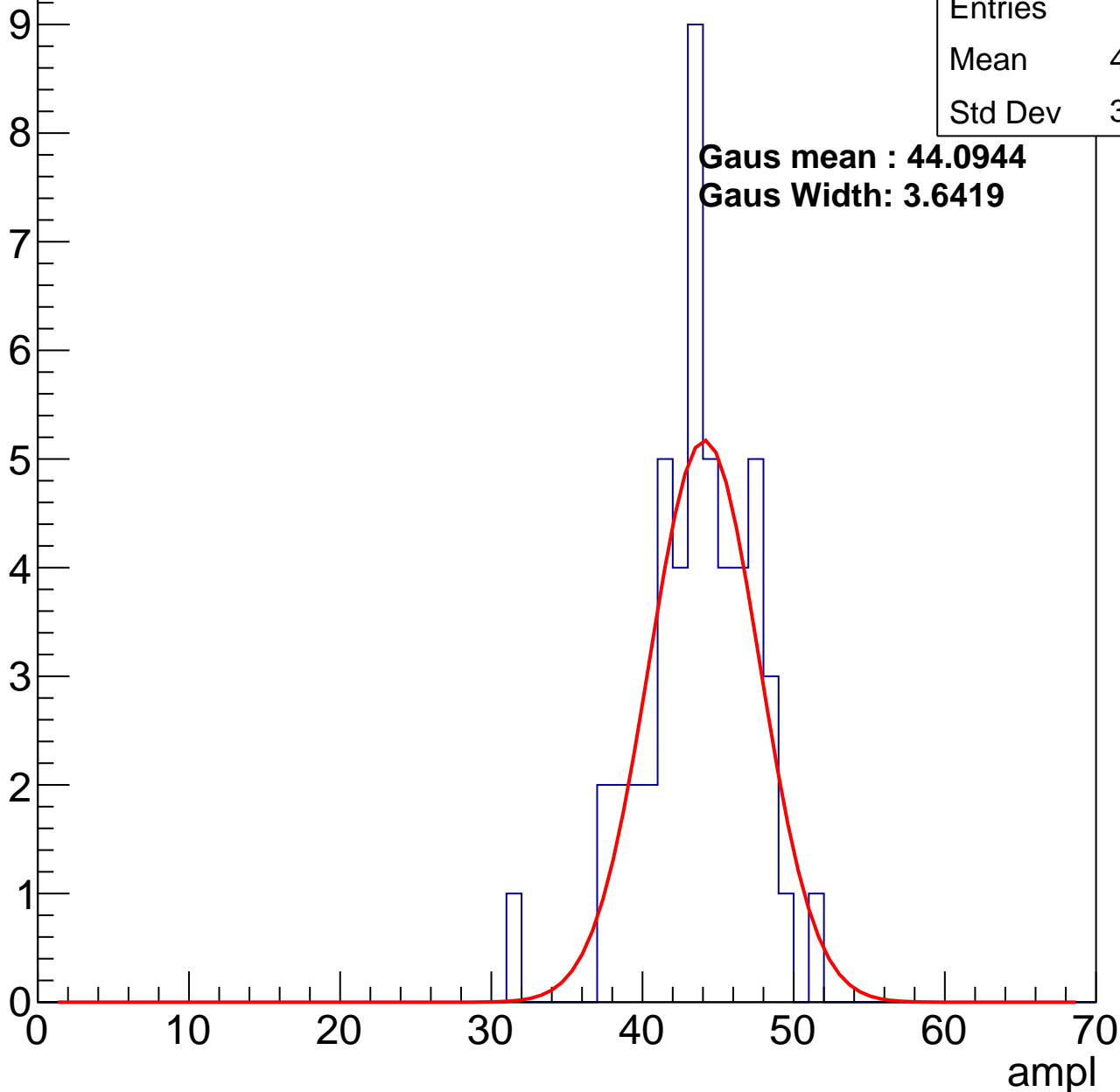
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	43.24
Std Dev	3.603

Gaus mean : 44.0944

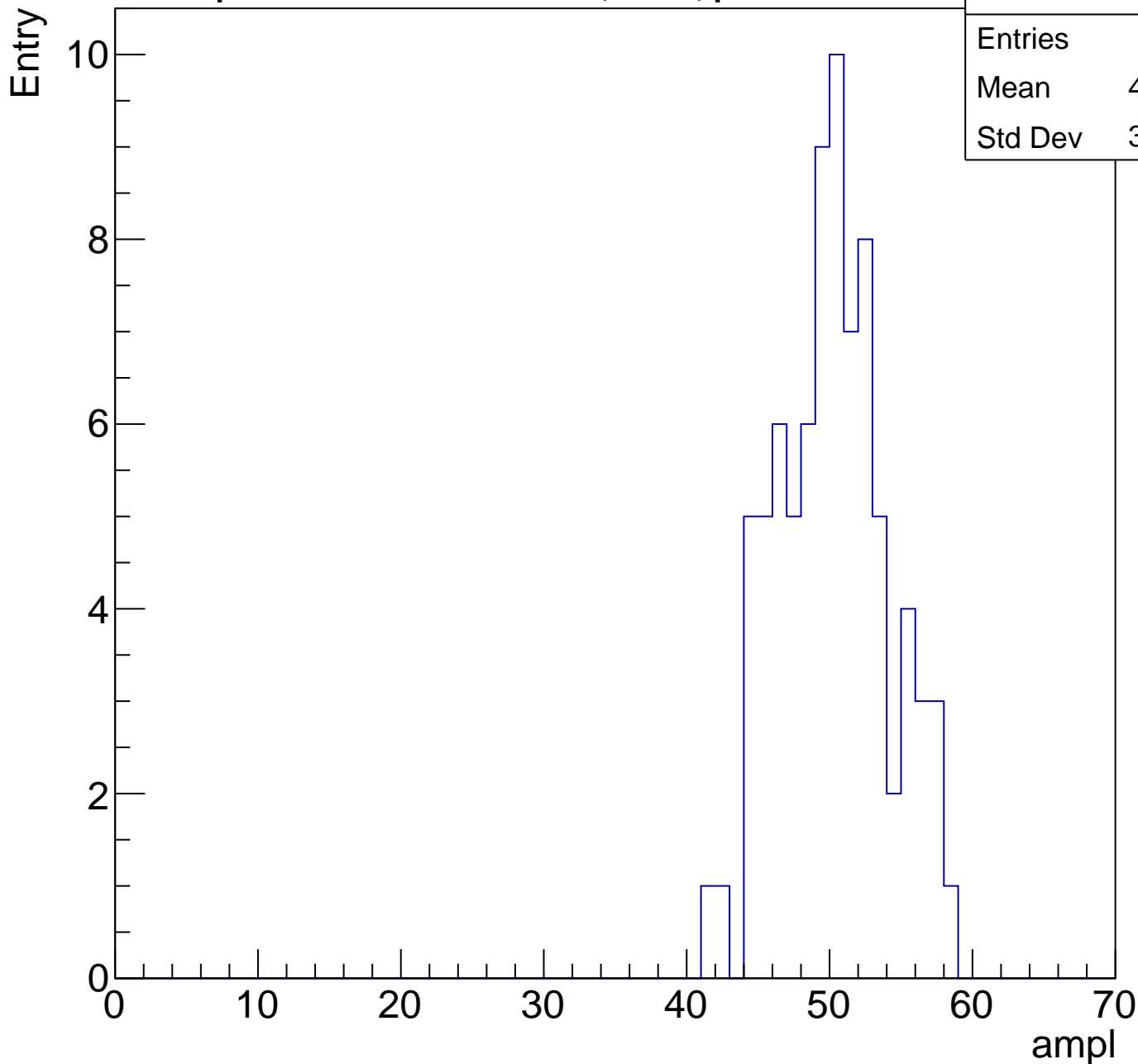
Gaus Width: 3.6419



B1L101S, U12-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	49.77
Std Dev	3.775

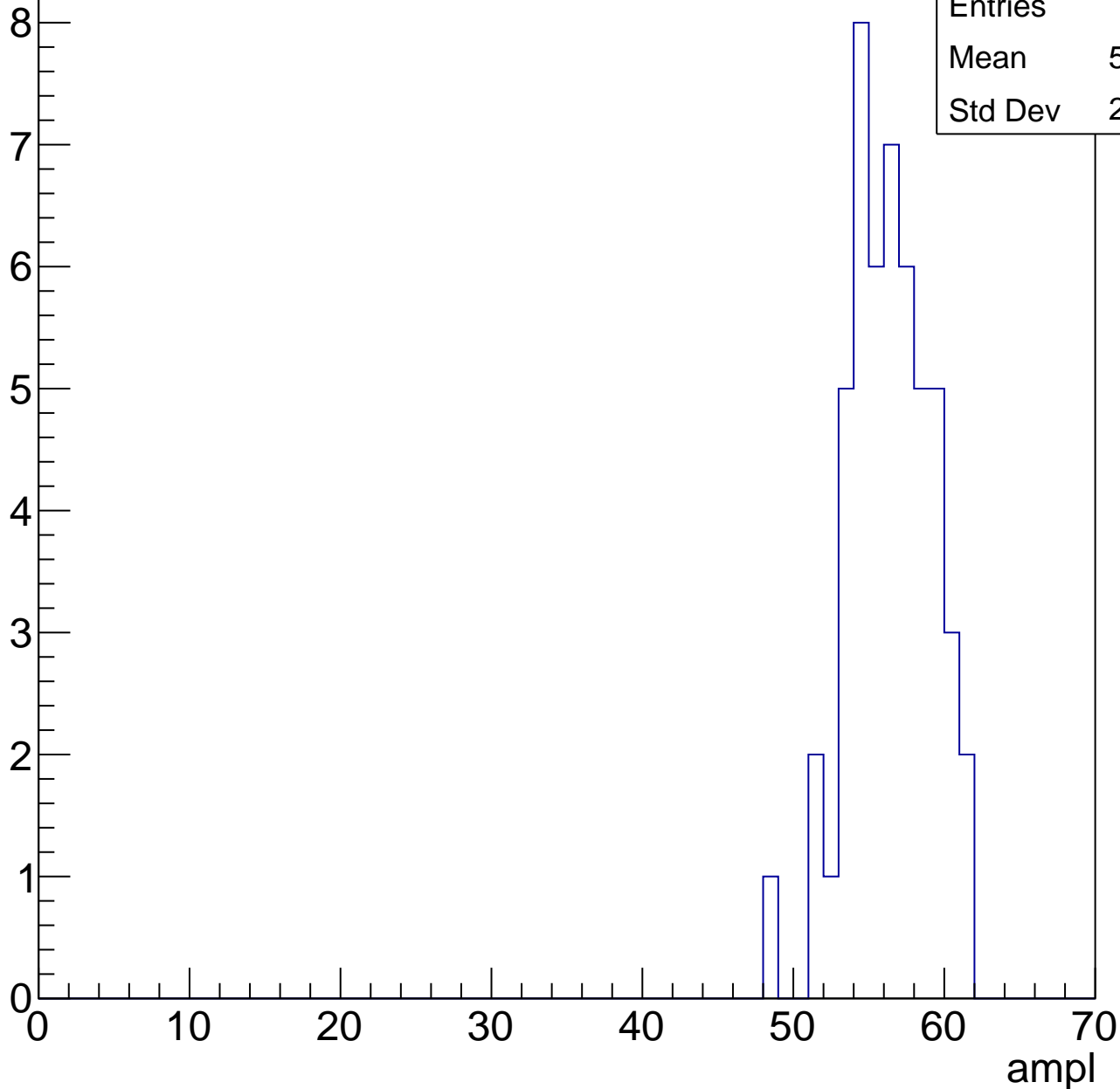


B1L101S, U12-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.88
Std Dev	2.734

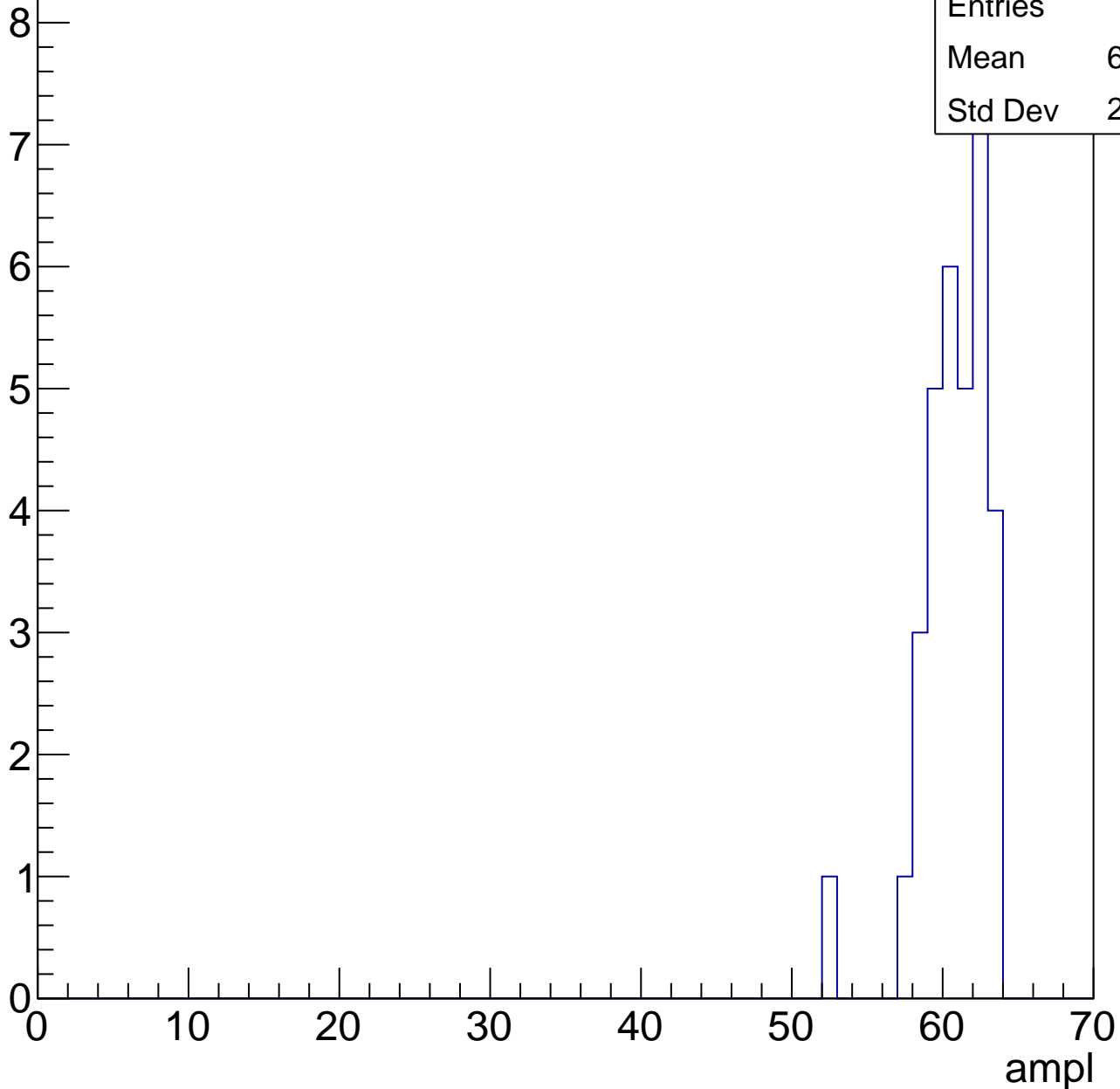


B1L101S, U12-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.33
Std Dev	2.197

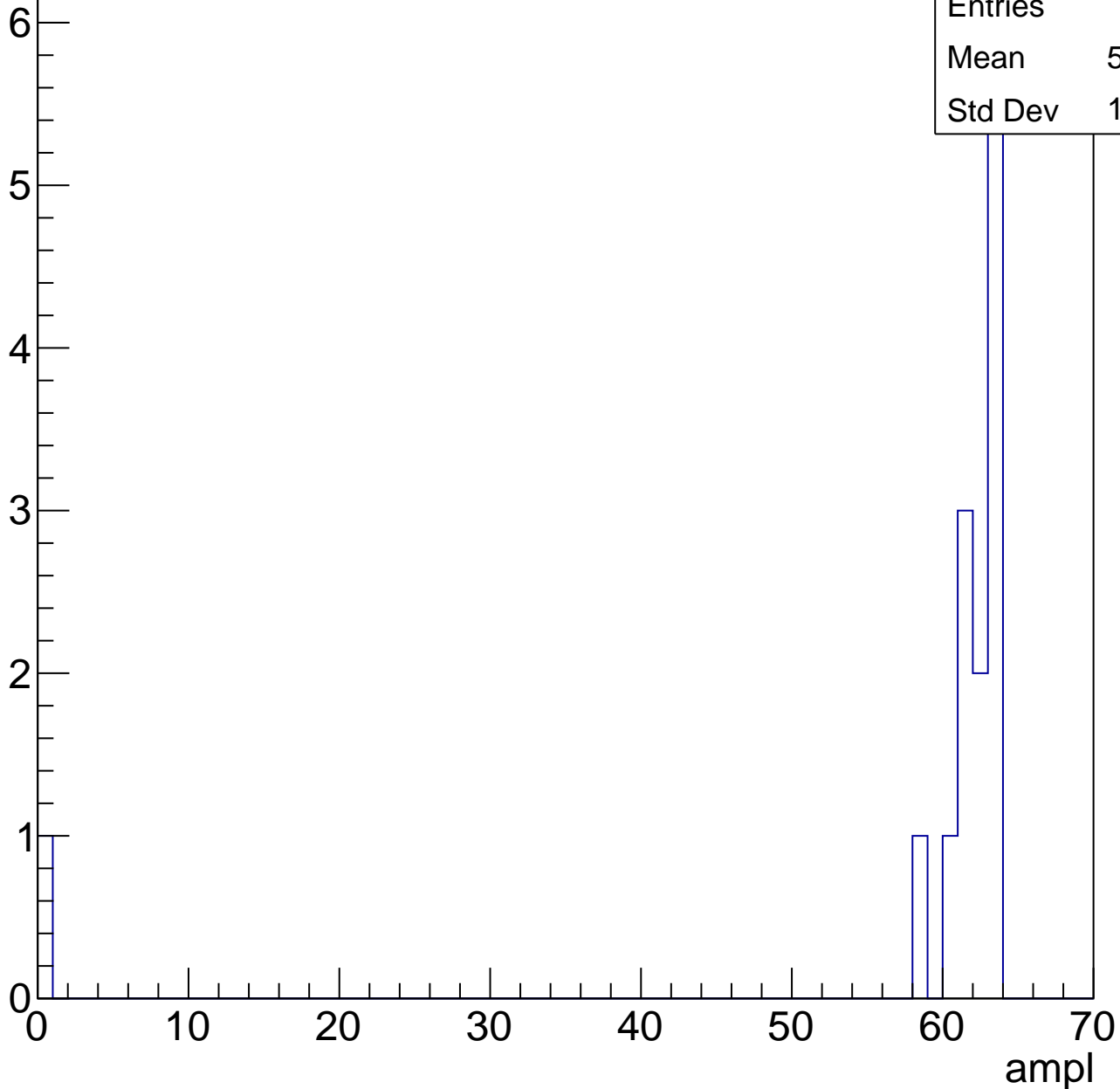


B1L101S, U12-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.36
Std Dev	15.97



B1L101S, U12-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U12-ch109, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	30.68
Std Dev	6.547

Gaus mean : 31.8271

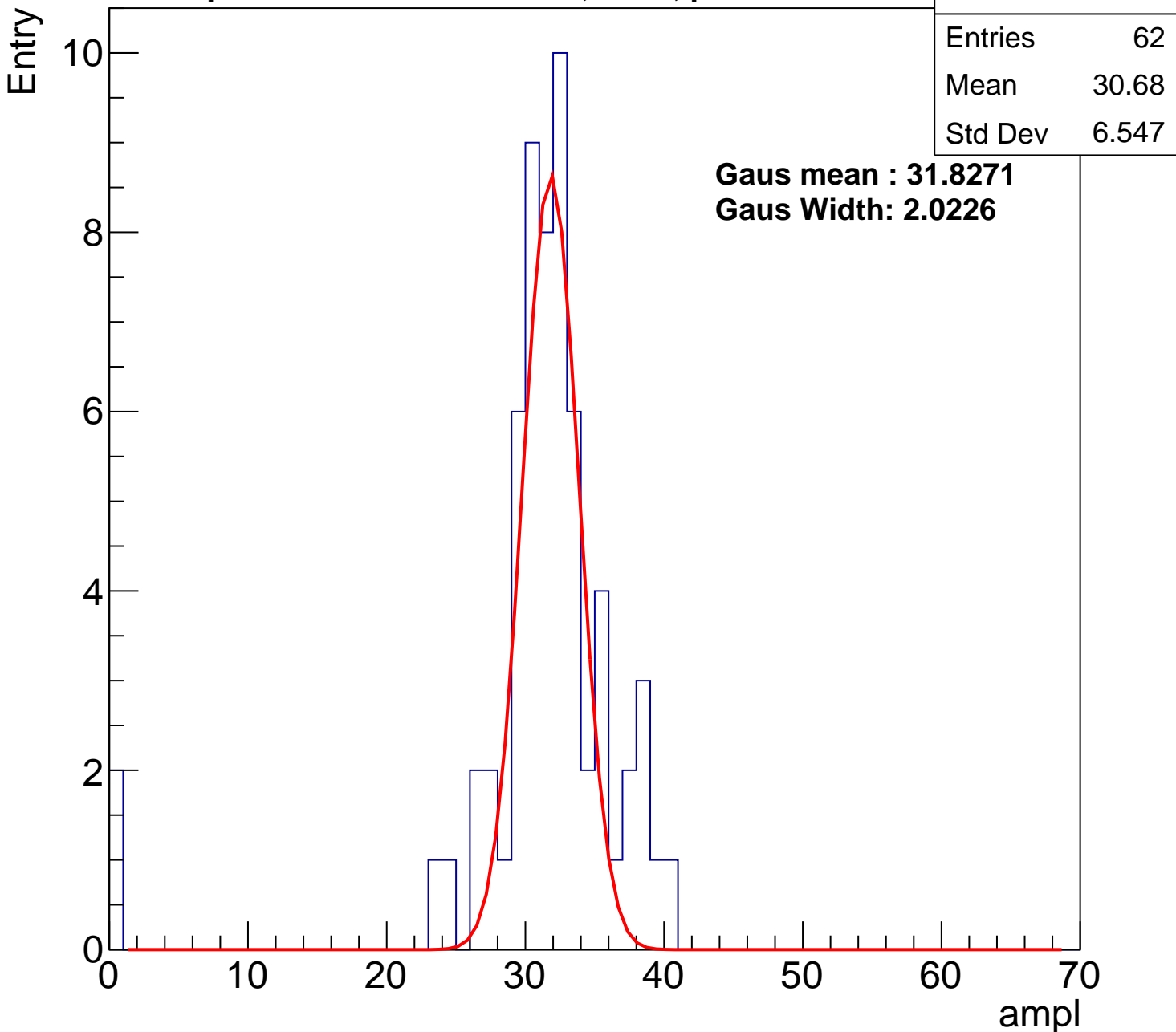
Gaus Width: 2.0226

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch109, adc1

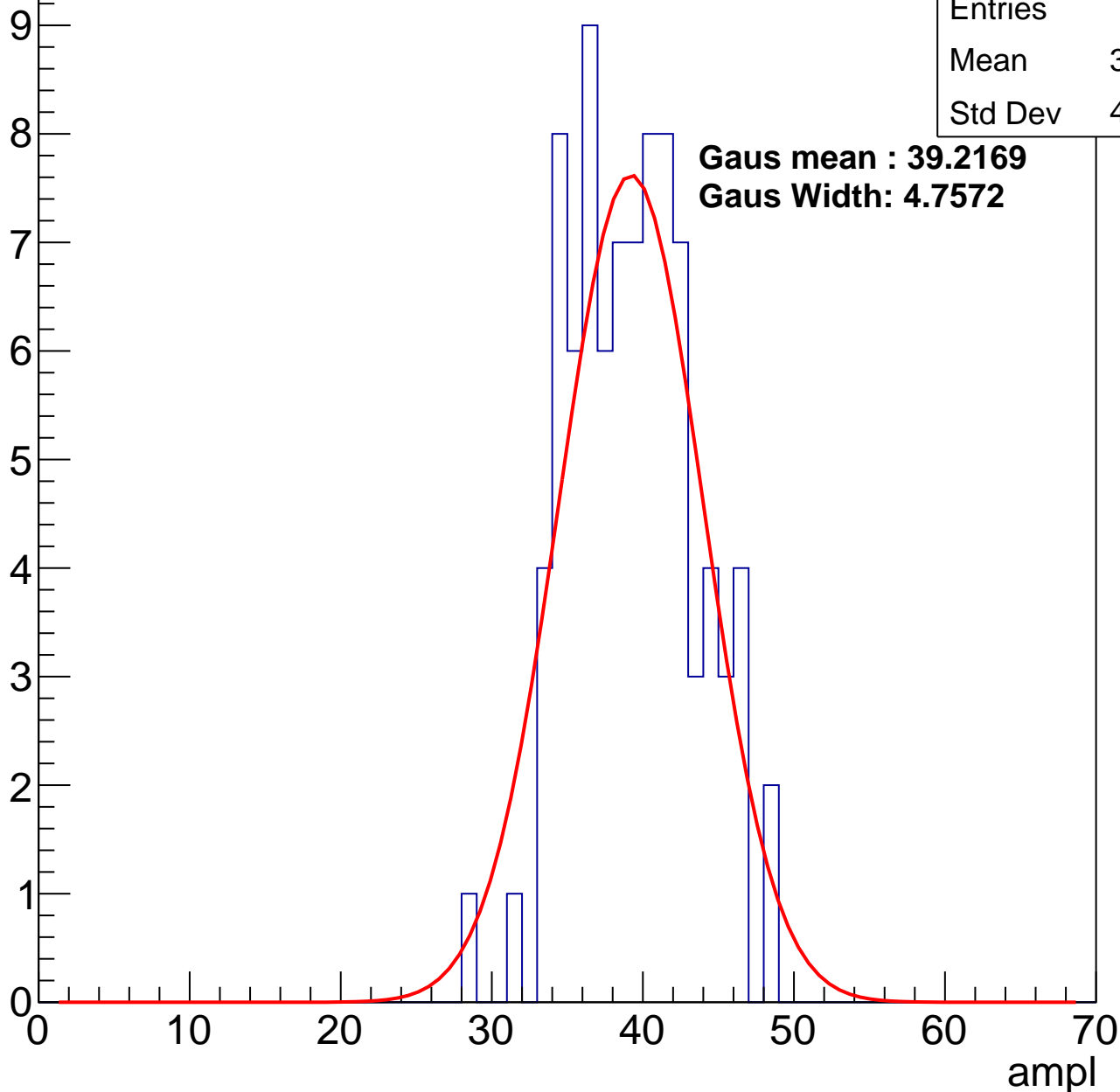
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	38.86
Std Dev	4.063

Gaus mean : 39.2169

Gaus Width: 4.7572



B1L101S, U12-ch109, adc2

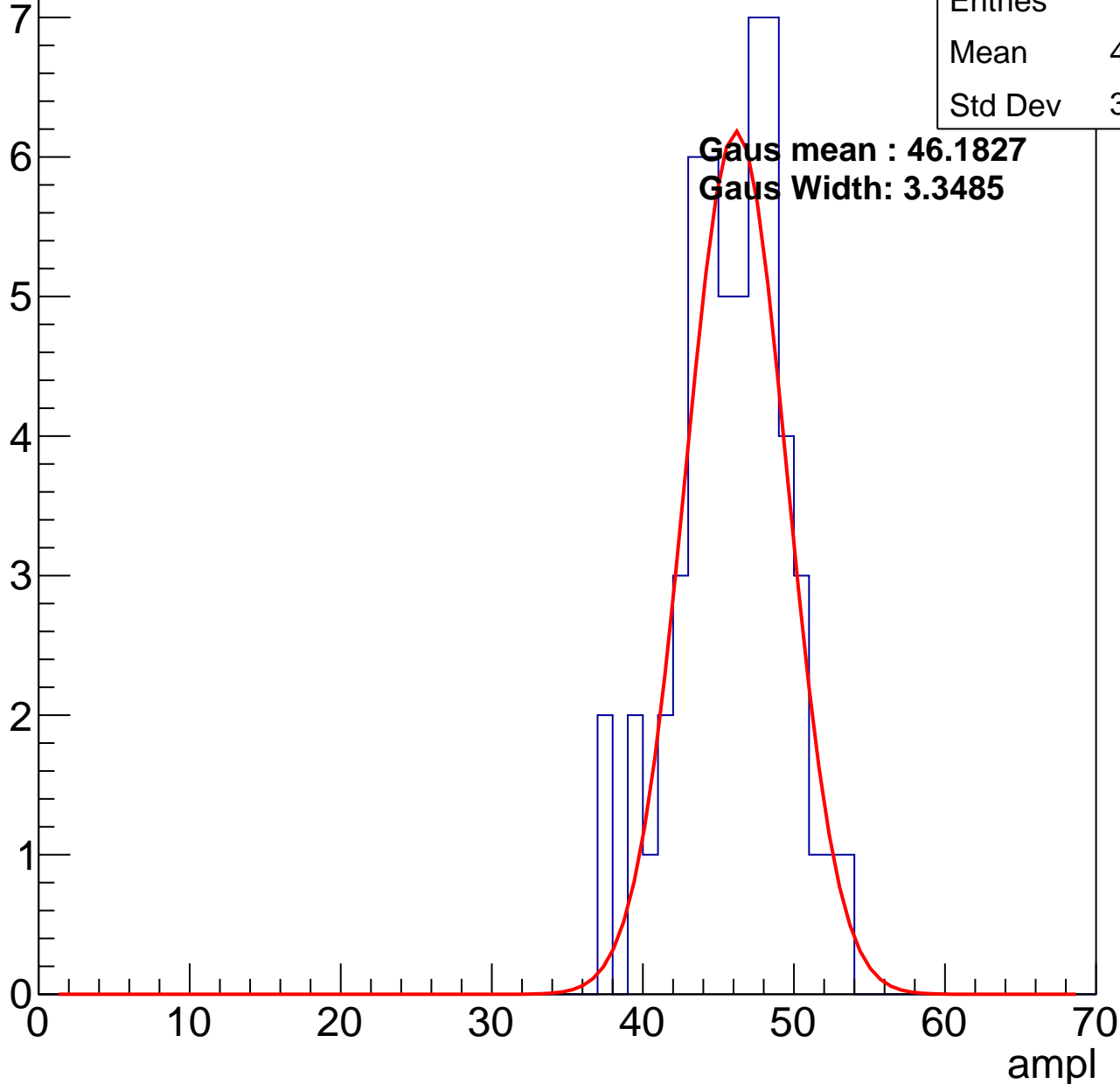
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	45.43
Std Dev	3.504

Gaus mean : 46.1827

Gaus Width: 3.3485

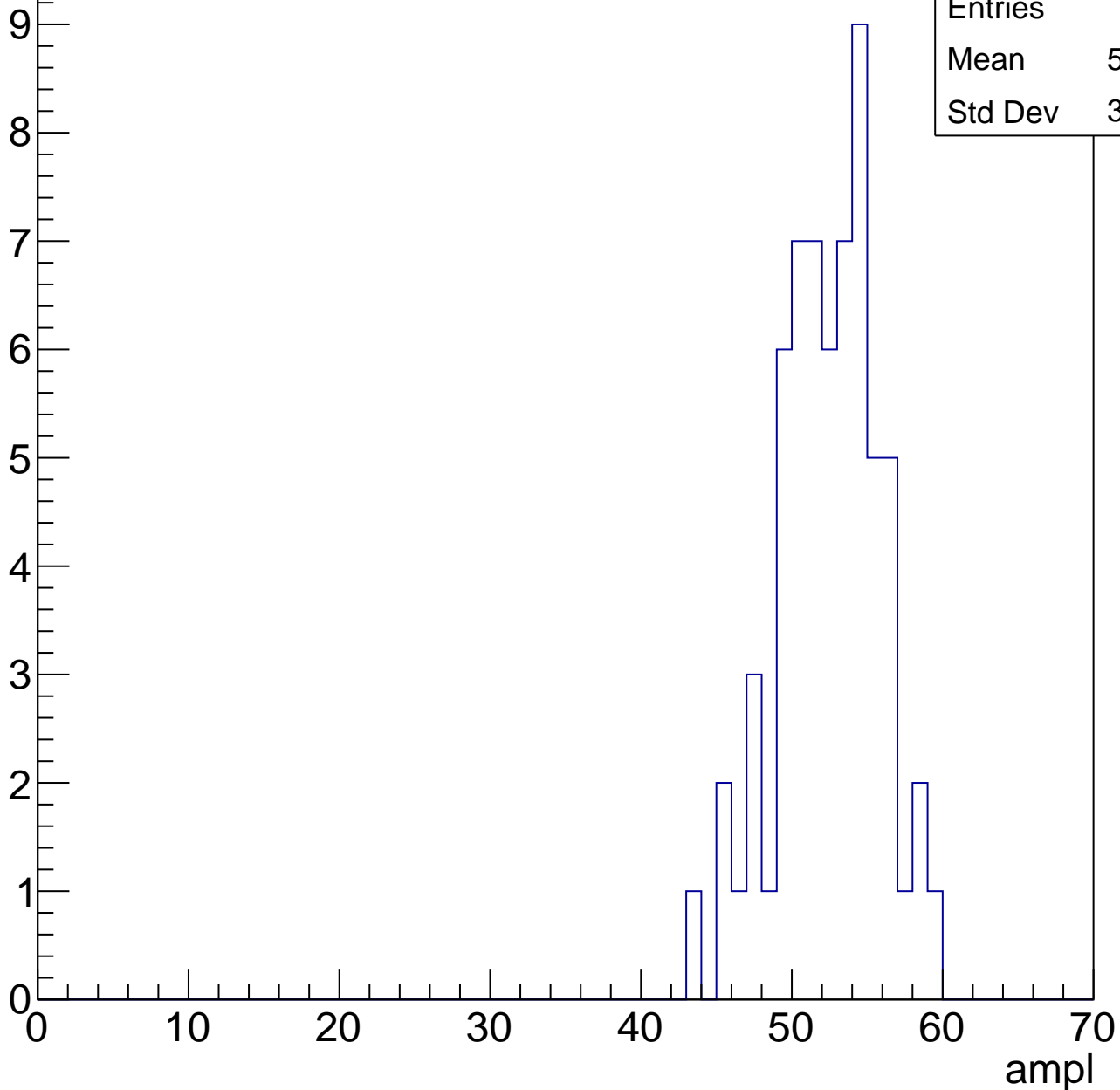


B1L101S, U12-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

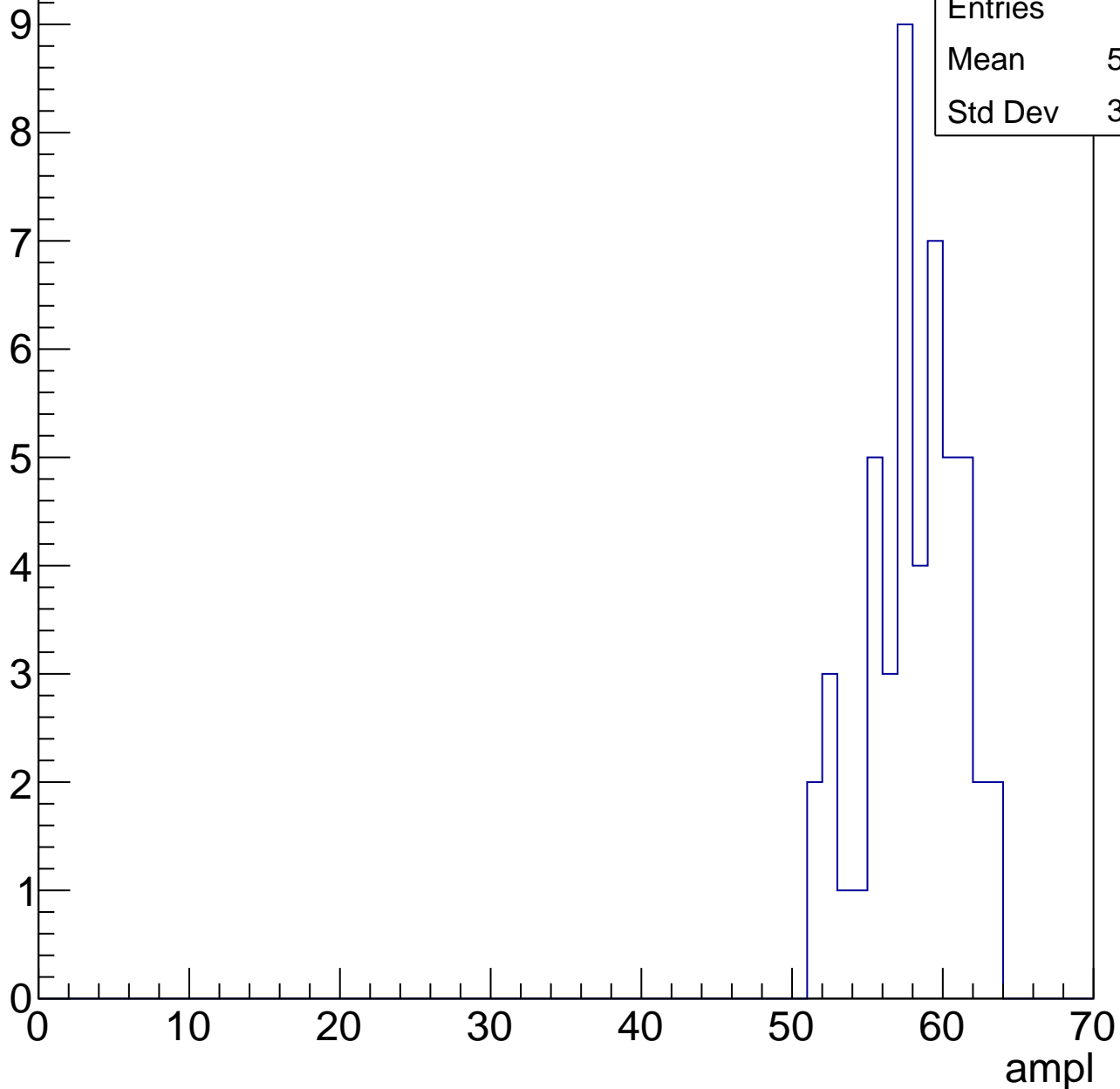
Entries	64
Mean	51.95
Std Dev	3.328



B1L101S, U12-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

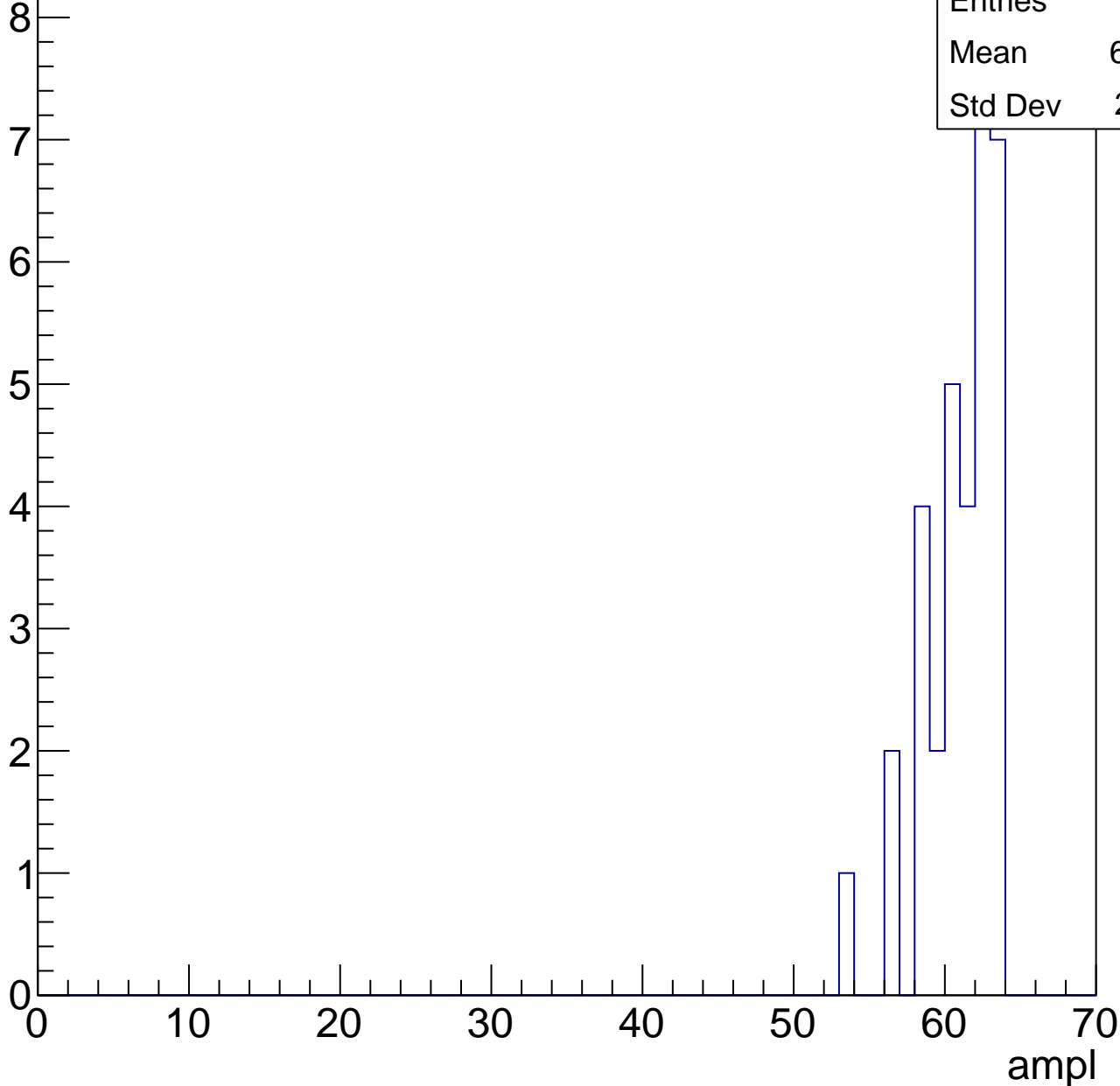


B1L101S, U12-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.48
Std Dev	2.401



B1L101S, U12-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L101S, U12-ch110, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	29.98
Std Dev	6.183

Gaus mean : 31.1167

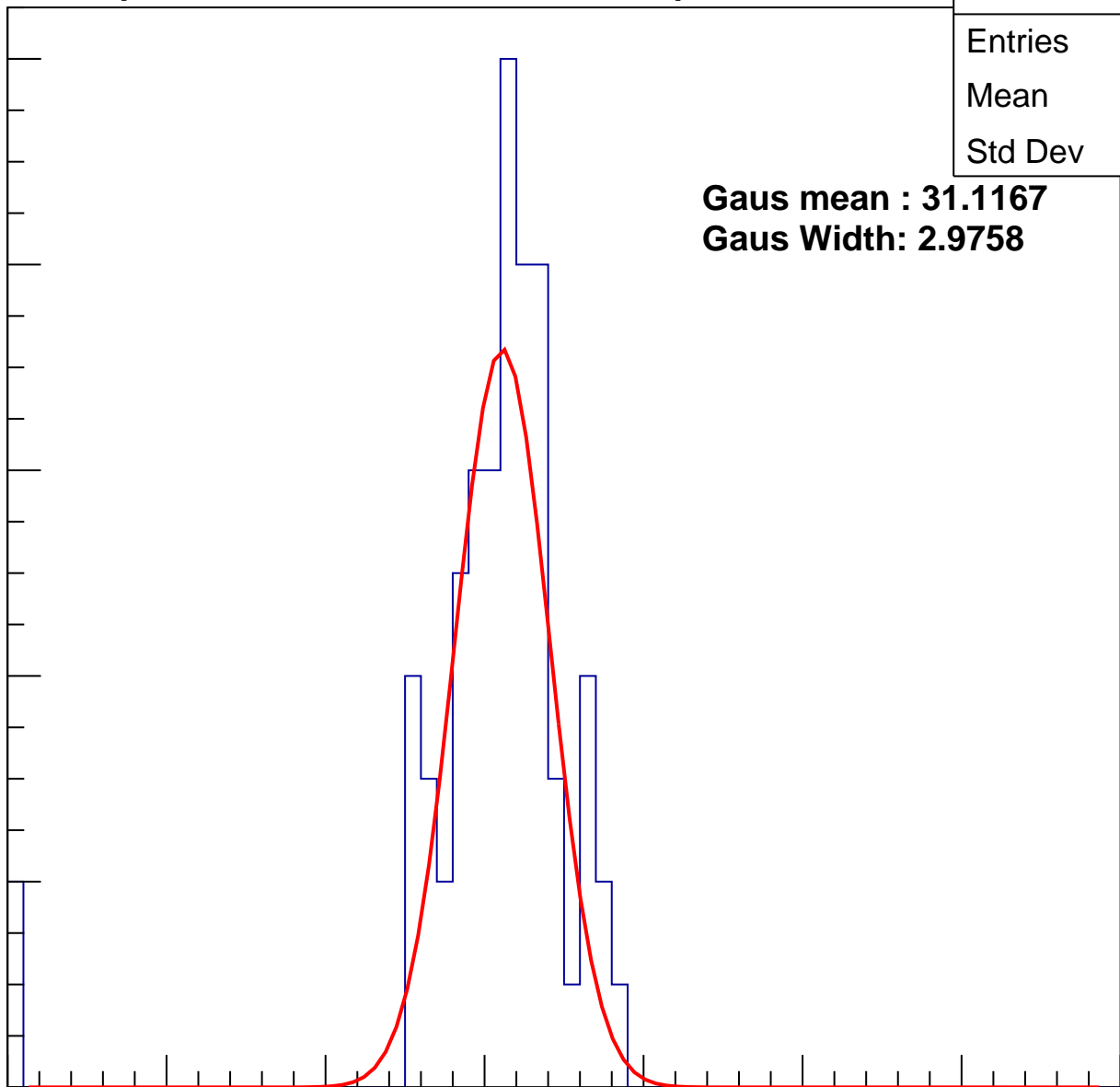
Gaus Width: 2.9758

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



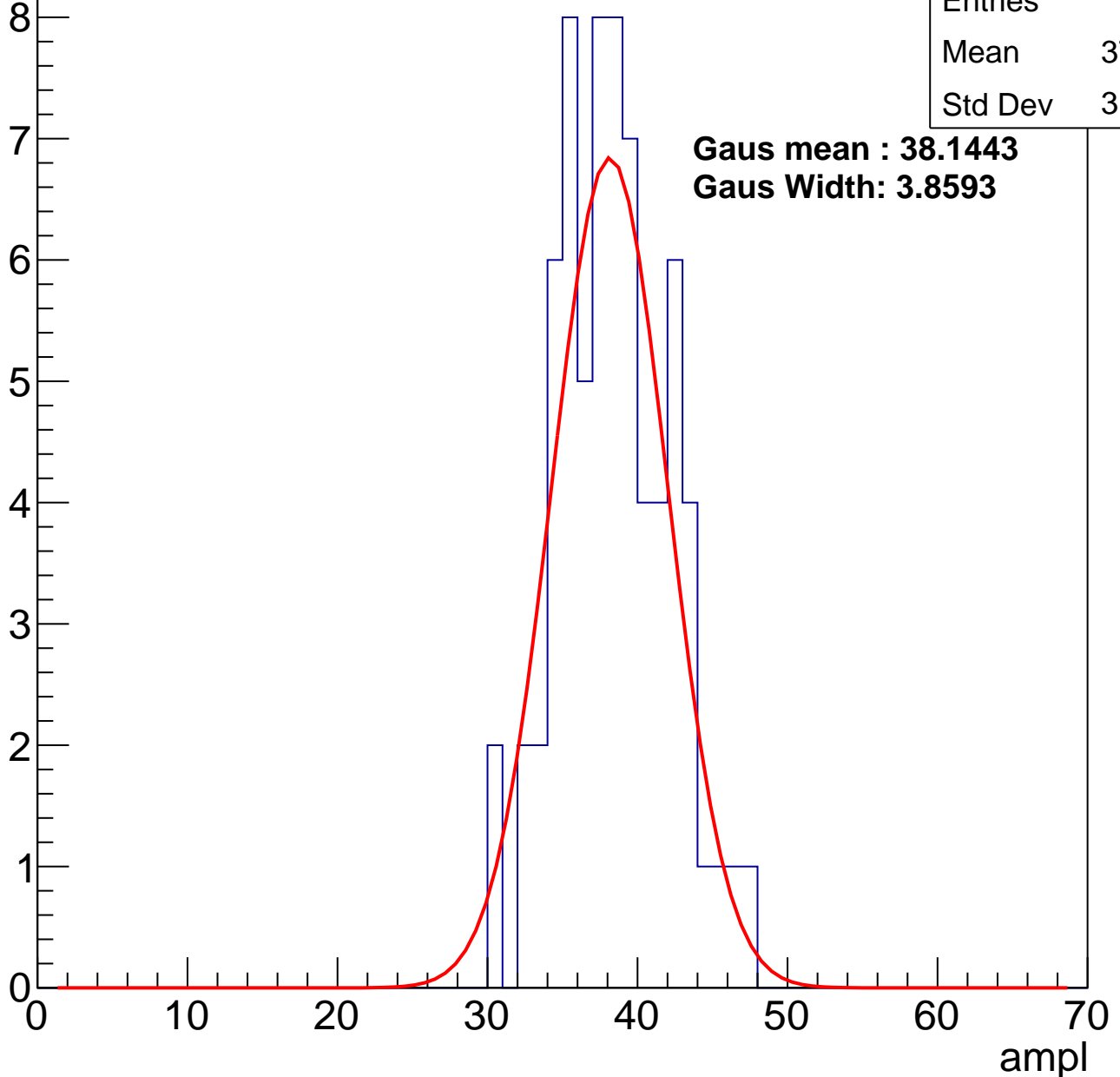
B1L101S, U12-ch110, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.96
Std Dev	3.647

Gaus mean : 38.1443
Gaus Width: 3.8593



B1L101S, U12-ch110, adc2

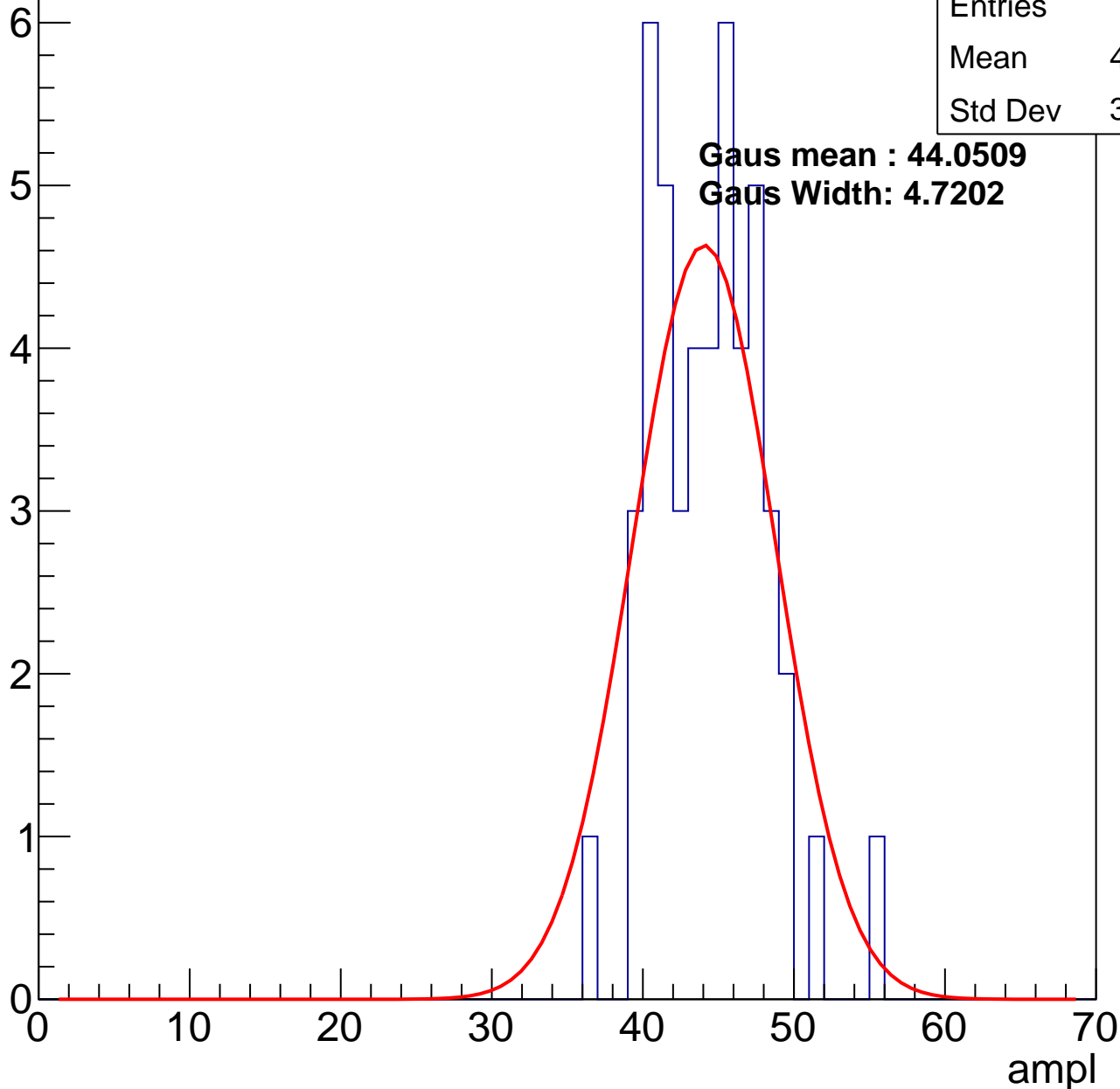
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	43.94
Std Dev	3.625

Gaus mean : 44.0509

Gaus Width: 4.7202

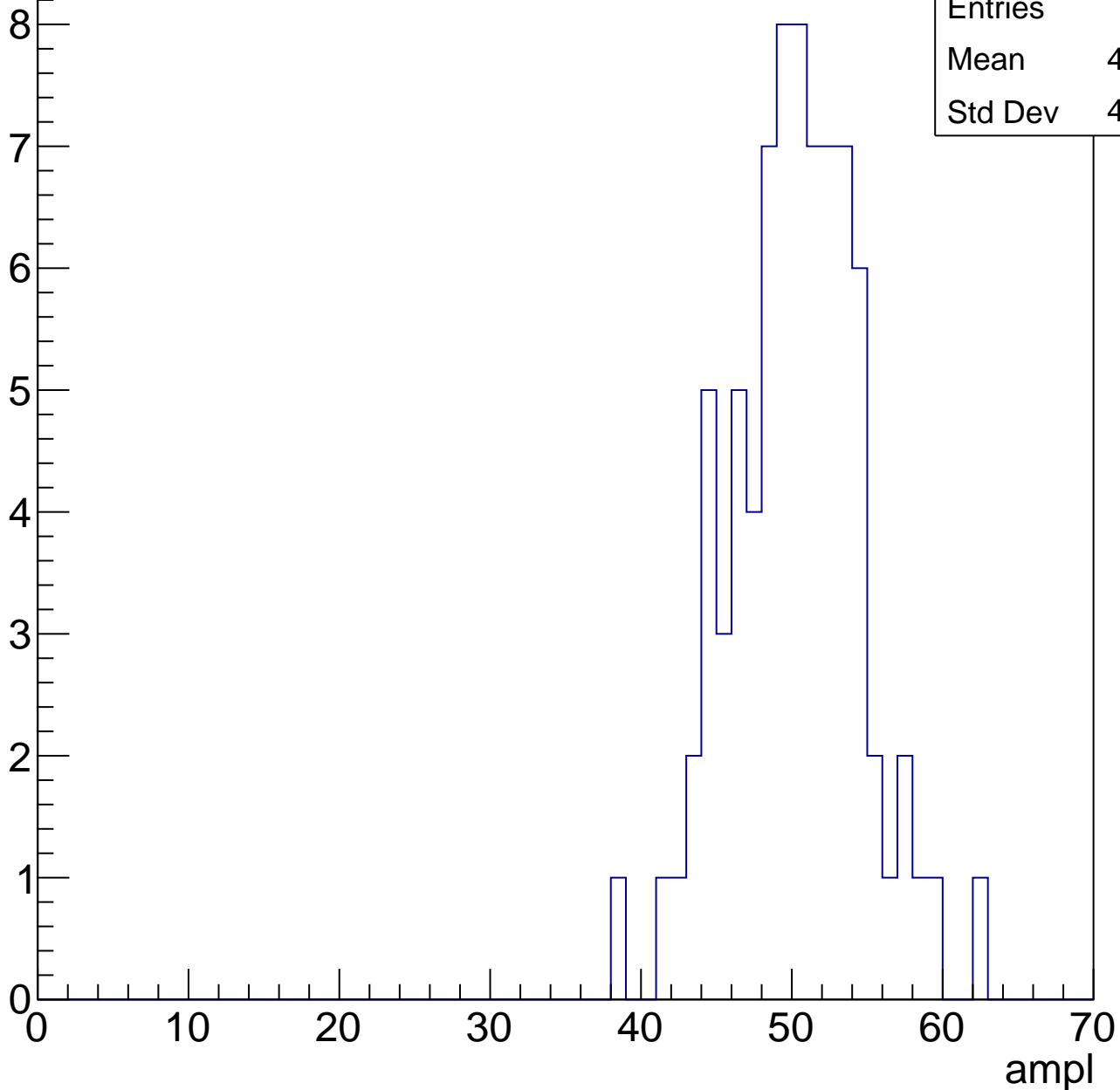


B1L101S, U12-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	49.79
Std Dev	4.268

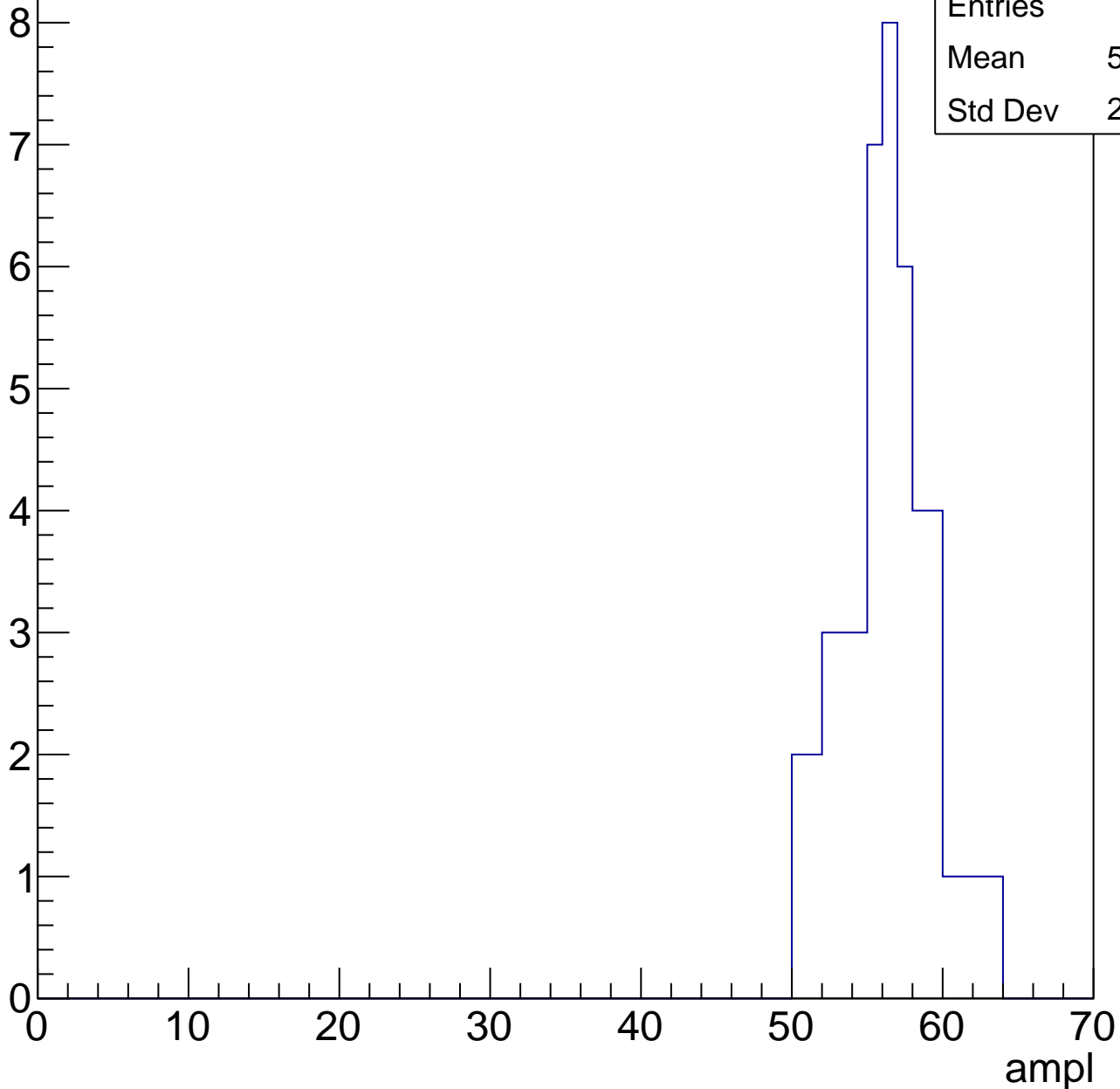


B1L101S, U12-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

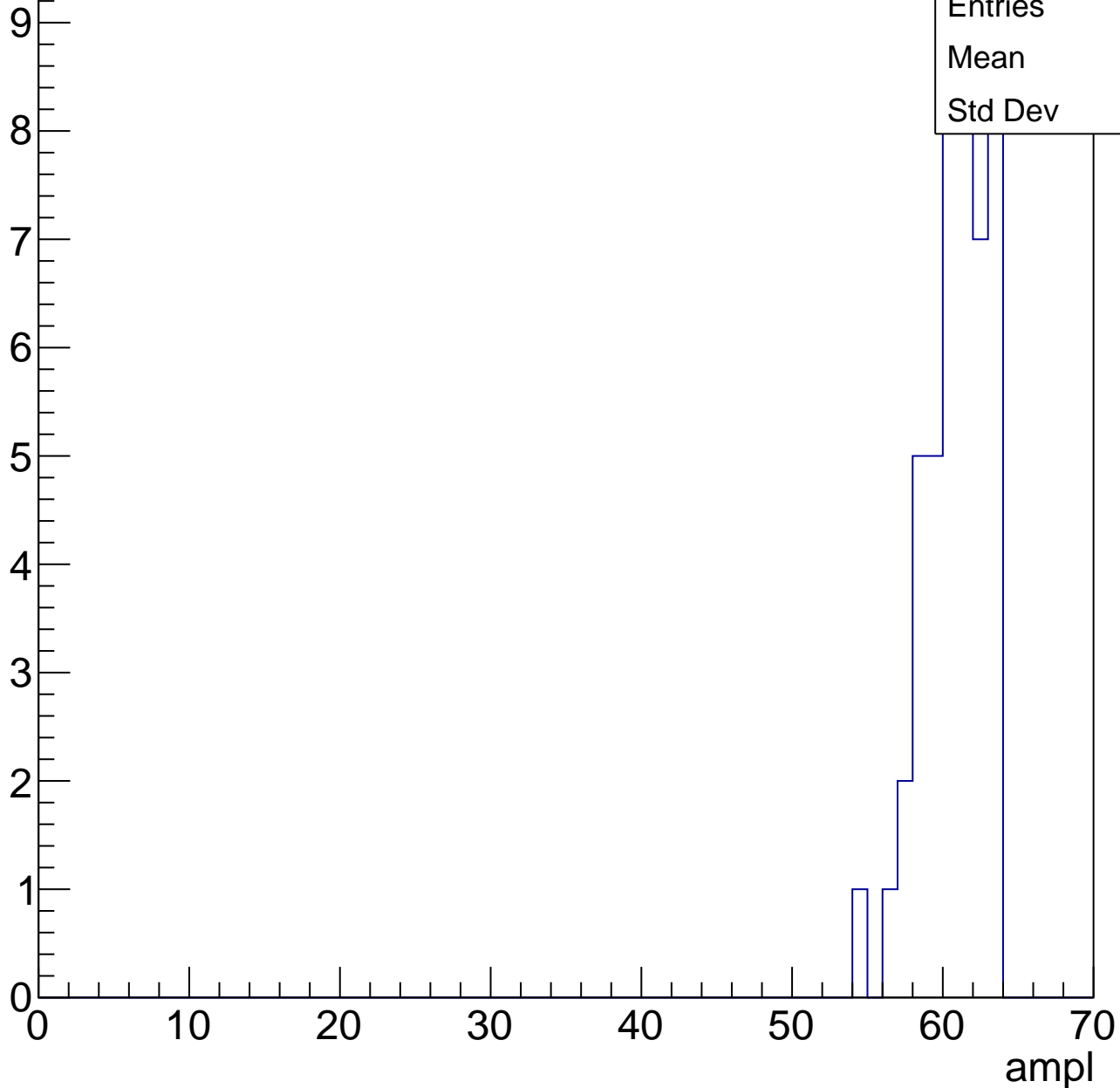
Entries	46
Mean	55.83
Std Dev	2.944



B1L101S, U12-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

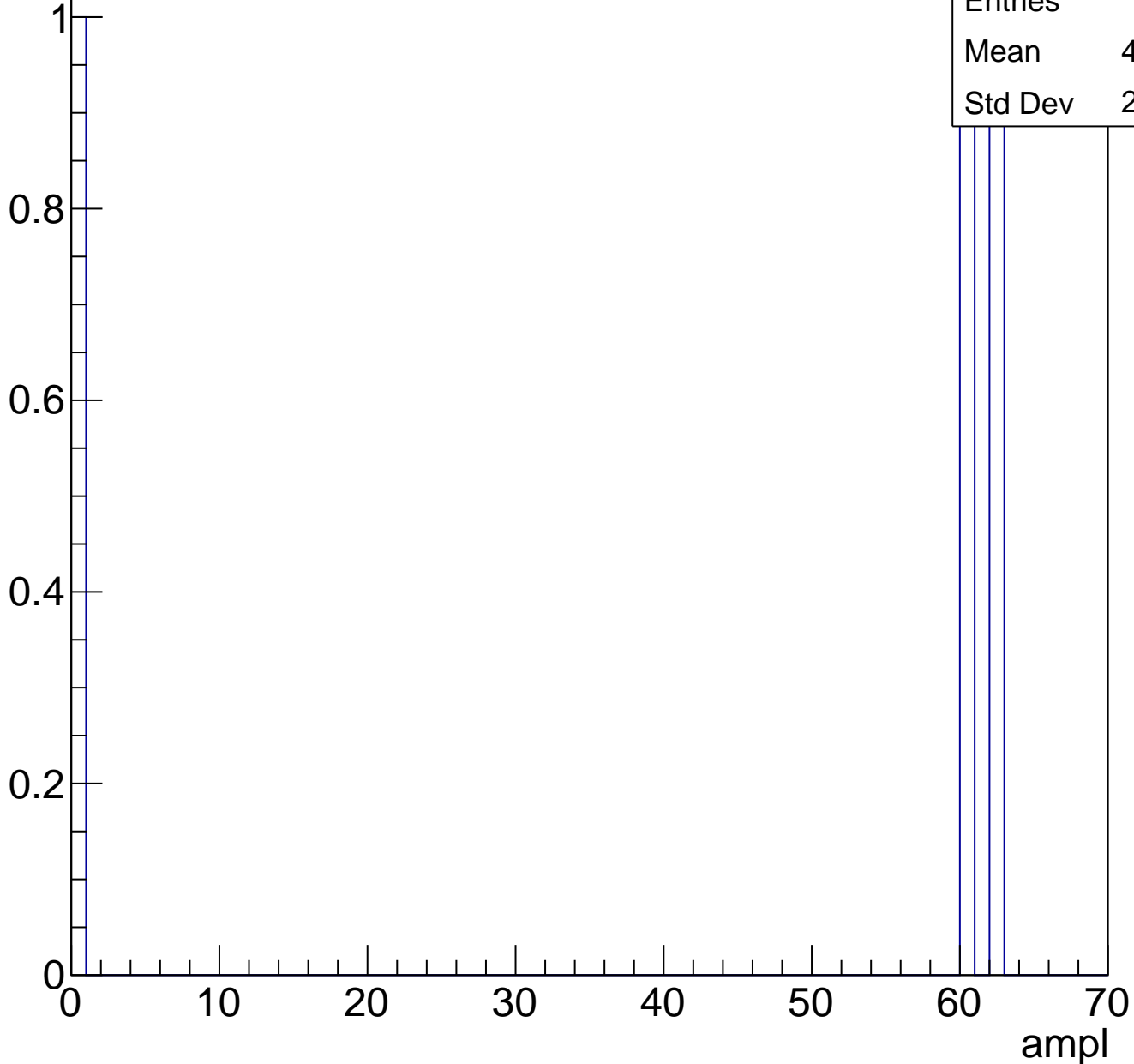
Entry



B1L101S, U12-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch111, adc0

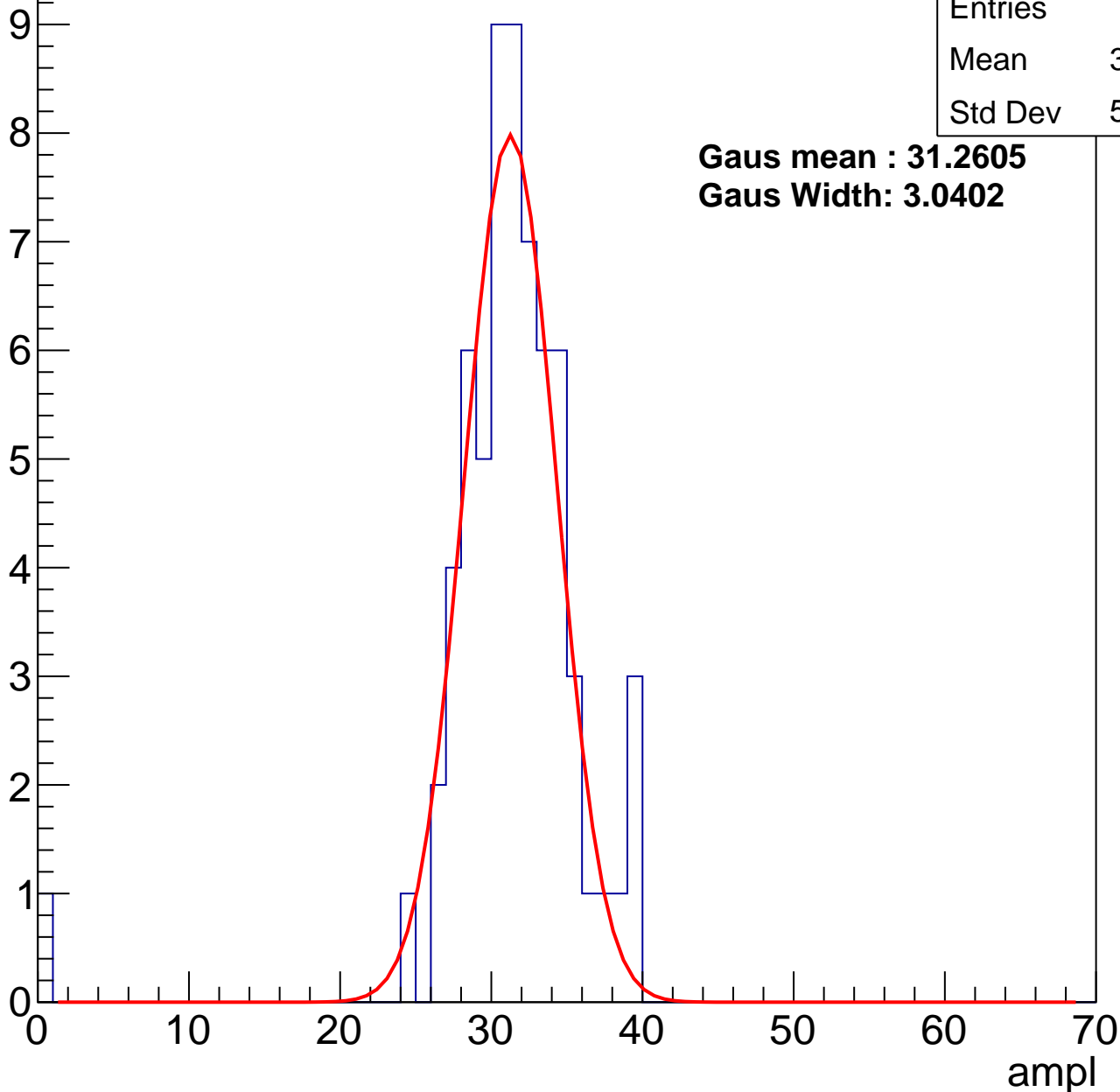
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.85
Std Dev	5.024

Gaus mean : 31.2605

Gaus Width: 3.0402



B1L101S, U12-ch111, adc1

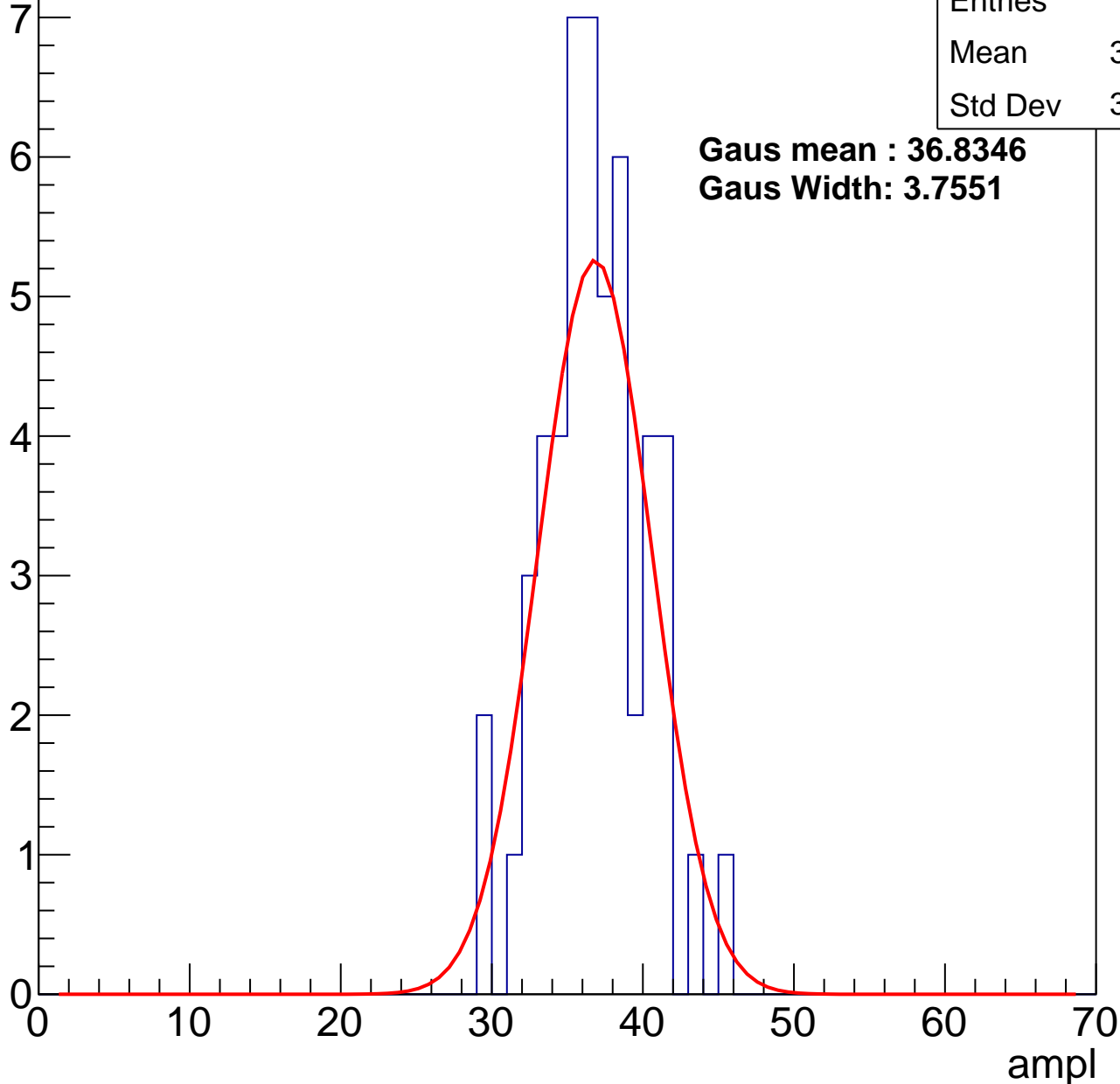
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	36.33
Std Dev	3.329

Gaus mean : 36.8346

Gaus Width: 3.7551



B1L101S, U12-ch111, adc2

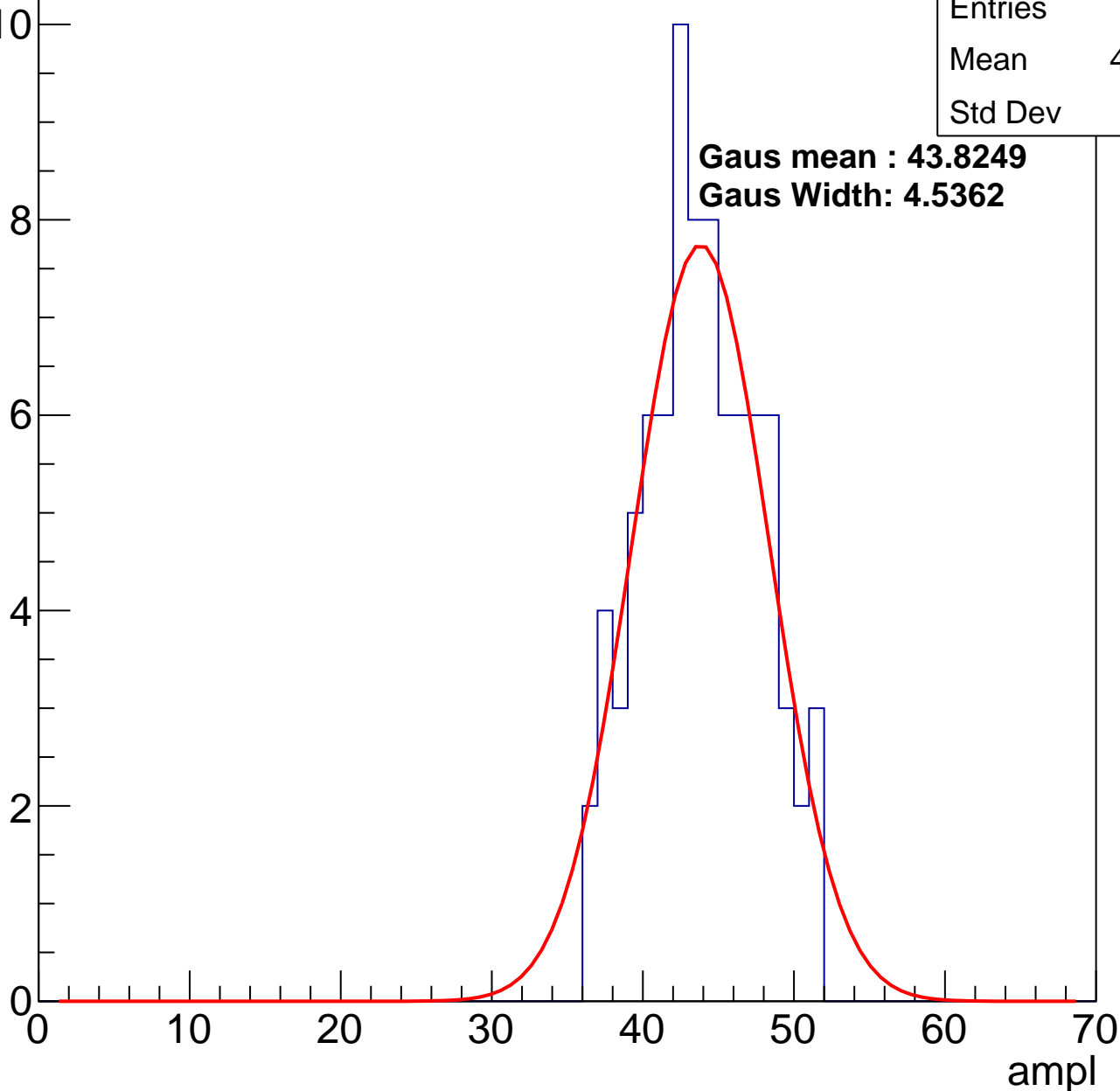
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	43.42
Std Dev	3.78

Gaus mean : 43.8249

Gaus Width: 4.5362

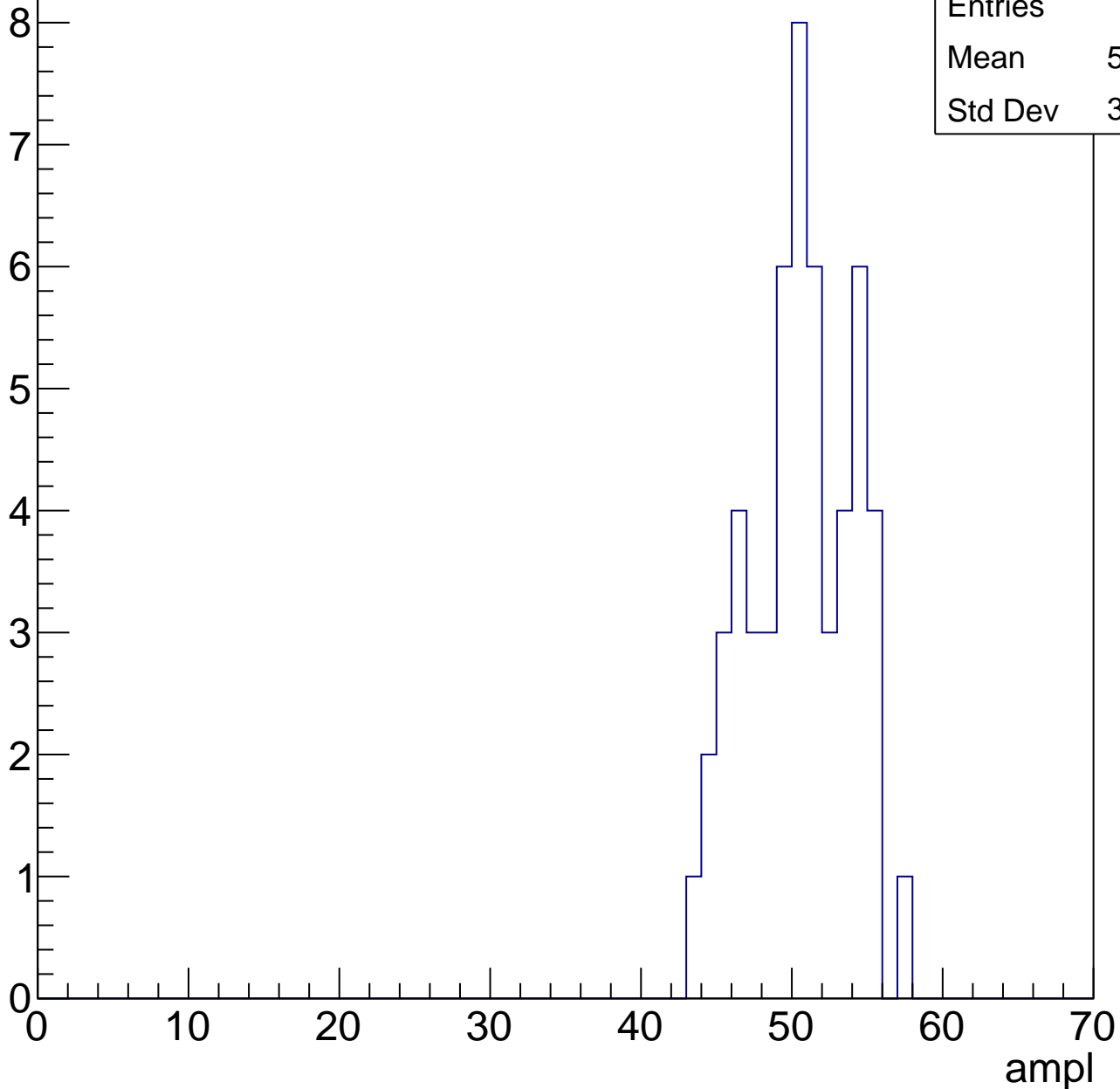


B1L101S, U12-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	50.07
Std Dev	3.344

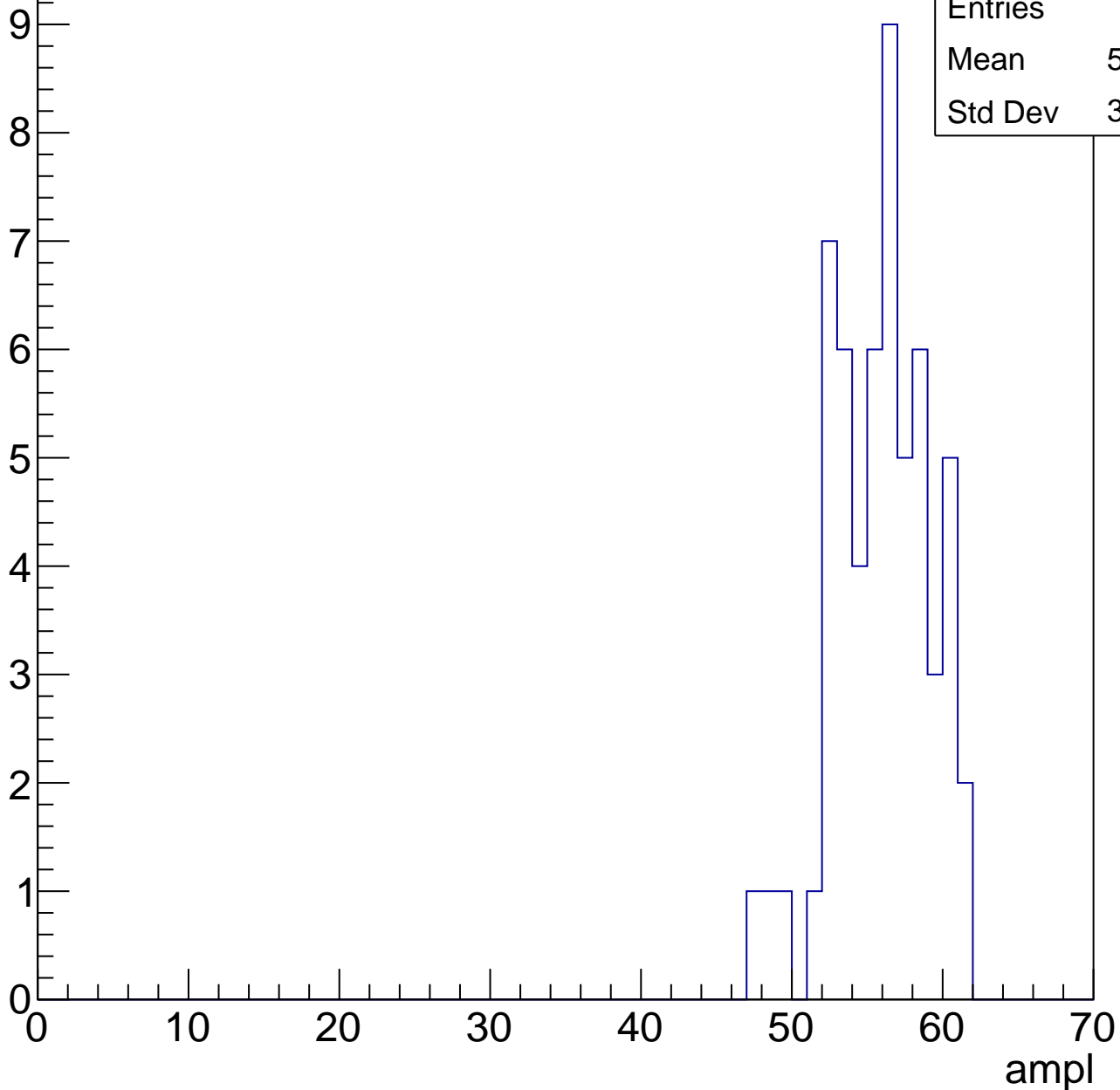


B1L101S, U12-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	55.42
Std Dev	3.173

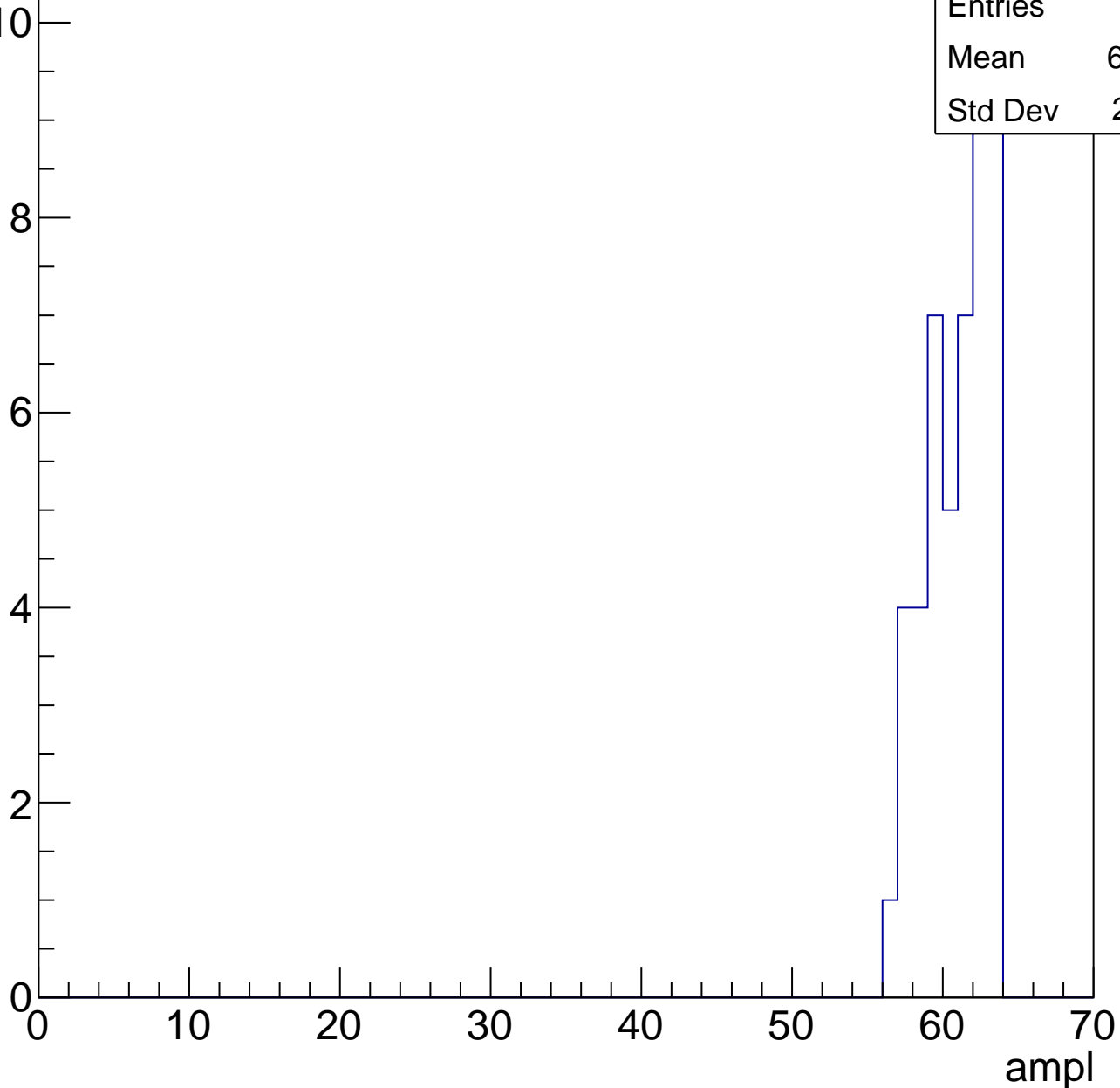


B1L101S, U12-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

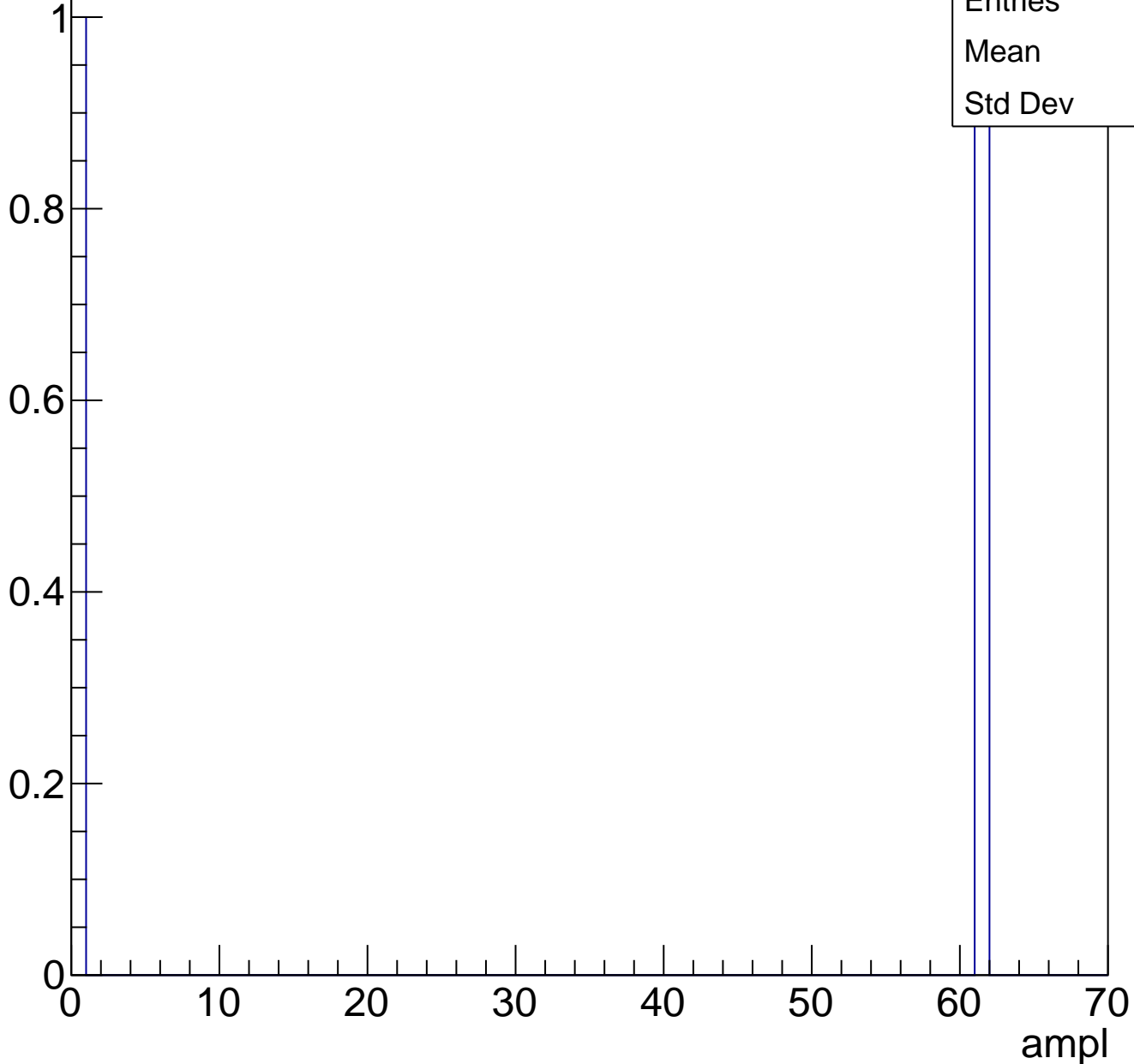
Entries	48
Mean	60.54
Std Dev	2.031



B1L101S, U12-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch112, adc0

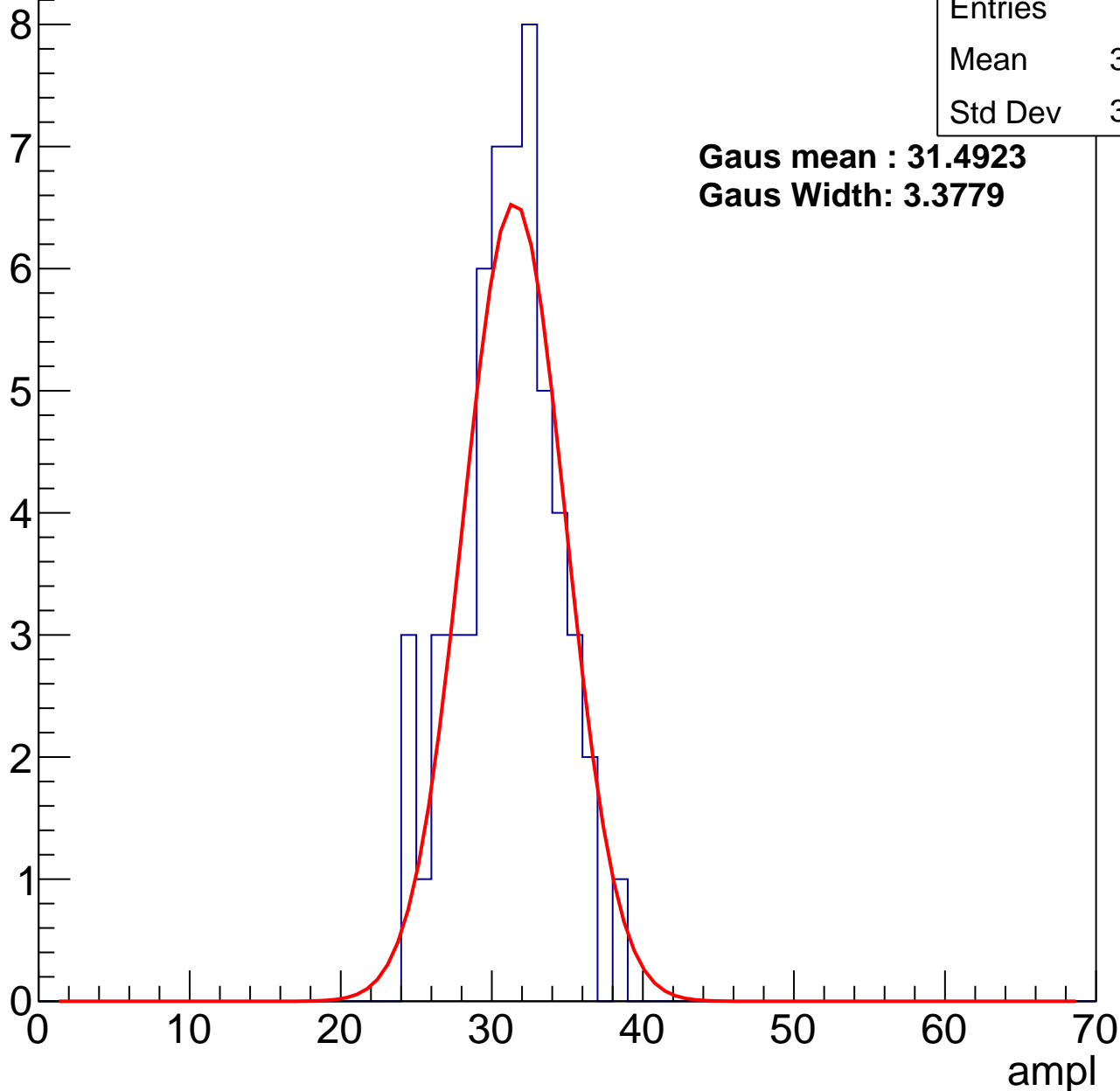
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	30.59
Std Dev	3.178

Gaus mean : 31.4923

Gaus Width: 3.3779



B1L101S, U12-ch112, adc1

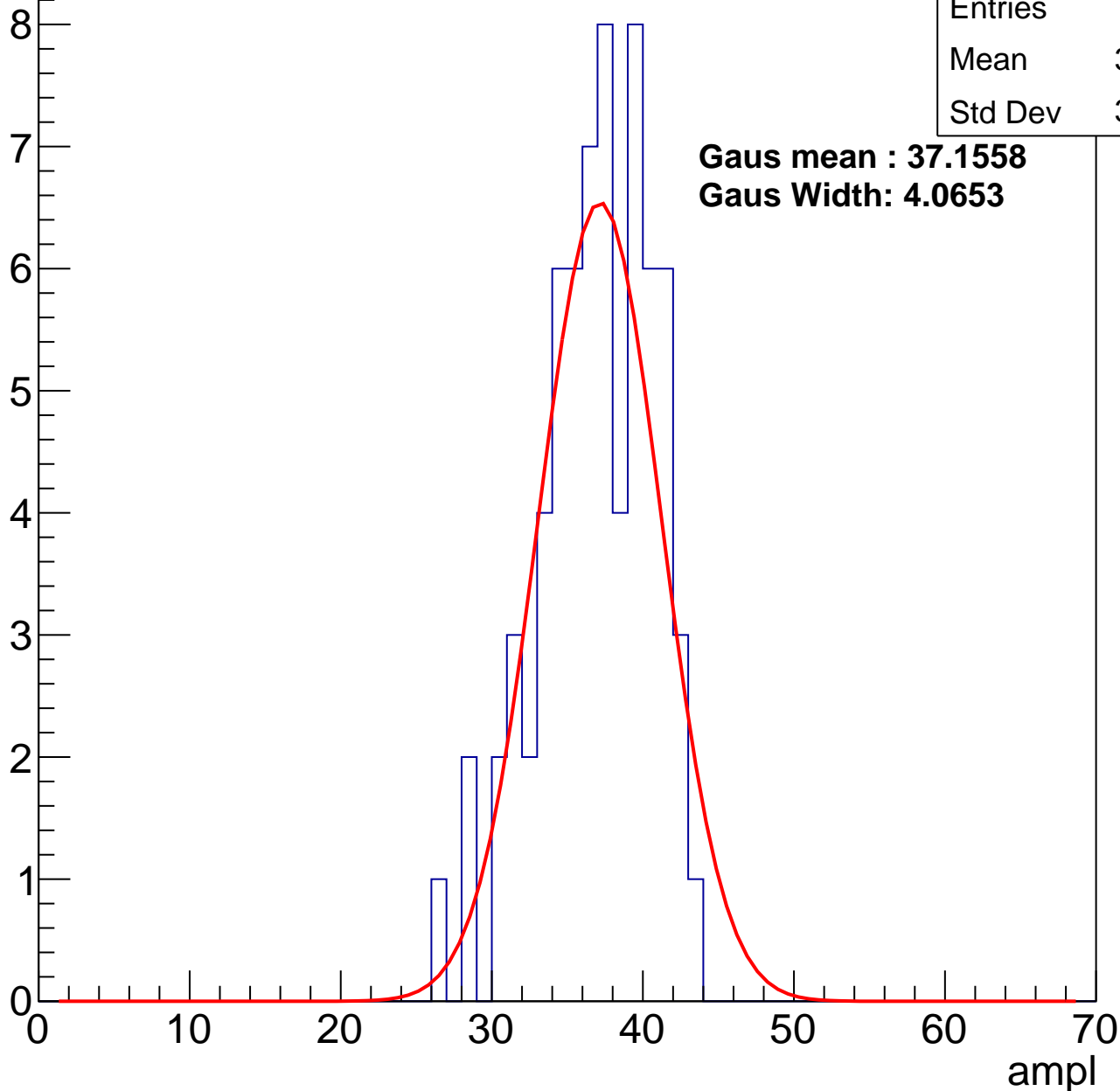
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.41
Std Dev	3.731

Gaus mean : 37.1558

Gaus Width: 4.0653



B1L101S, U12-ch112, adc2

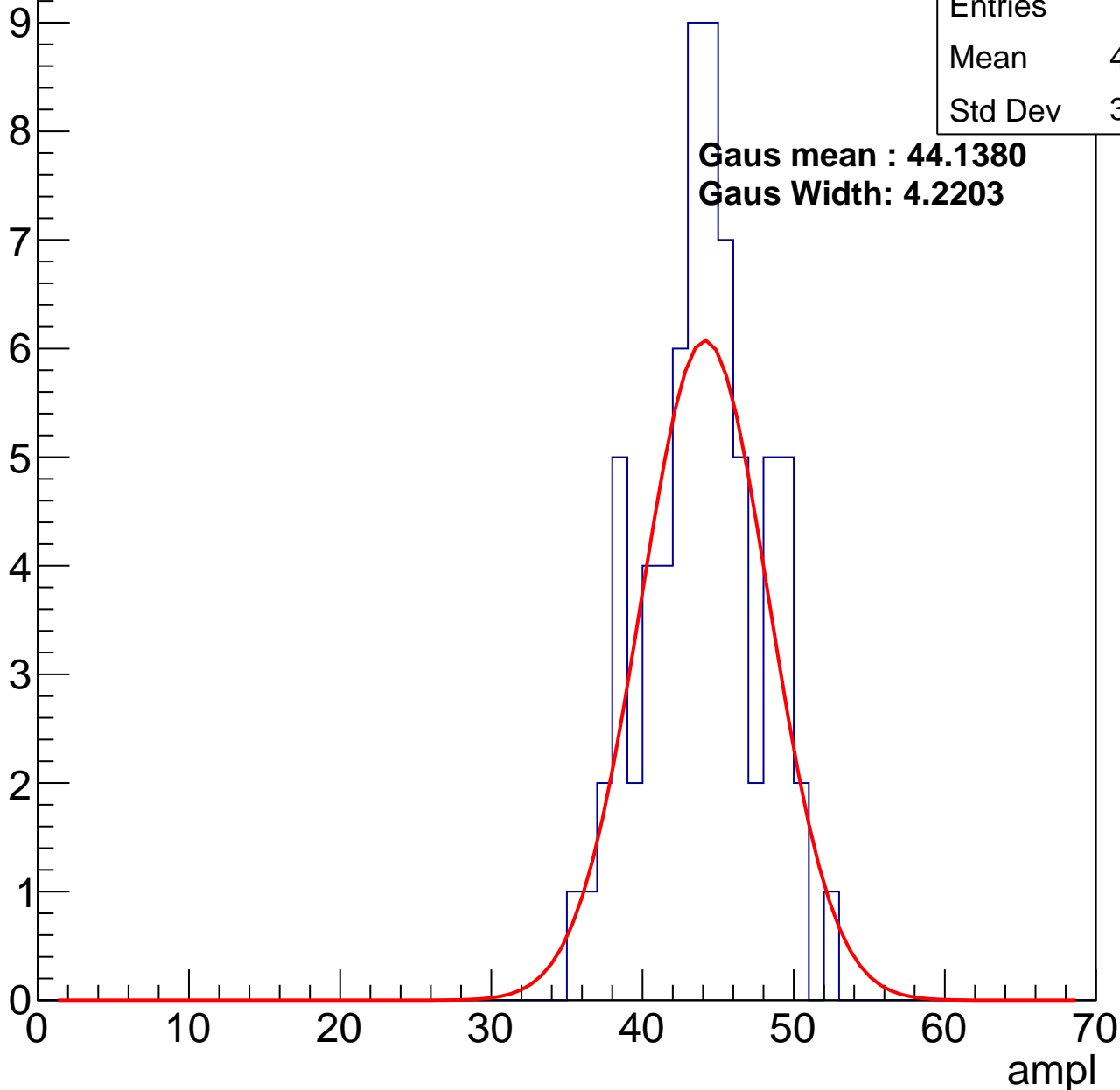
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	43.54
Std Dev	3.737

Gaus mean : 44.1380

Gaus Width: 4.2203

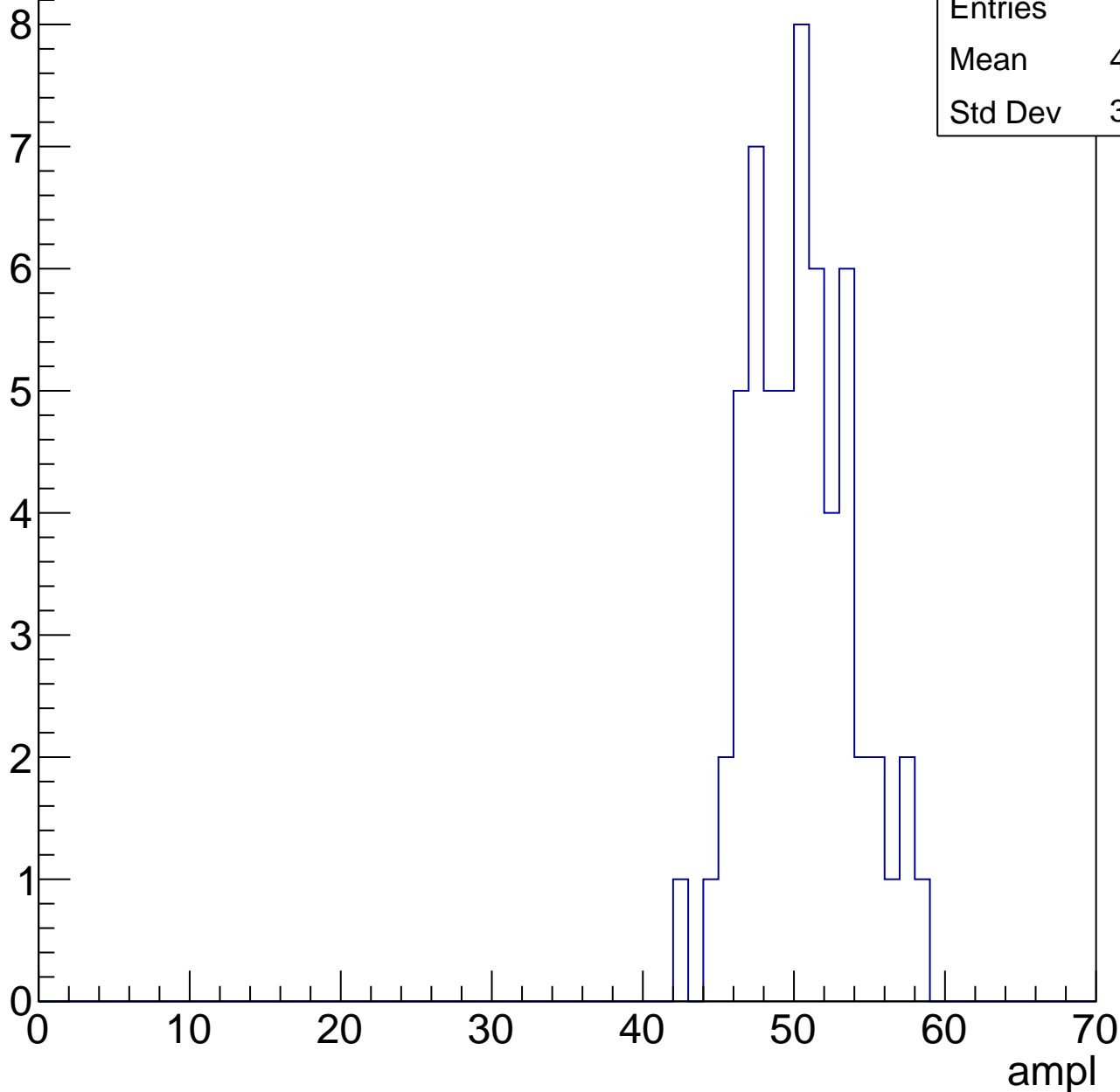


B1L101S, U12-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	49.97
Std Dev	3.409

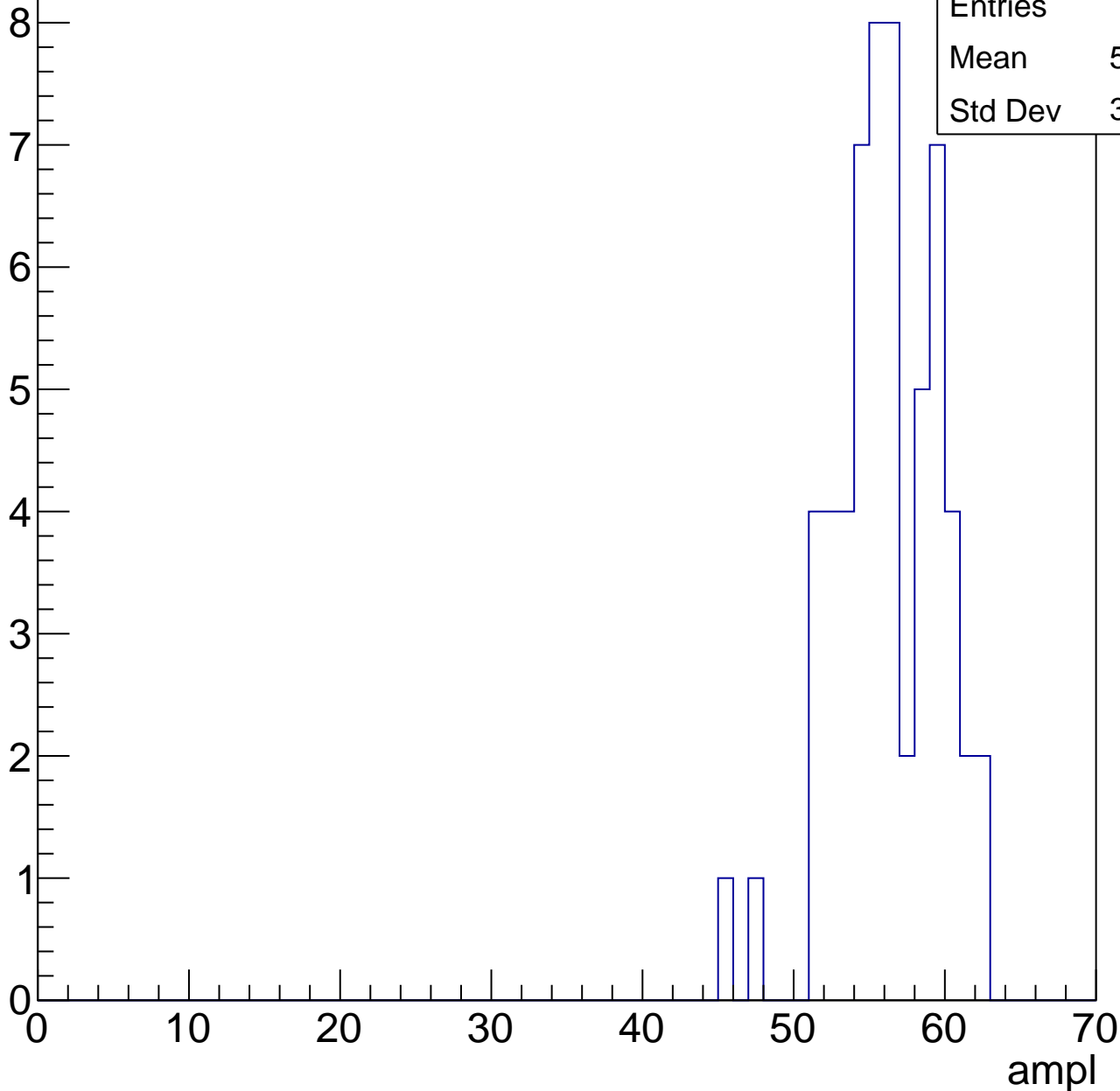


B1L101S, U12-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.68
Std Dev	3.452

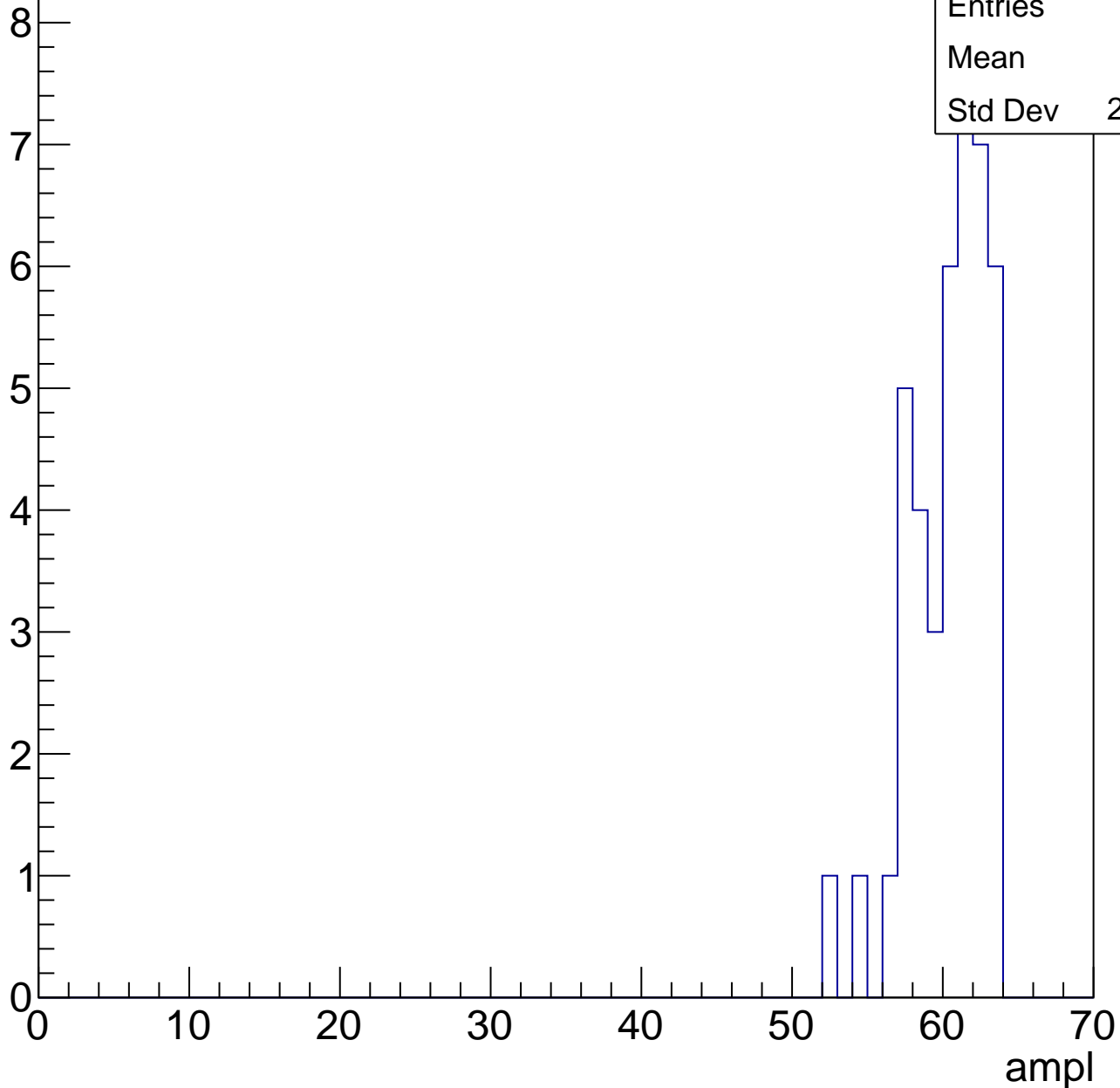


B1L101S, U12-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

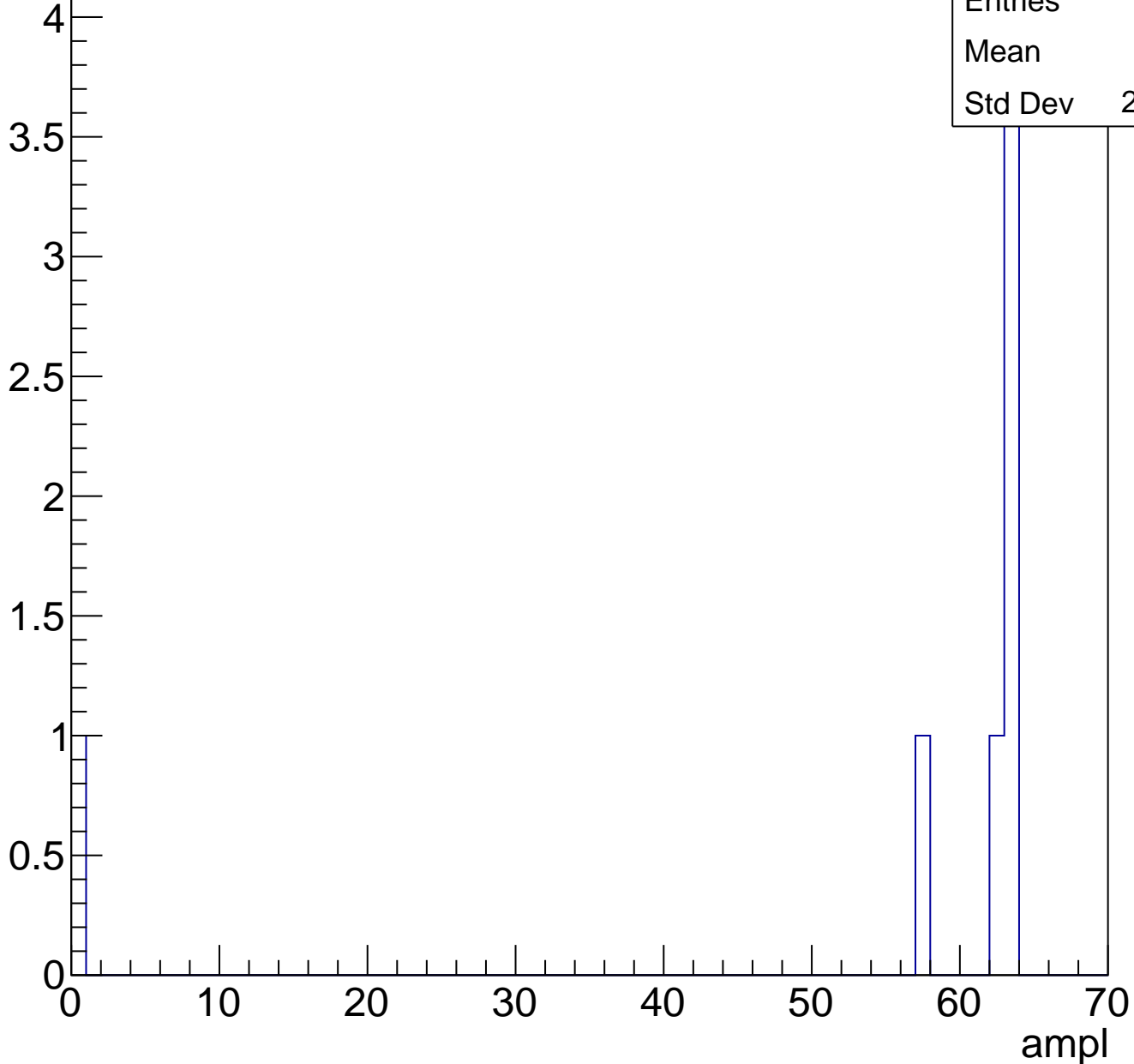
Entries	42
Mean	59.9
Std Dev	2.534



B1L101S, U12-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U12-ch113, adc0

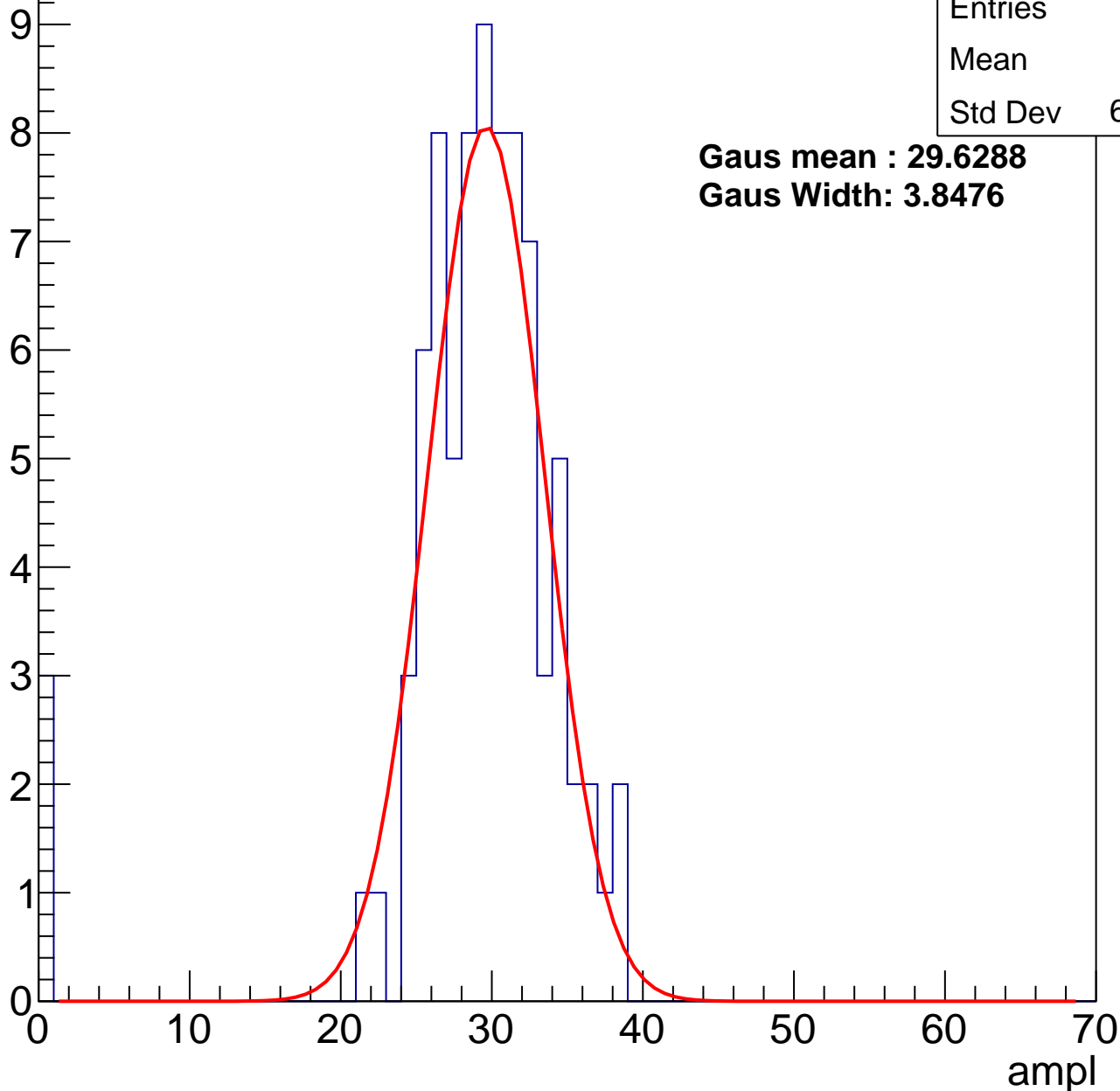
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	28.4
Std Dev	6.579

Gaus mean : 29.6288

Gaus Width: 3.8476



B1L101S, U12-ch113, adc1

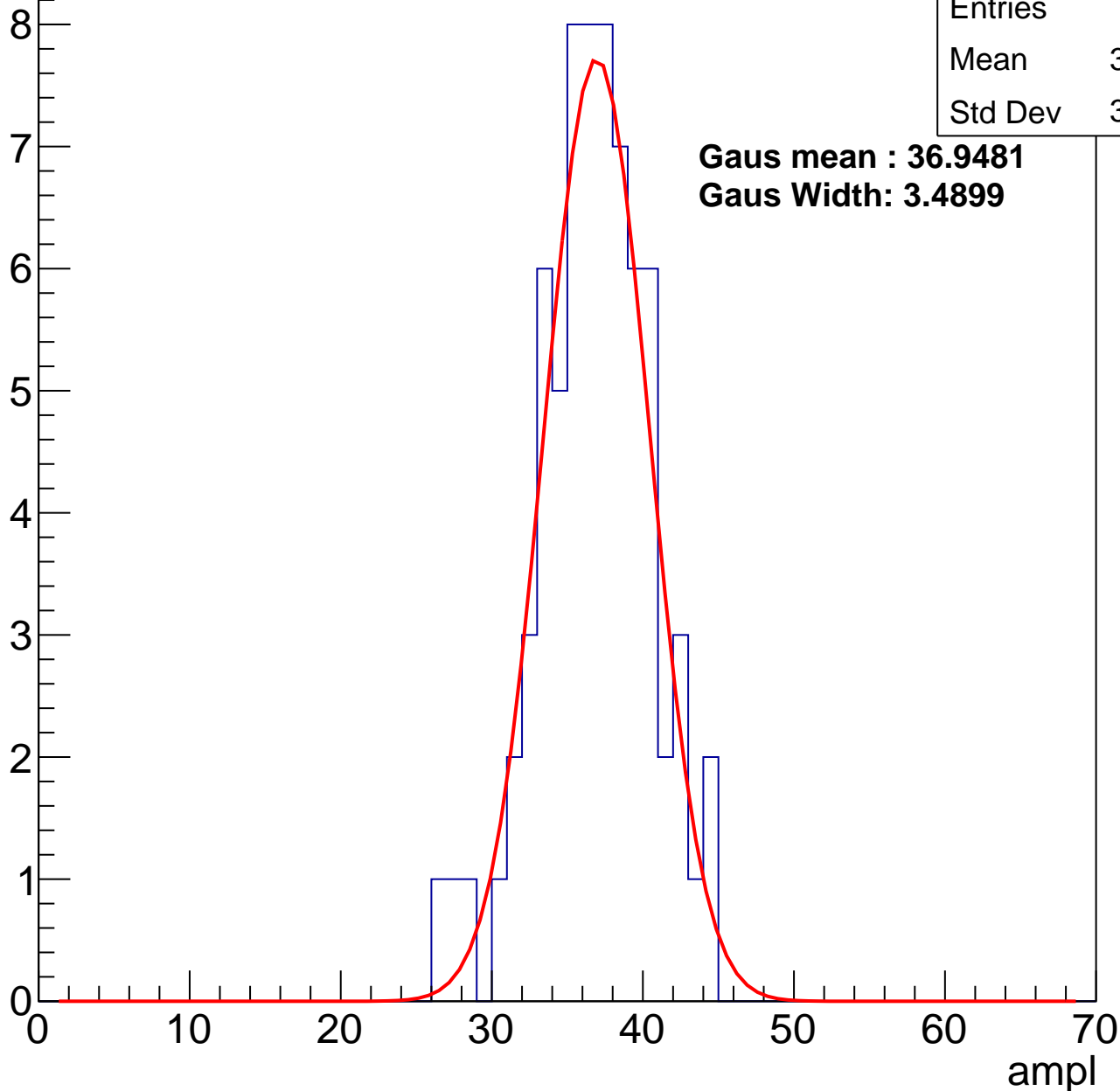
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.34
Std Dev	3.704

Gaus mean : 36.9481

Gaus Width: 3.4899



B1L101S, U12-ch113, adc2

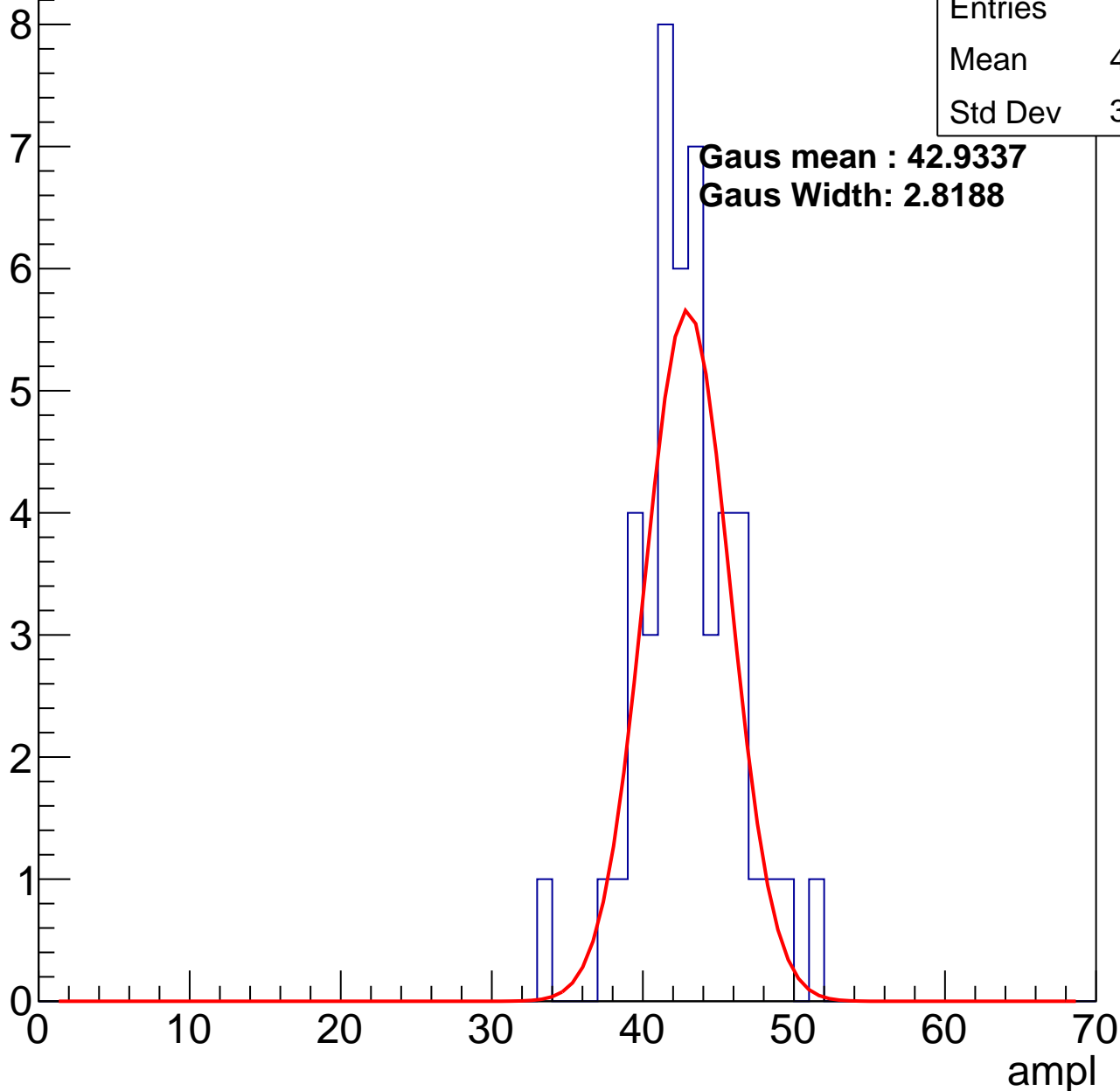
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	42.52
Std Dev	3.215

Gaus mean : 42.9337

Gaus Width: 2.8188

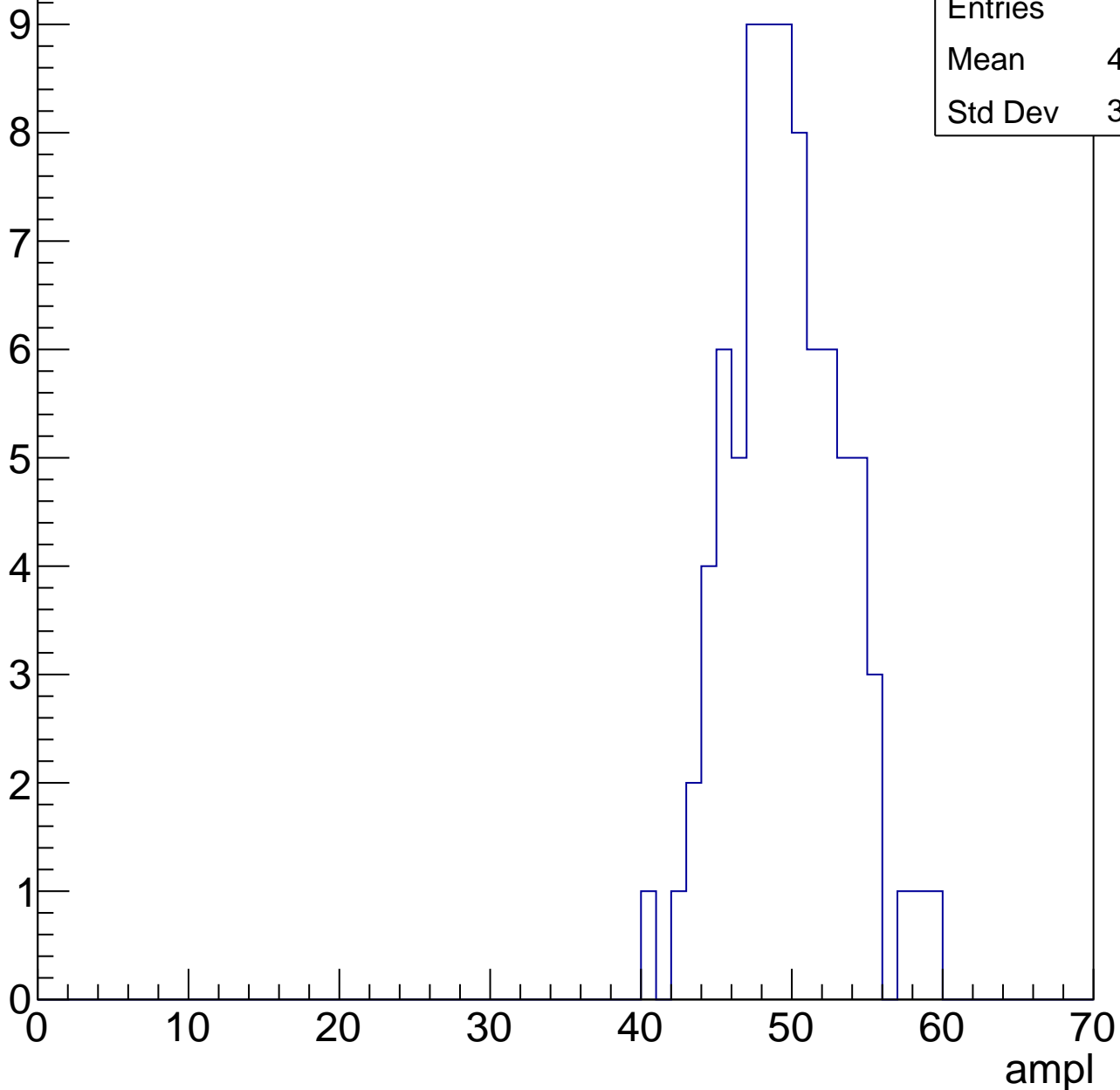


B1L101S, U12-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	49.17
Std Dev	3.712

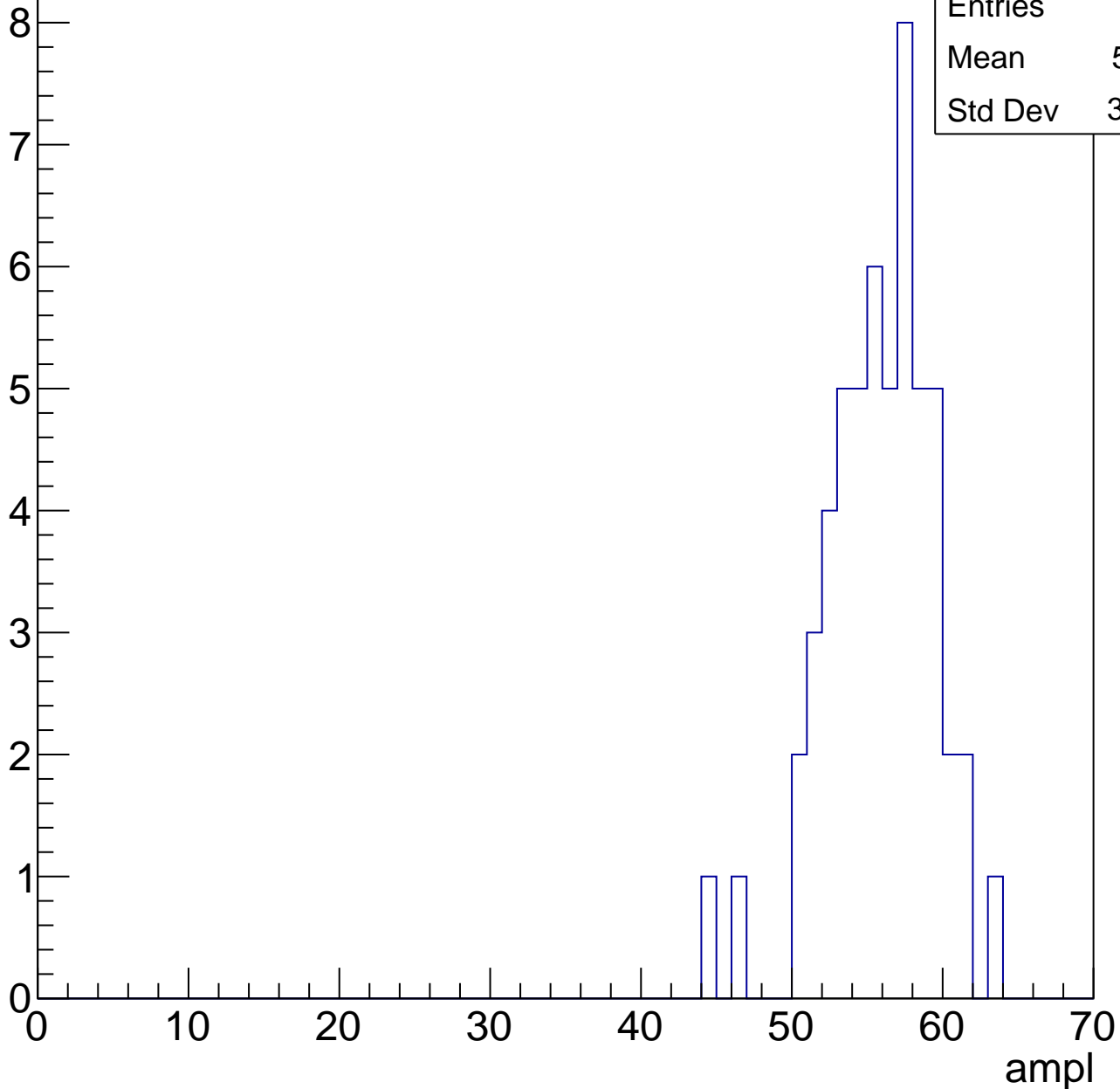


B1L101S, U12-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.31
Std Dev	3.577

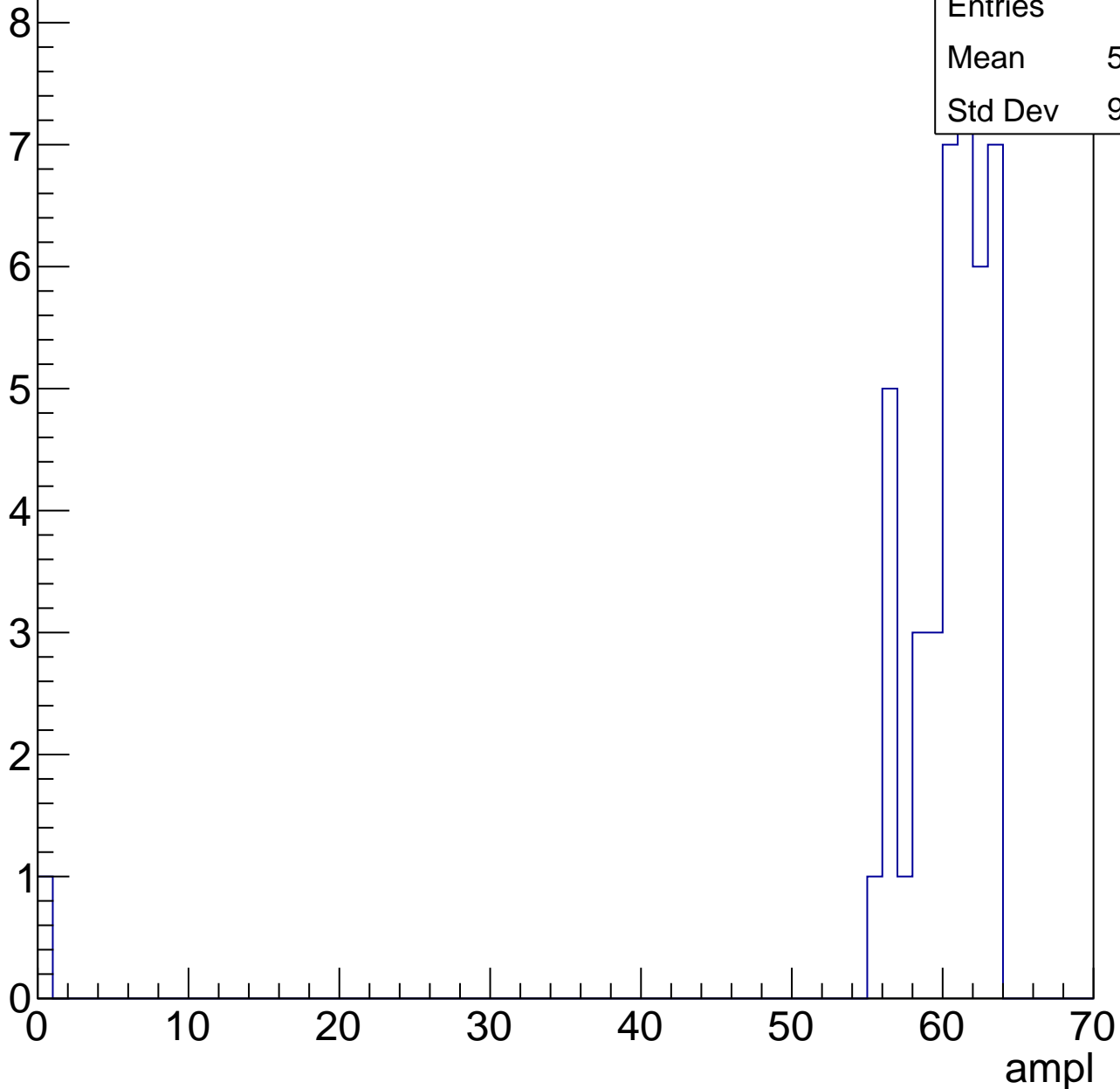


B1L101S, U12-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

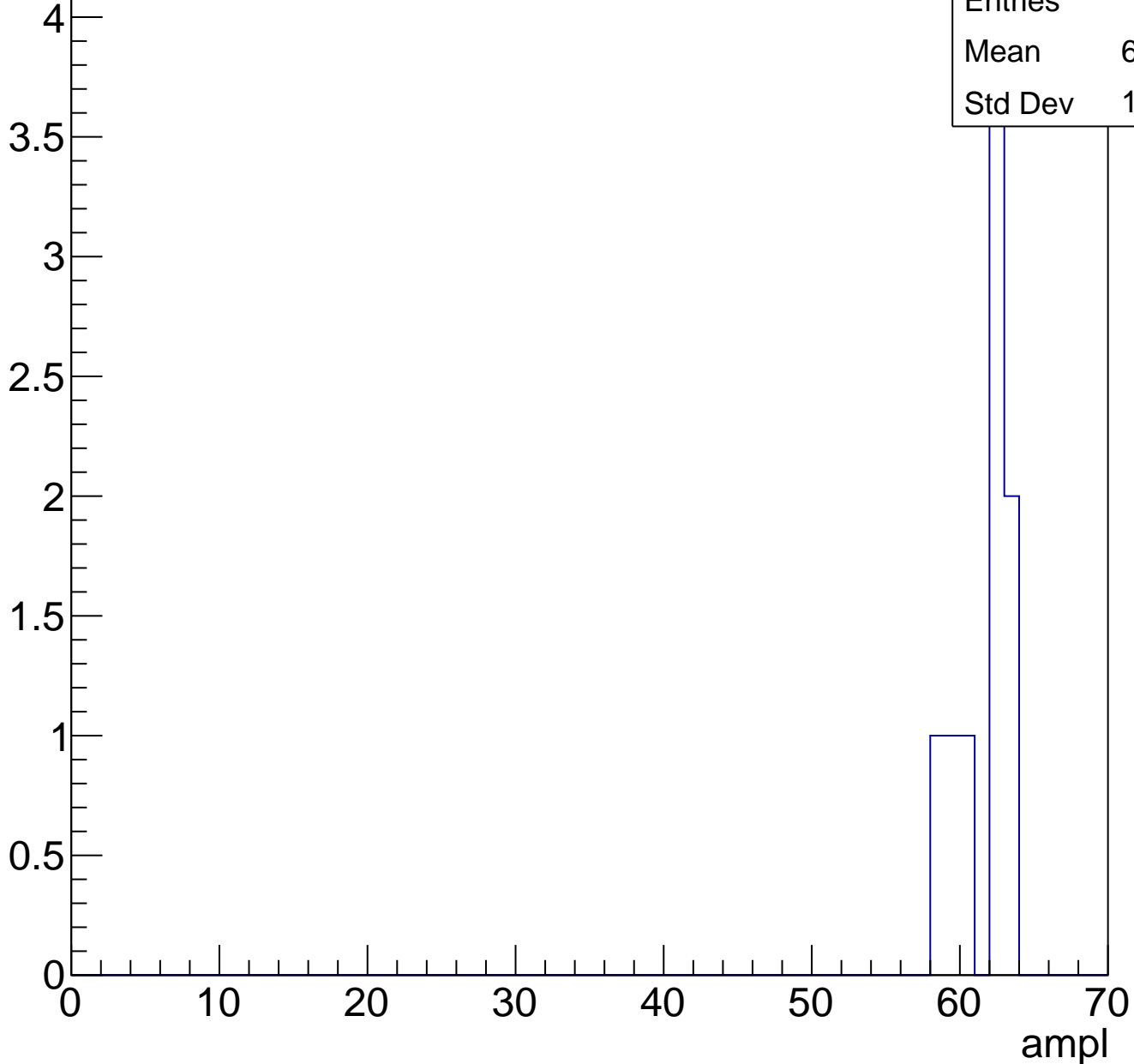
Entries	42
Mean	58.67
Std Dev	9.448



B1L101S, U12-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch114, adc0

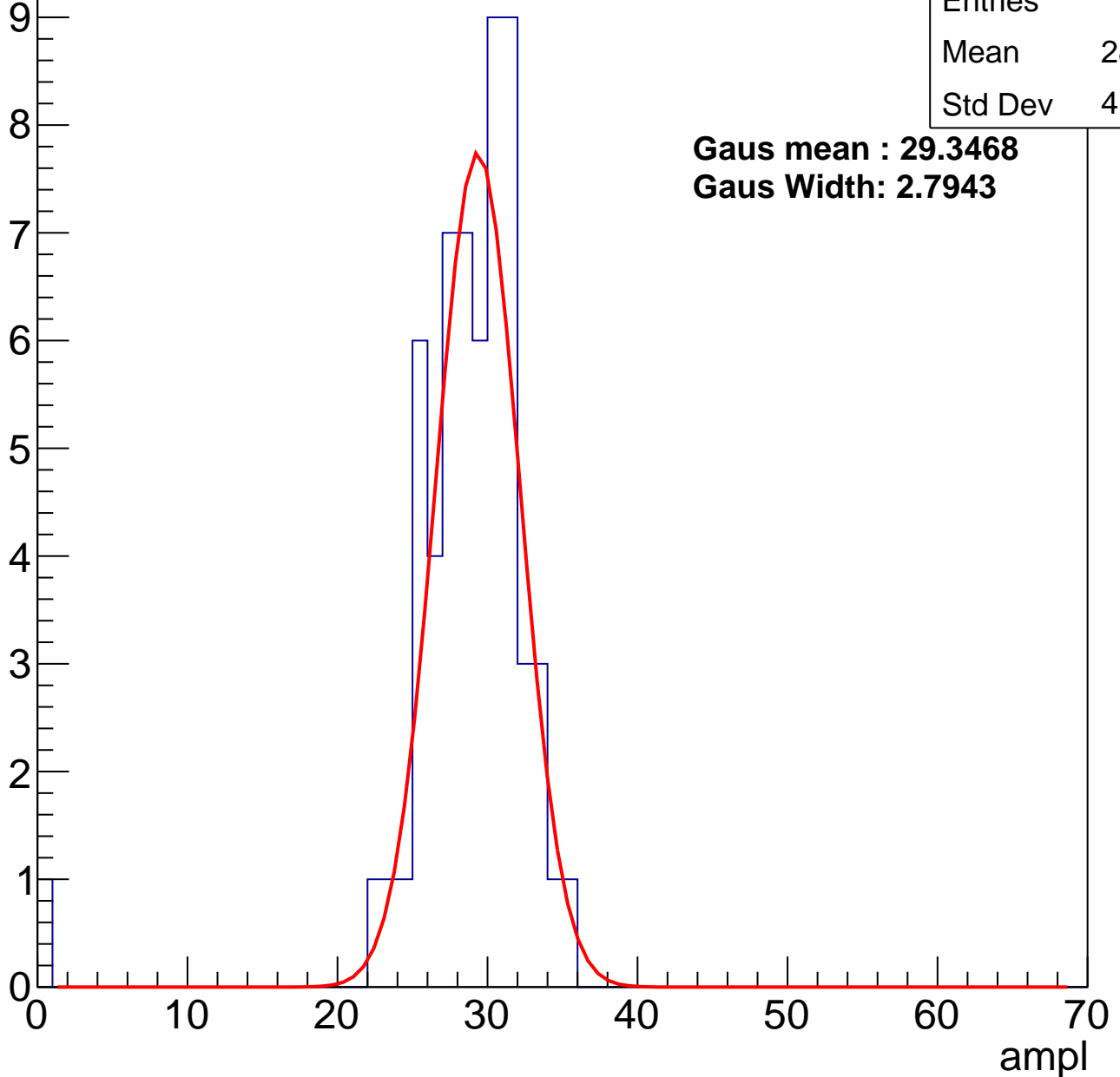
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.25
Std Dev	4.592

Gaus mean : 29.3468

Gaus Width: 2.7943



B1L101S, U12-ch114, adc1

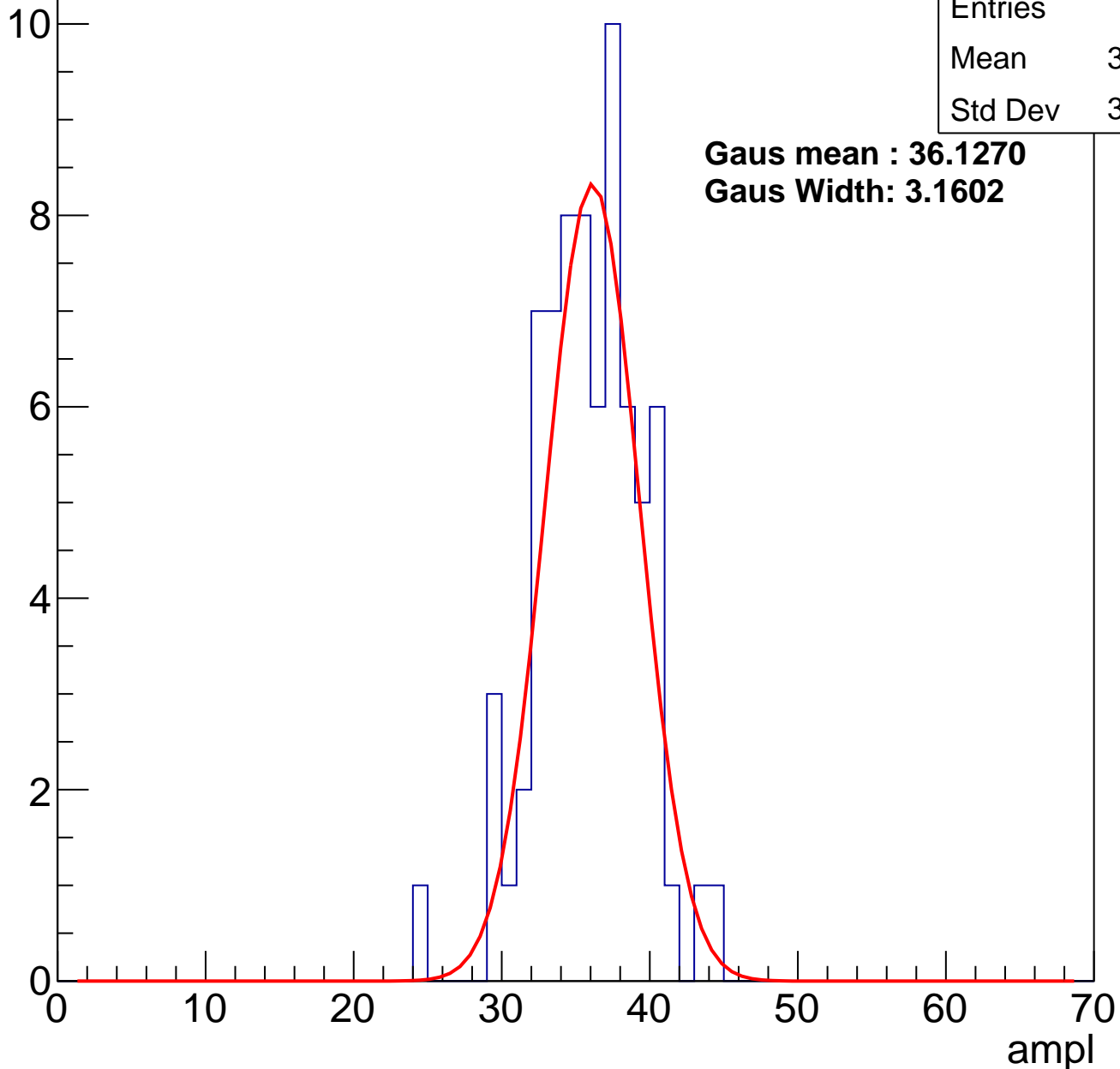
calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	35.44
Std Dev	3.476

Gaus mean : 36.1270

Gaus Width: 3.1602

Entry



B1L101S, U12-ch114, adc2

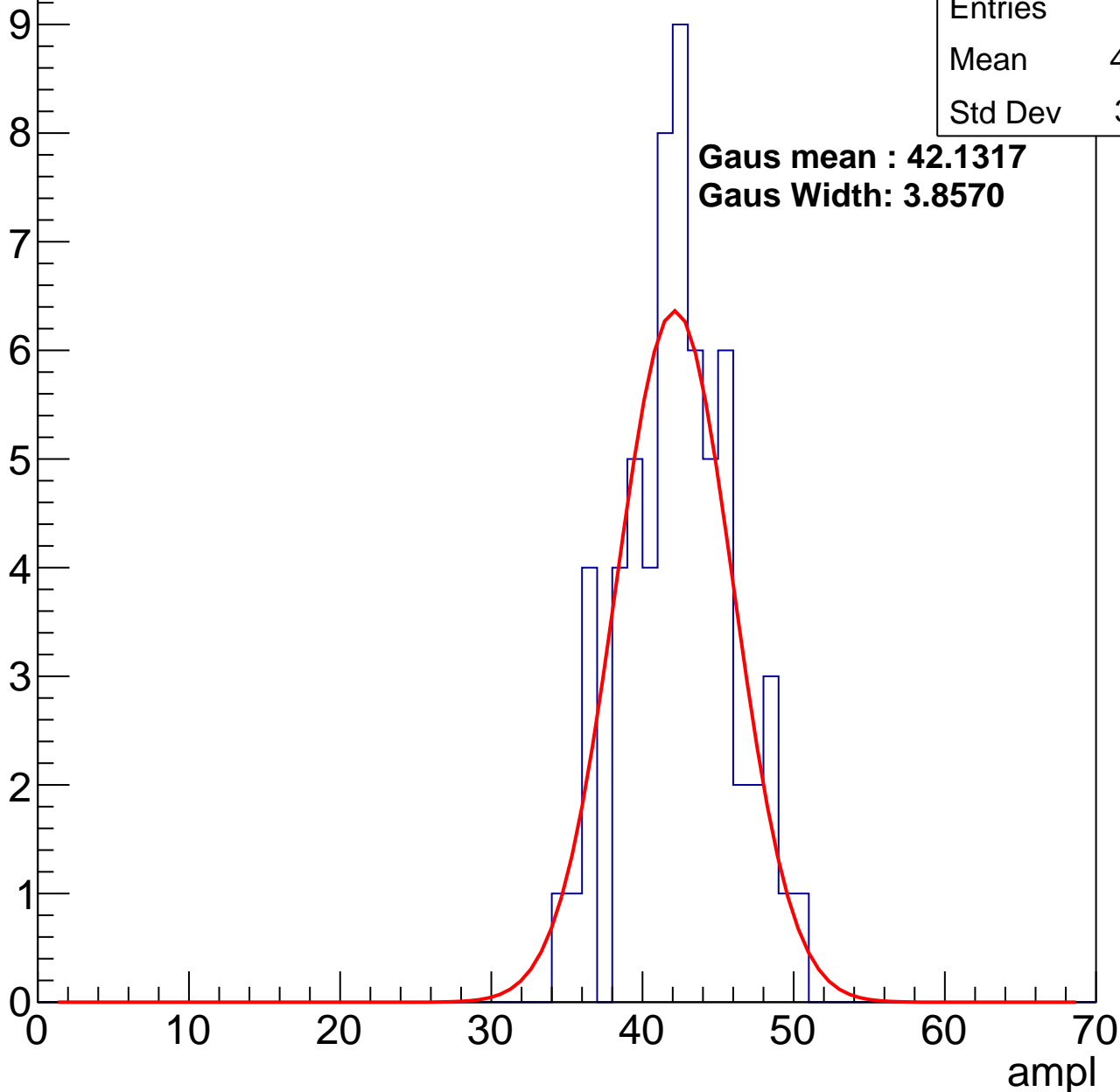
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.98
Std Dev	3.531

Gaus mean : 42.1317

Gaus Width: 3.8570

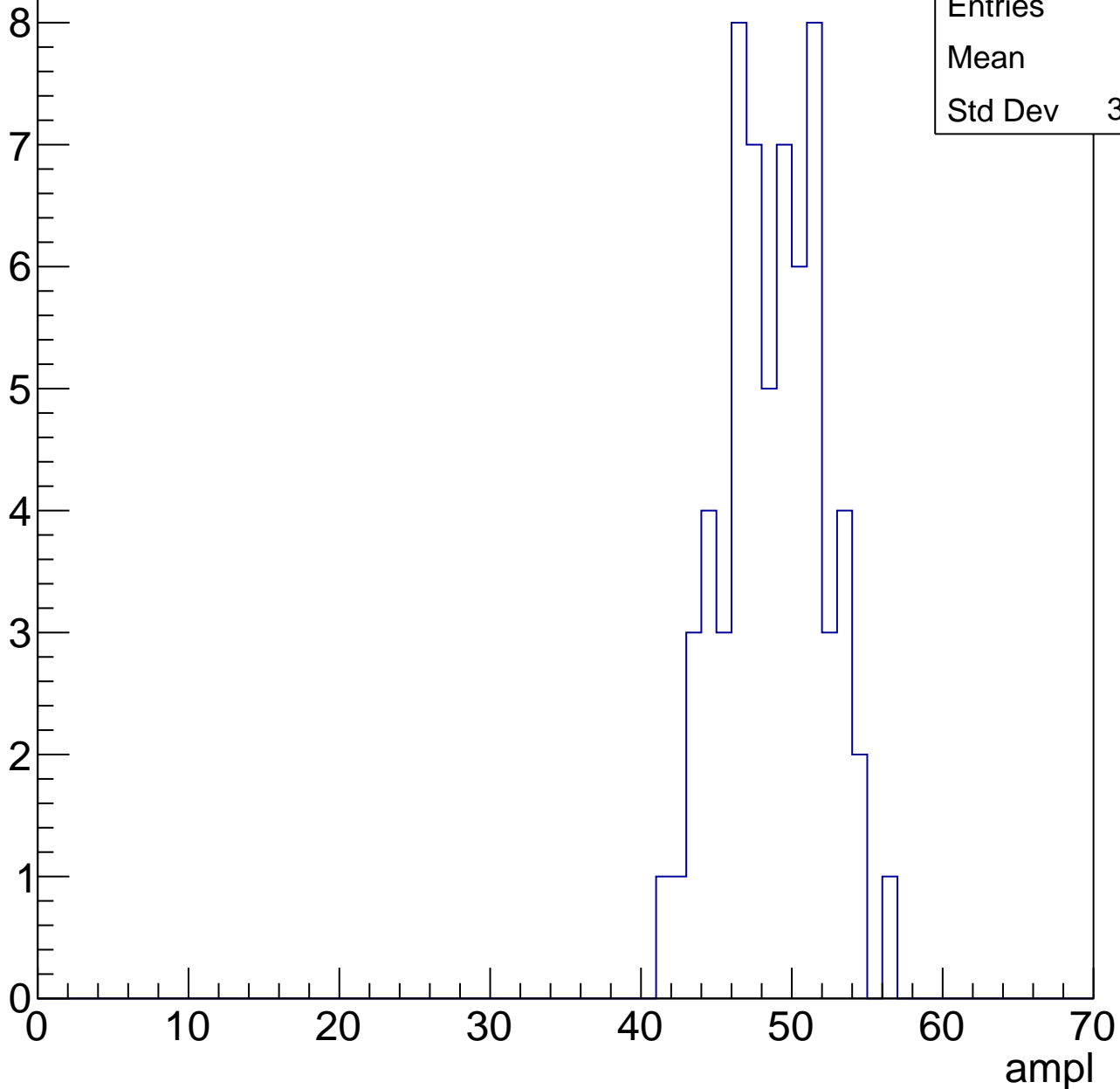


B1L101S, U12-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	48.3
Std Dev	3.264

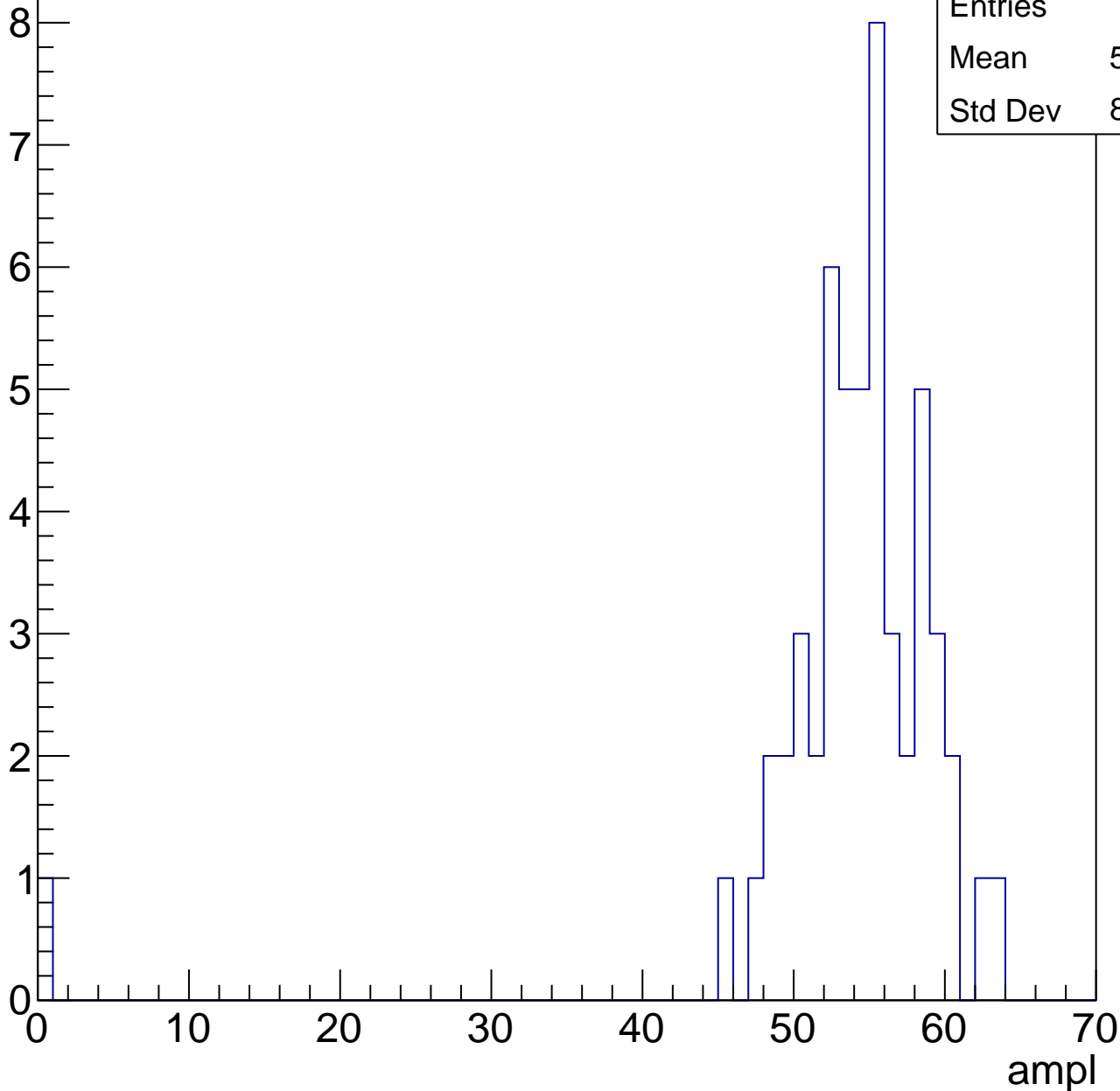


B1L101S, U12-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	53.19
Std Dev	8.285

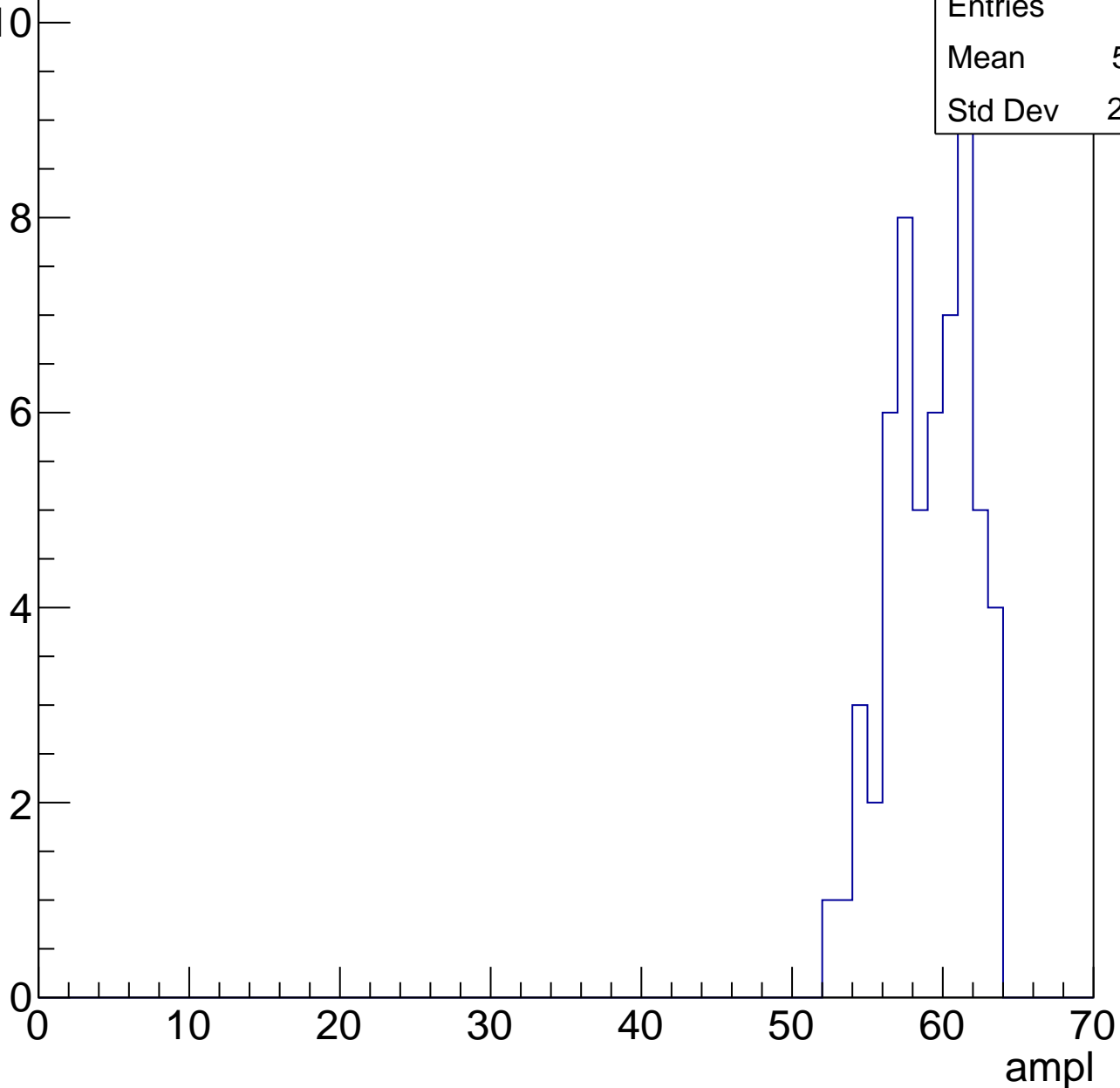


B1L101S, U12-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	58.71
Std Dev	2.736

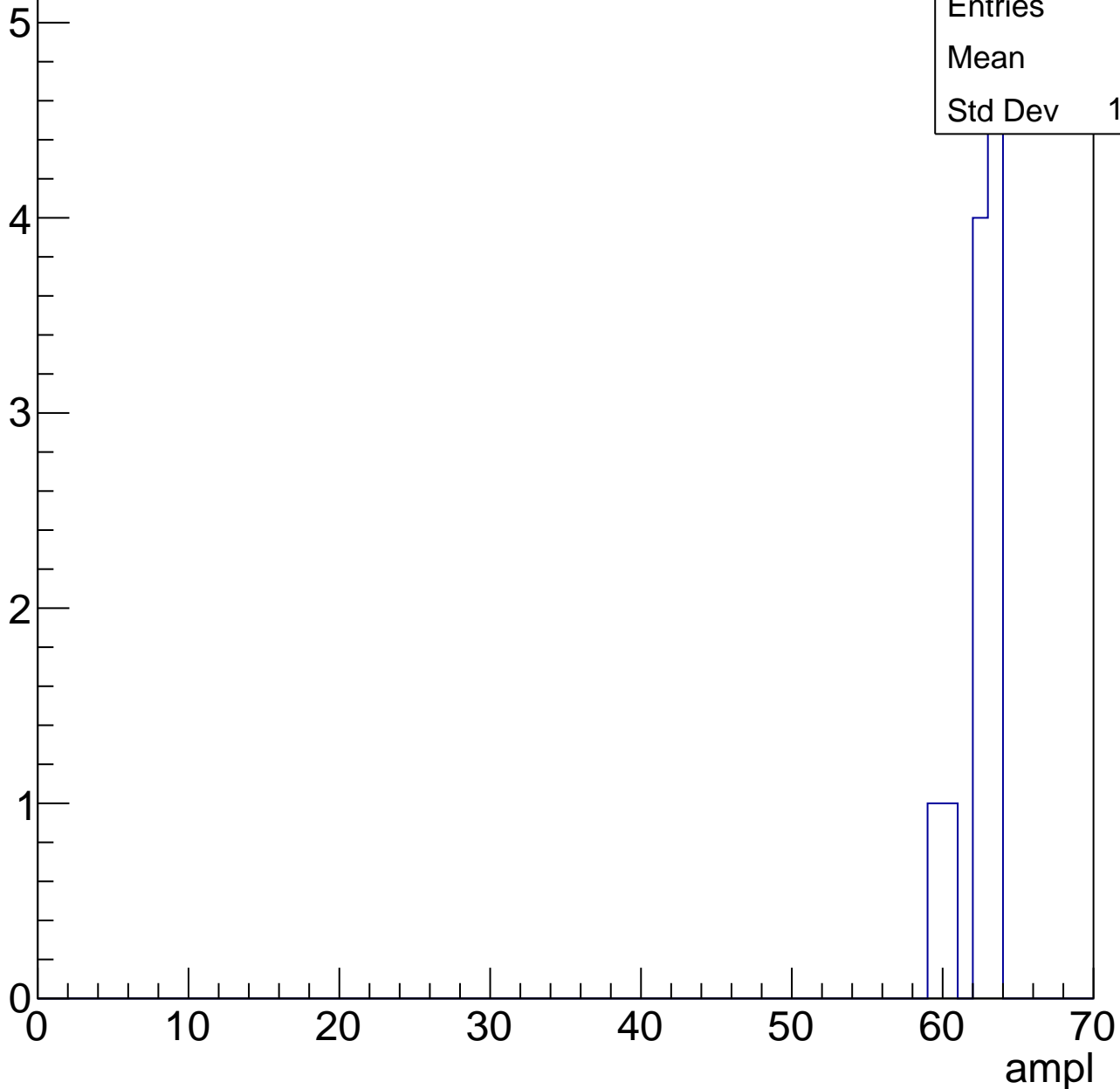


B1L101S, U12-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	62
Std Dev	1.279



B1L101S, U12-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch115, adc0

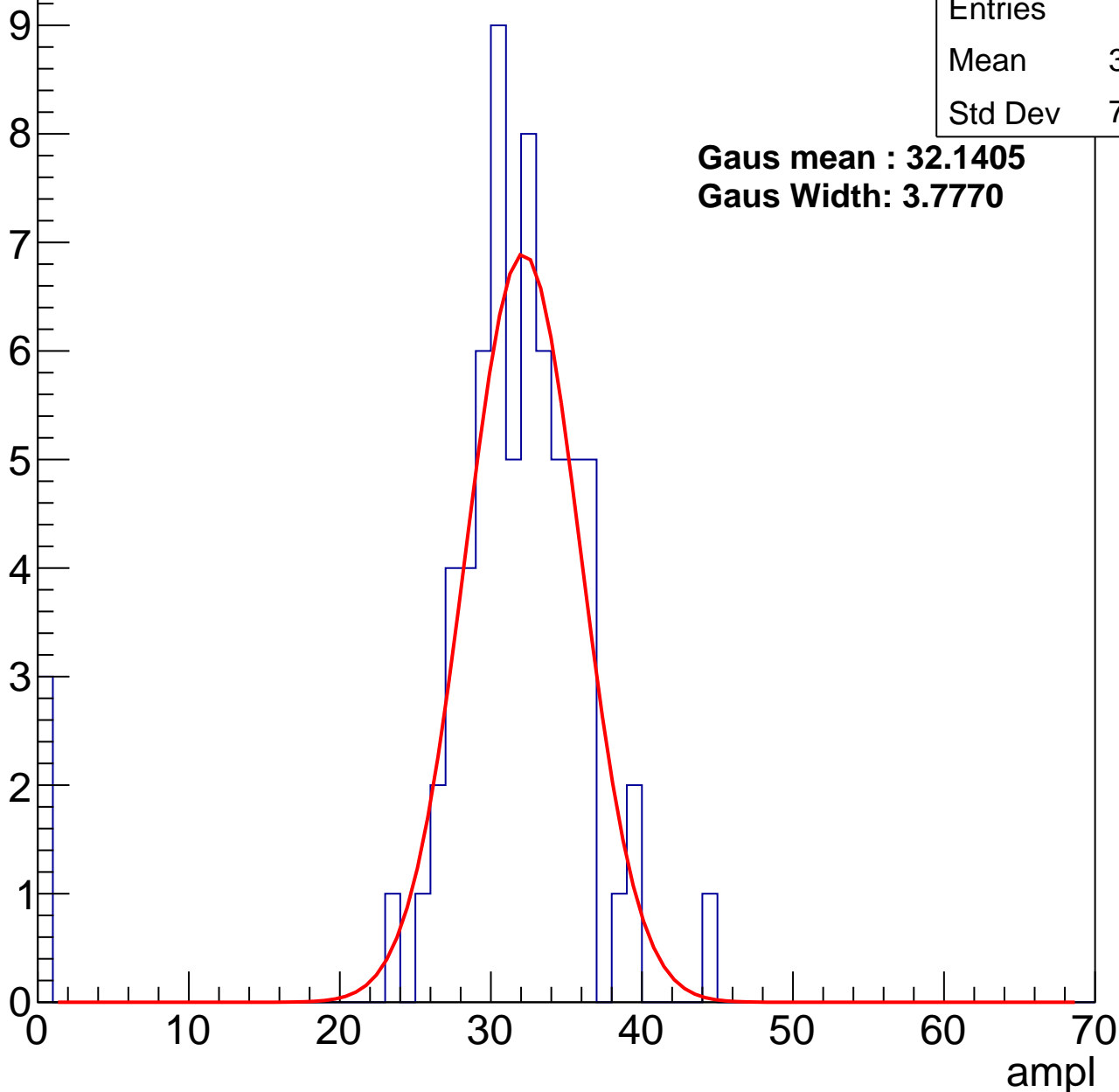
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.26
Std Dev	7.433

Gaus mean : 32.1405

Gaus Width: 3.7770



B1L101S, U12-ch115, adc1

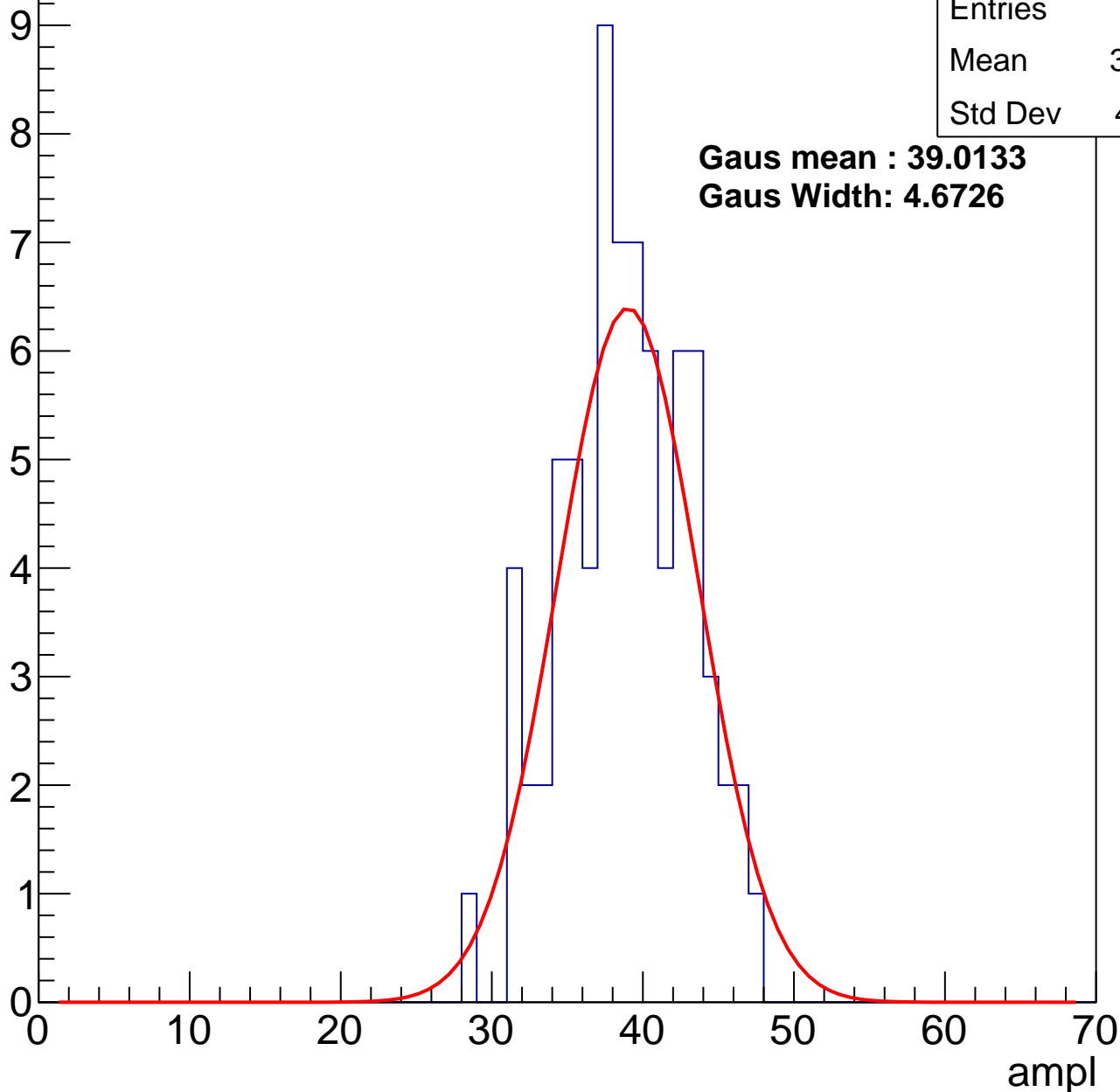
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	38.39
Std Dev	4.111

Gaus mean : 39.0133

Gaus Width: 4.6726



B1L101S, U12-ch115, adc2

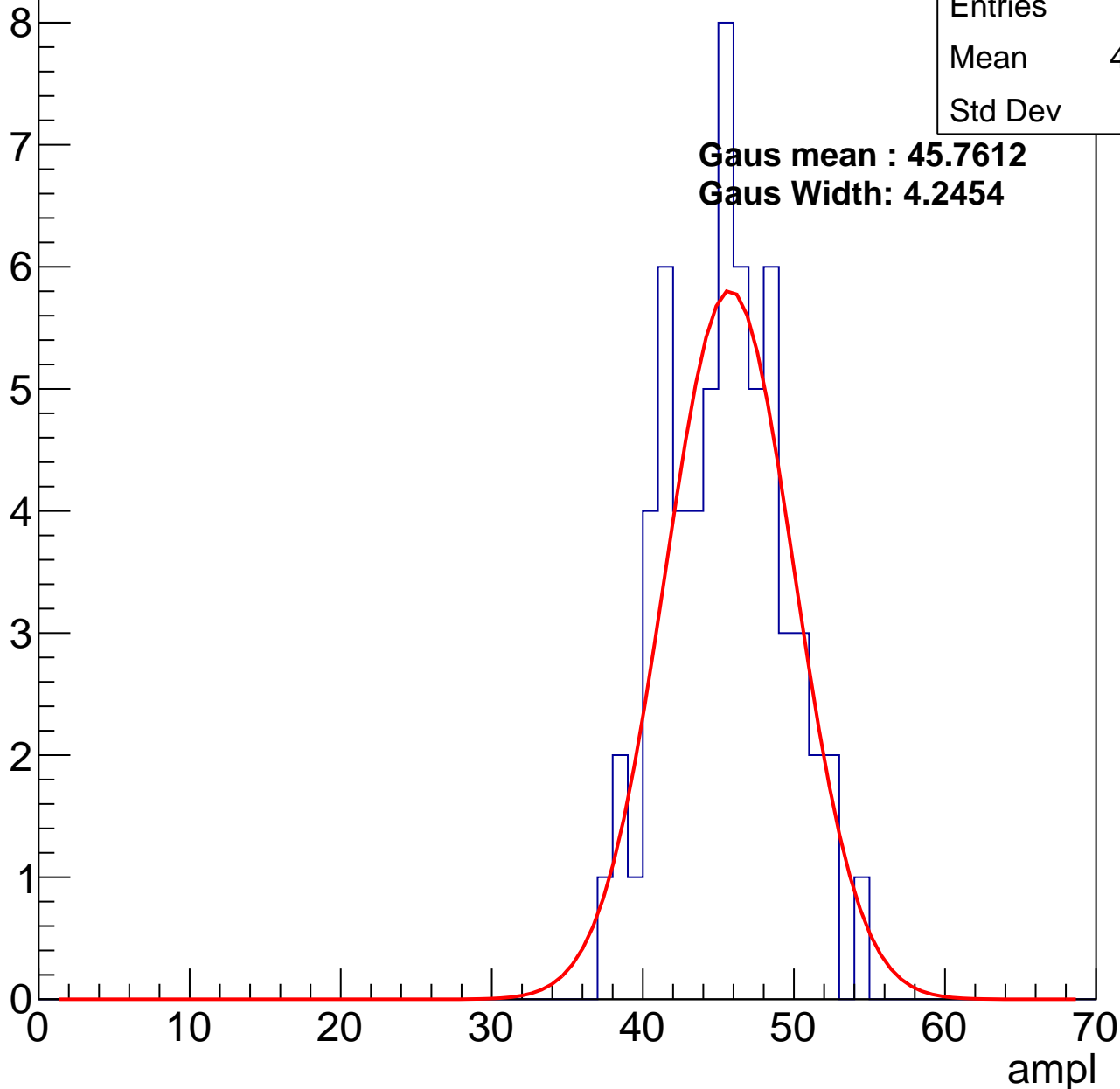
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.98
Std Dev	3.79

Gaus mean : 45.7612

Gaus Width: 4.2454

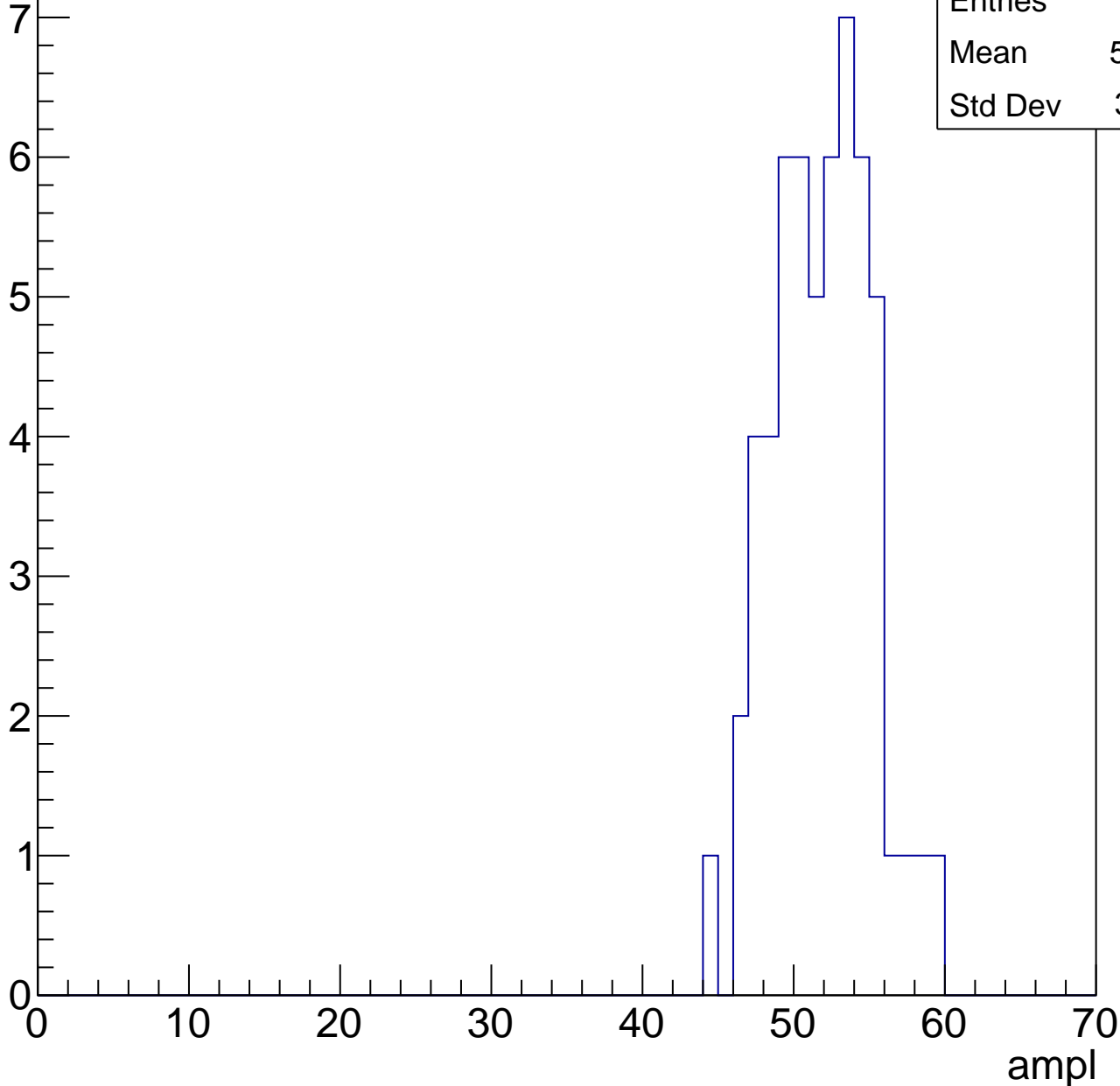


B1L101S, U12-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	51.38
Std Dev	3.171

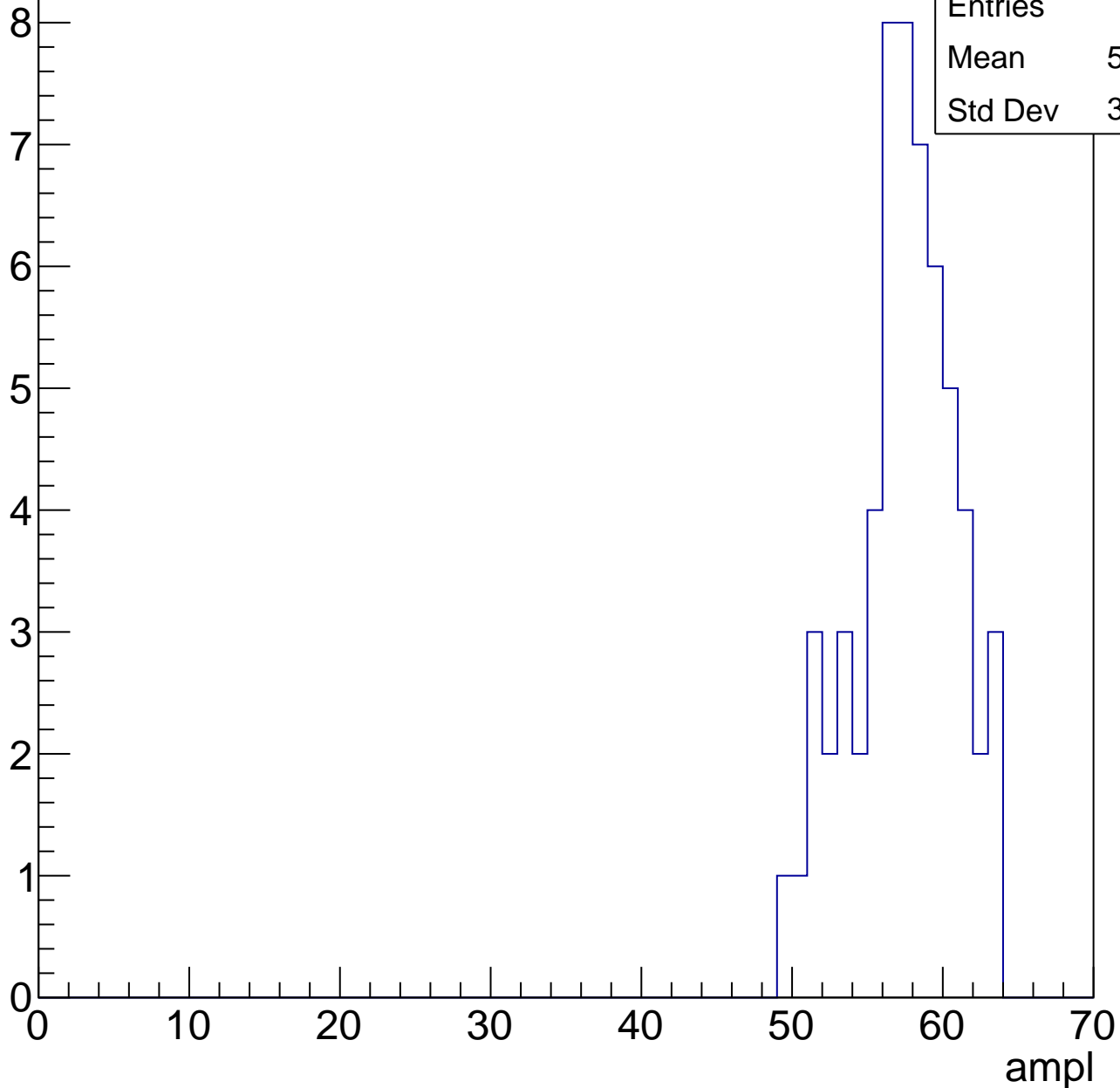


B1L101S, U12-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	57.02
Std Dev	3.342

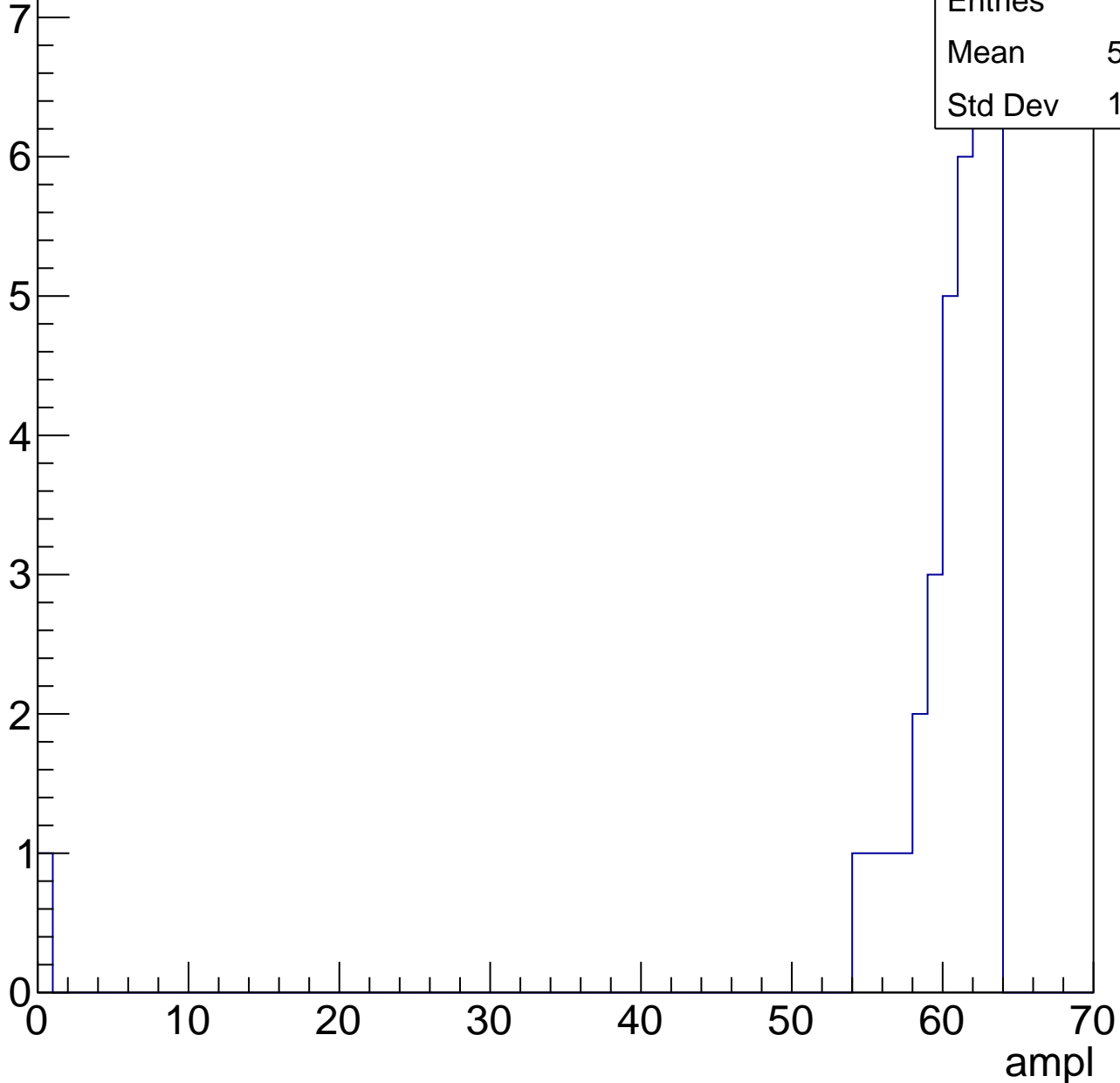


B1L101S, U12-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.74
Std Dev	10.34



B1L101S, U12-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U12-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch116, adc0

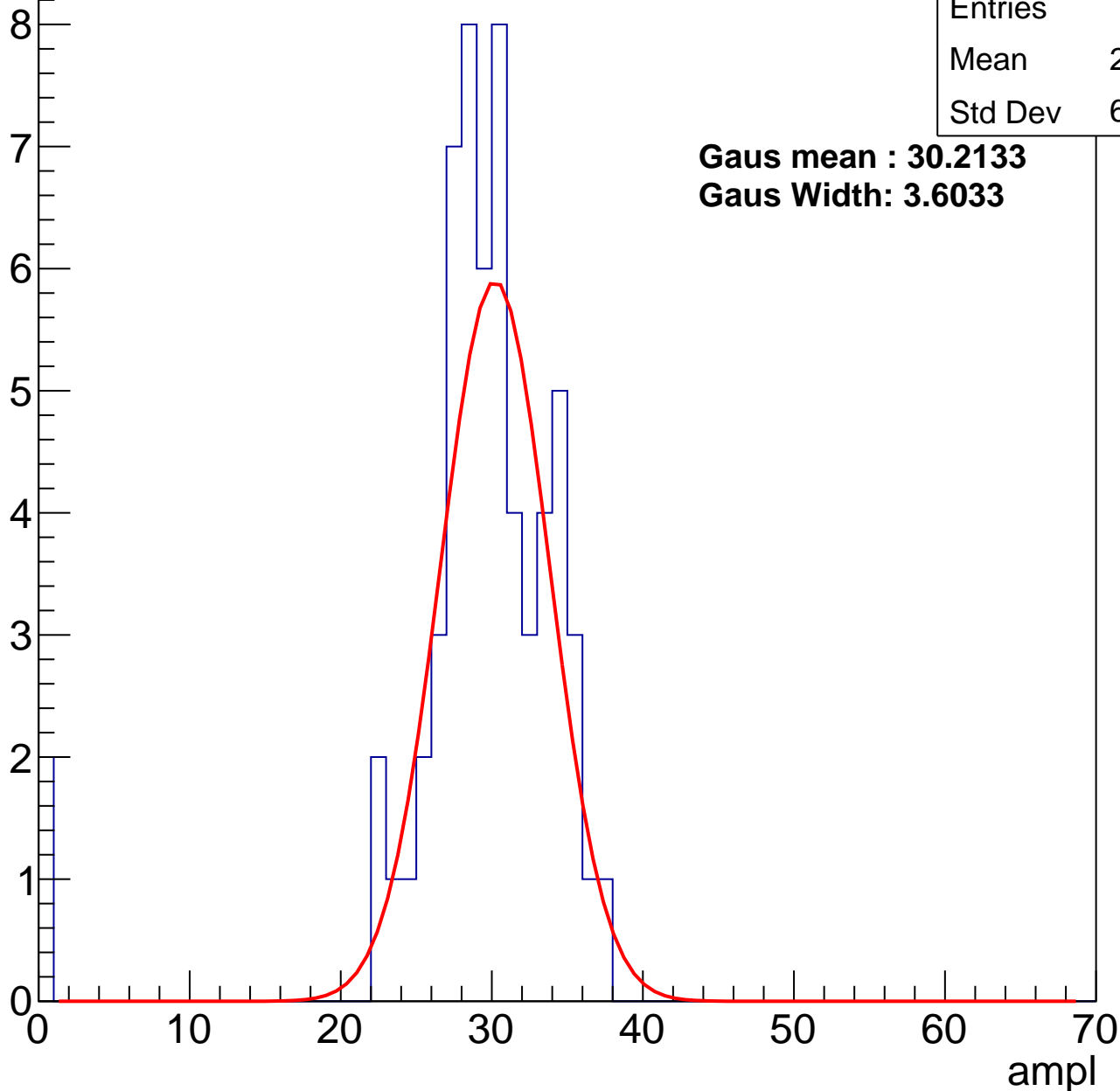
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	28.62
Std Dev	6.257

Gaus mean : 30.2133

Gaus Width: 3.6033



B1L101S, U12-ch116, adc1

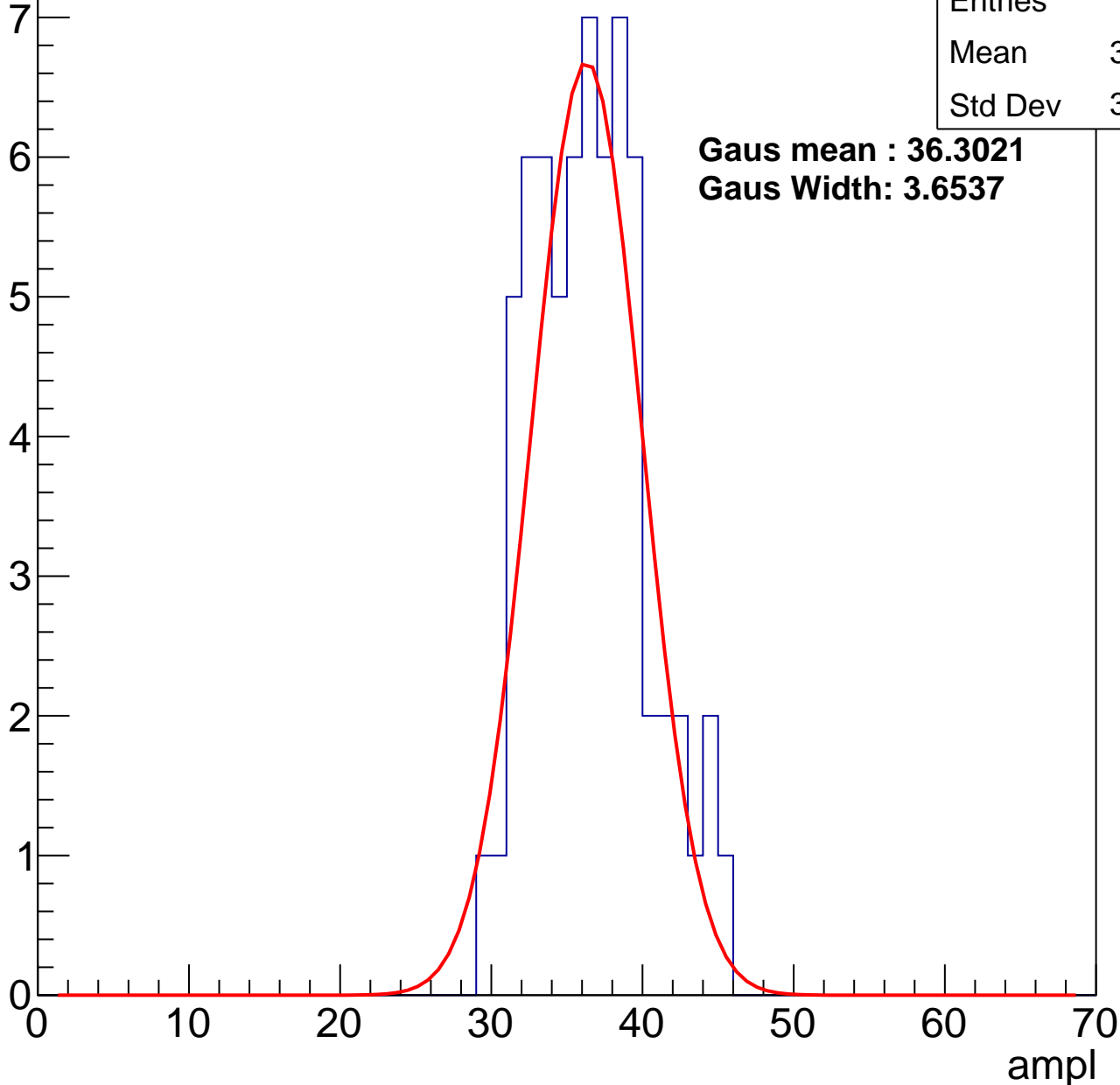
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.06
Std Dev	3.663

Gaus mean : 36.3021

Gaus Width: 3.6537



B1L101S, U12-ch116, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	100
Mean	43.86
Std Dev	4.167

Entry

Gaus mean : 44.5641

Gaus Width: 4.5486

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

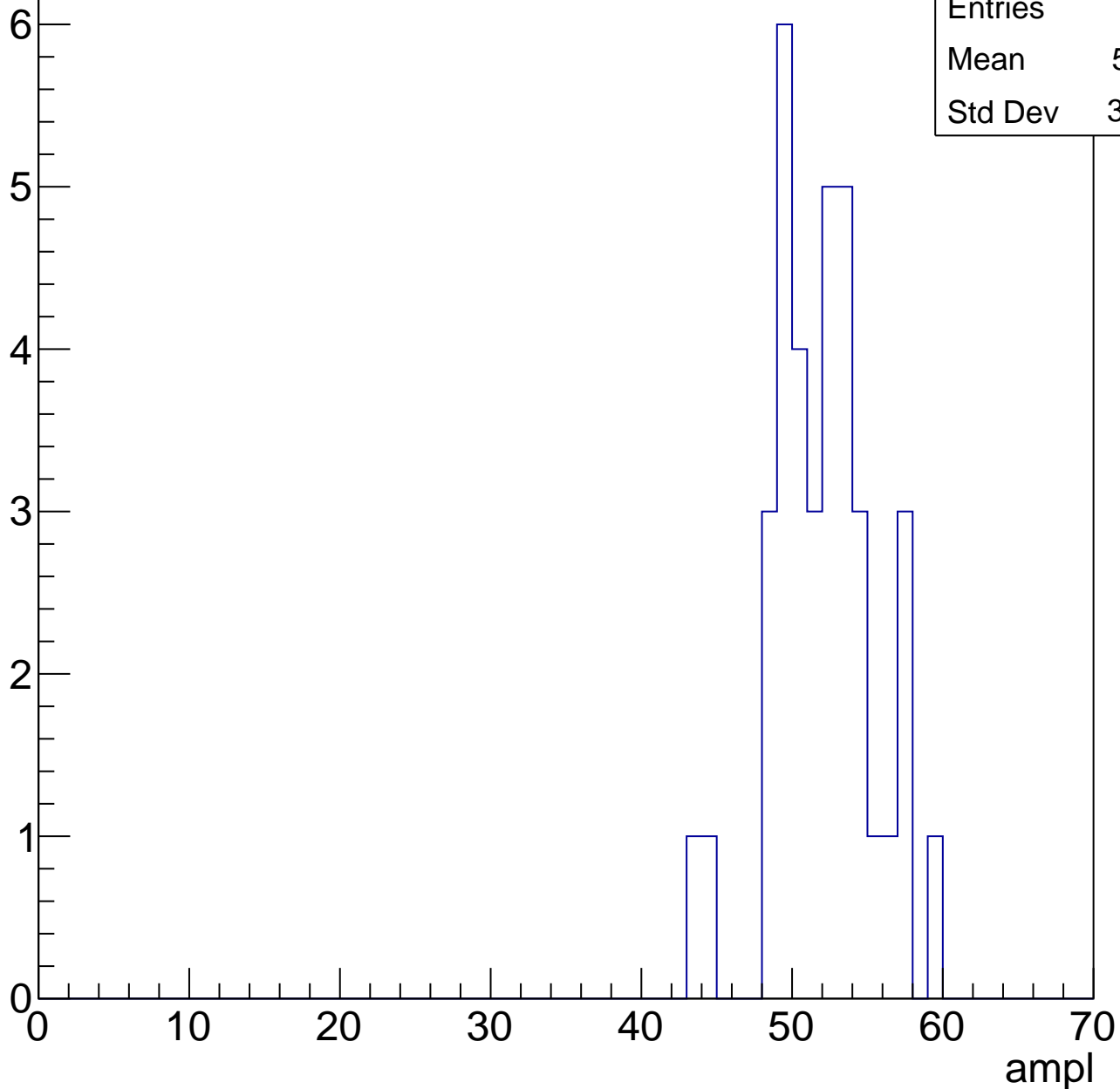
70

B1L101S, U12-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	51.51
Std Dev	3.382

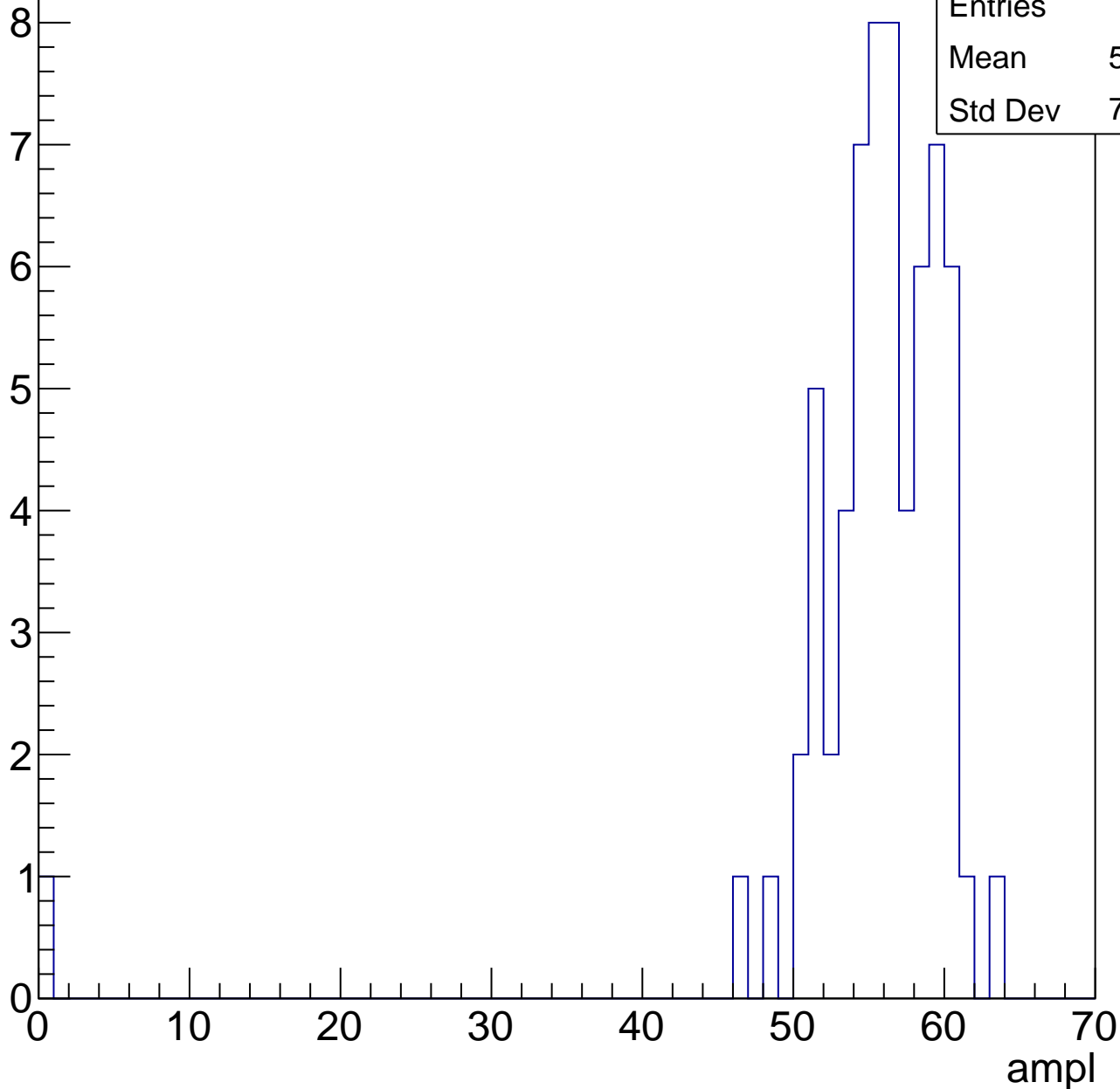


B1L101S, U12-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	54.75
Std Dev	7.667

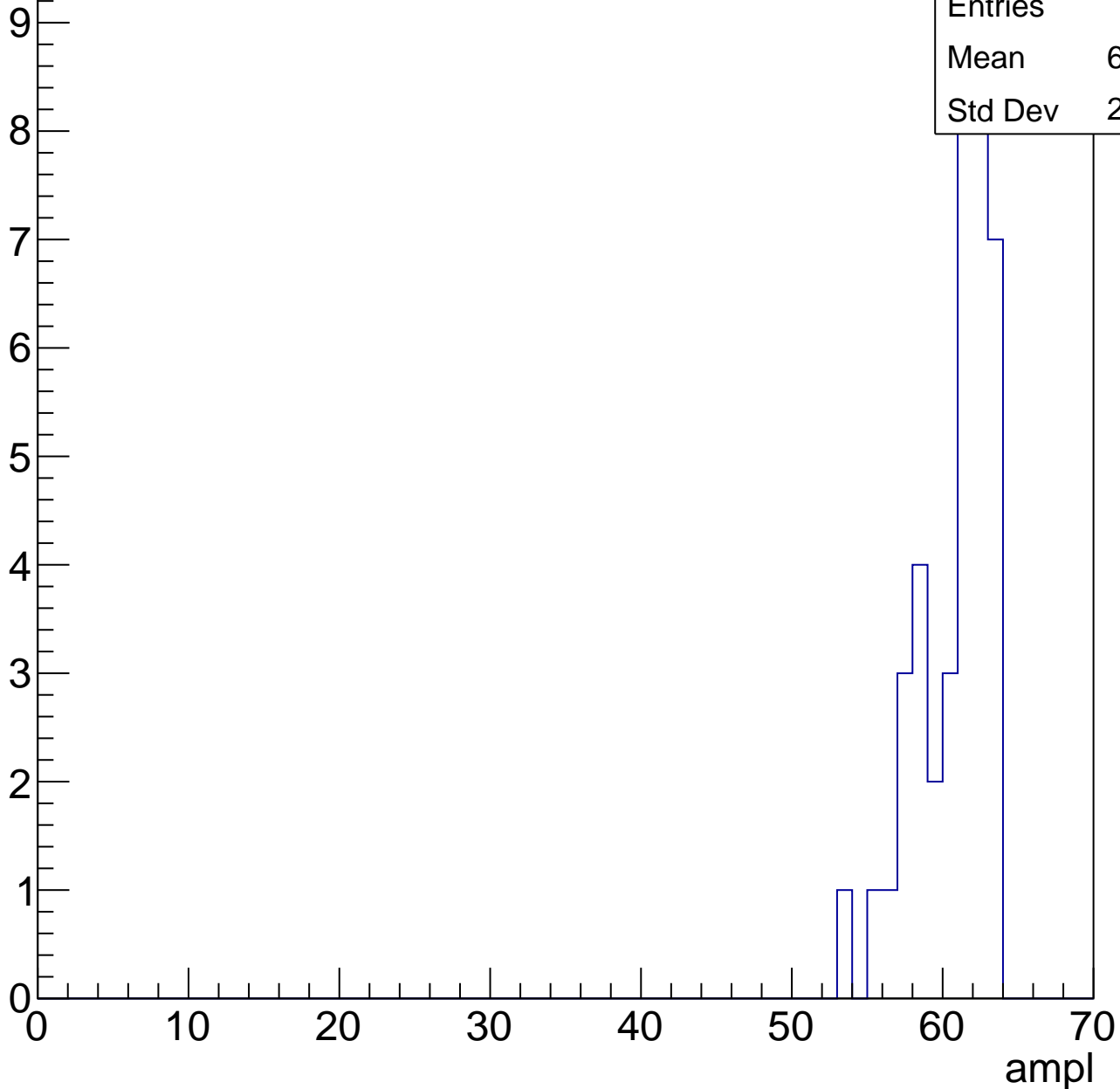


B1L101S, U12-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	60.33
Std Dev	2.443



B1L101S, U12-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U12-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch117, adc0

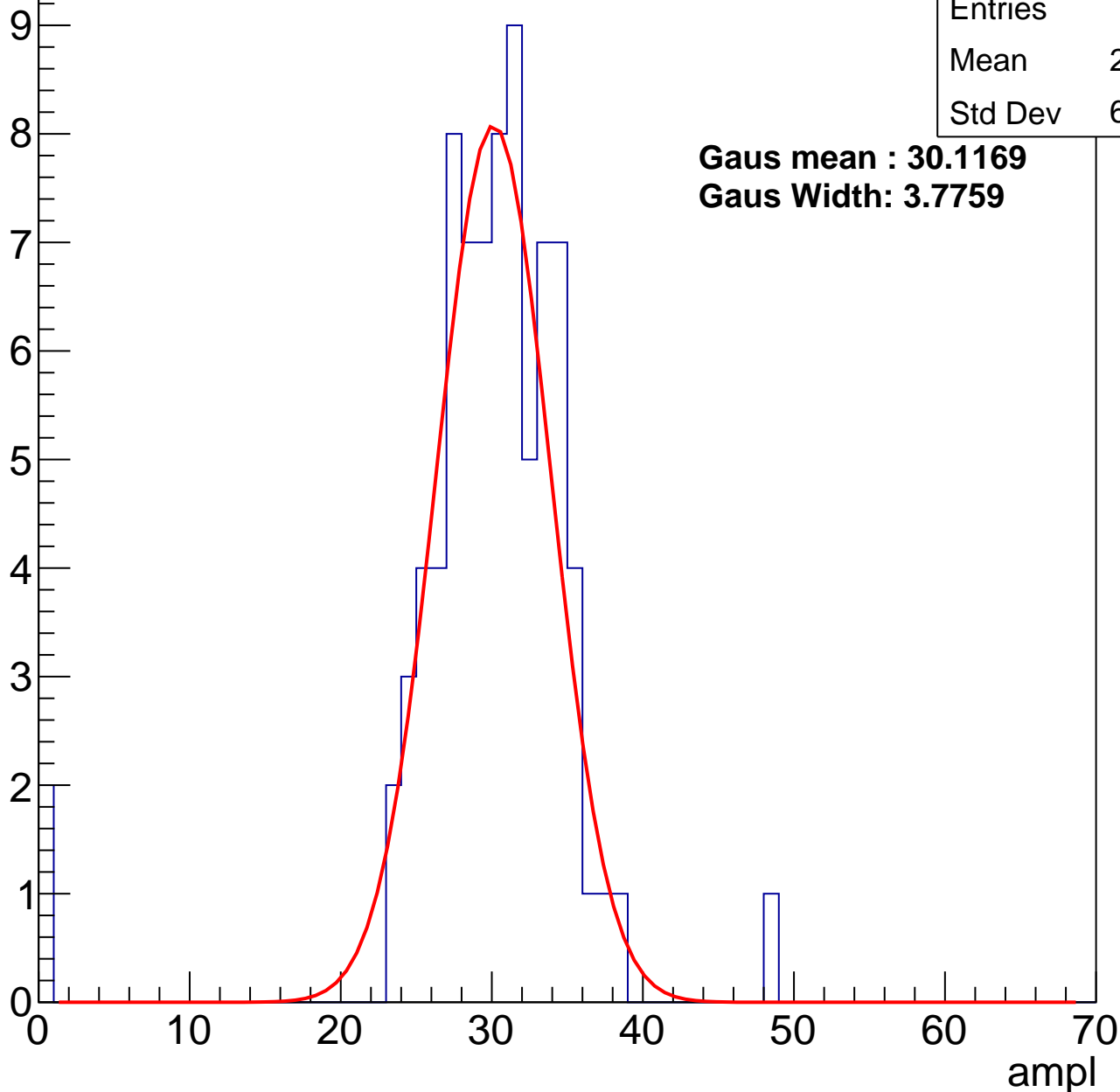
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	29.43
Std Dev	6.114

Gaus mean : 30.1169

Gaus Width: 3.7759



B1L101S, U12-ch117, adc1

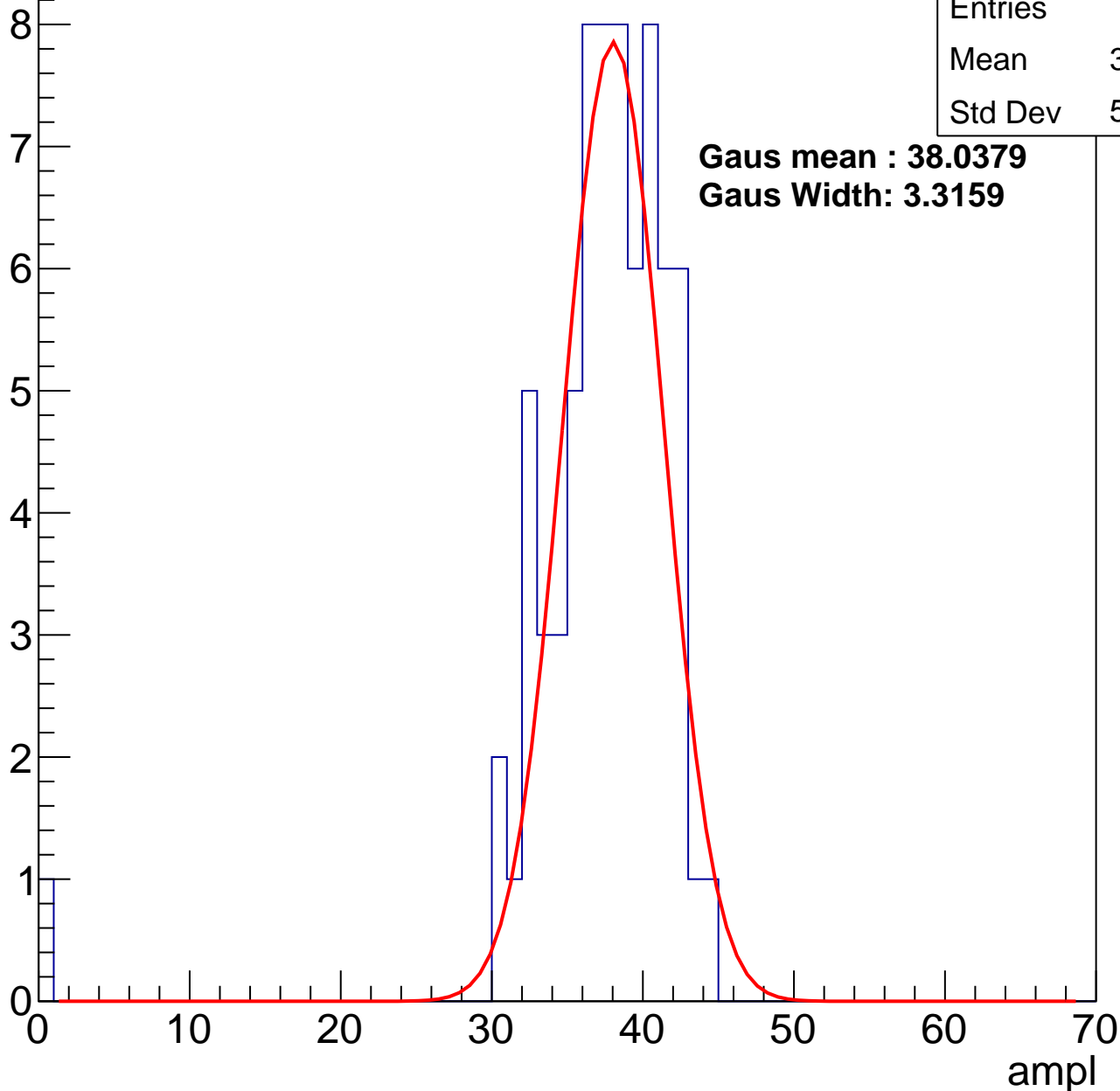
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.86
Std Dev	5.483

Gaus mean : 38.0379

Gaus Width: 3.3159



B1L101S, U12-ch117, adc2

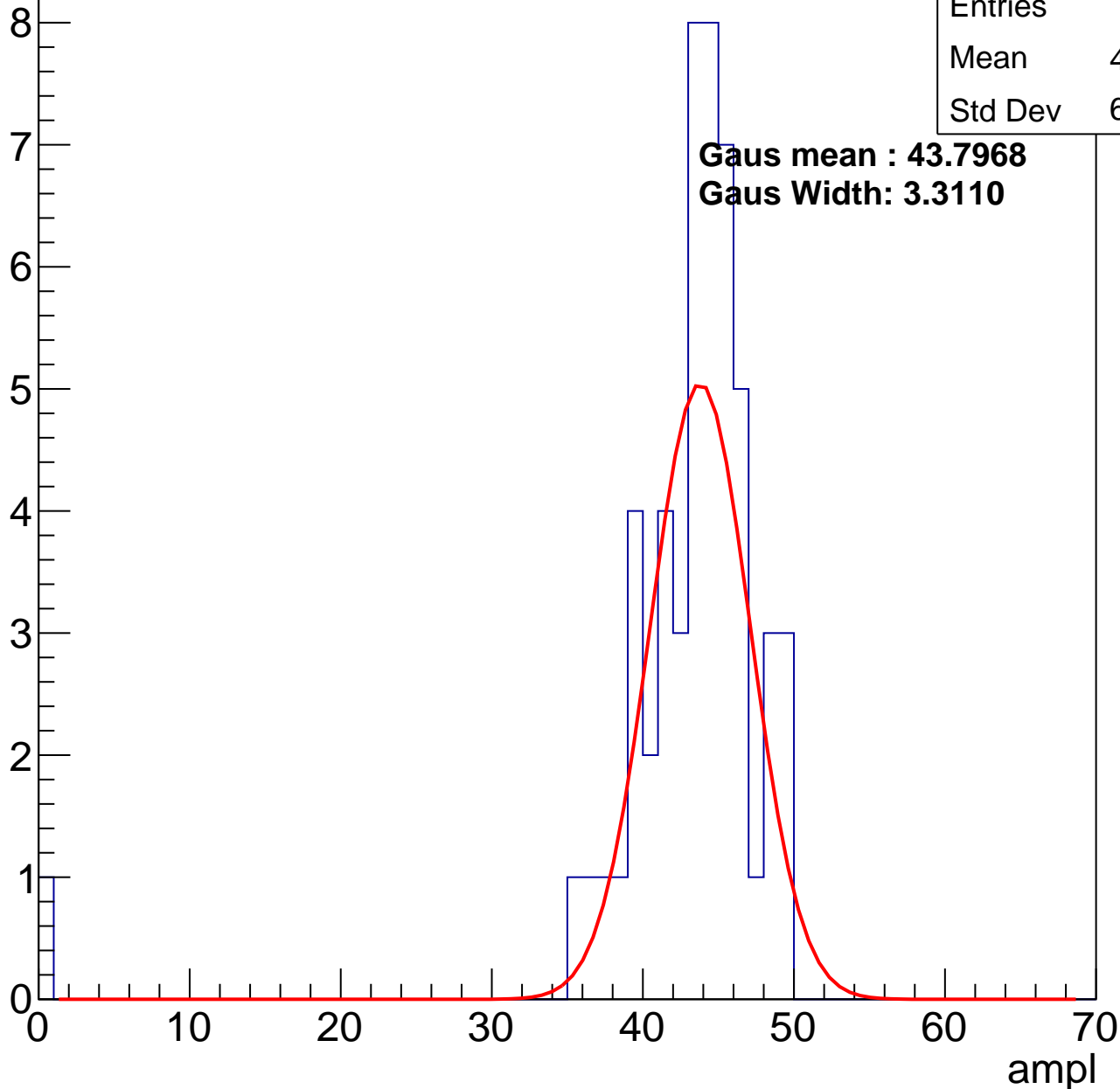
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.47
Std Dev	6.714

Gaus mean : 43.7968

Gaus Width: 3.3110

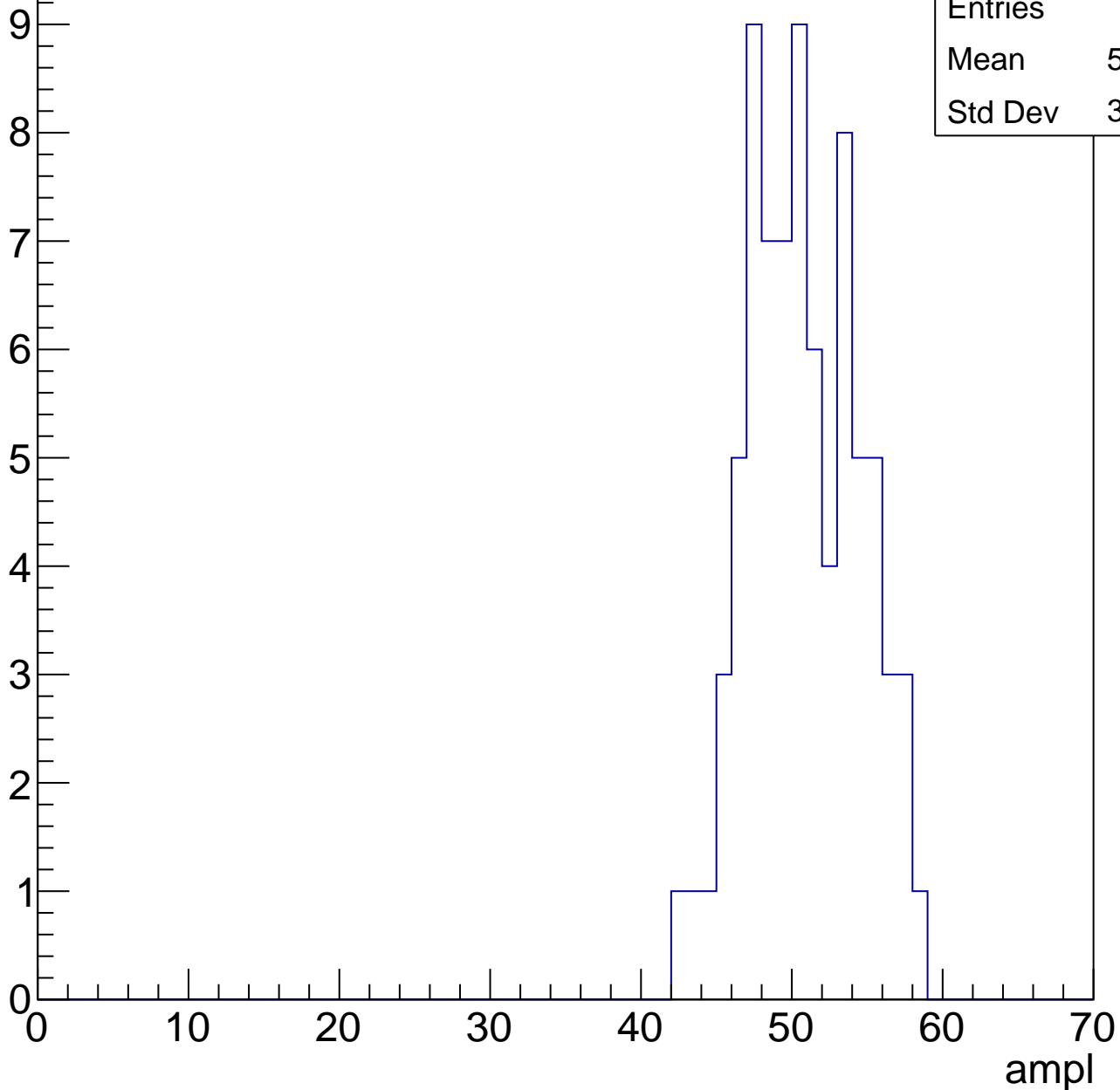


B1L101S, U12-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	50.33
Std Dev	3.633

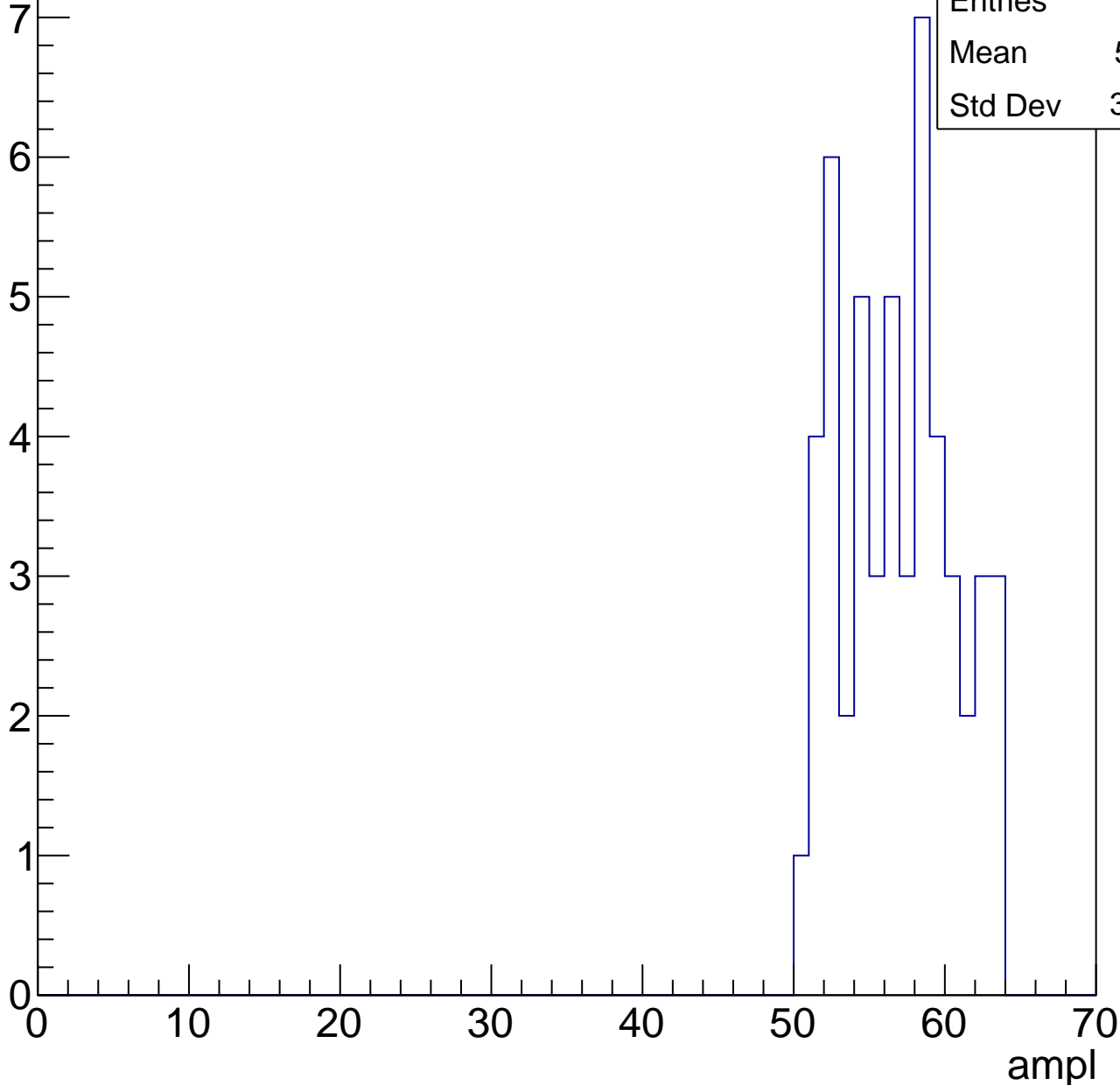


B1L101S, U12-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.41
Std Dev	3.663

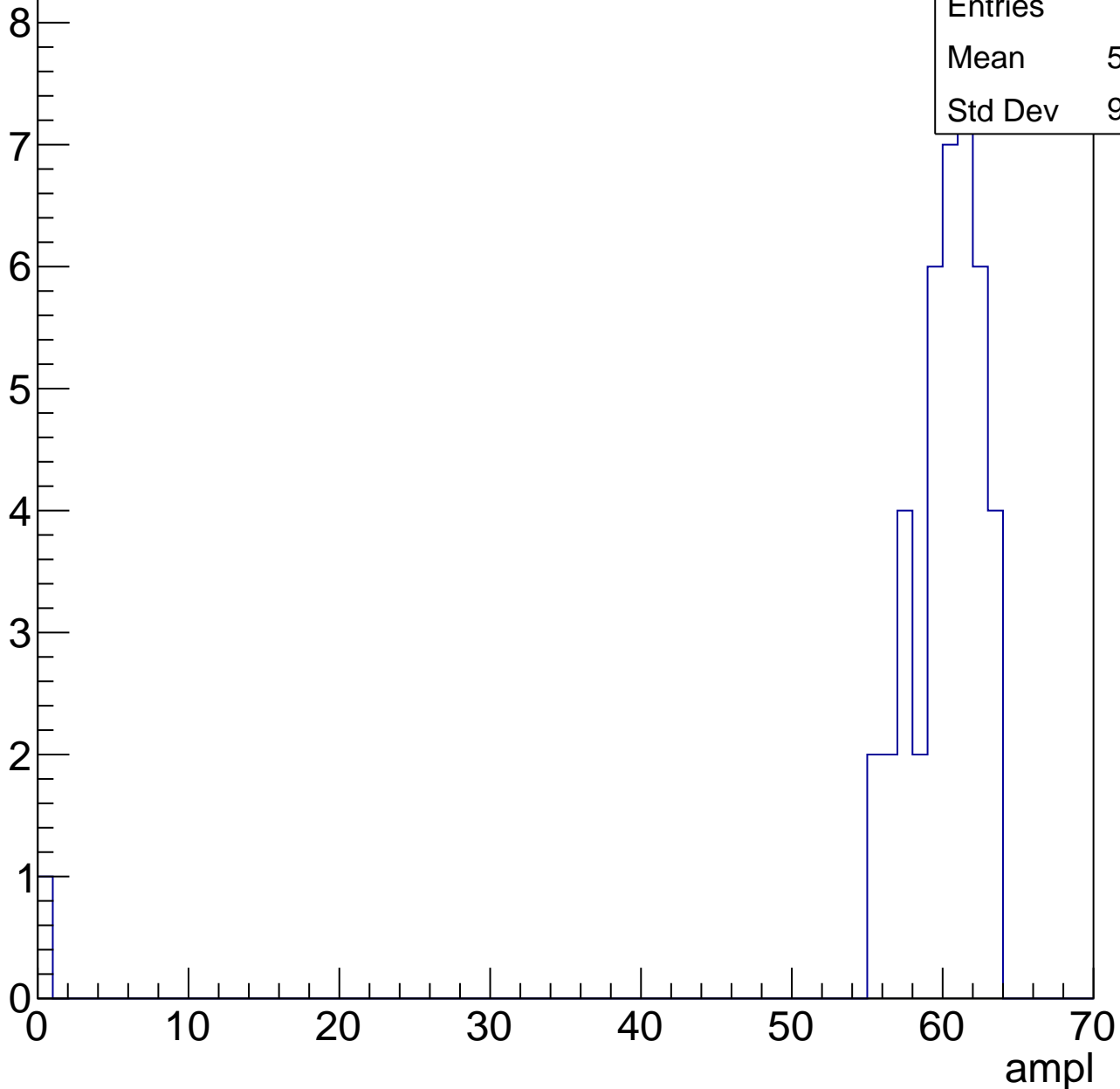


B1L101S, U12-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.38
Std Dev	9.373



B1L101S, U12-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch118, adc0

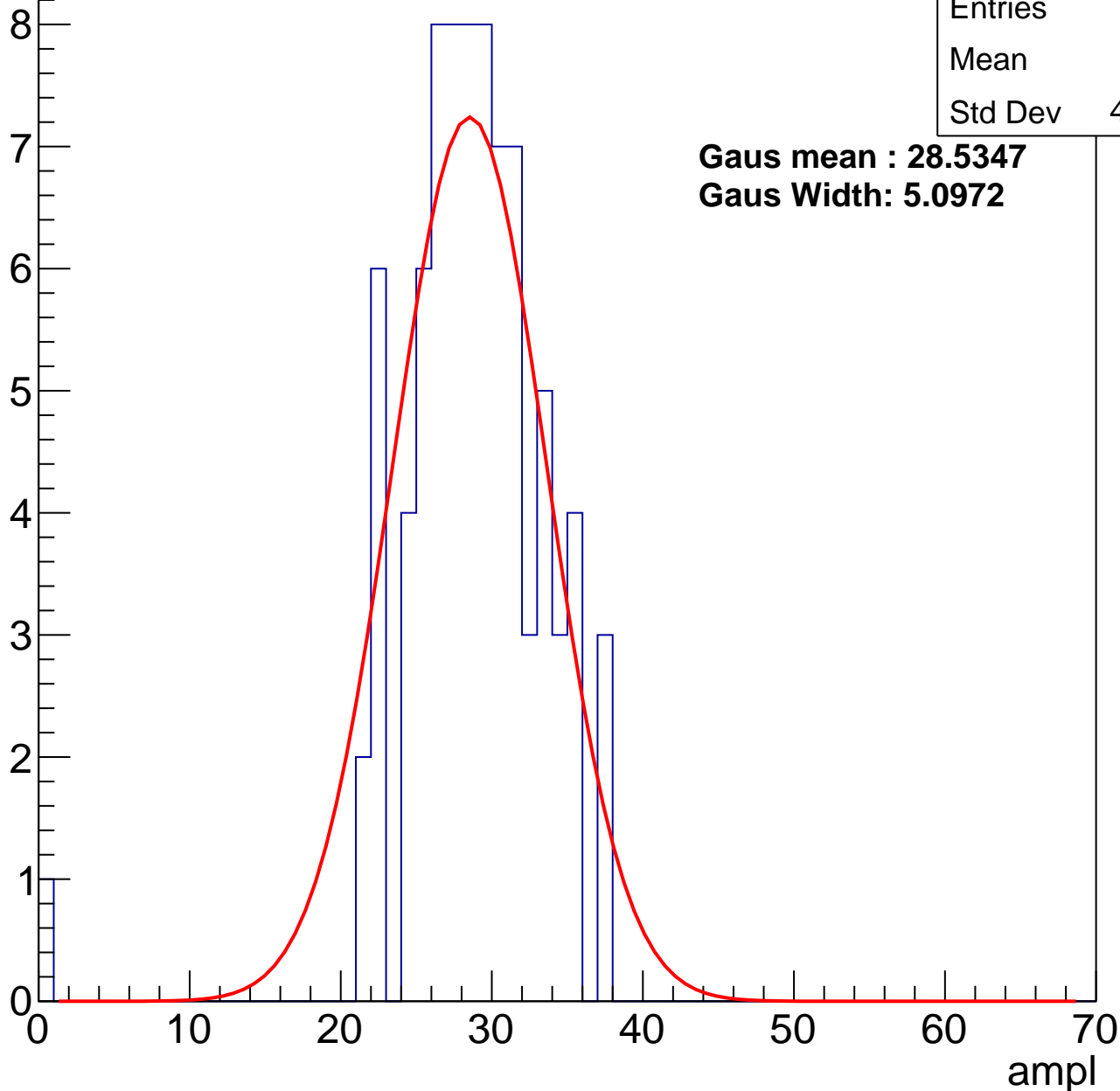
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	28.2
Std Dev	4.996

Gaus mean : 28.5347

Gaus Width: 5.0972



B1L101S, U12-ch118, adc1

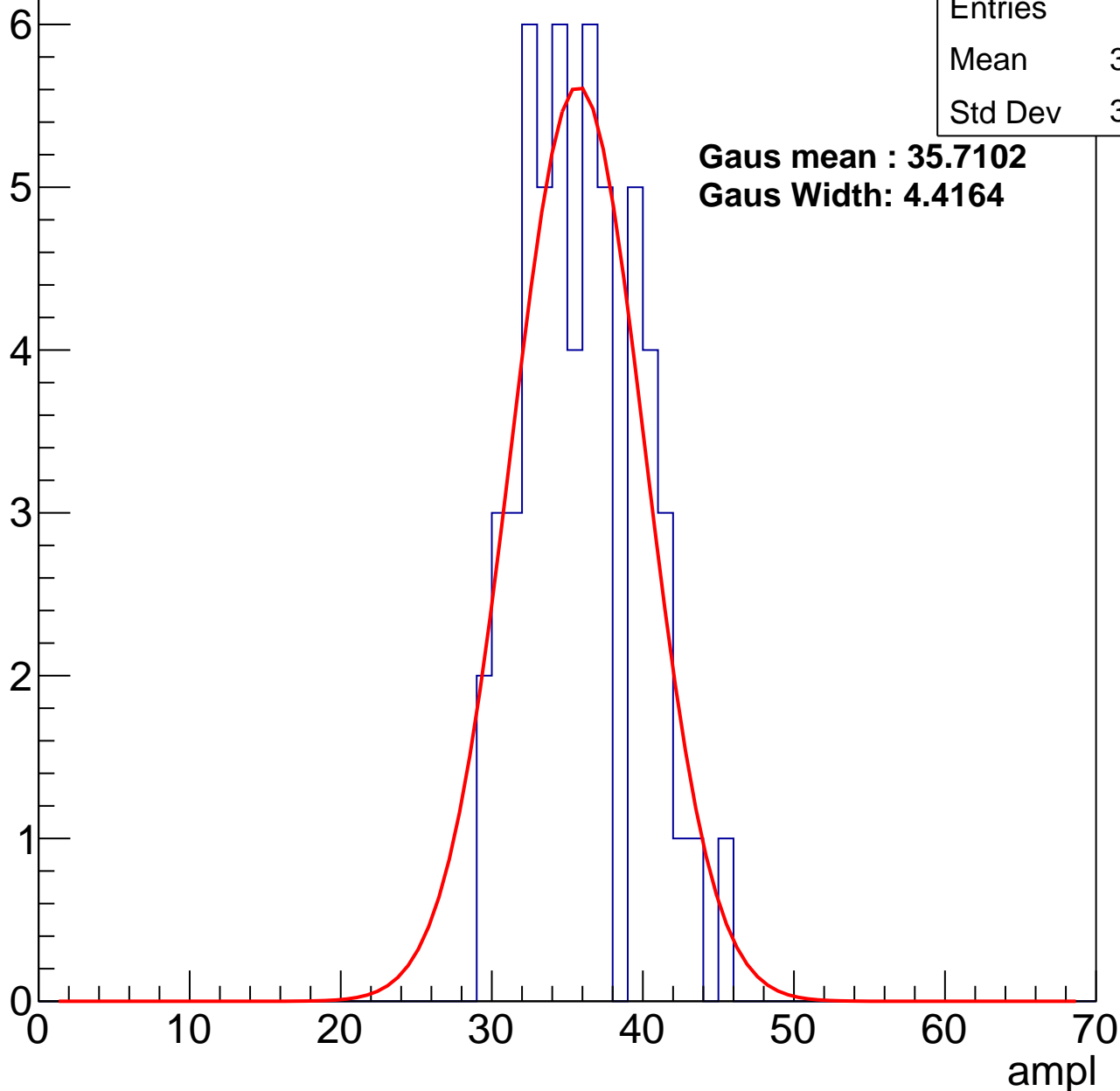
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.47
Std Dev	3.789

Gaus mean : 35.7102

Gaus Width: 4.4164



B1L101S, U12-ch118, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	41.33
Std Dev	3.594

Gaus mean : 42.1942

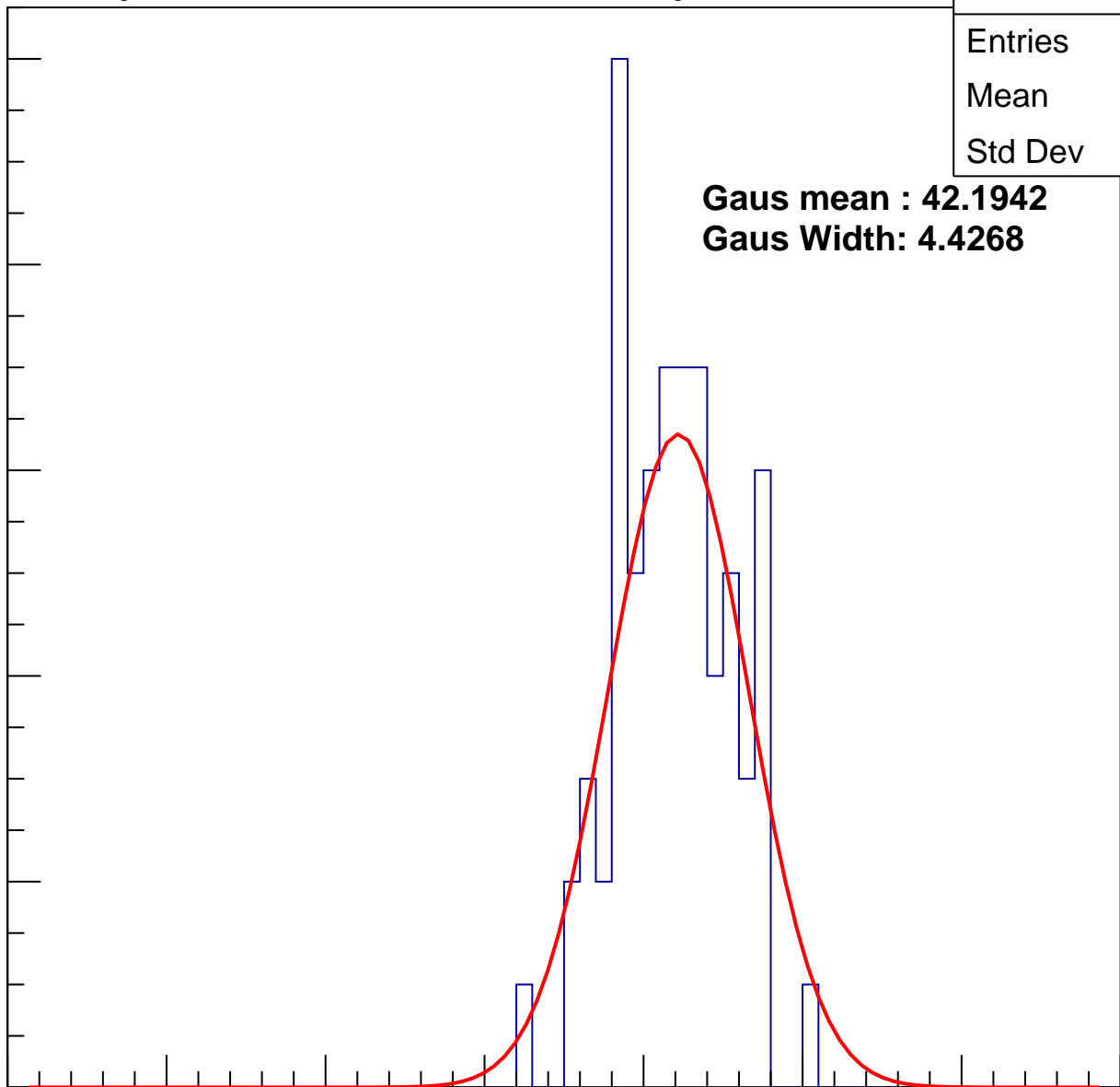
Gaus Width: 4.4268

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

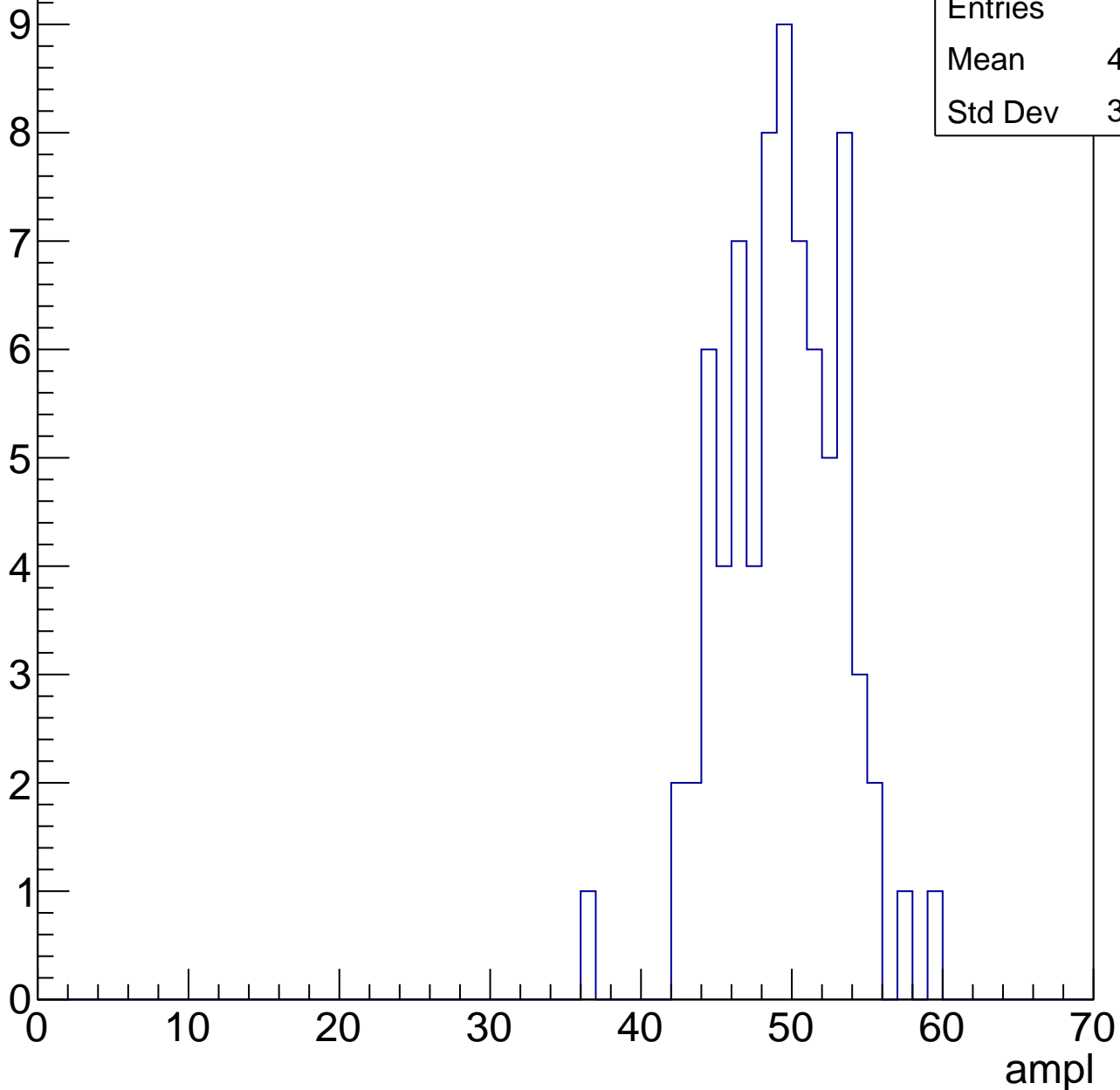


B1L101S, U12-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

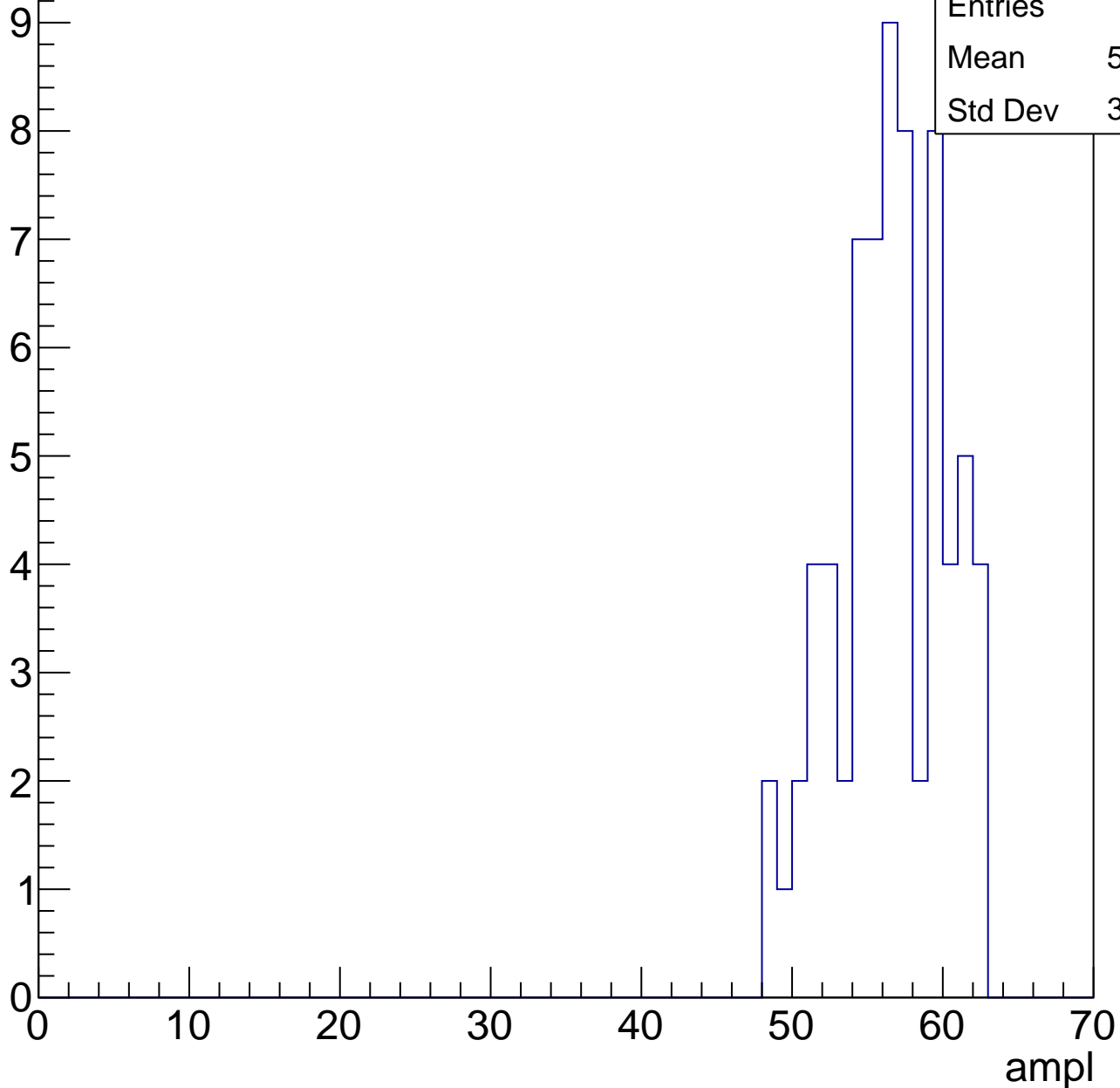
Entries	76
Mean	48.86
Std Dev	3.896



B1L101S, U12-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



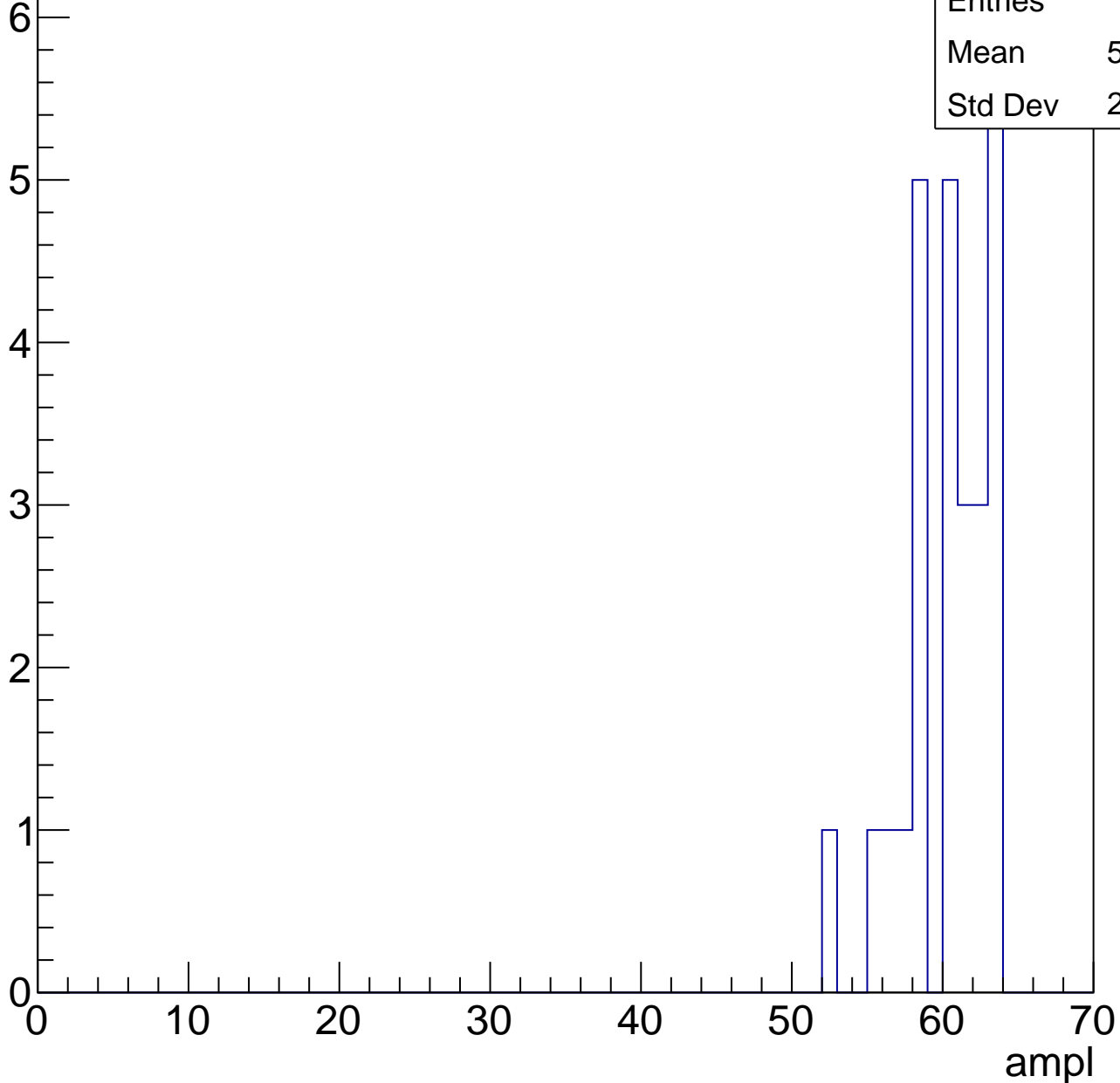
Entries	69
Mean	56.04
Std Dev	3.585

B1L101S, U12-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

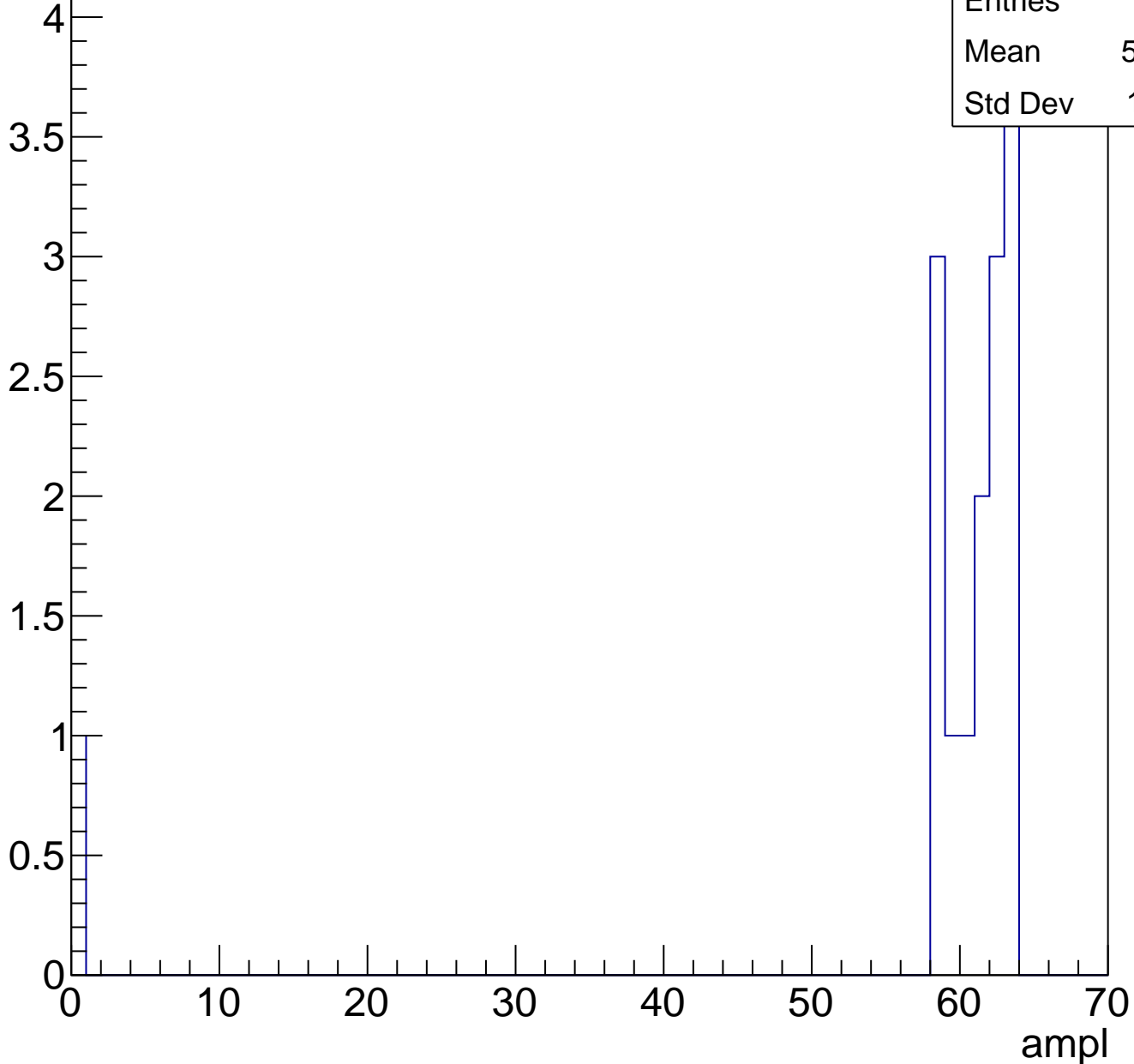
Entries	26
Mean	59.88
Std Dev	2.792



B1L101S, U12-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch119, adc0

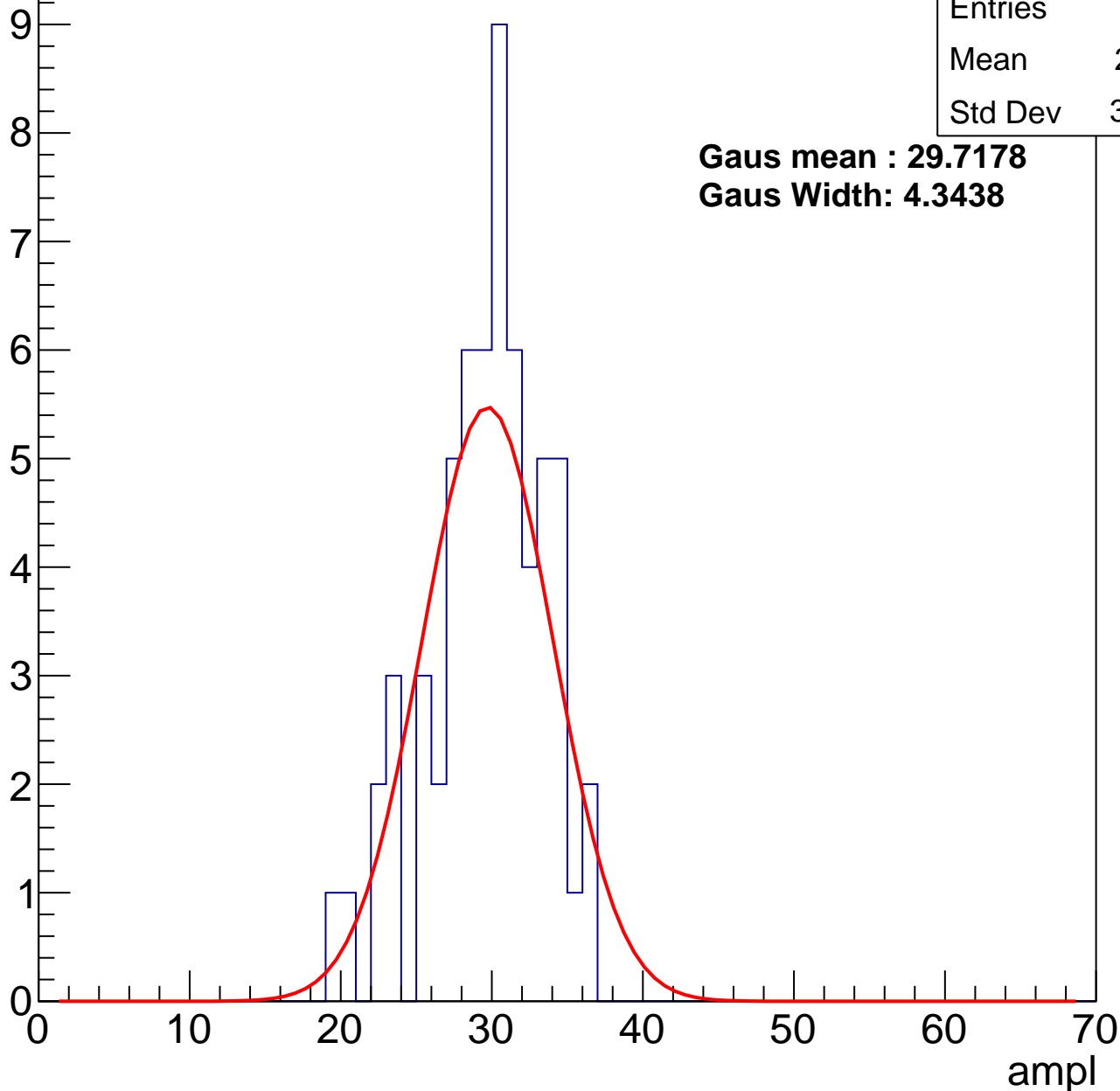
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.21
Std Dev	3.807

Gaus mean : 29.7178

Gaus Width: 4.3438



B1L101S, U12-ch119, adc1

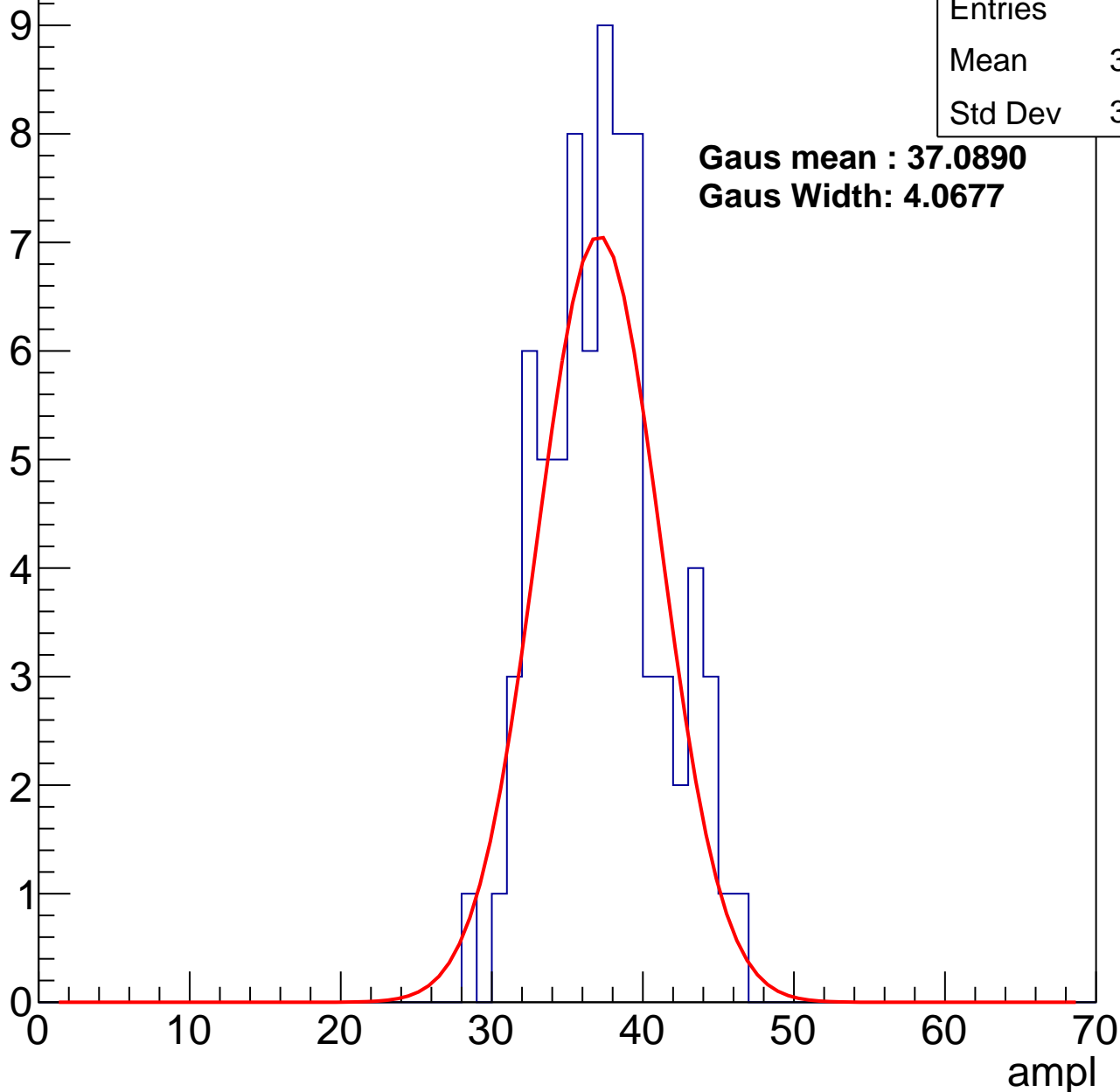
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.95
Std Dev	3.868

Gaus mean : 37.0890

Gaus Width: 4.0677

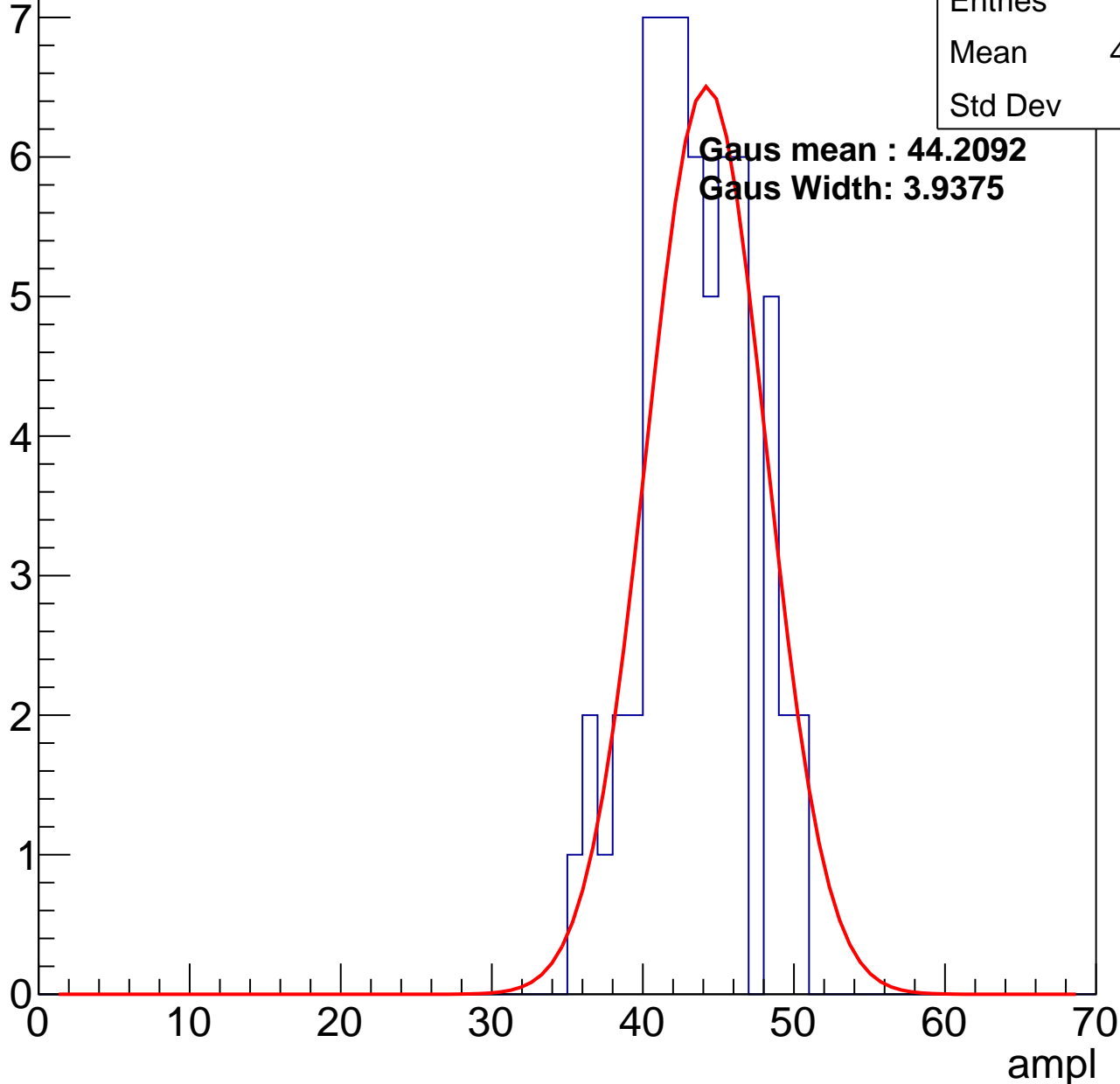


B1L101S, U12-ch119, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.97
Std Dev	3.52

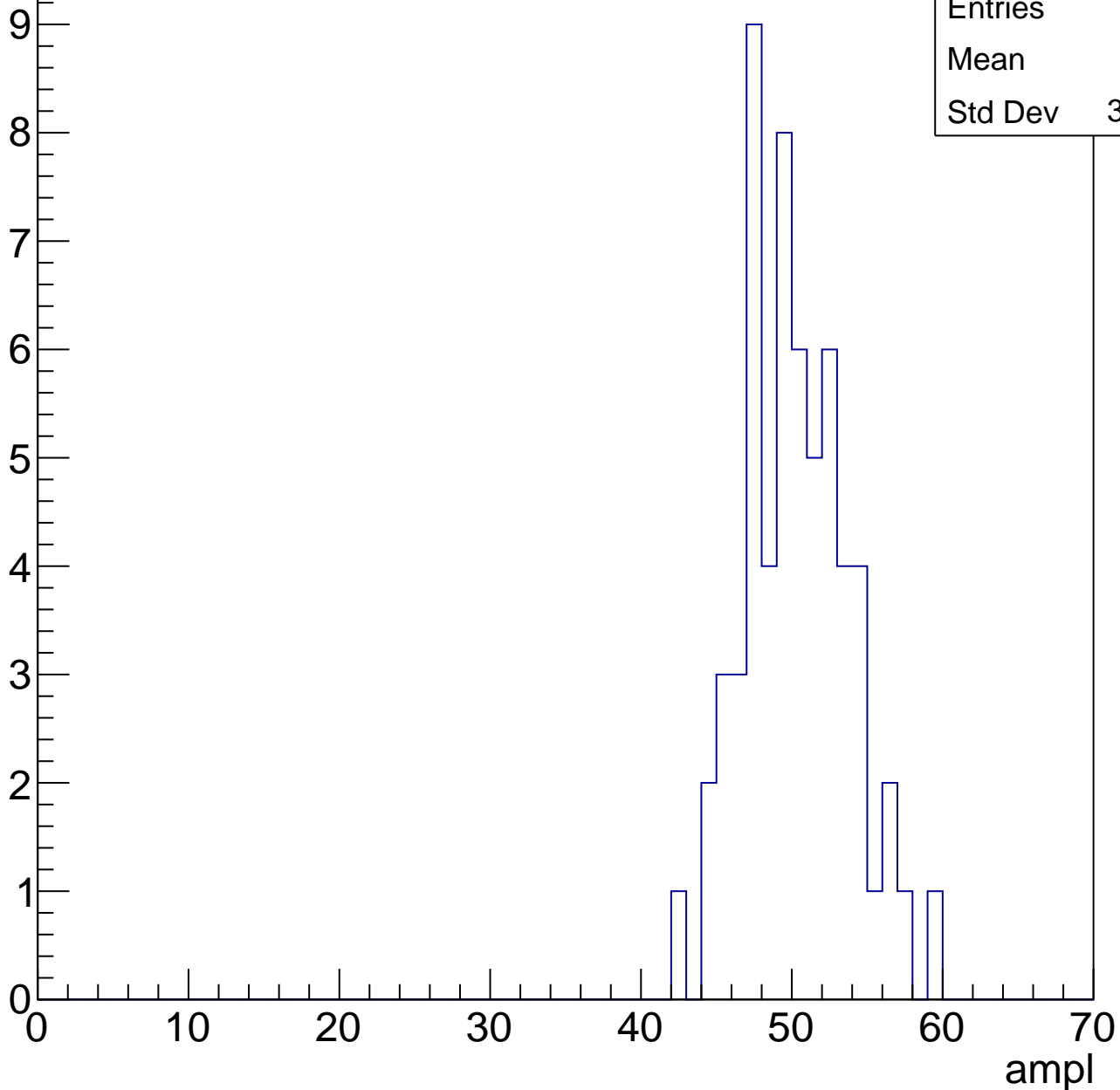


B1L101S, U12-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

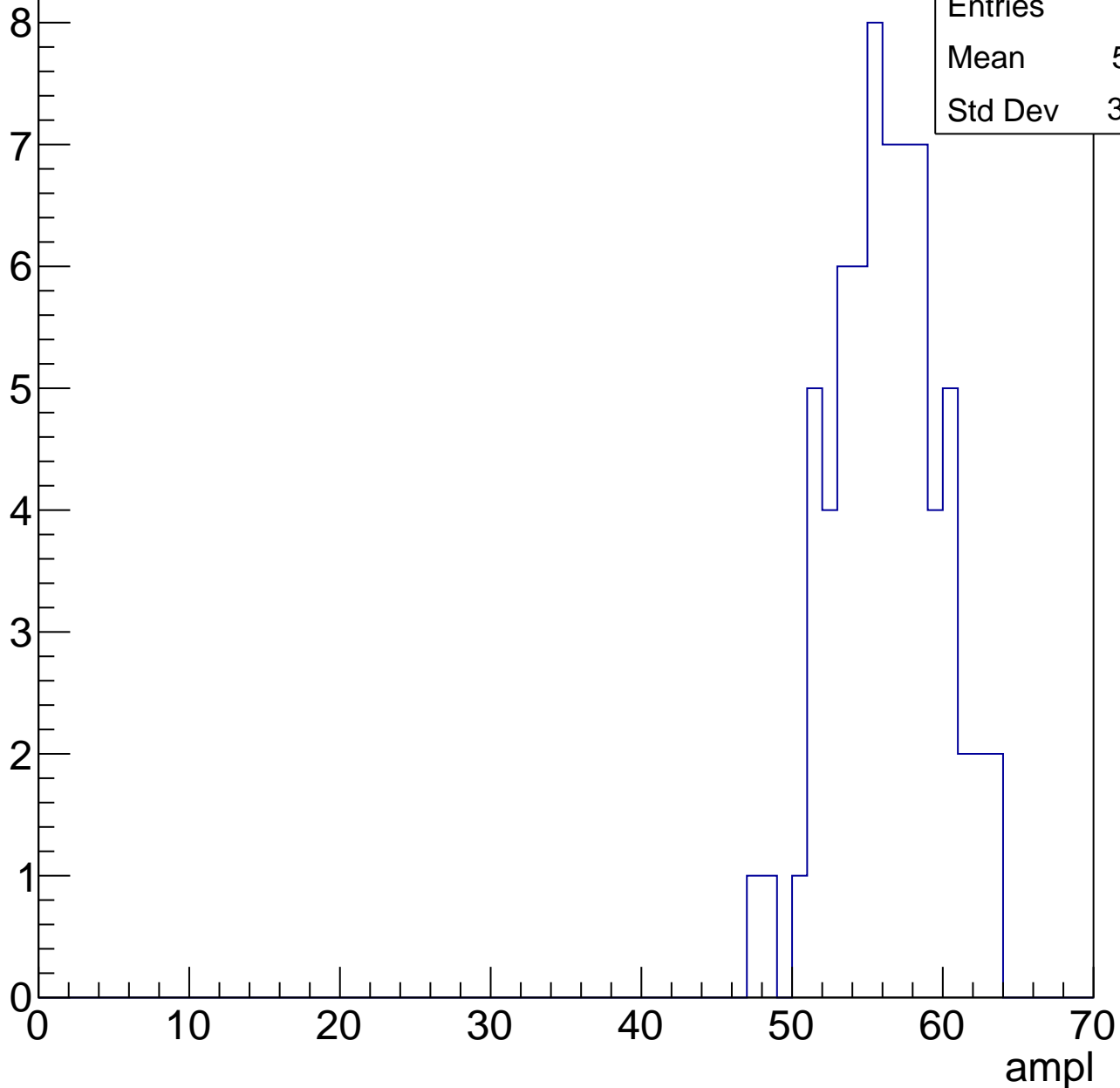
Entries	60
Mean	49.8
Std Dev	3.458



B1L101S, U12-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



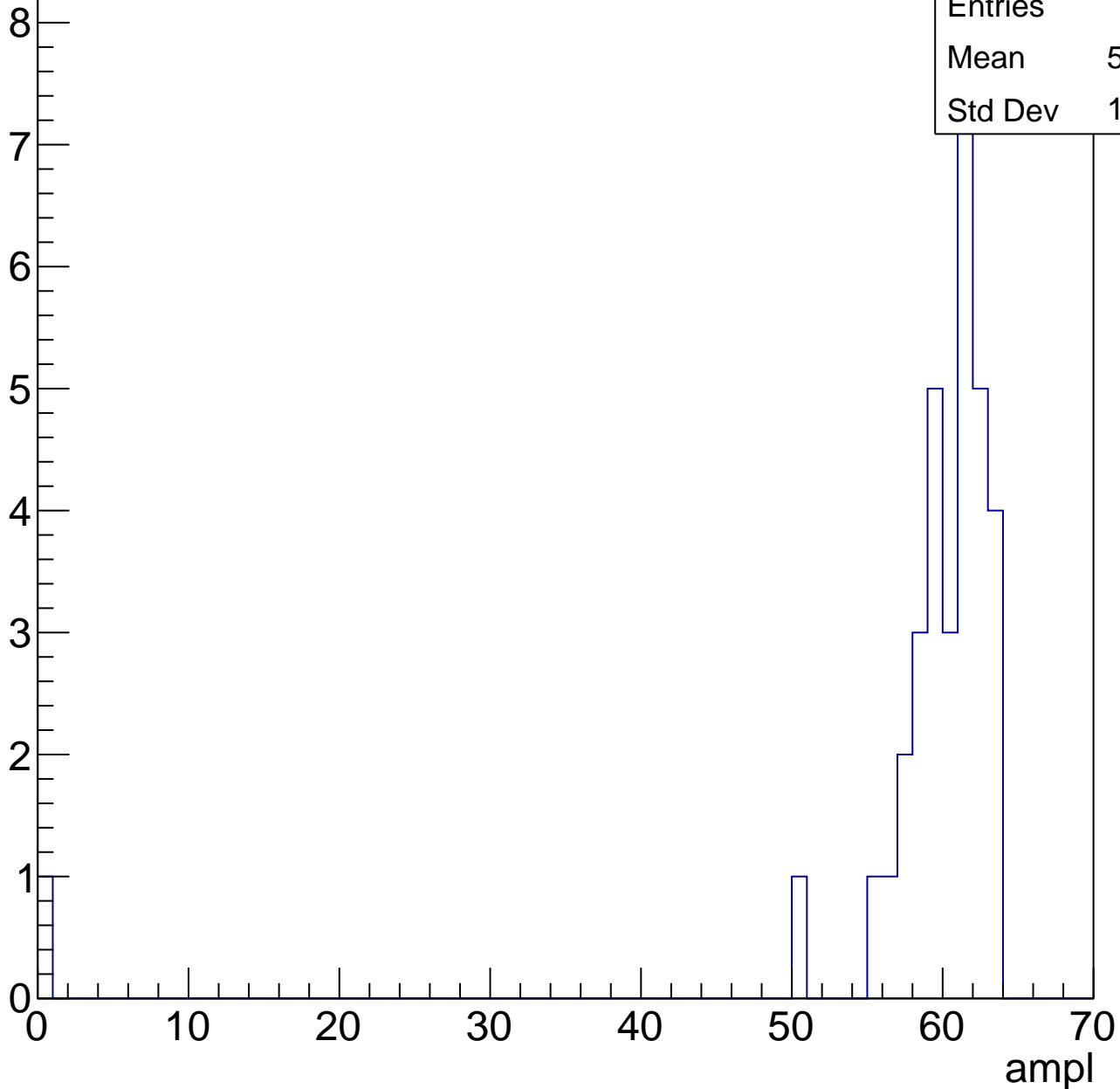
Entries	68
Mean	55.81
Std Dev	3.482

B1L101S, U12-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

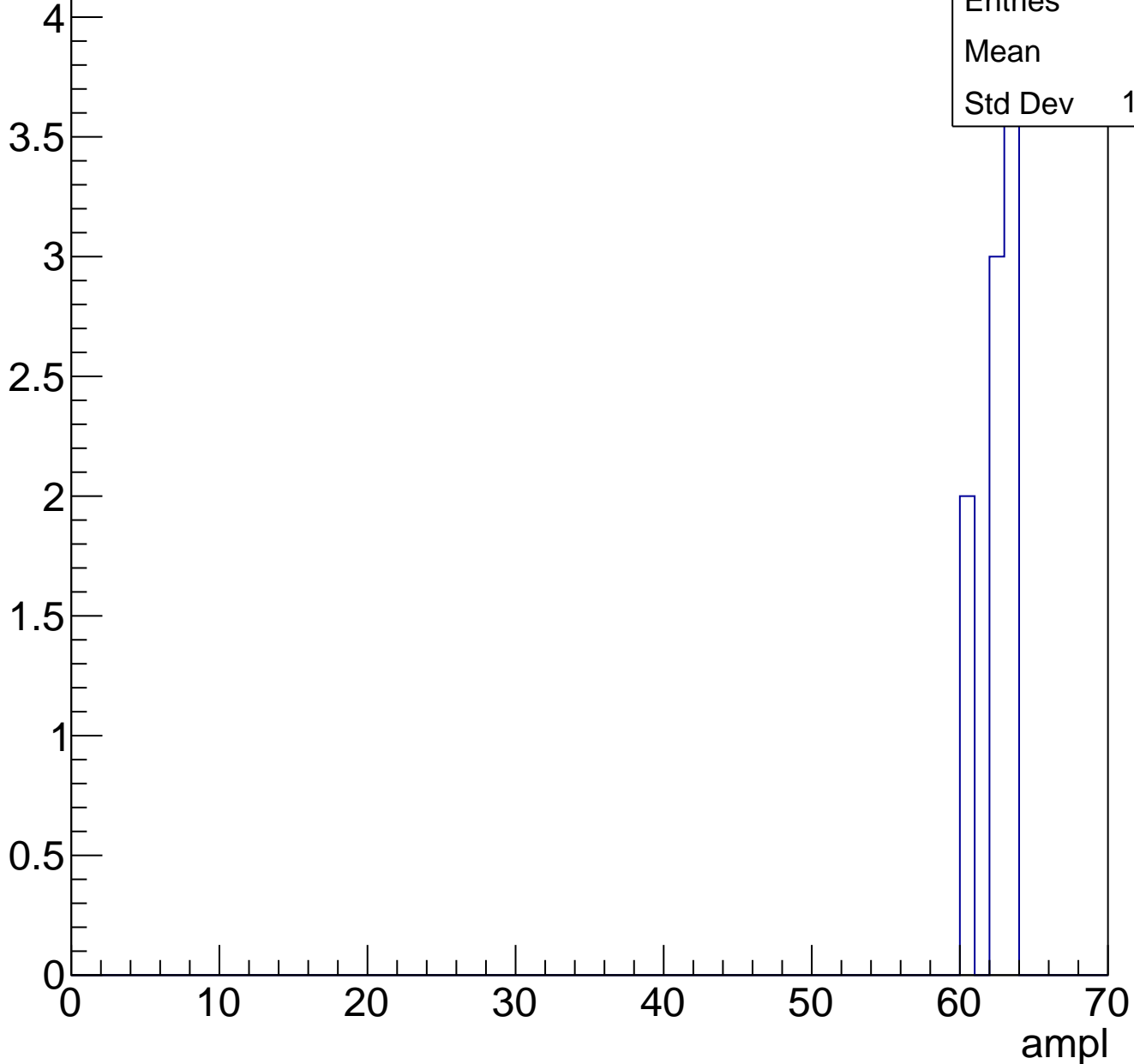
Entries	34
Mean	58.06
Std Dev	10.45



B1L101S, U12-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch120, adc0

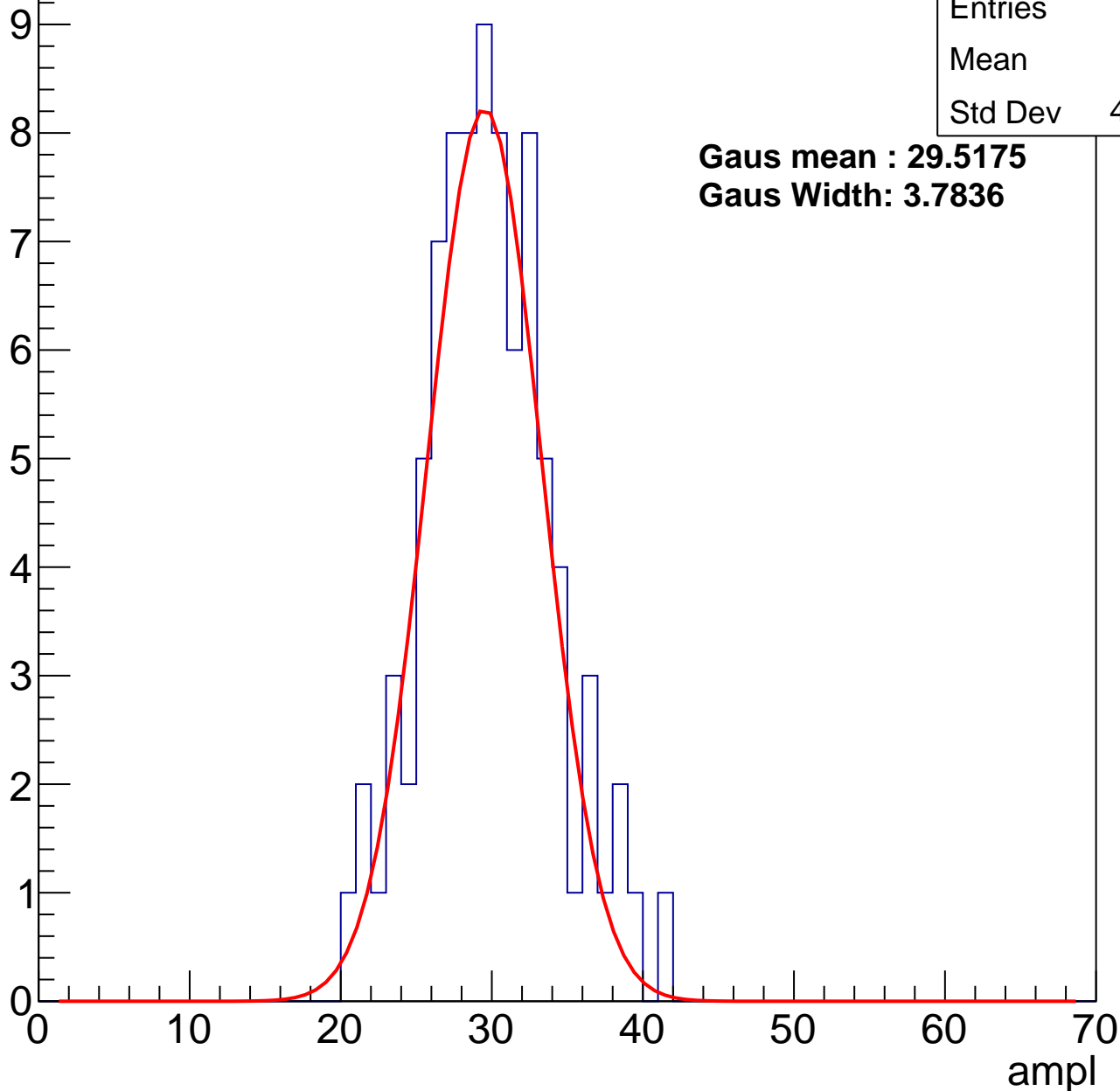
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.4
Std Dev	4.208

Gaus mean : 29.5175

Gaus Width: 3.7836



B1L101S, U12-ch120, adc1

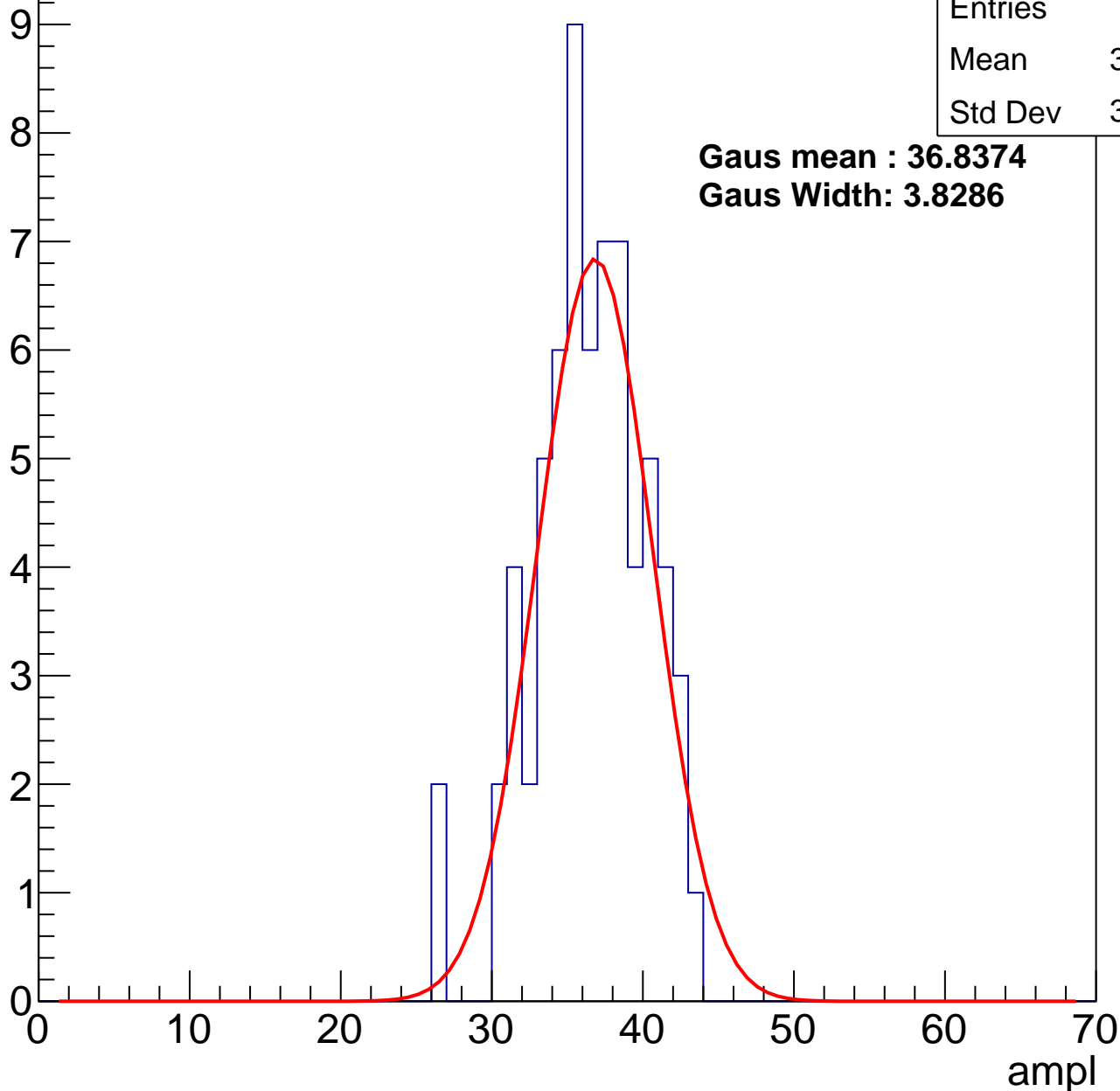
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.03
Std Dev	3.645

Gaus mean : 36.8374

Gaus Width: 3.8286



B1L101S, U12-ch120, adc2

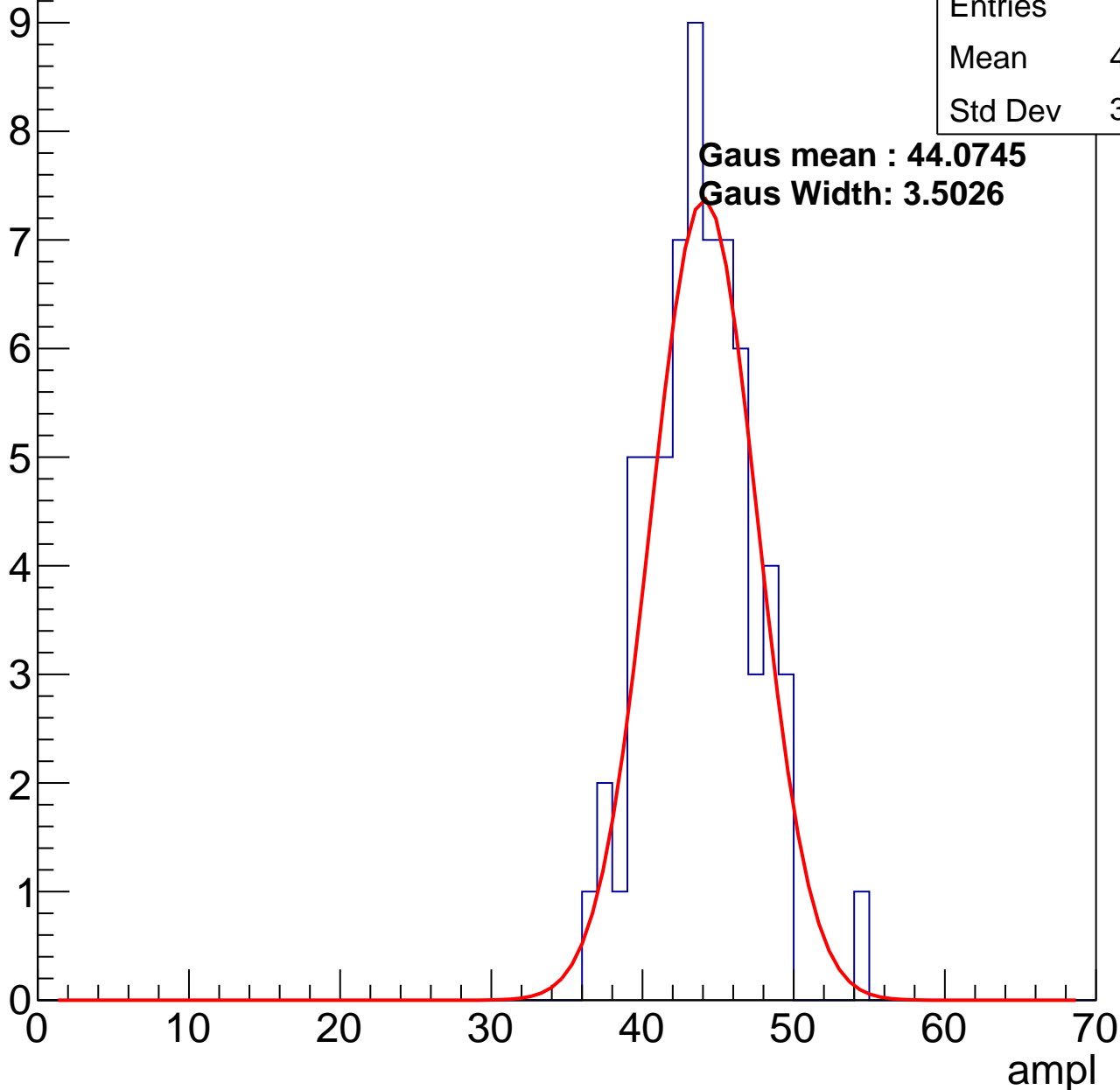
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.36
Std Dev	3.383

Gaus mean : 44.0745

Gaus Width: 3.5026

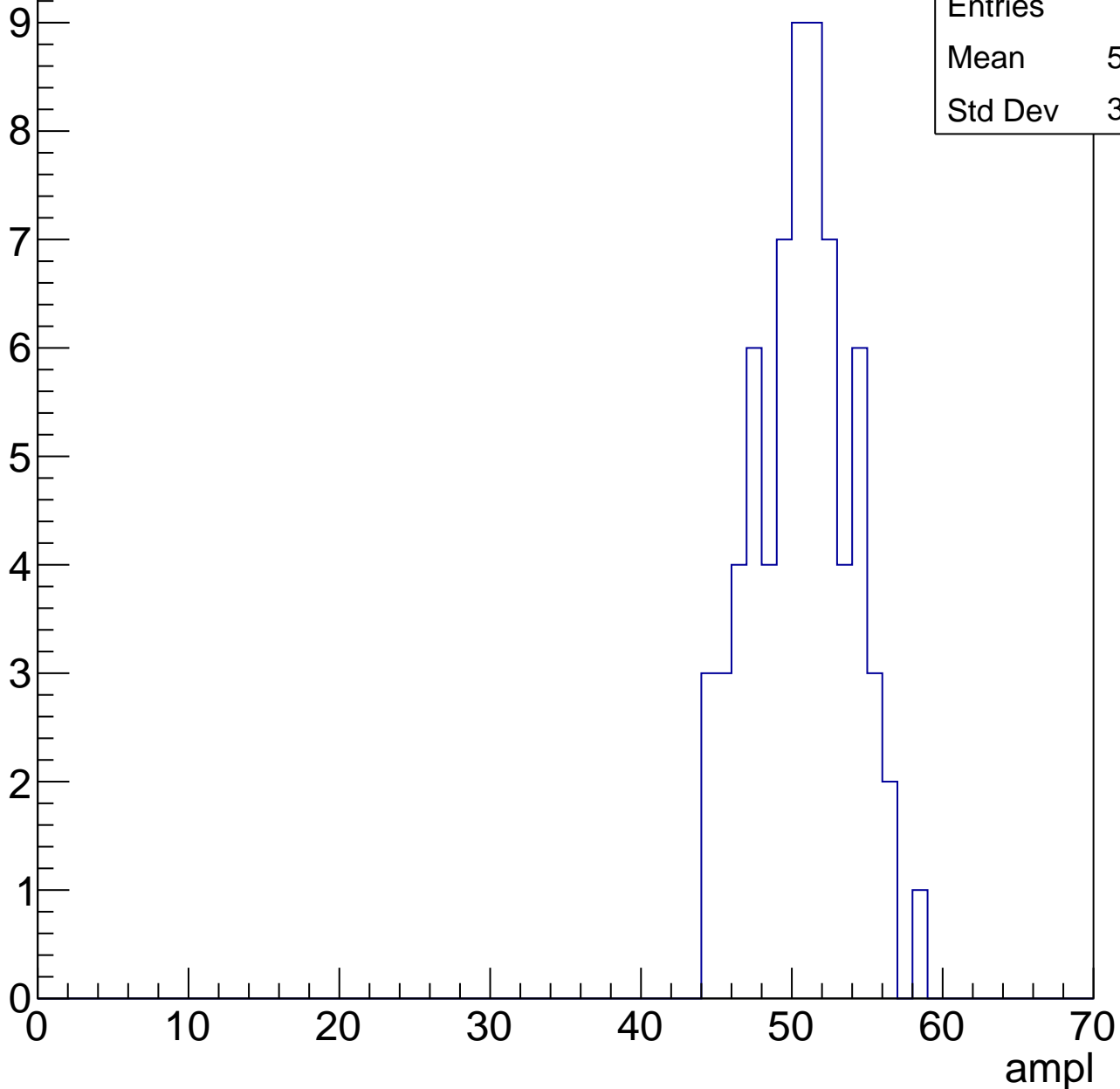


B1L101S, U12-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	50.18
Std Dev	3.213

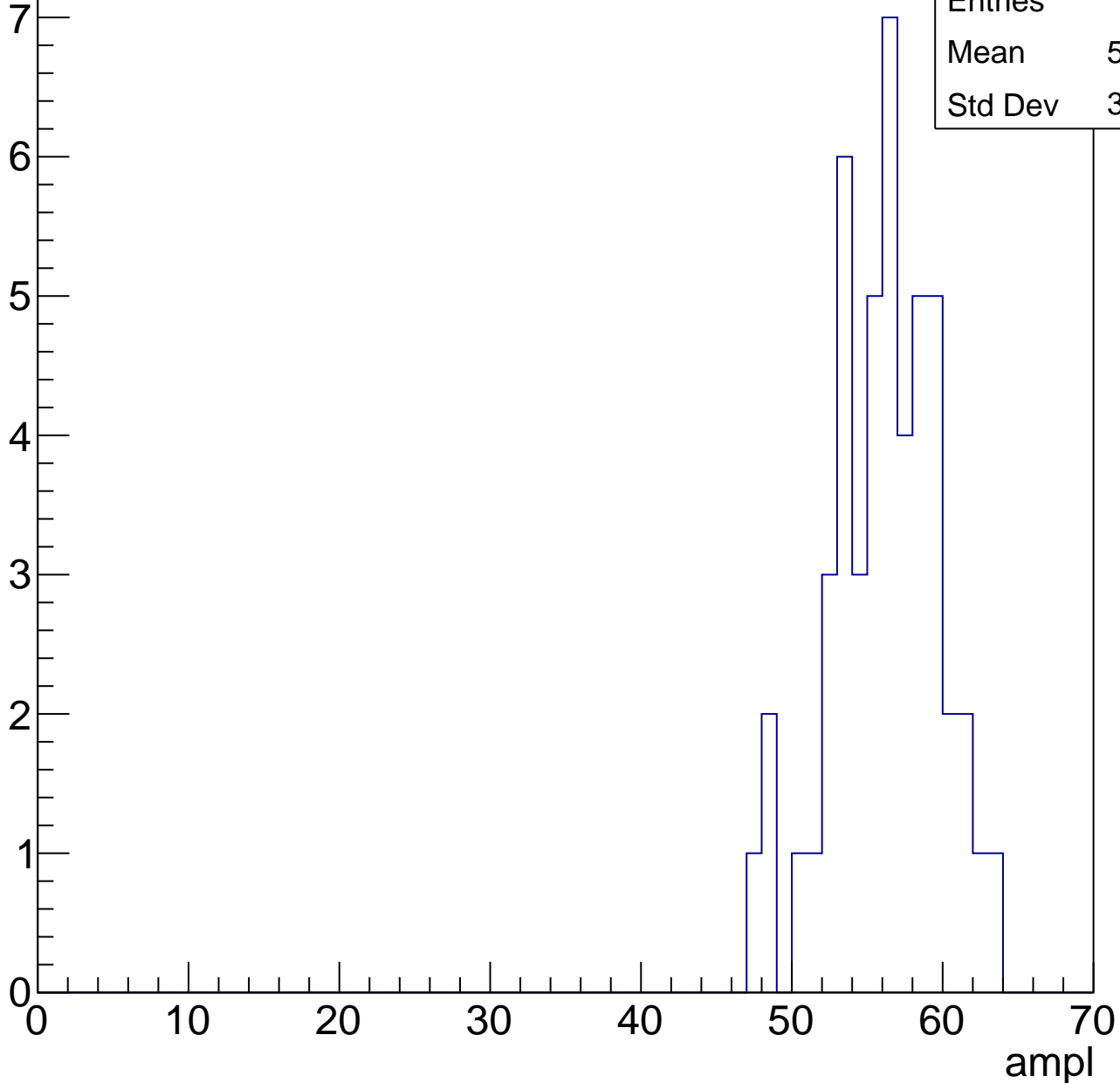


B1L101S, U12-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.65
Std Dev	3.555

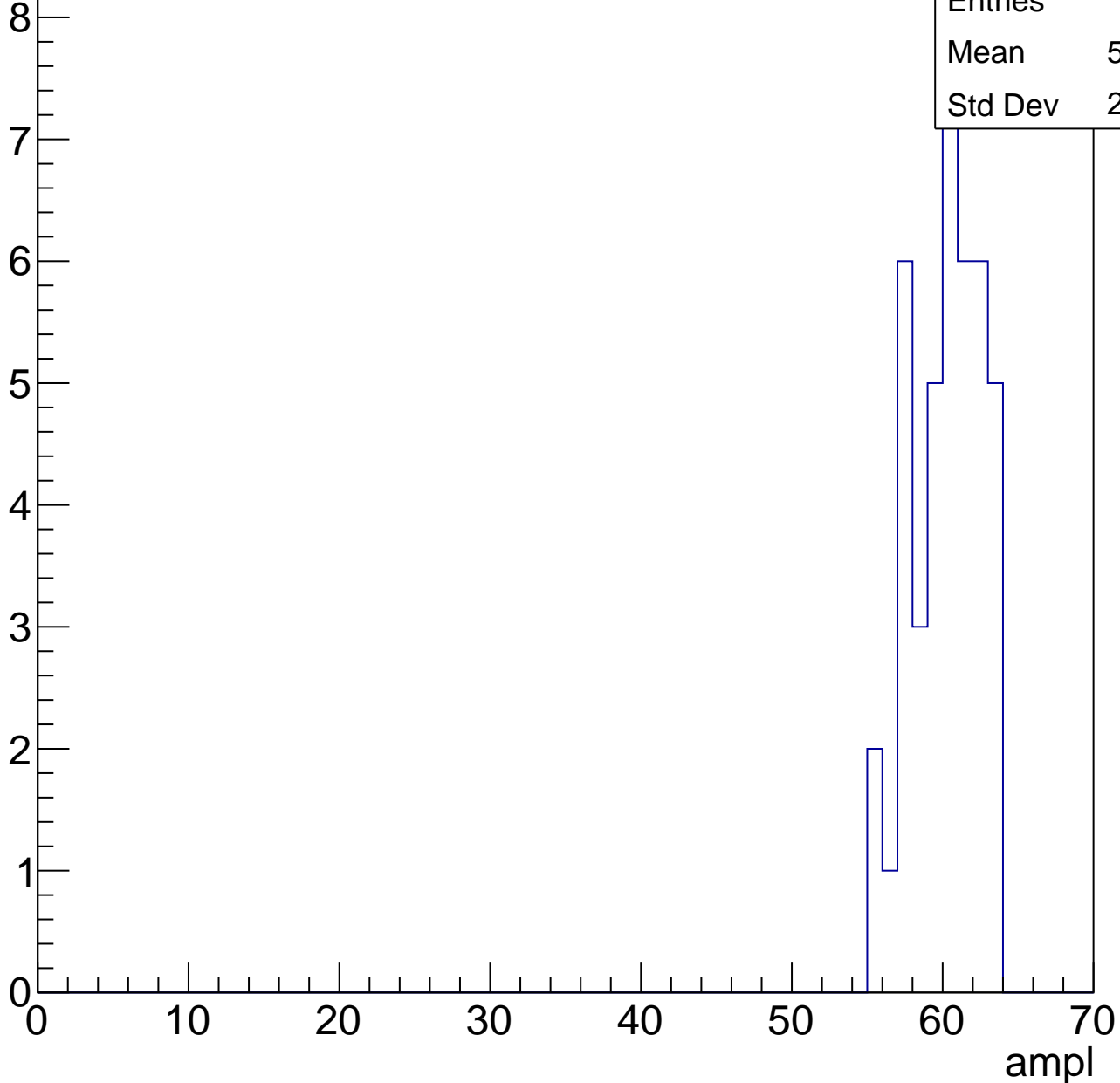


B1L101S, U12-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

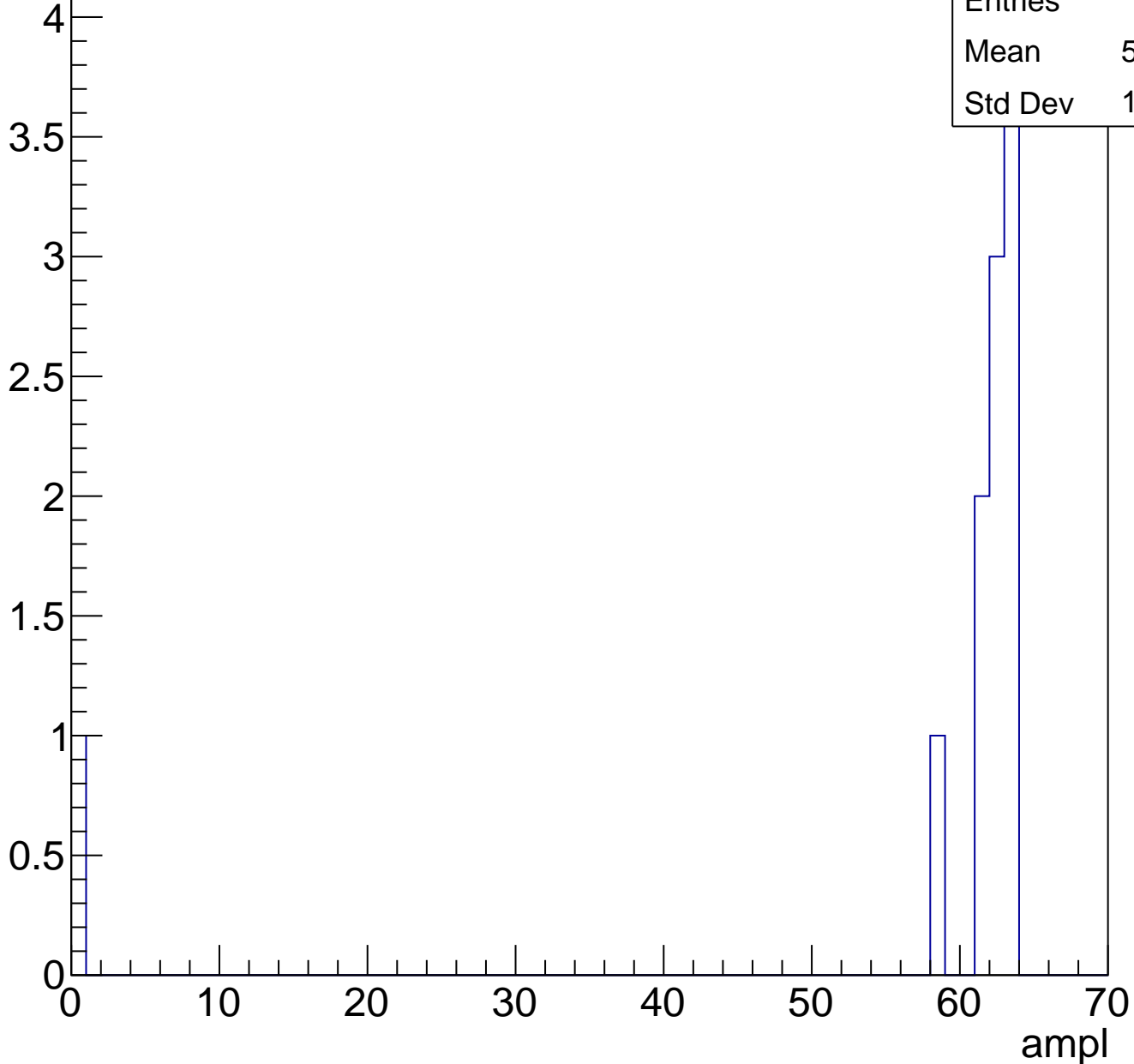
Entries	42
Mean	59.76
Std Dev	2.234



B1L101S, U12-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

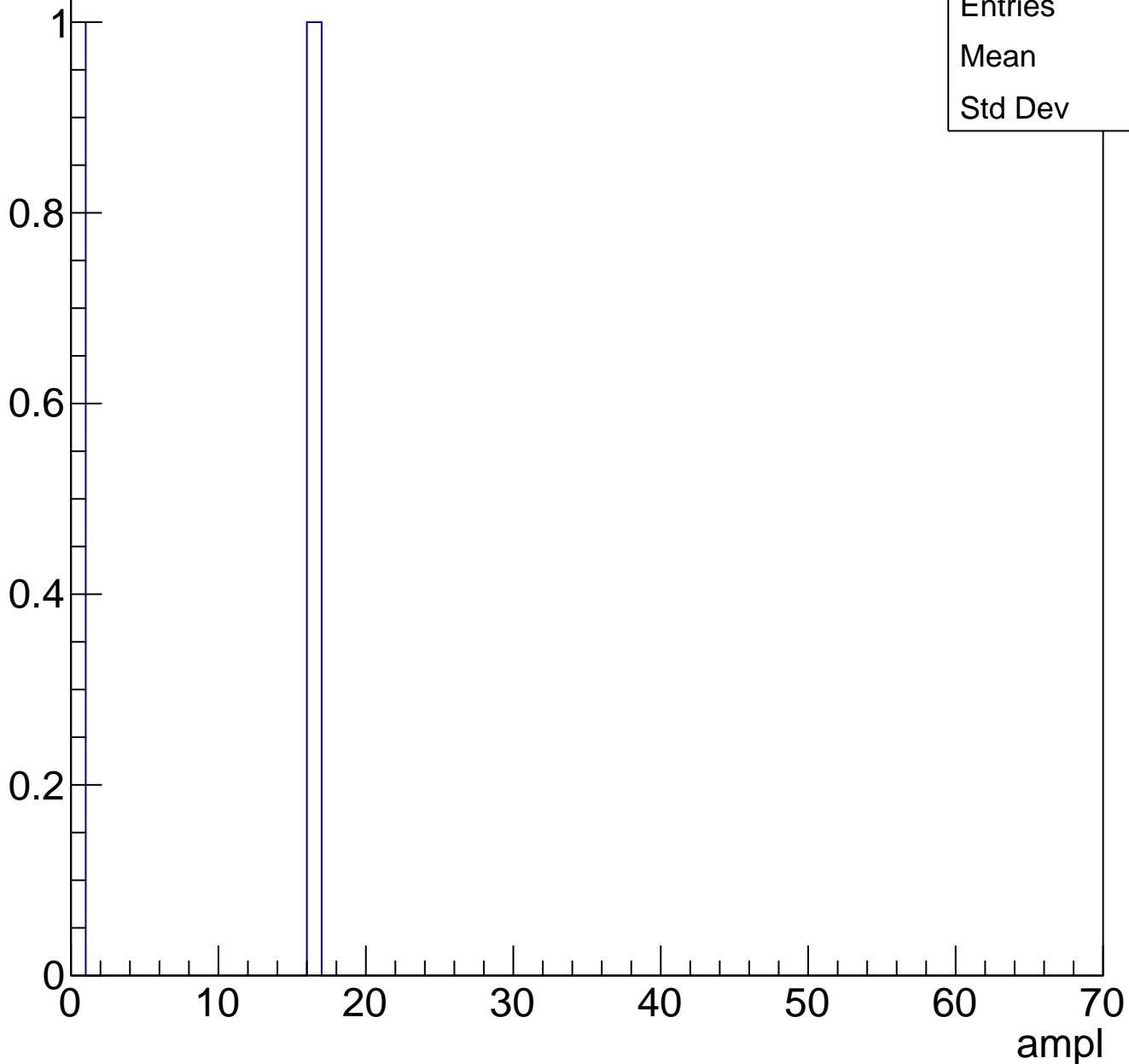
Entry



B1L101S, U12-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch121, adc0

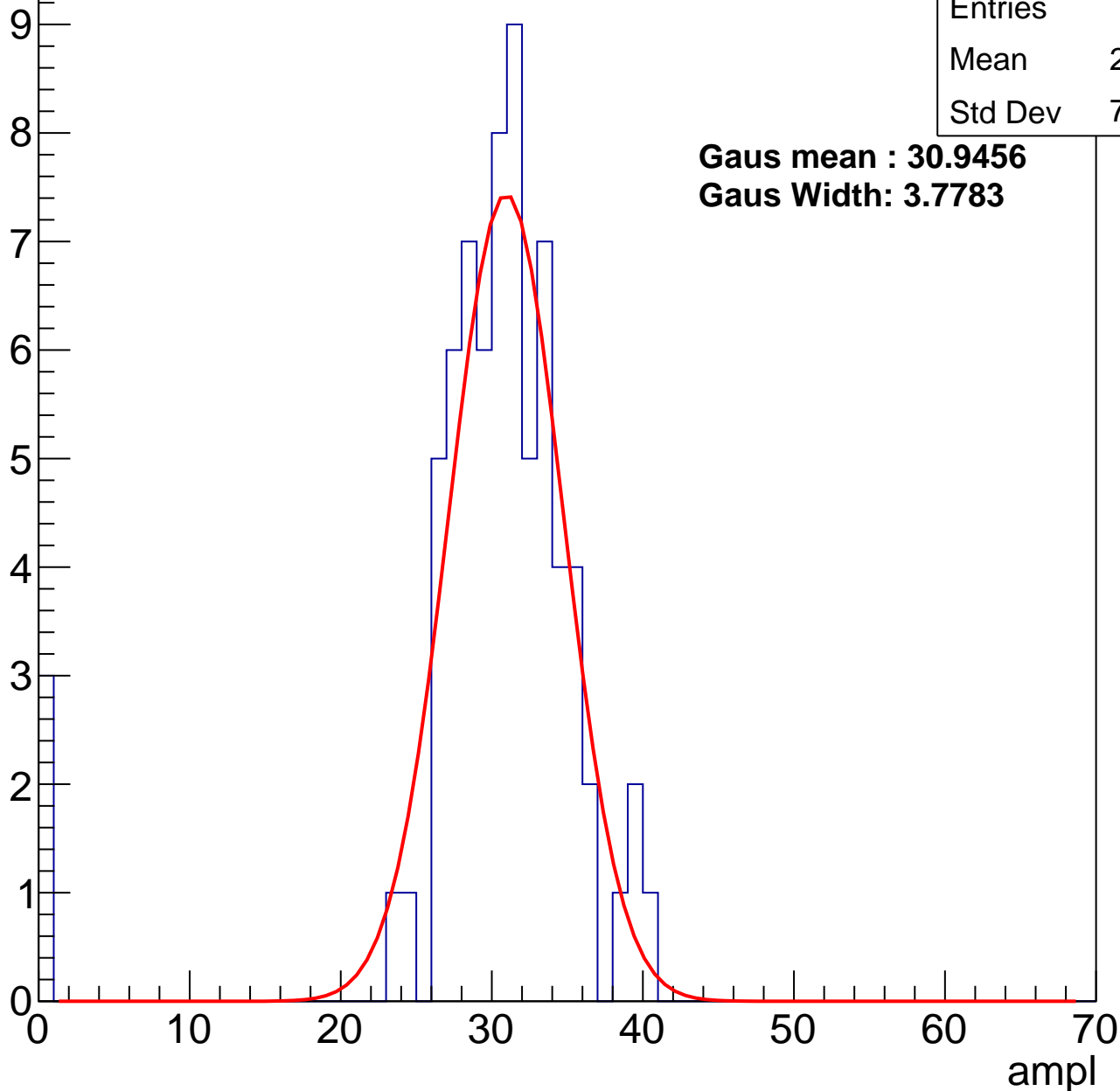
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.49
Std Dev	7.059

Gaus mean : 30.9456

Gaus Width: 3.7783



B1L101S, U12-ch121, adc1

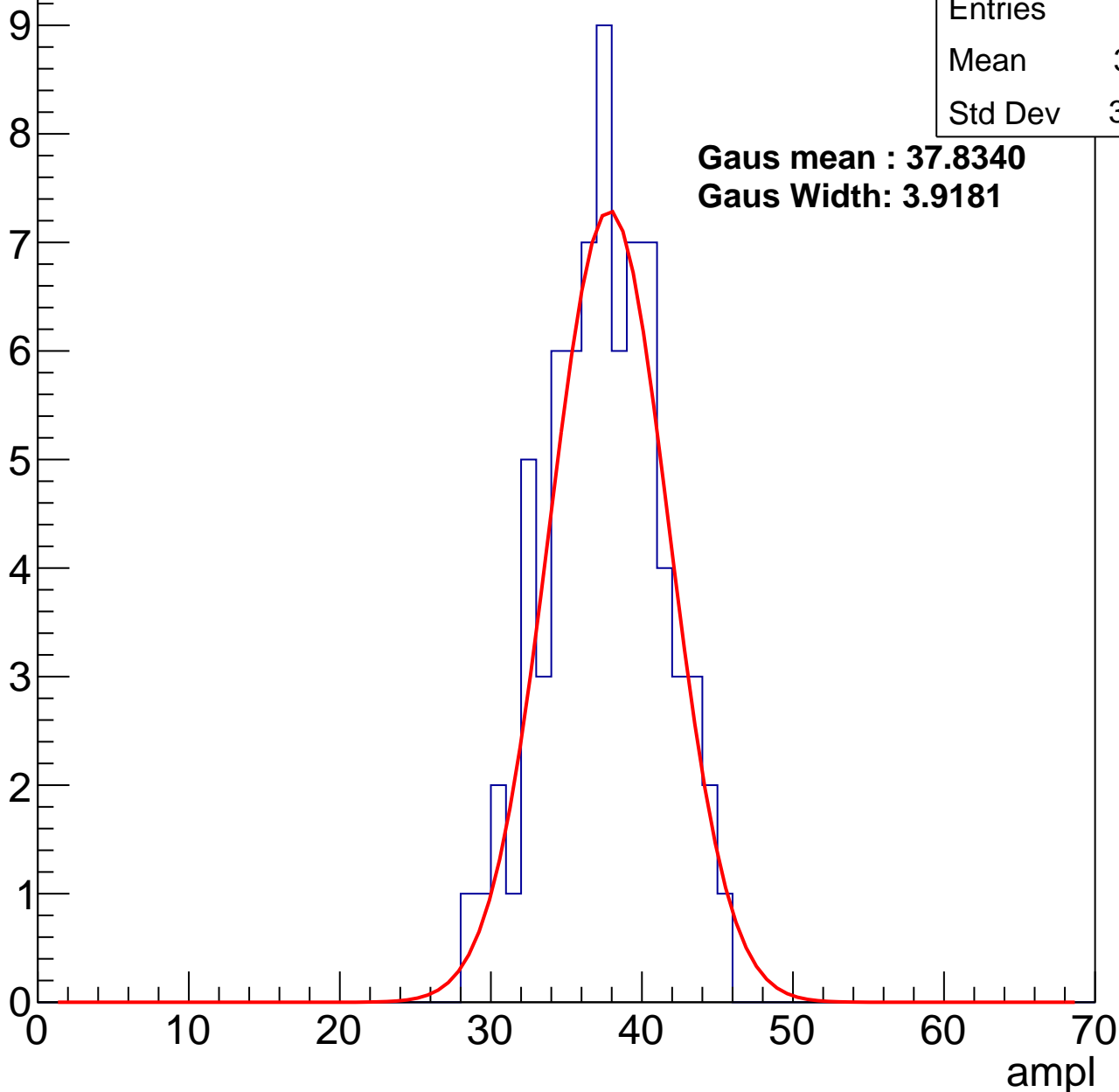
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.01
Std Dev	3.754

Gaus mean : 37.8340

Gaus Width: 3.9181



B1L101S, U12-ch121, adc2

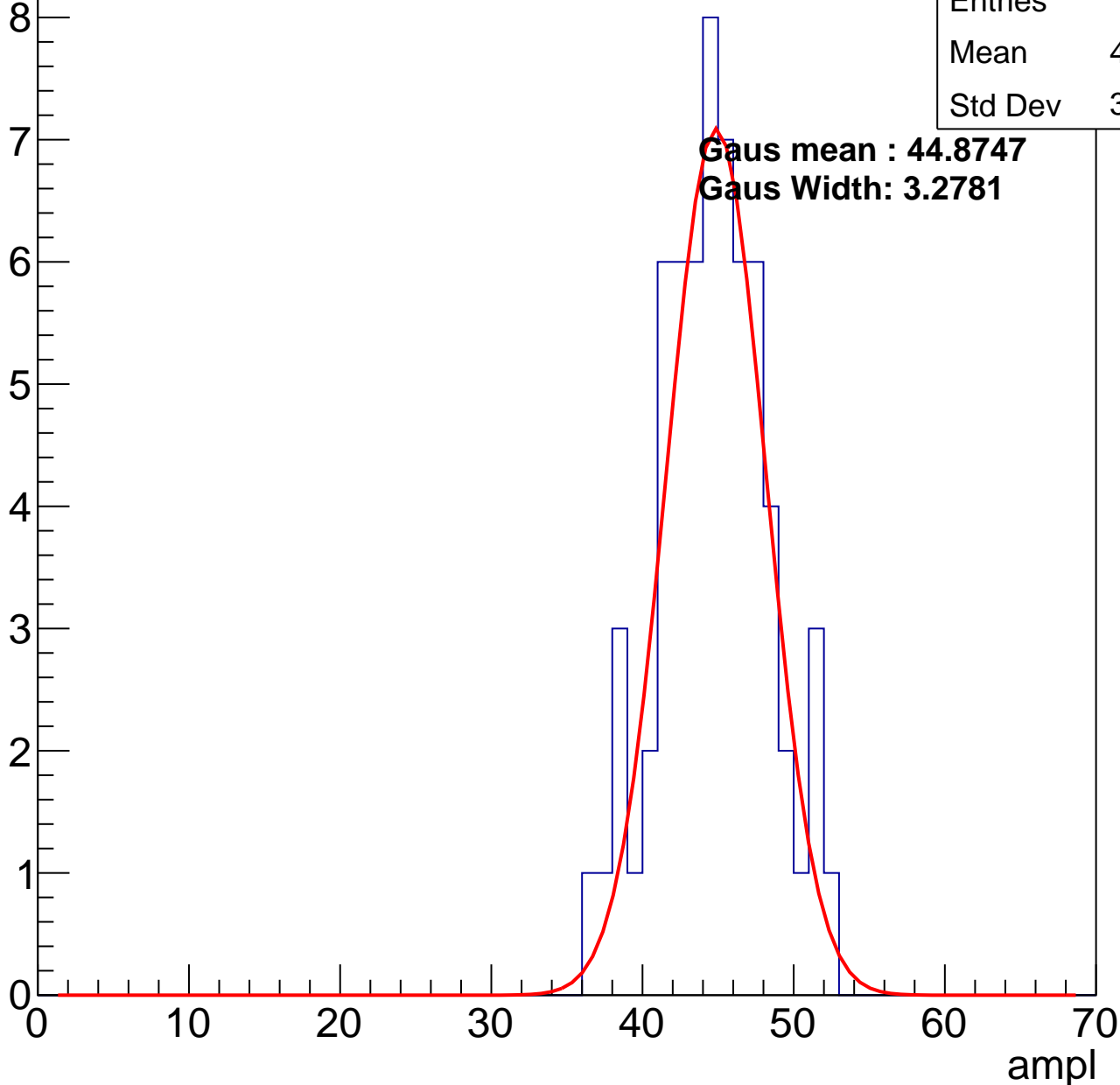
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.25
Std Dev	3.544

Gaus mean : 44.8747

Gaus Width: 3.2781

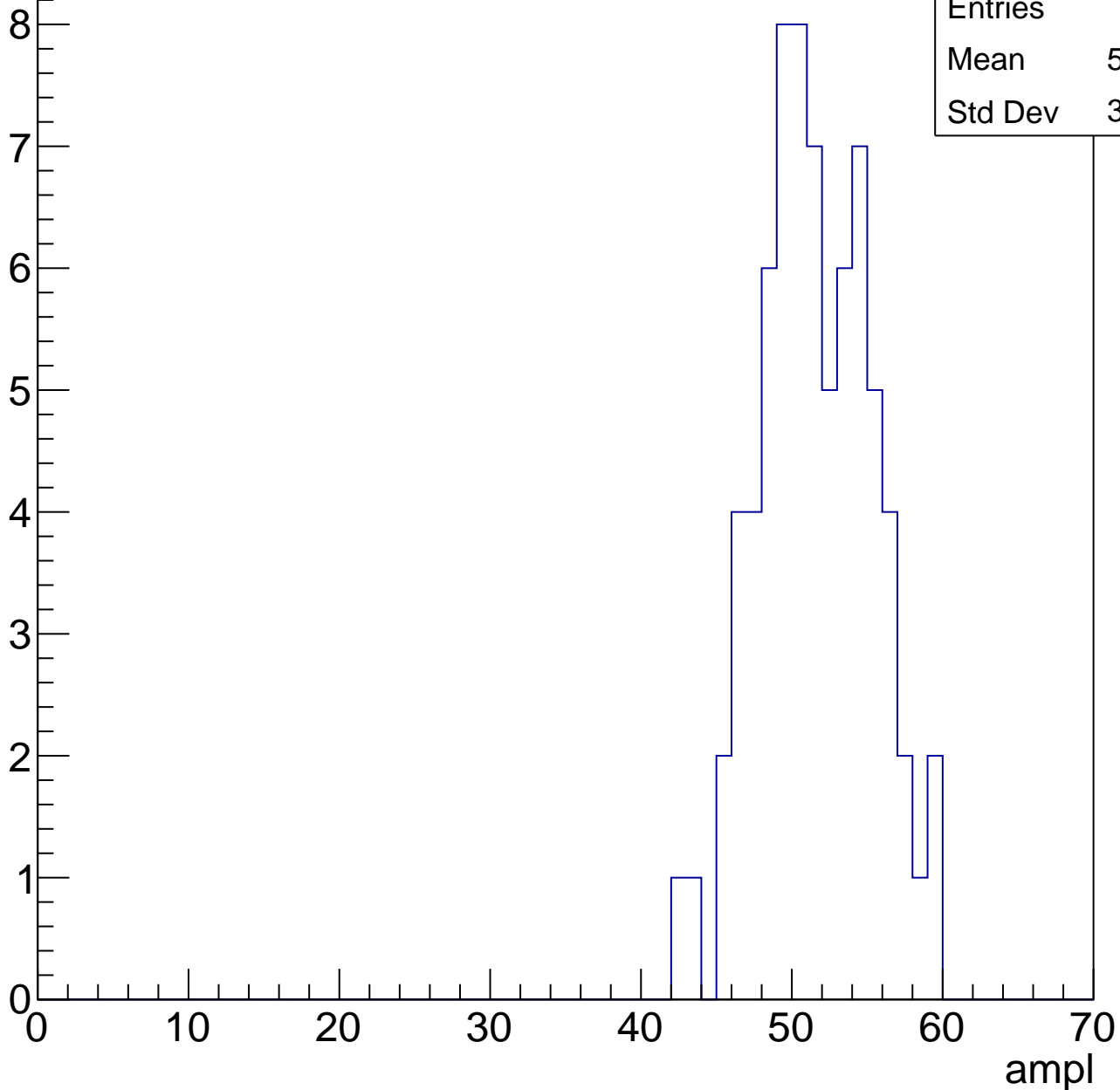


B1L101S, U12-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	51.08
Std Dev	3.693

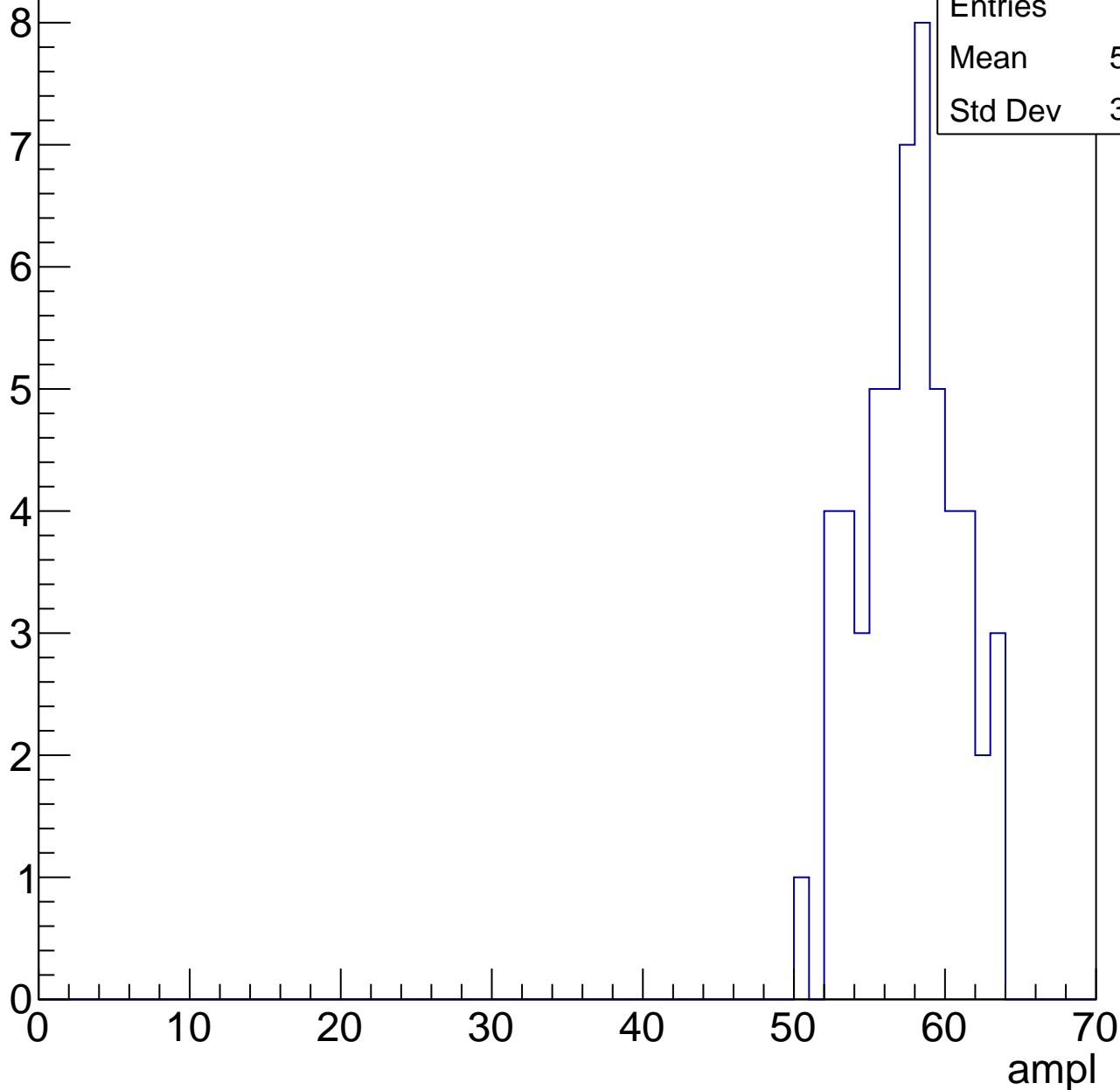


B1L101S, U12-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.13
Std Dev	3.163

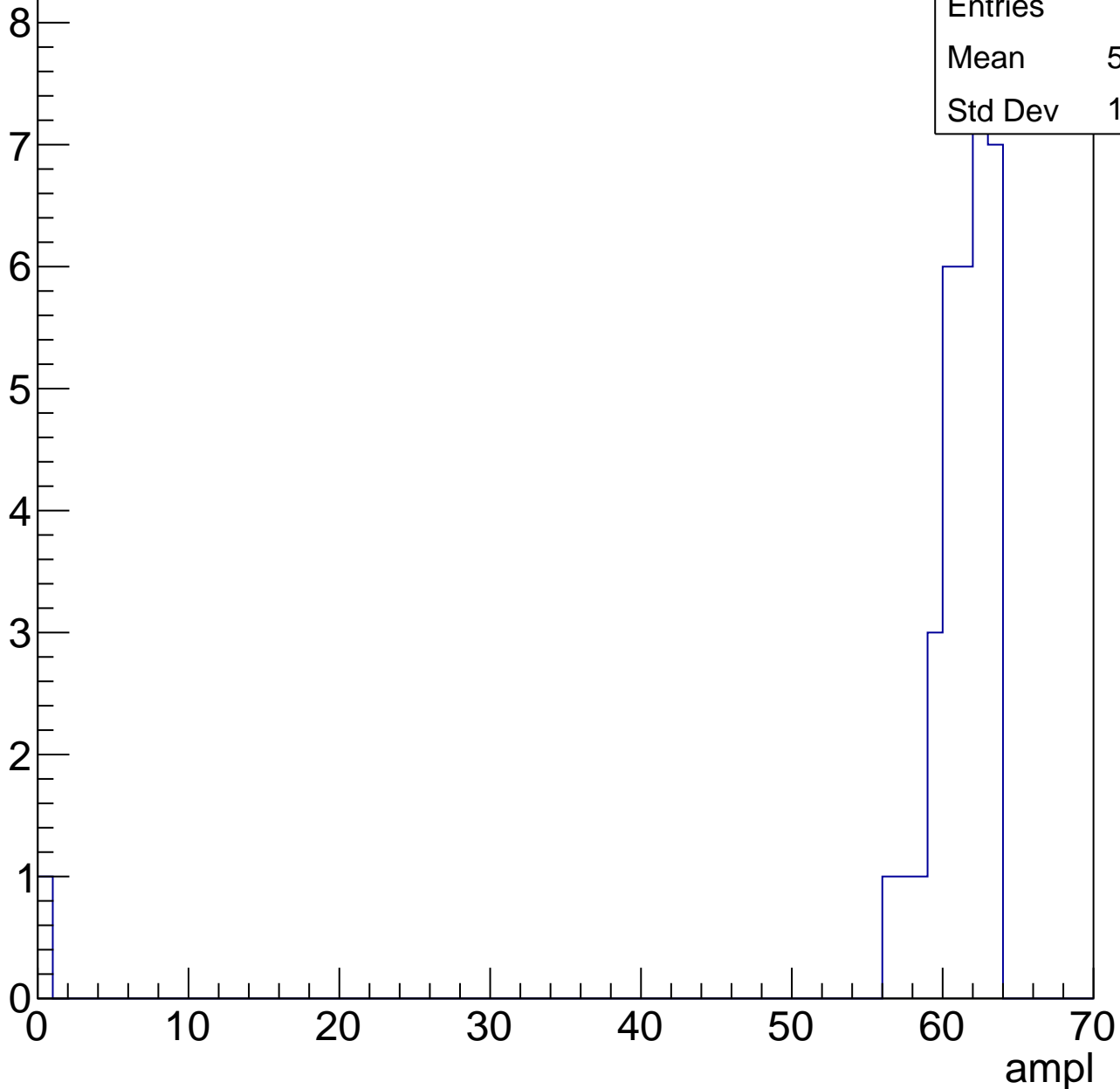


B1L101S, U12-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.15
Std Dev	10.44



B1L101S, U12-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U12-ch122, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	29
Std Dev	5.978

Gaus mean : 30.8450

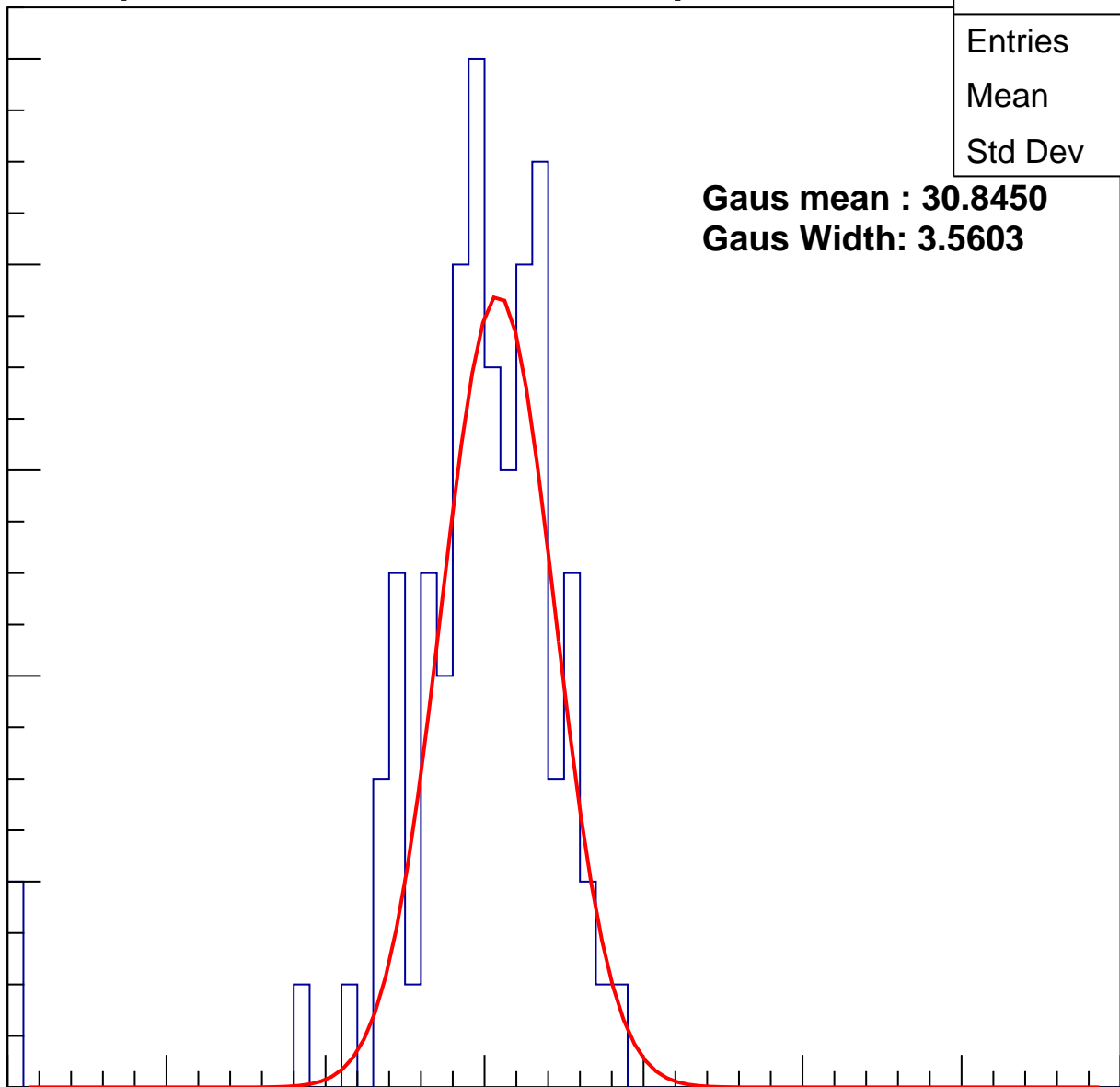
Gaus Width: 3.5603

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch122, adc1

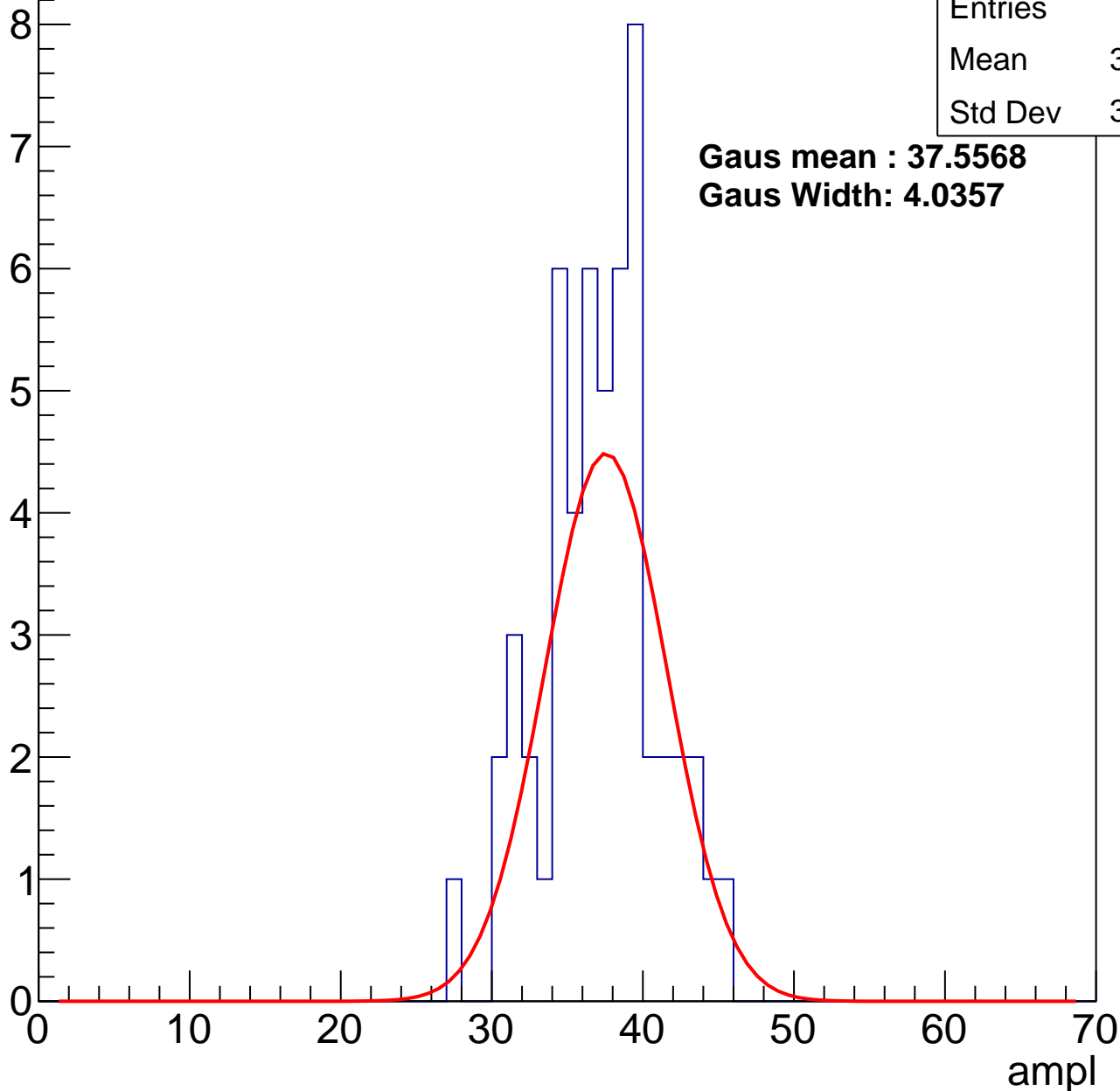
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.72
Std Dev	3.768

Gaus mean : 37.5568

Gaus Width: 4.0357



B1L101S, U12-ch122, adc2

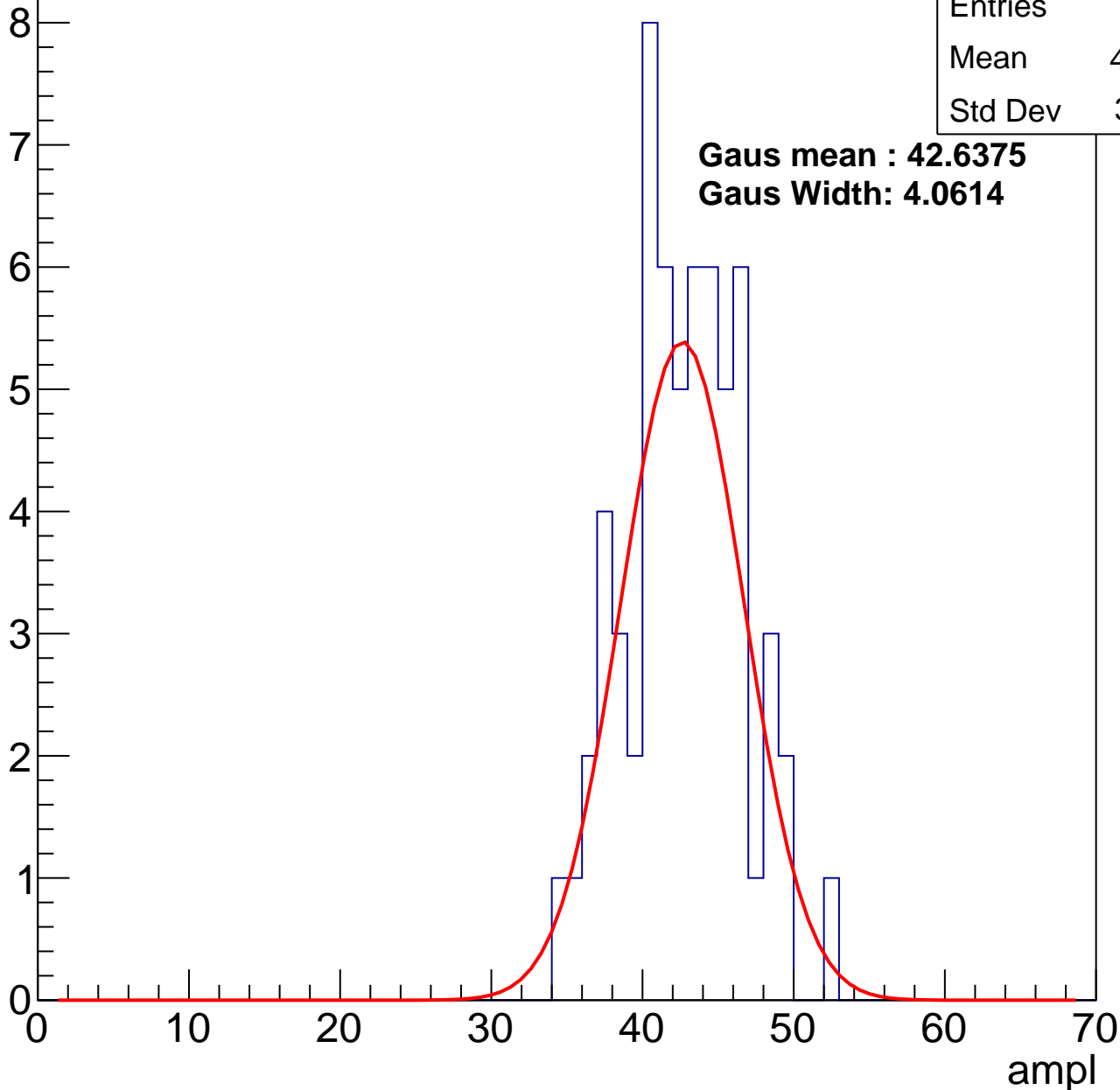
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.27
Std Dev	3.781

Gaus mean : 42.6375

Gaus Width: 4.0614

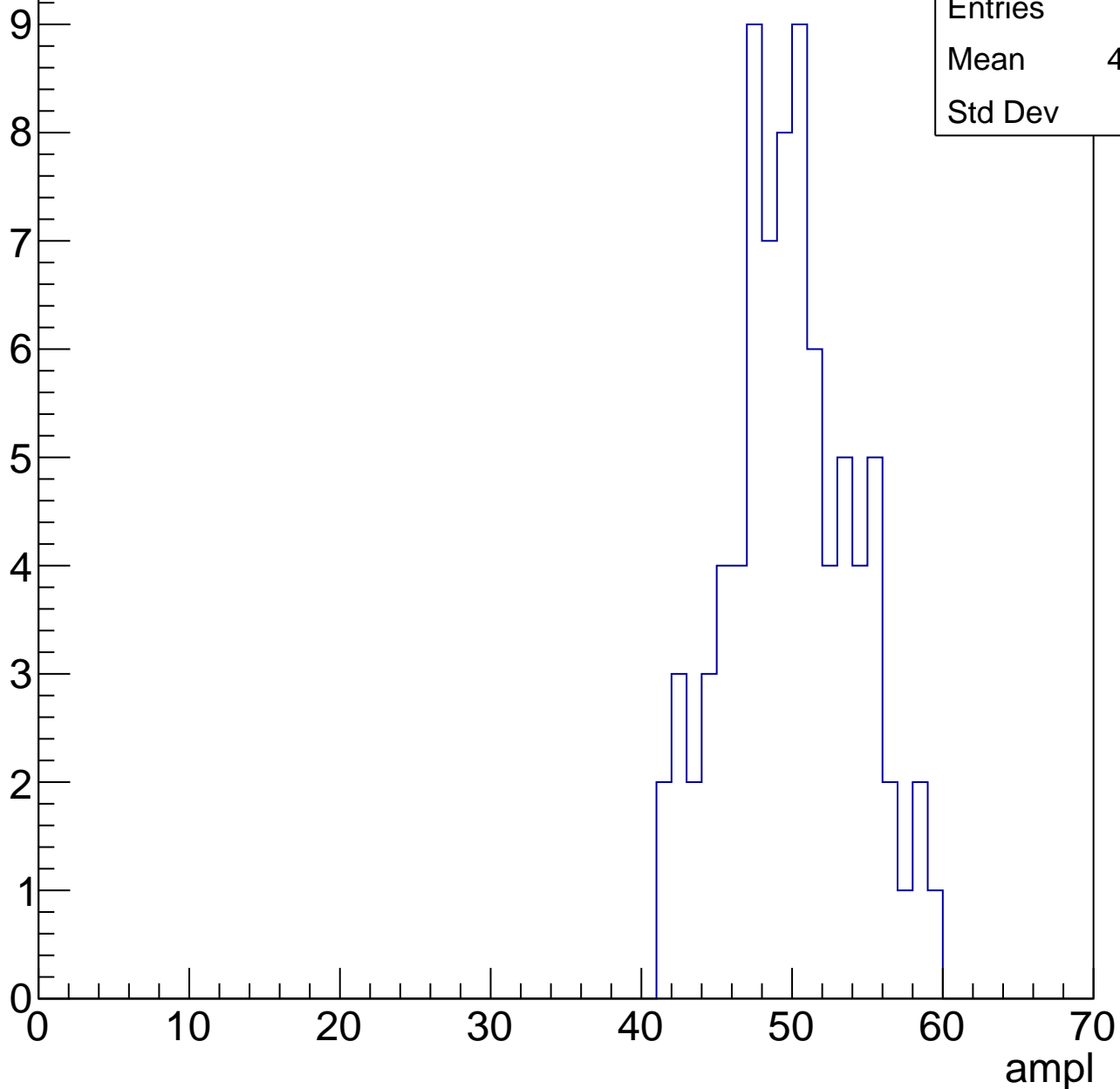


B1L101S, U12-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	49.44
Std Dev	4.16

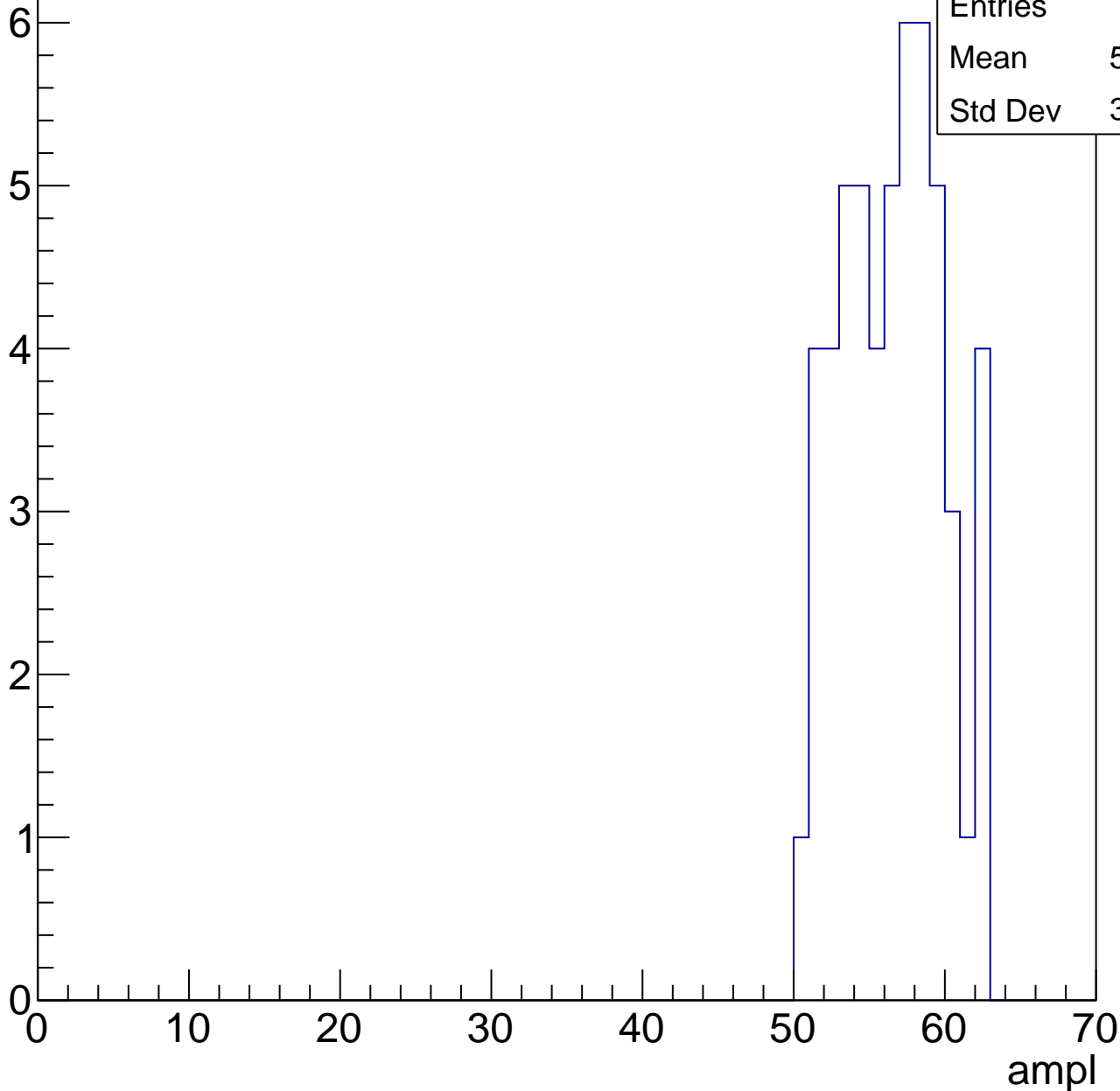


B1L101S, U12-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.06
Std Dev	3.253

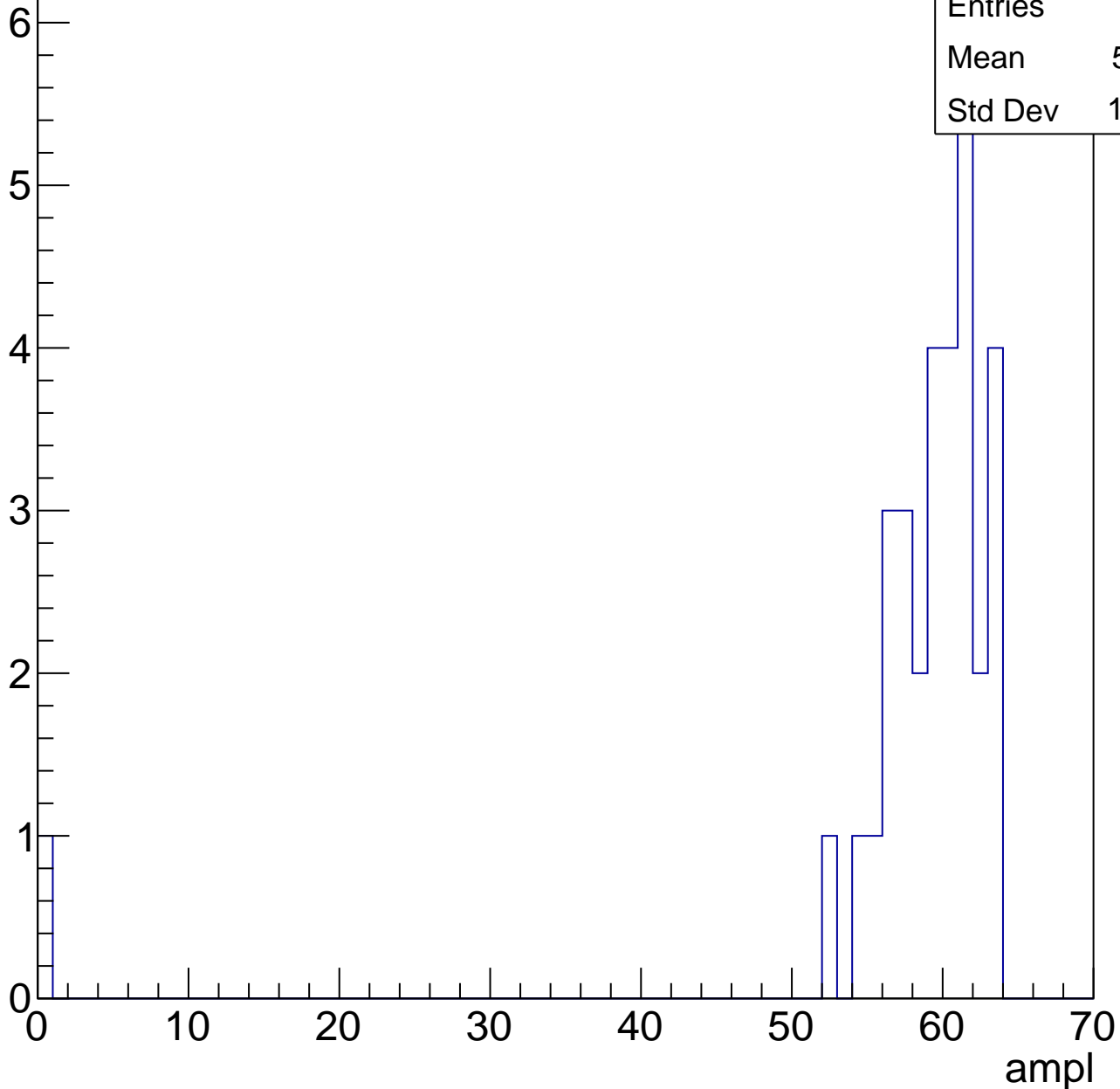


B1L101S, U12-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	57.31
Std Dev	10.65

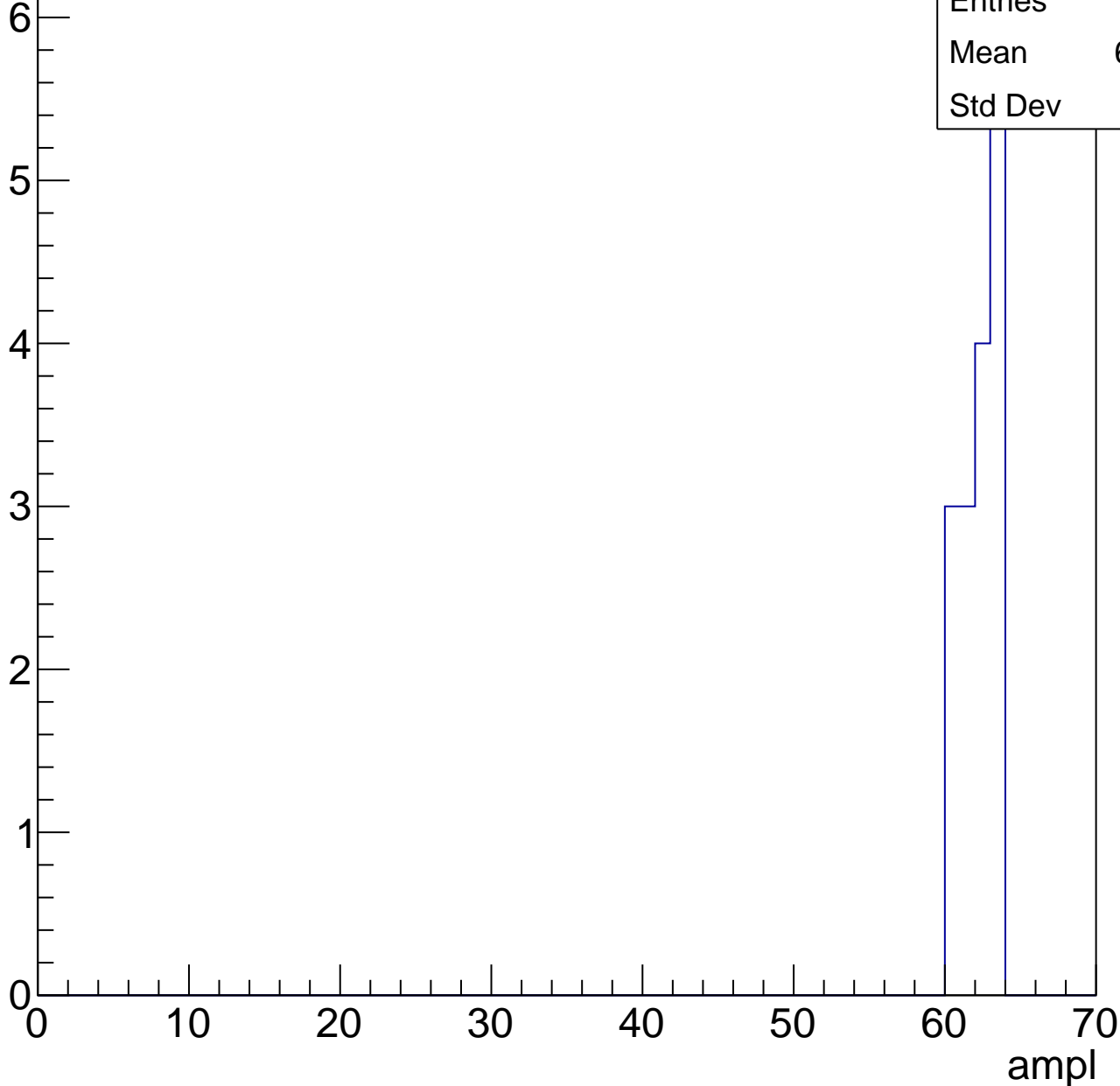


B1L101S, U12-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.81
Std Dev	1.13



B1L101S, U12-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch123, adc0

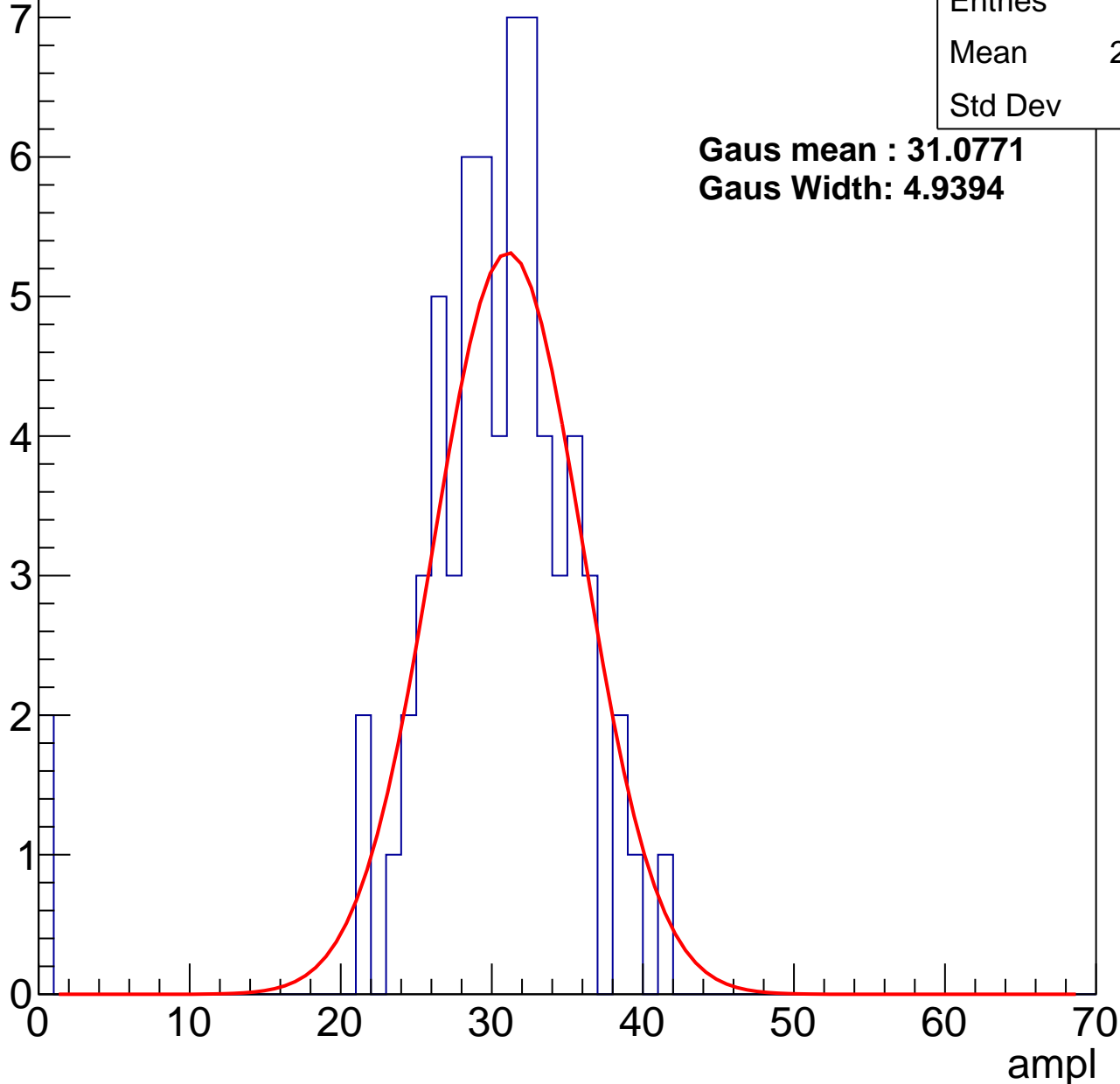
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.39
Std Dev	6.66

Gaus mean : 31.0771

Gaus Width: 4.9394



B1L101S, U12-ch123, adc1

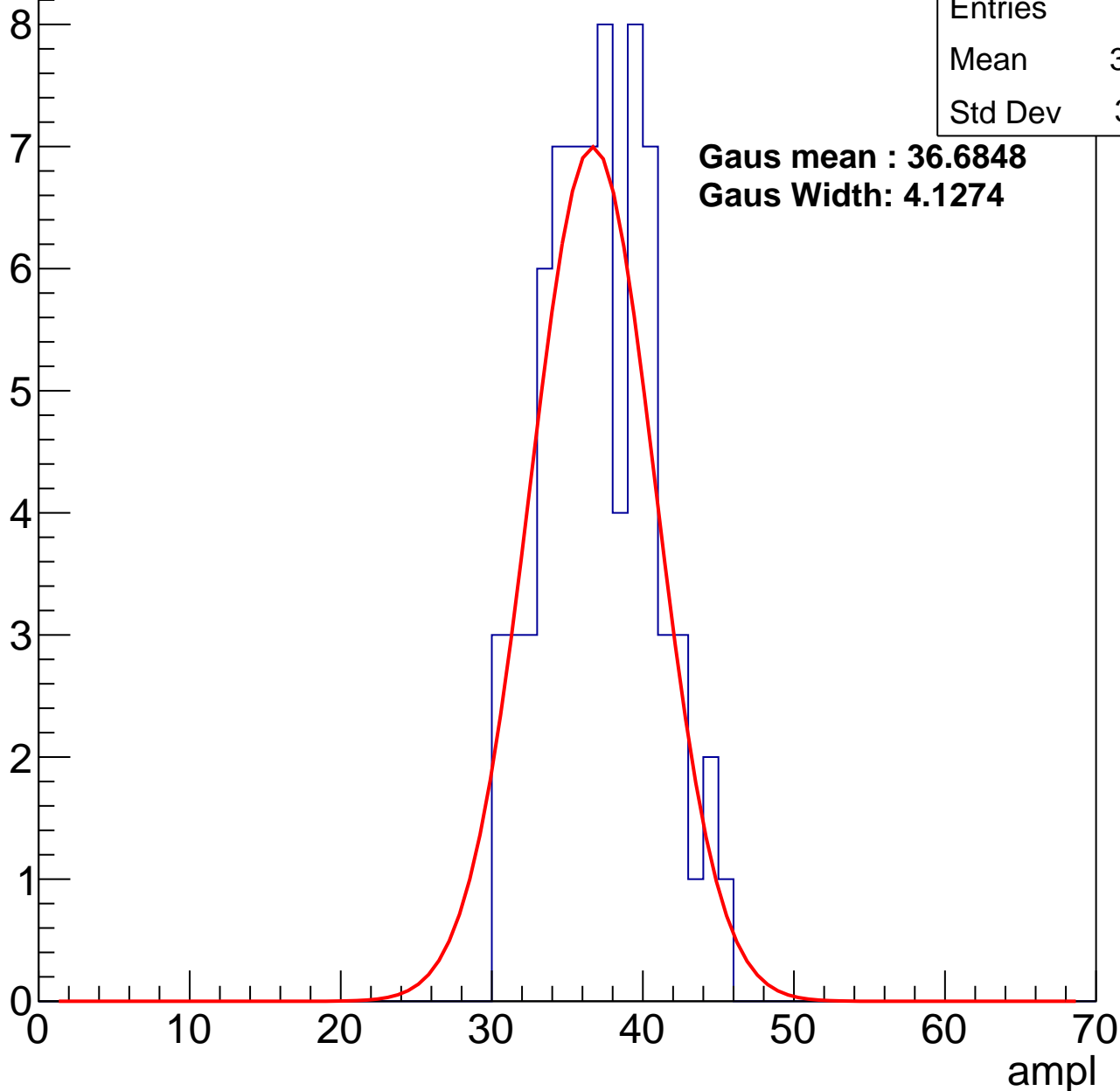
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.67
Std Dev	3.581

Gaus mean : 36.6848

Gaus Width: 4.1274



B1L101S, U12-ch123, adc2

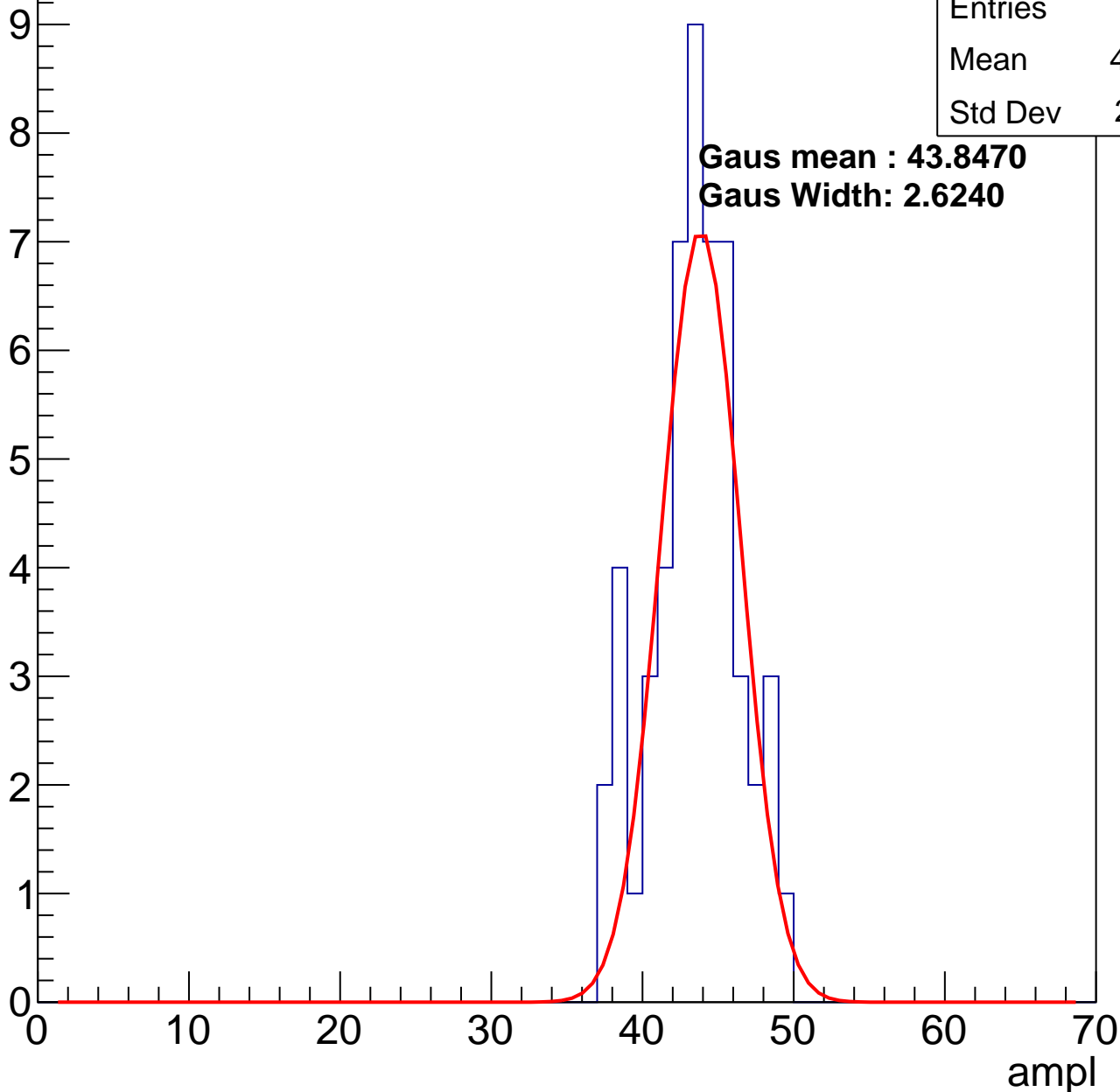
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	42.98
Std Dev	2.891

Gaus mean : 43.8470

Gaus Width: 2.6240

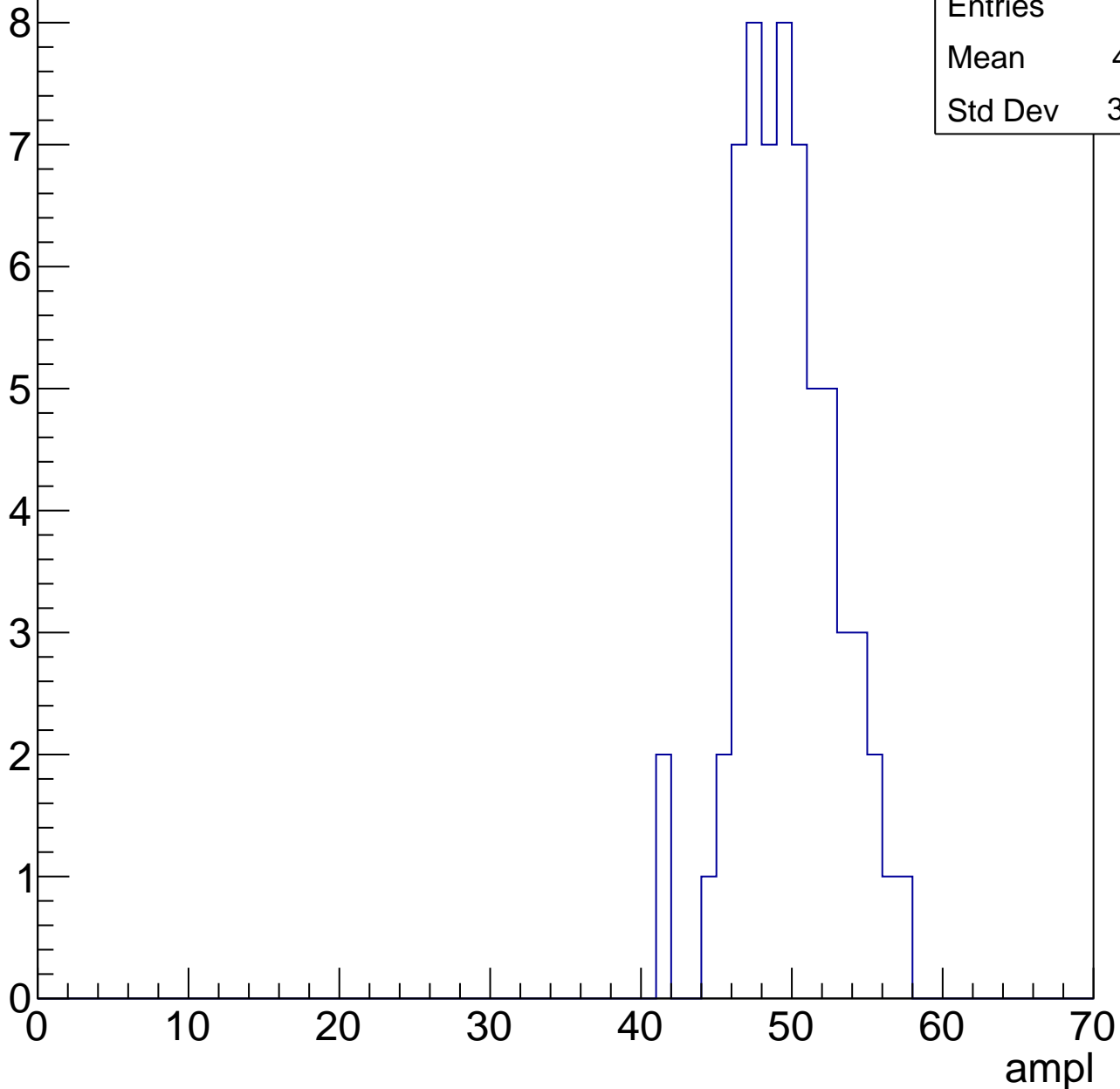


B1L101S, U12-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.21
Std Dev	3.273

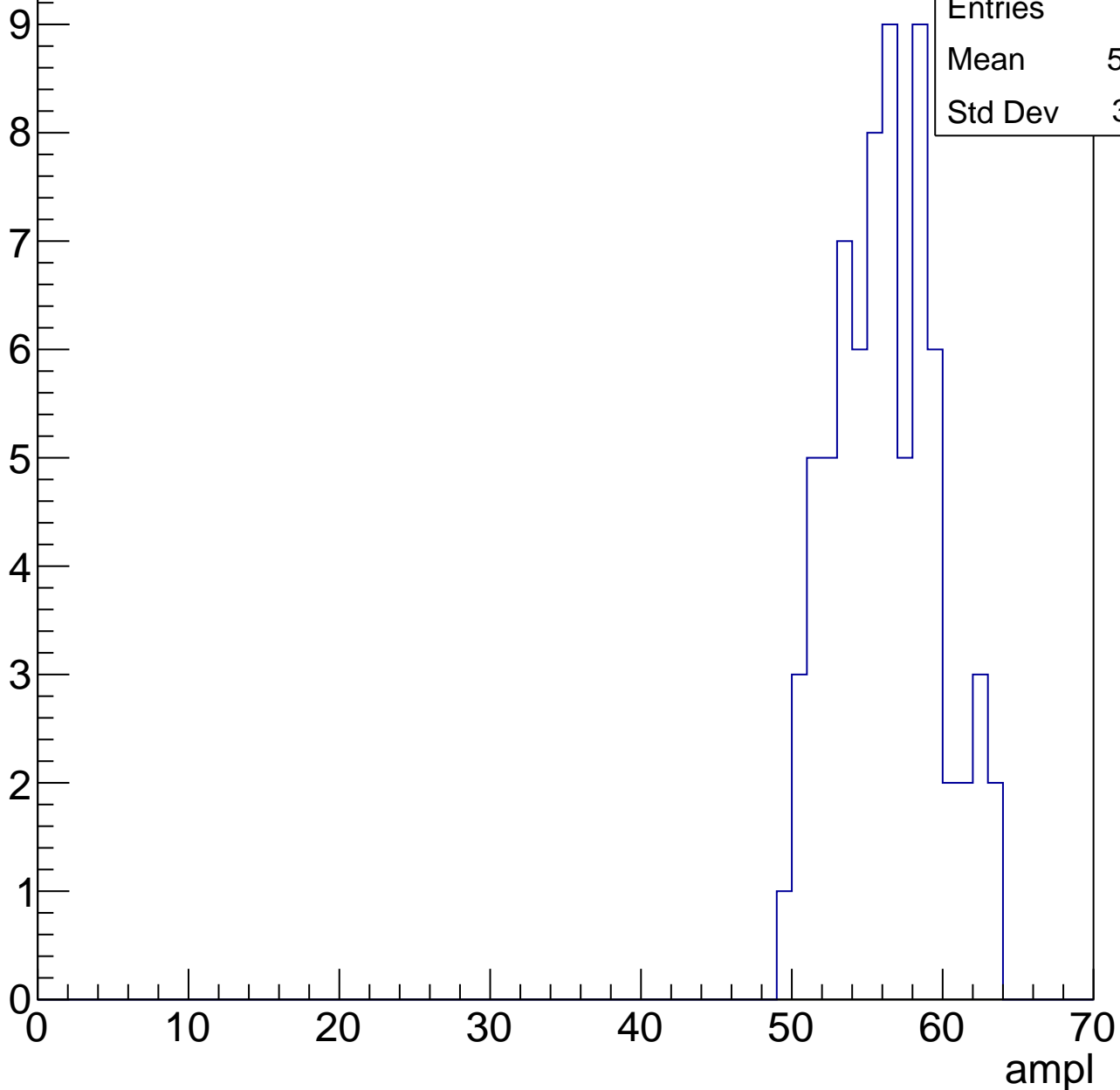


B1L101S, U12-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	55.73
Std Dev	3.381

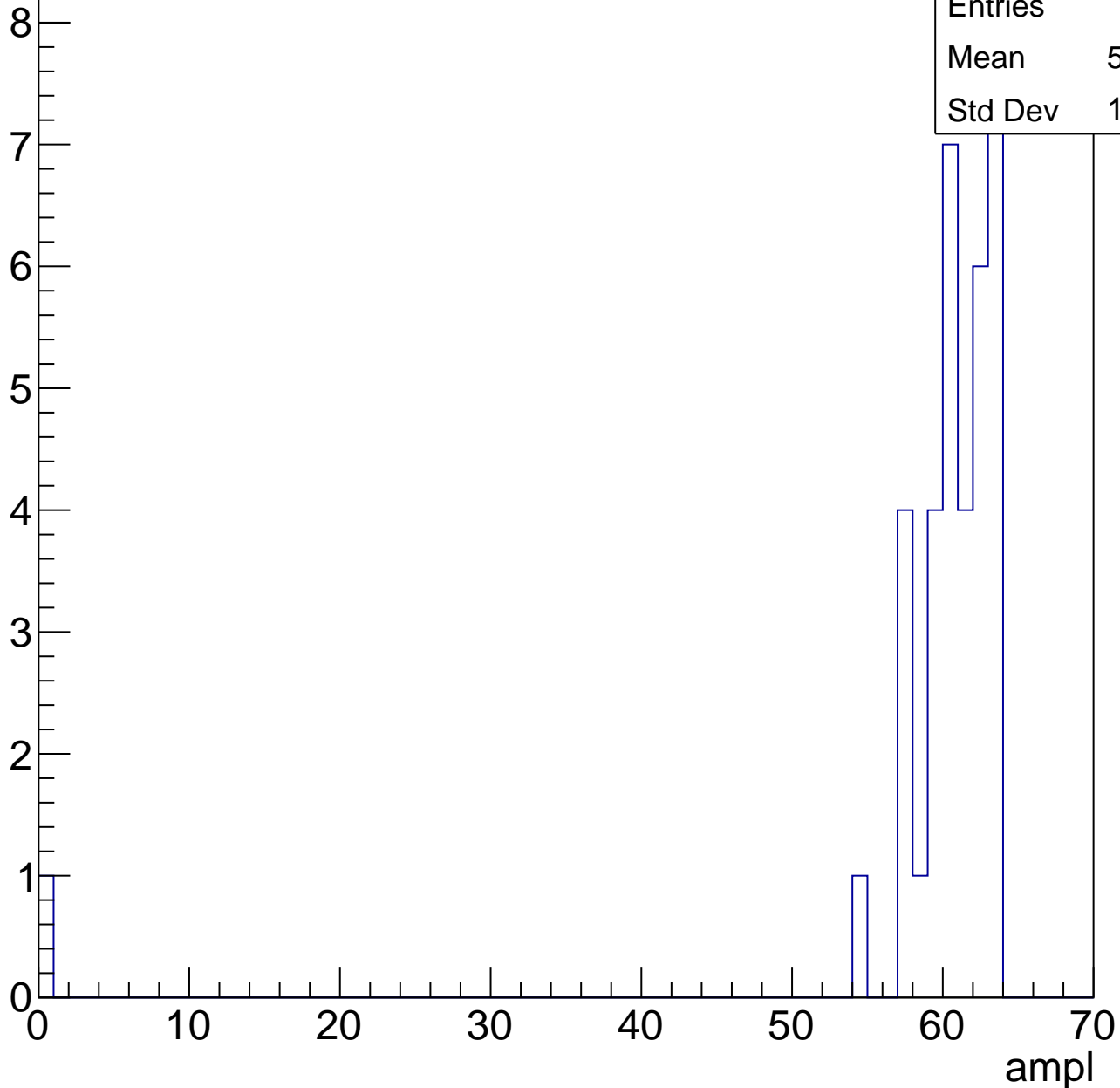


B1L101S, U12-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

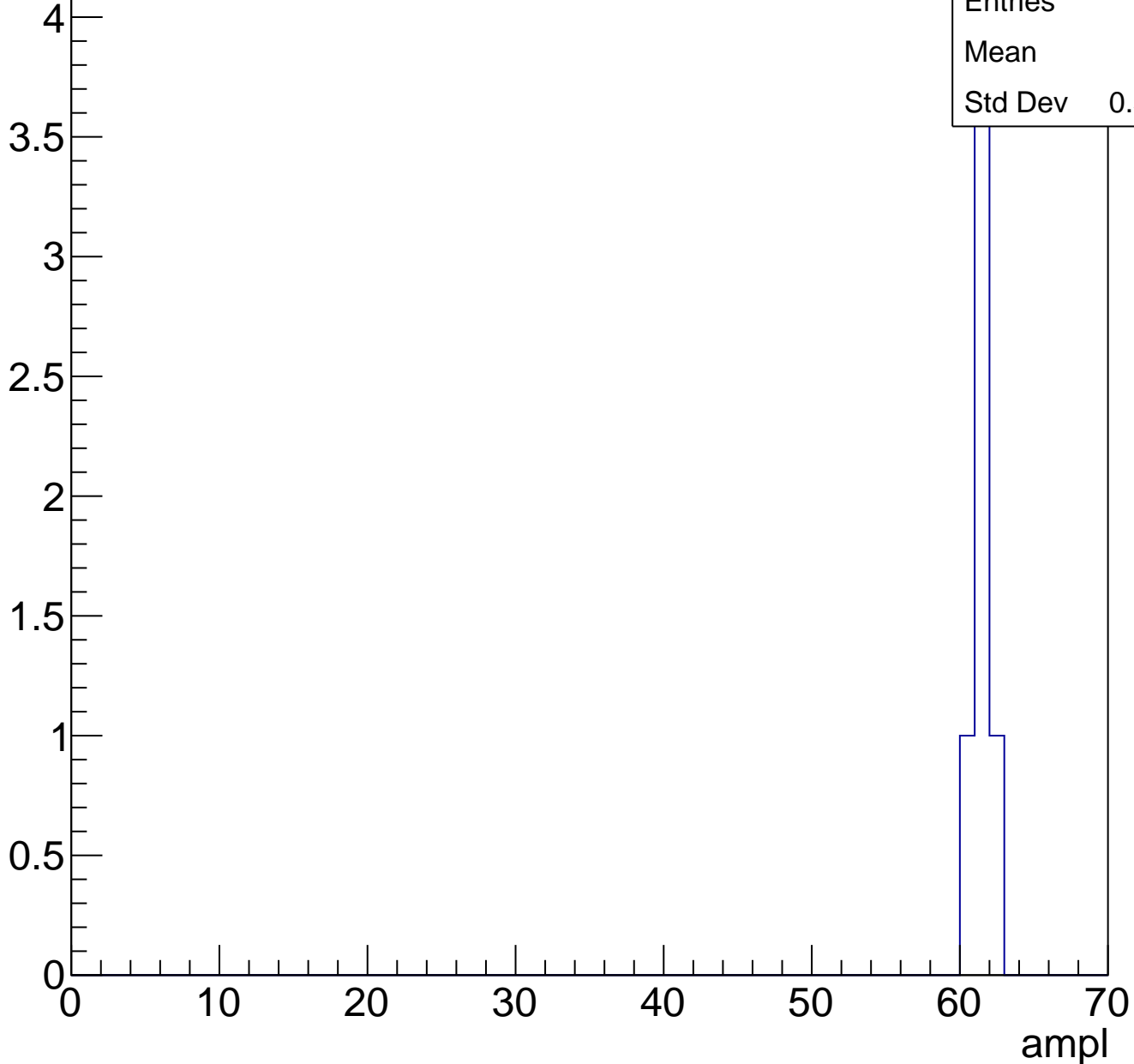
Entries	36
Mean	58.78
Std Dev	10.17



B1L101S, U12-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch124, adc0

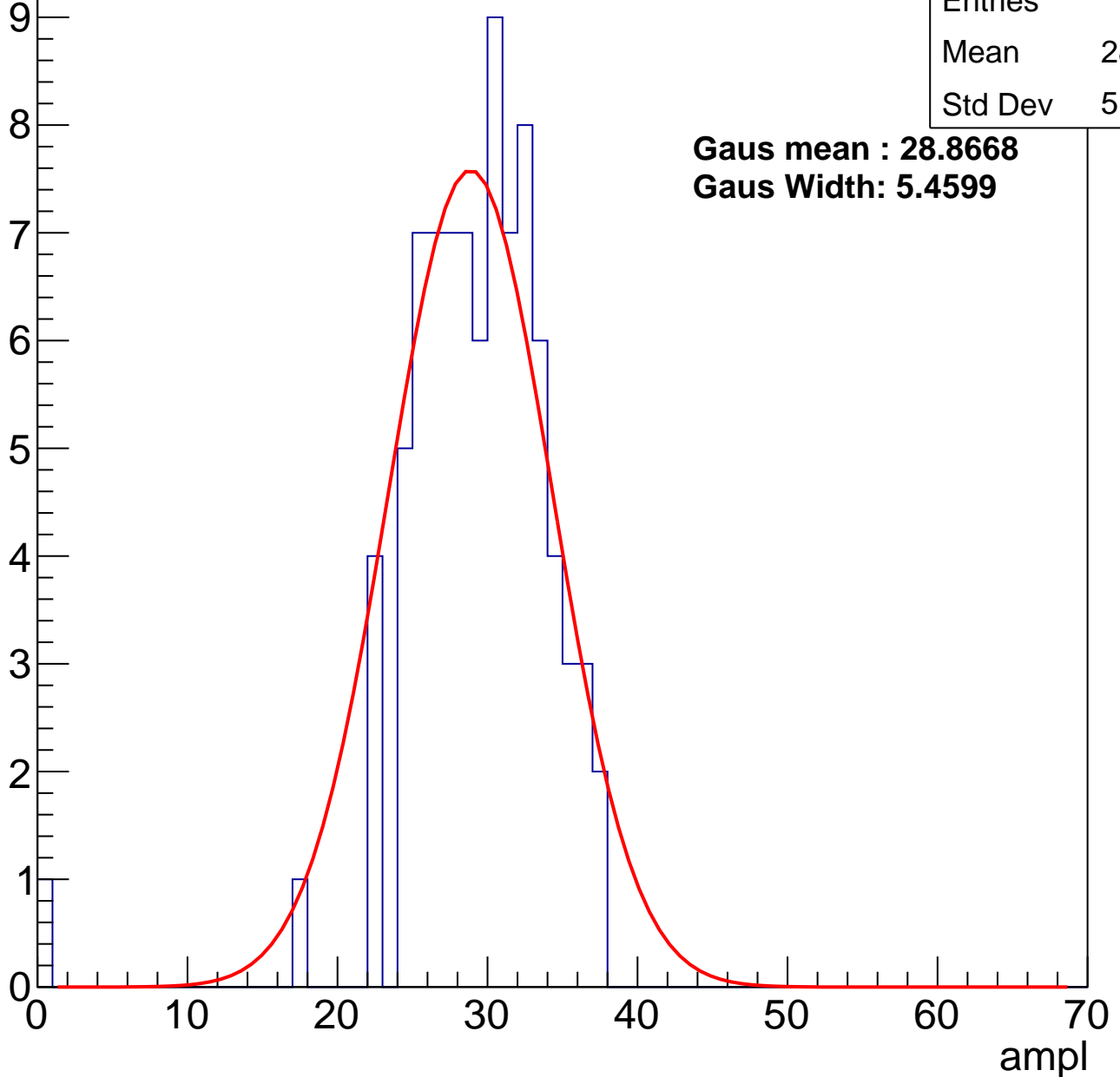
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	28.79
Std Dev	5.029

Gaus mean : 28.8668

Gaus Width: 5.4599



B1L101S, U12-ch124, adc1

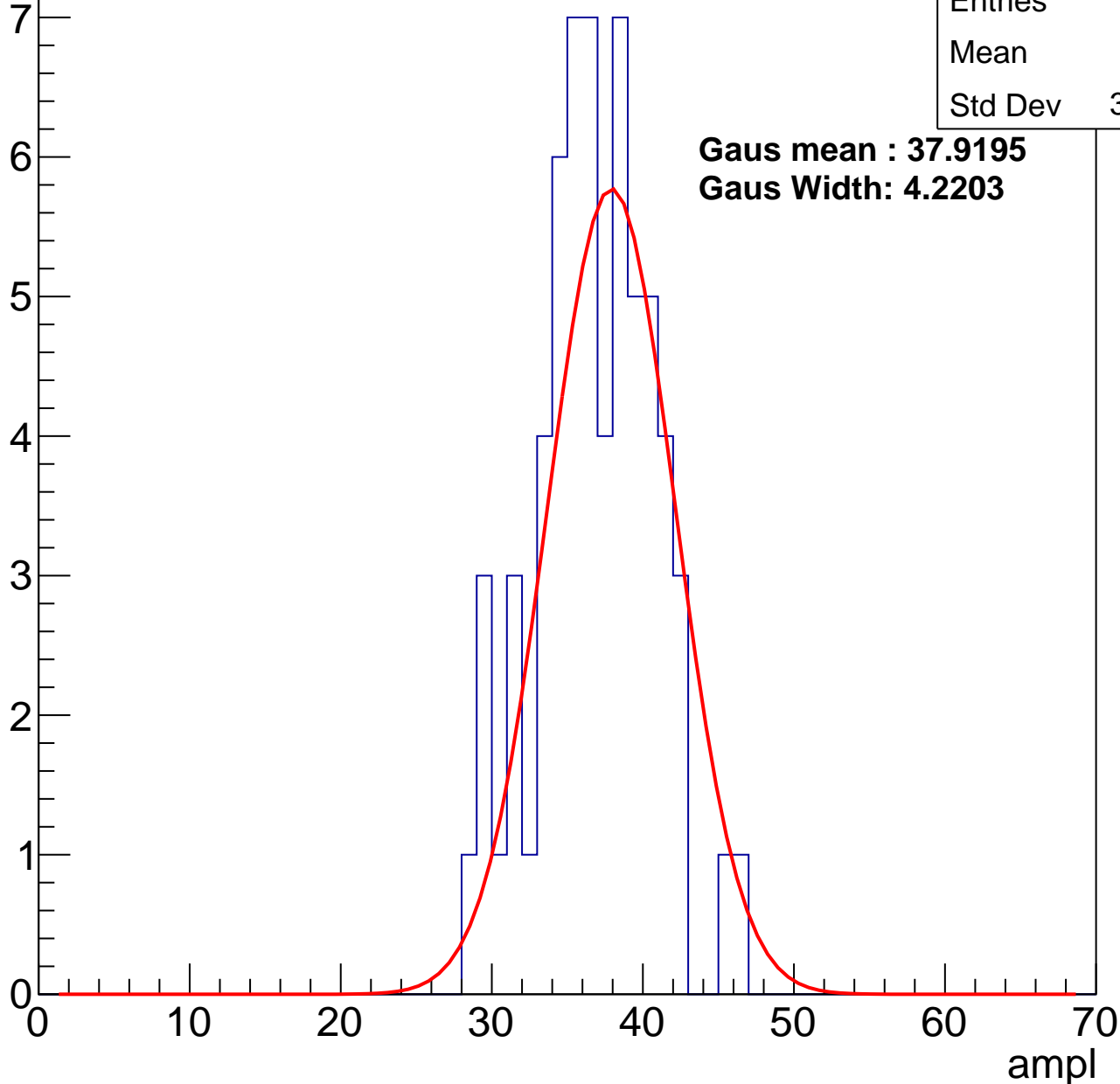
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.4
Std Dev	3.865

Gaus mean : 37.9195

Gaus Width: 4.2203



B1L101S, U12-ch124, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	43.37
Std Dev	3.776

Gaus mean : 43.3769

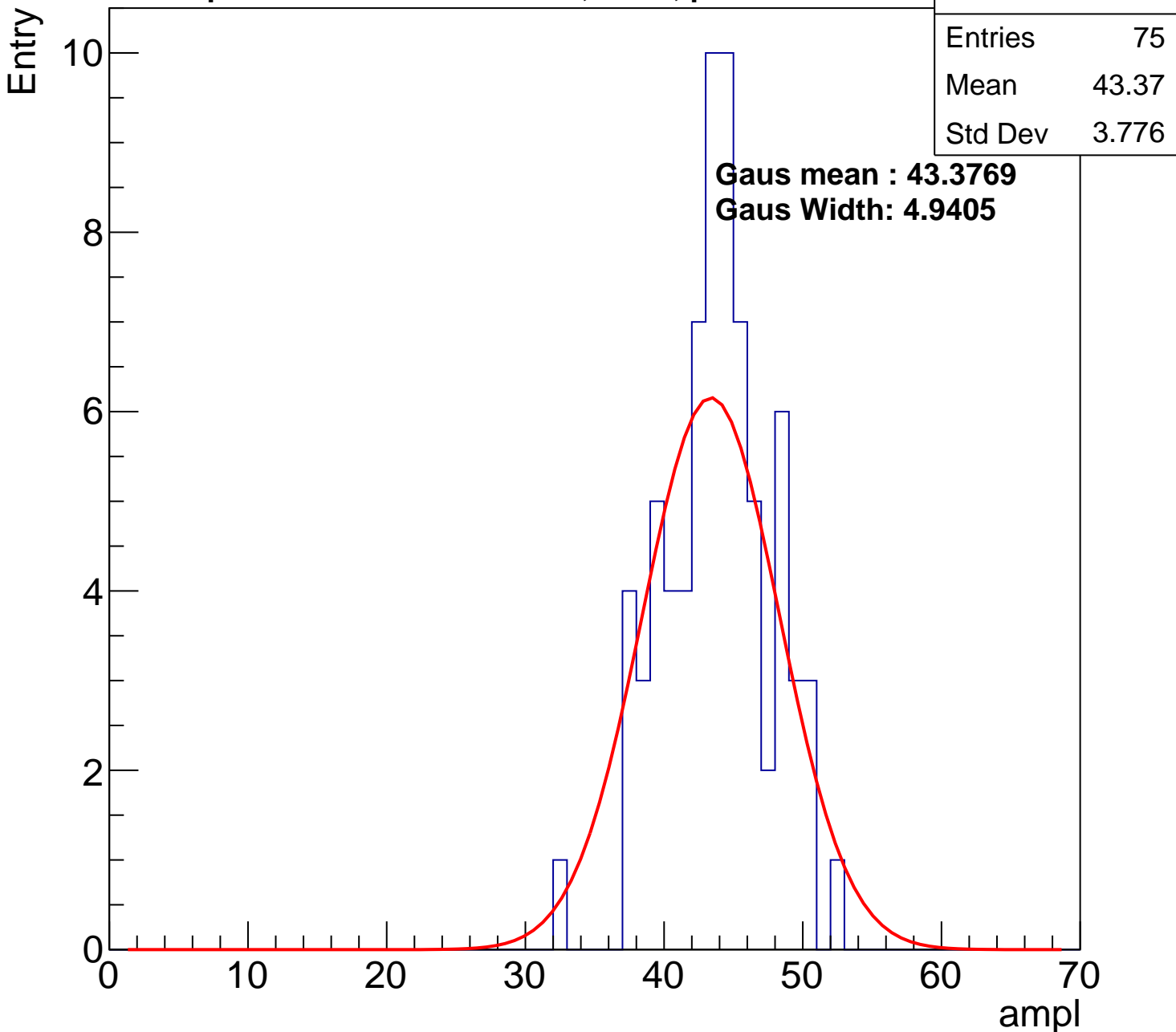
Gaus Width: 4.9405

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

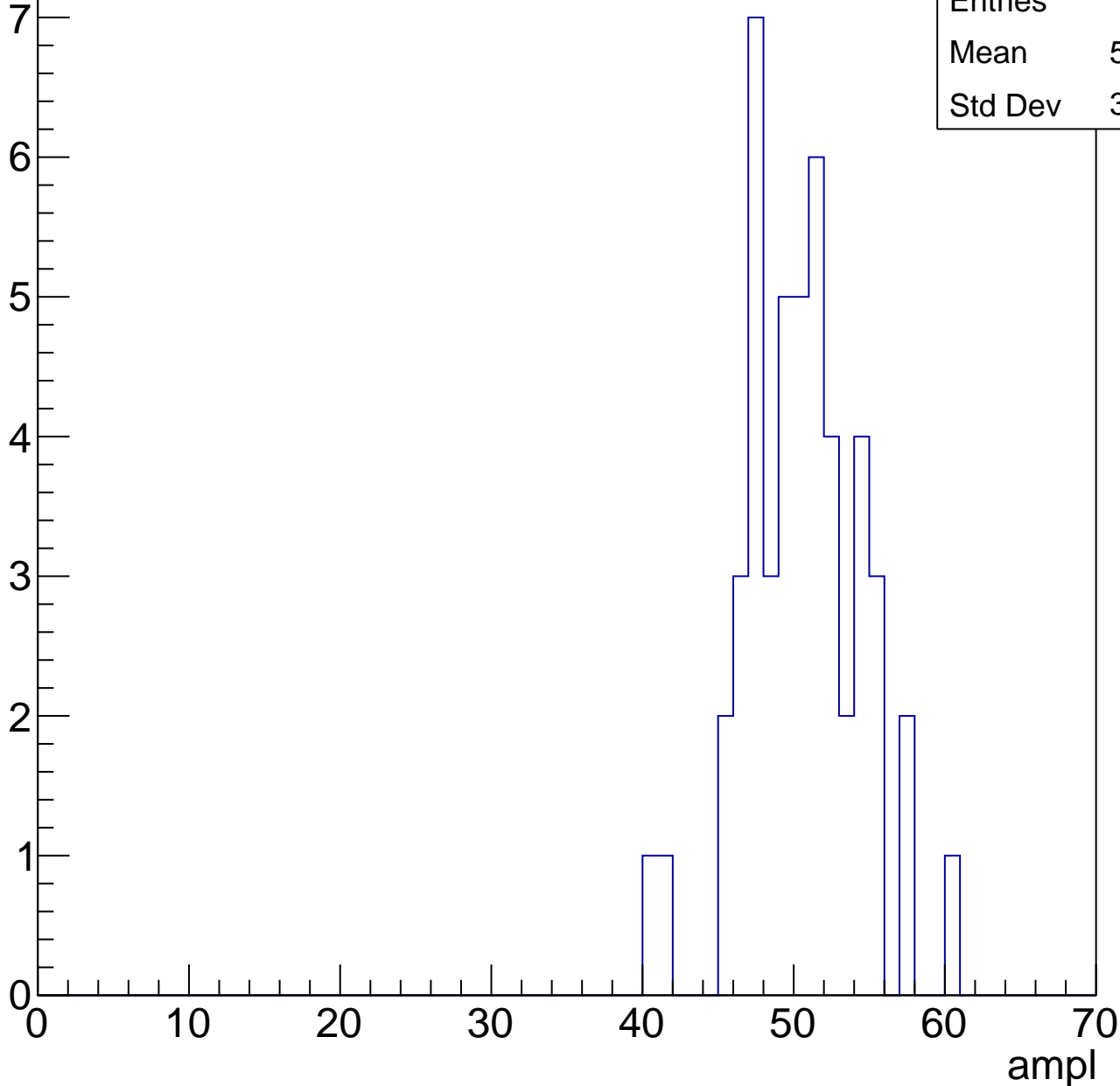


B1L101S, U12-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	50.04
Std Dev	3.875

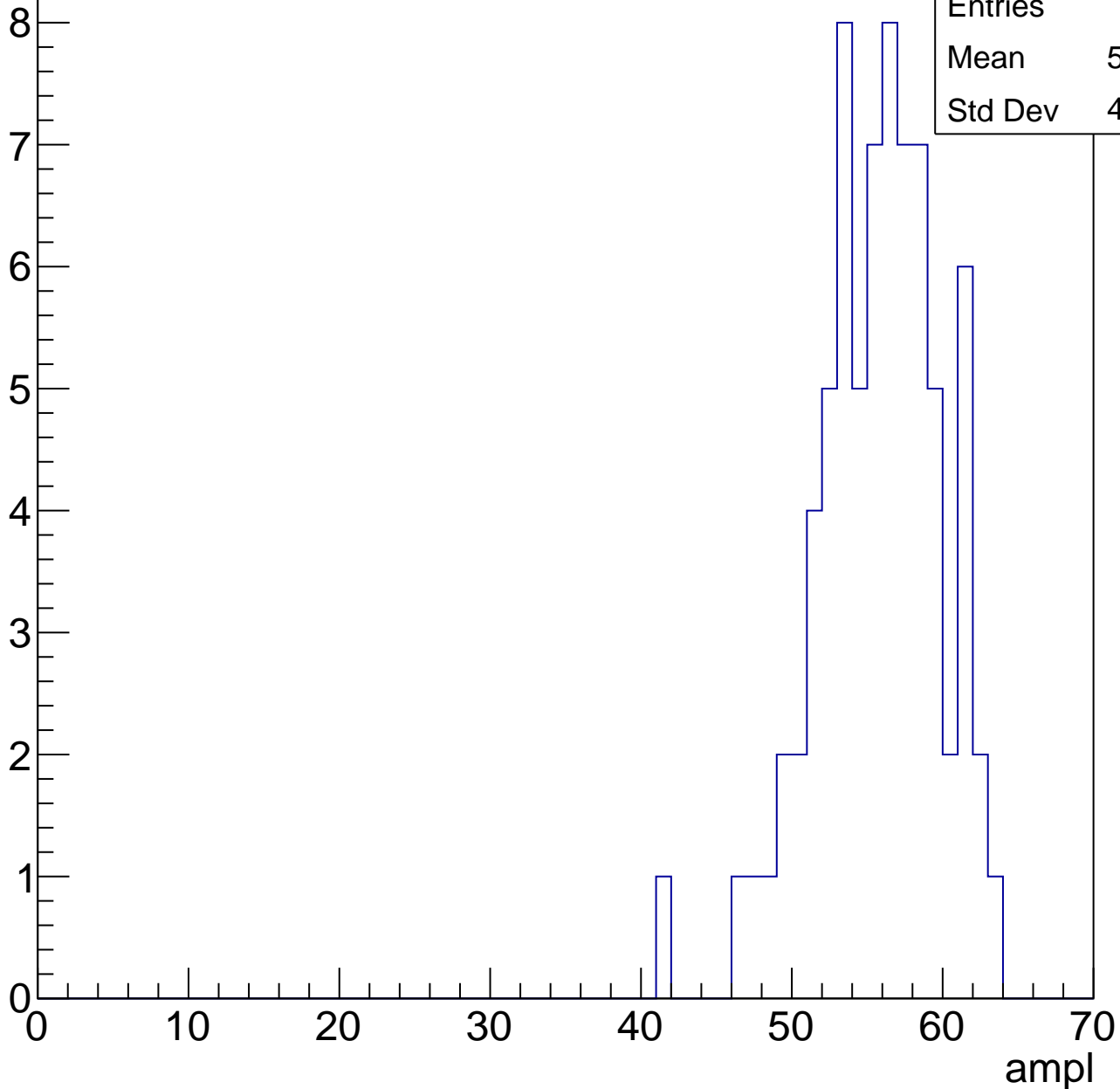


B1L101S, U12-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

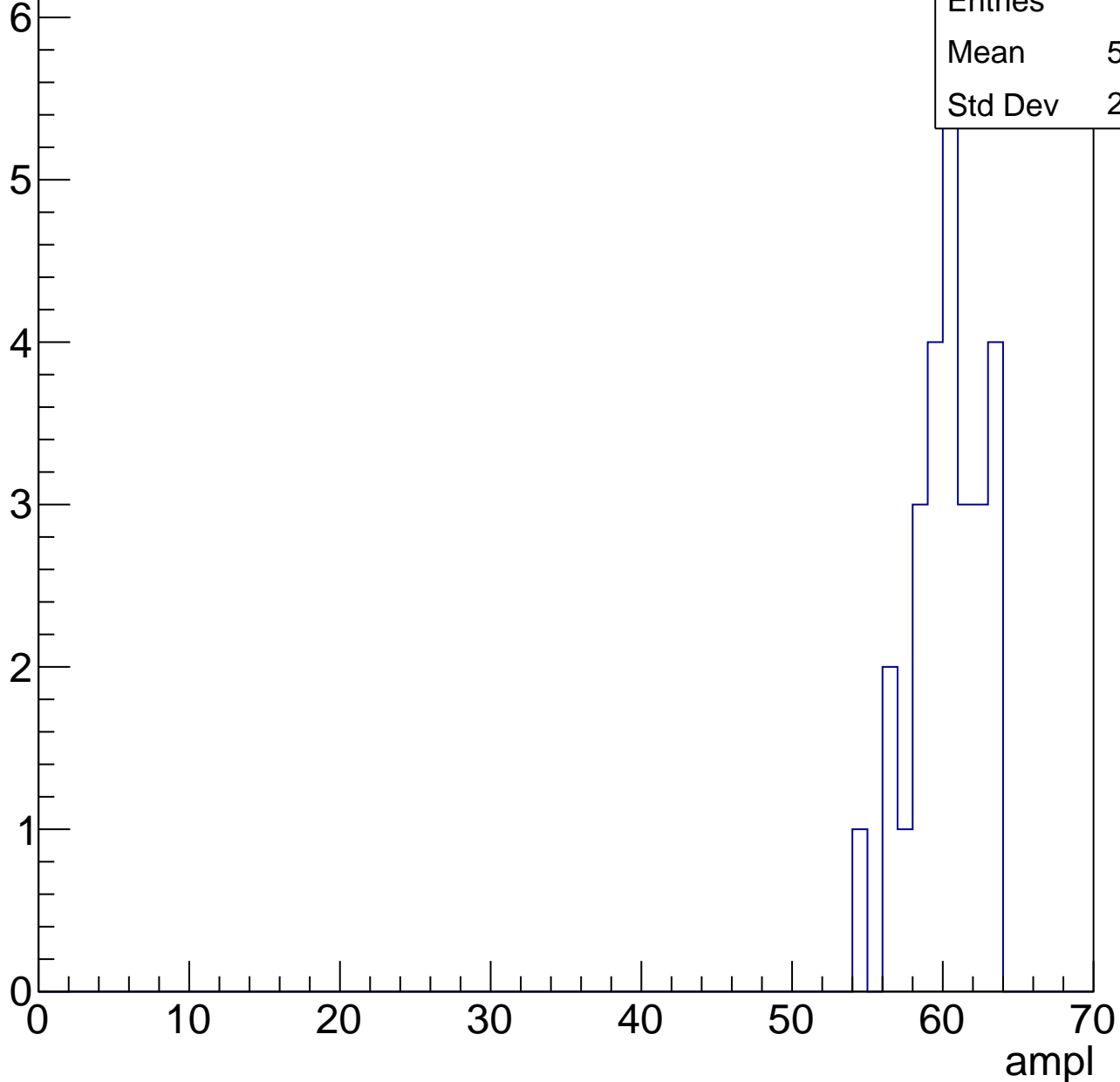
Entries	75
Mean	55.25
Std Dev	4.099



B1L101S, U12-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

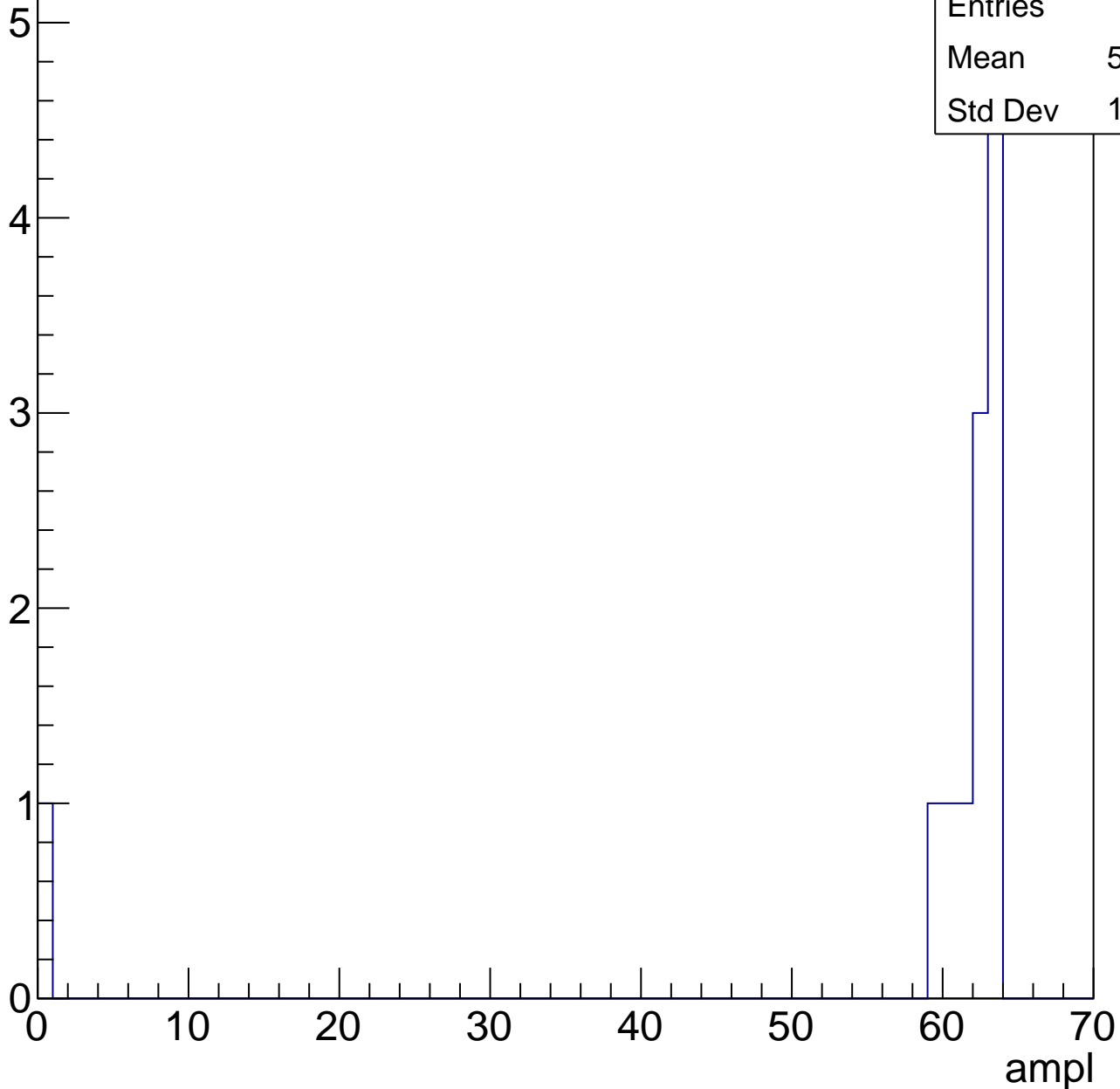


B1L101S, U12-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

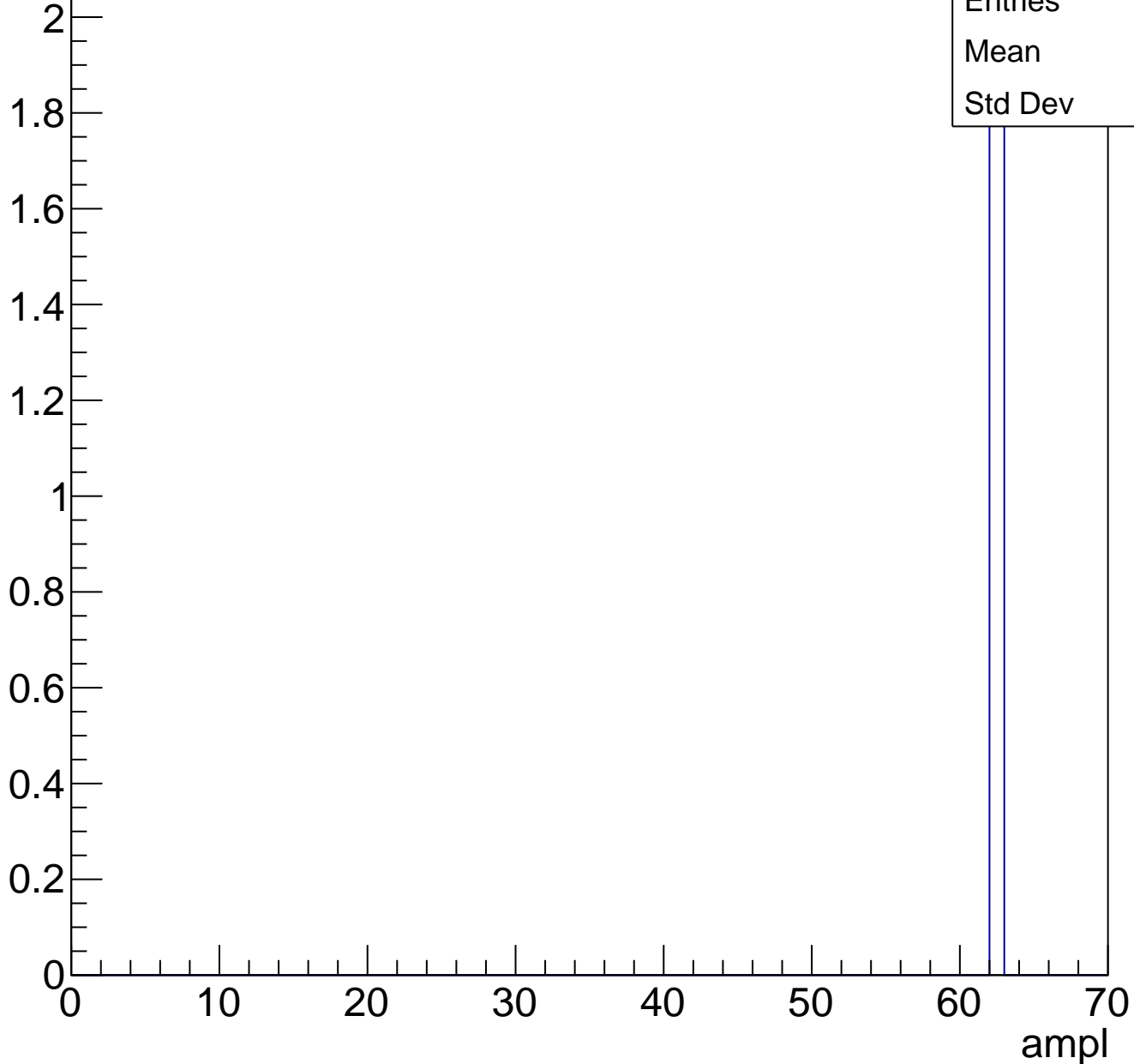
Entries	12
Mean	56.75
Std Dev	17.16



B1L101S, U12-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch125, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	92
Mean	31.86
Std Dev	3.961

Gaus mean : 31.4390

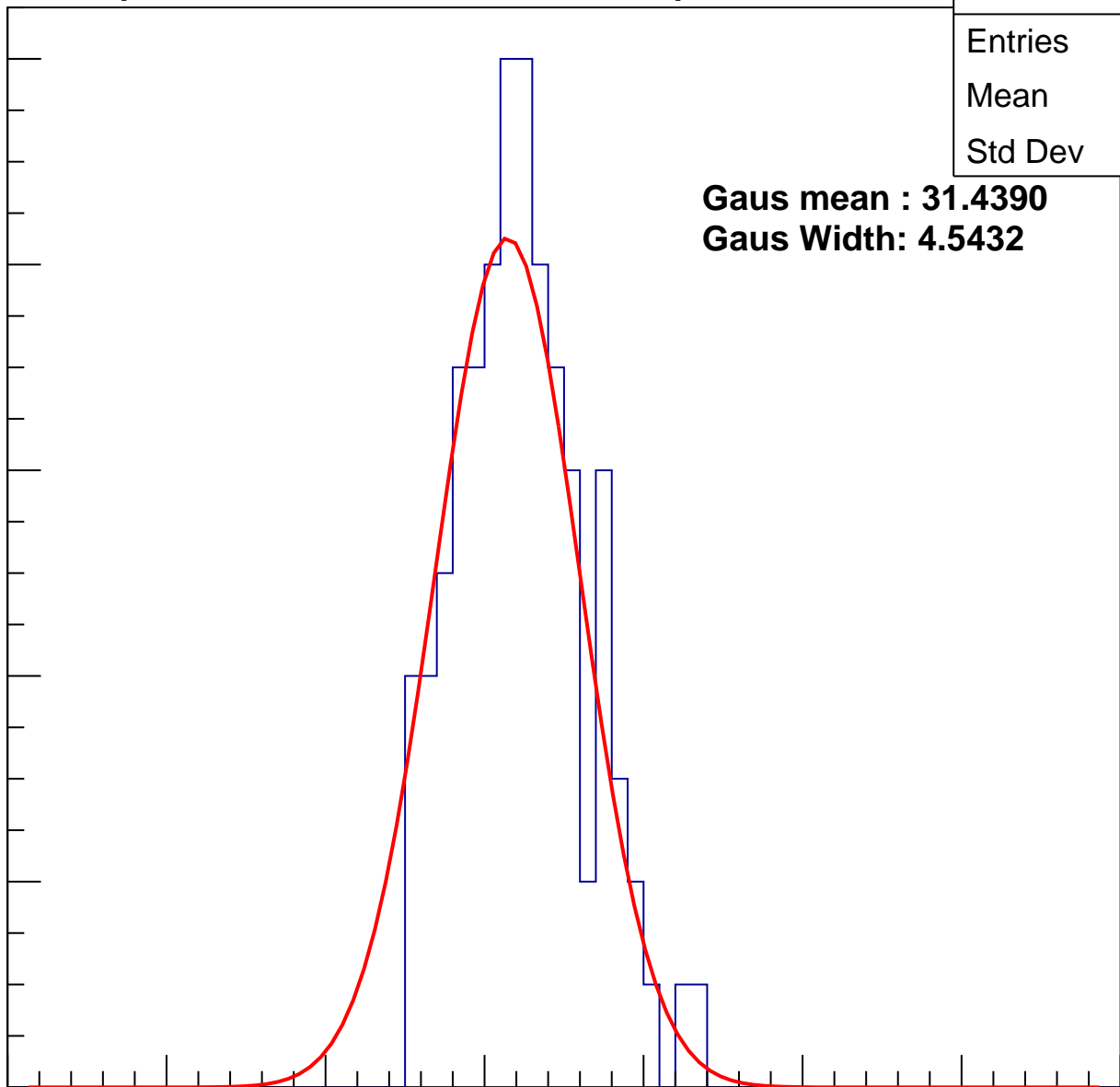
Gaus Width: 4.5432

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U12-ch125, adc1

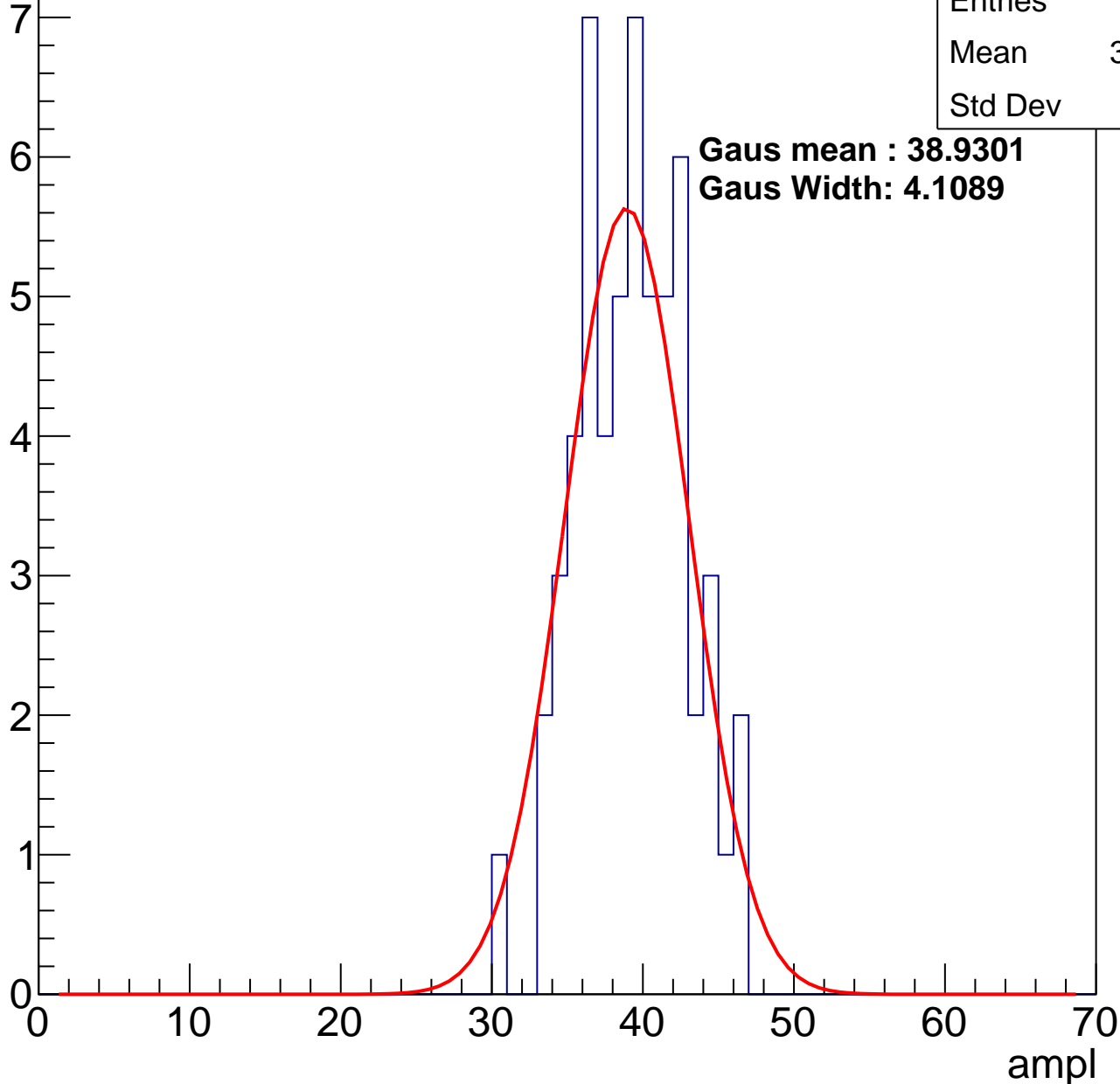
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	38.82
Std Dev	3.49

Gaus mean : 38.9301

Gaus Width: 4.1089



B1L101S, U12-ch125, adc2

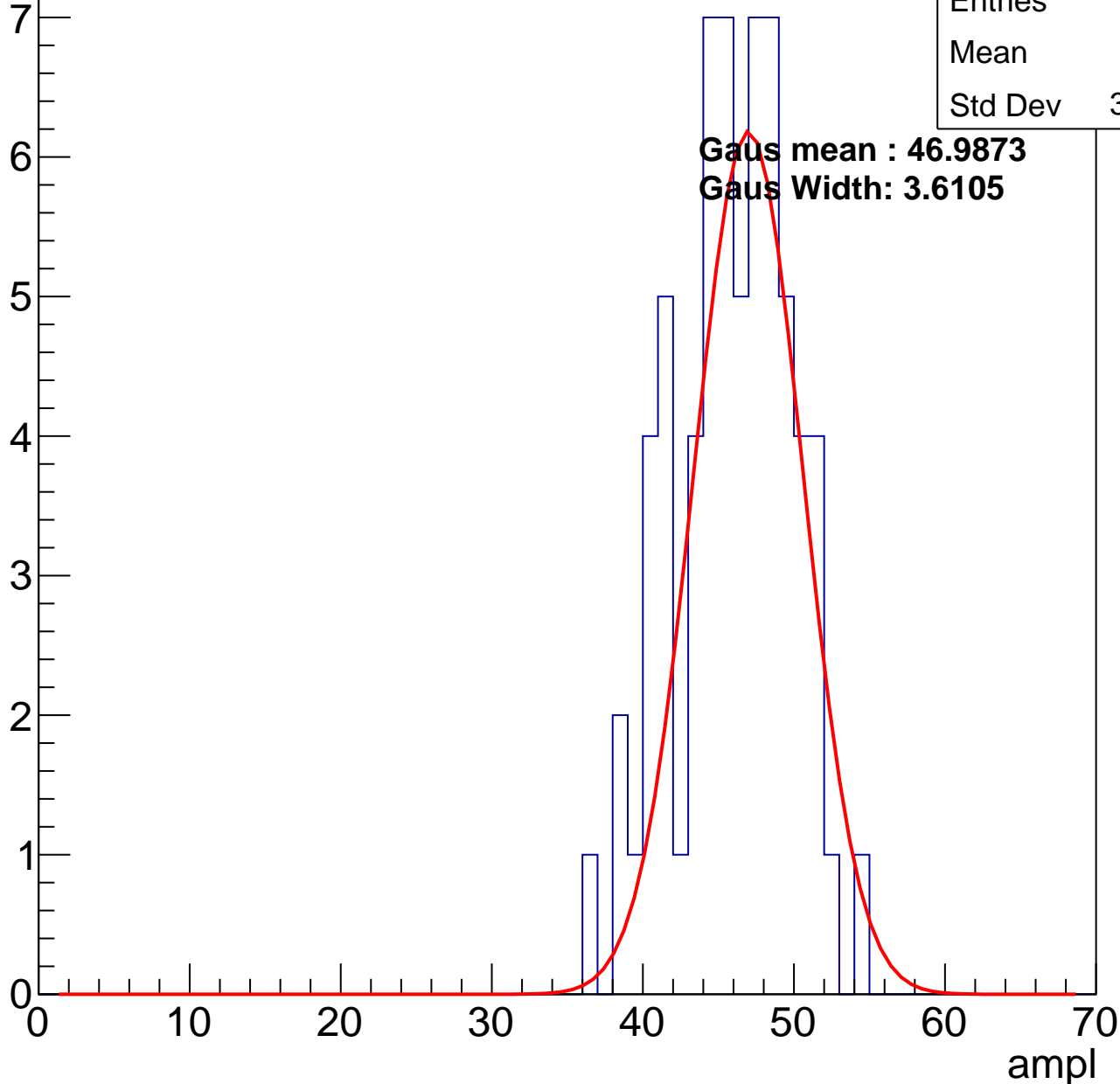
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.5
Std Dev	3.815

Gaus mean : 46.9873

Gaus Width: 3.6105

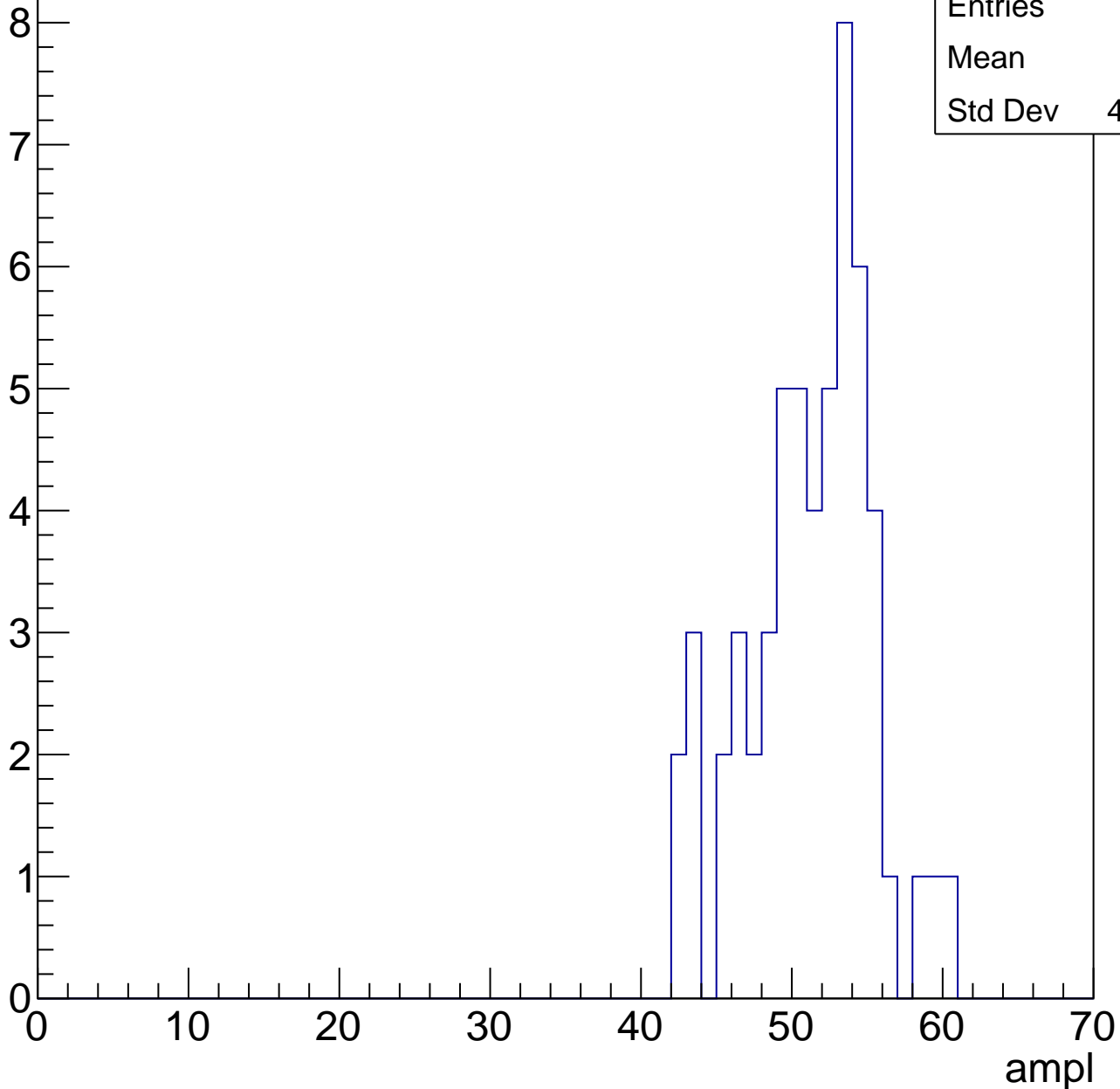


B1L101S, U12-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.7
Std Dev	4.127

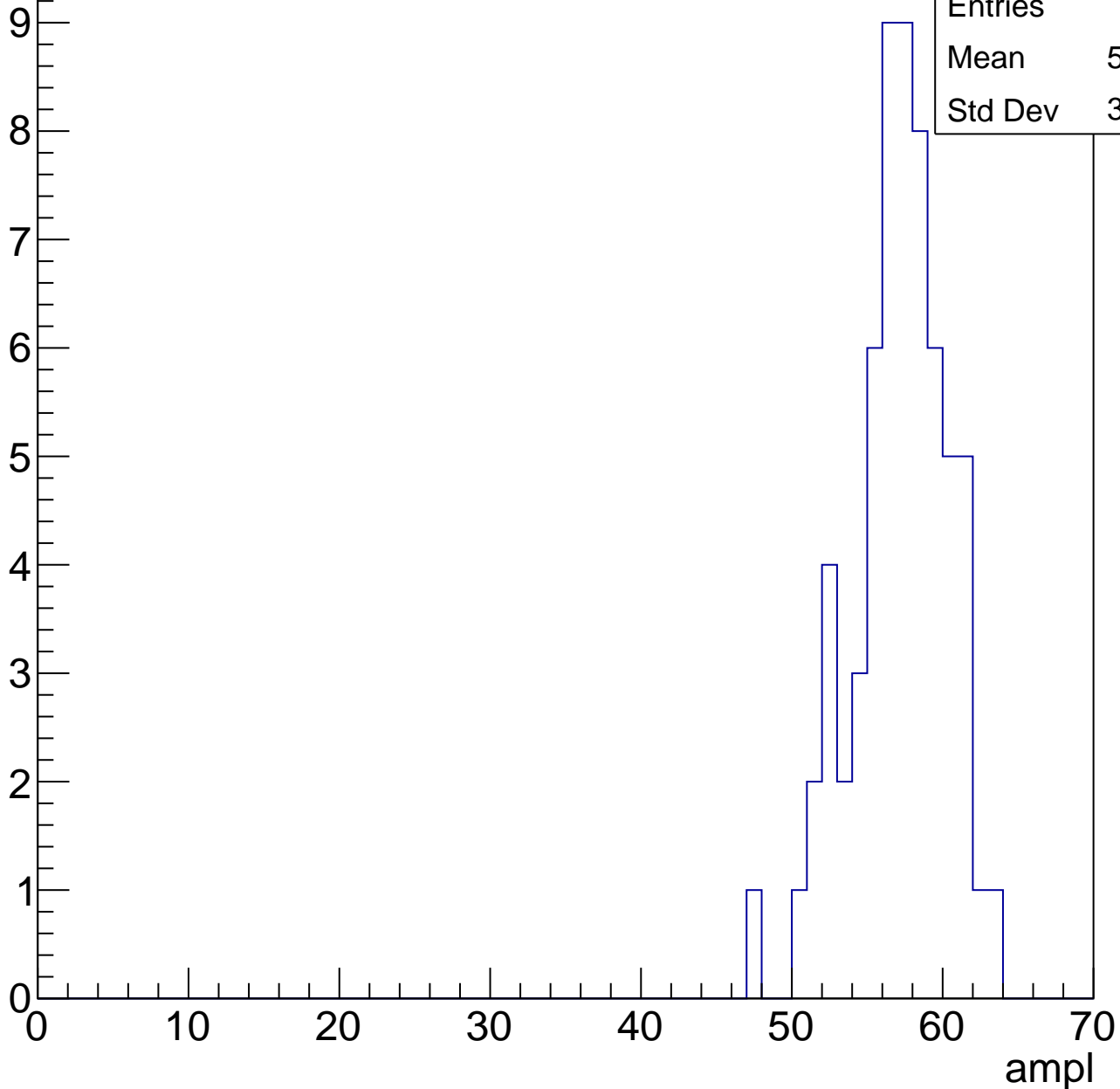


B1L101S, U12-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	56.67
Std Dev	3.147

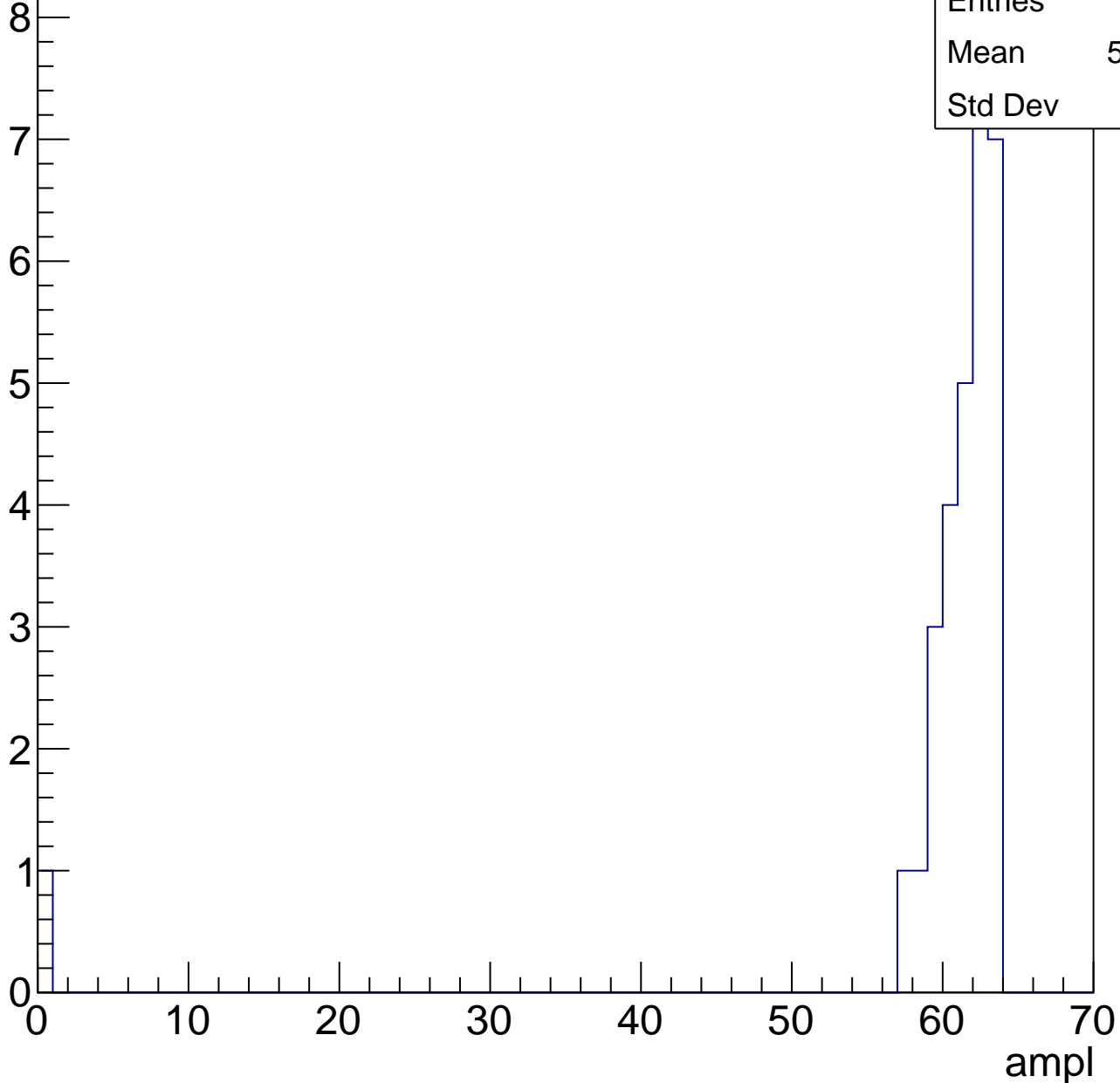


B1L101S, U12-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	59.13
Std Dev	11.1



B1L101S, U12-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U12-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch126, adc0

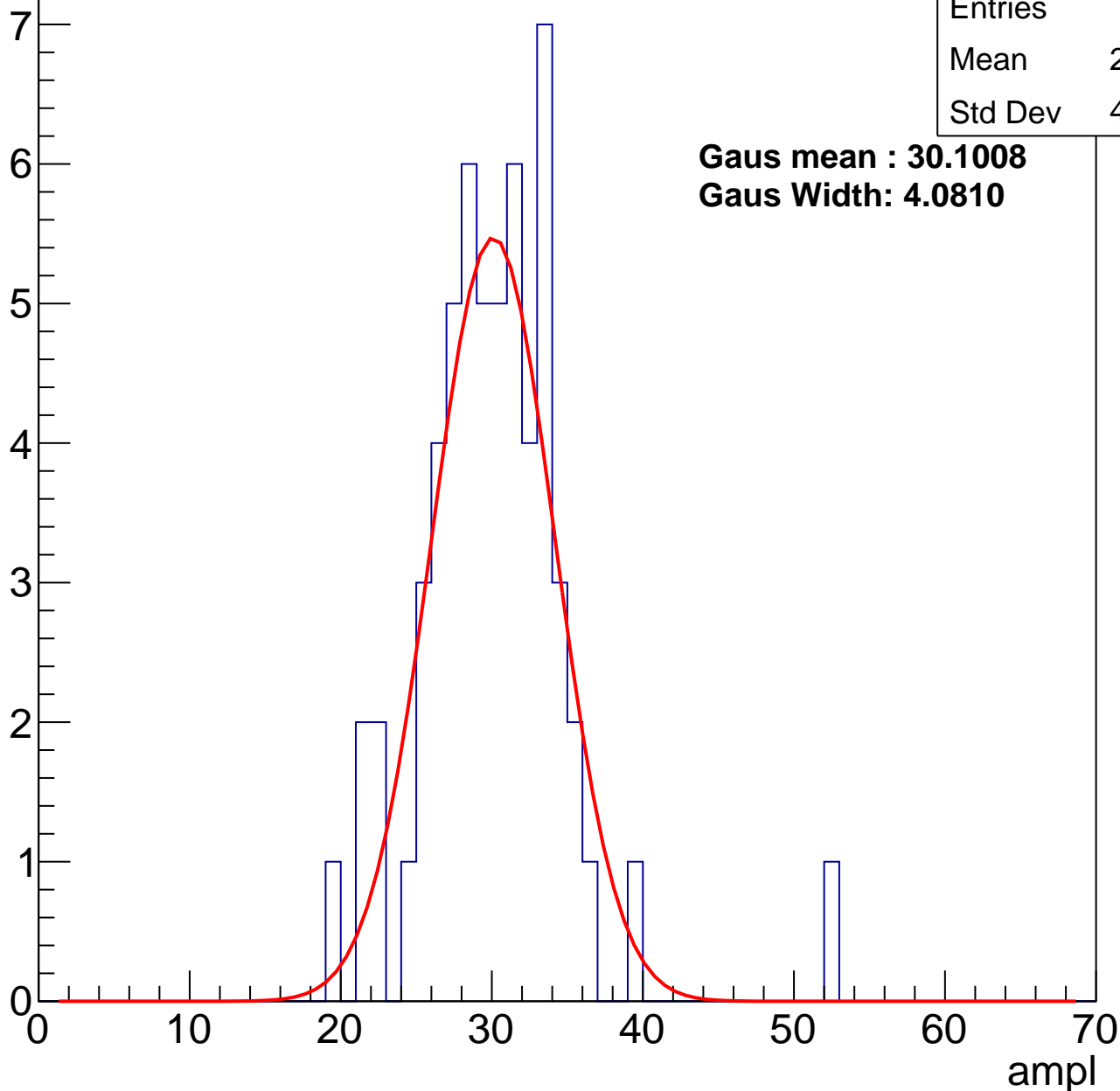
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.66
Std Dev	4.936

Gaus mean : 30.1008

Gaus Width: 4.0810



B1L101S, U12-ch126, adc1

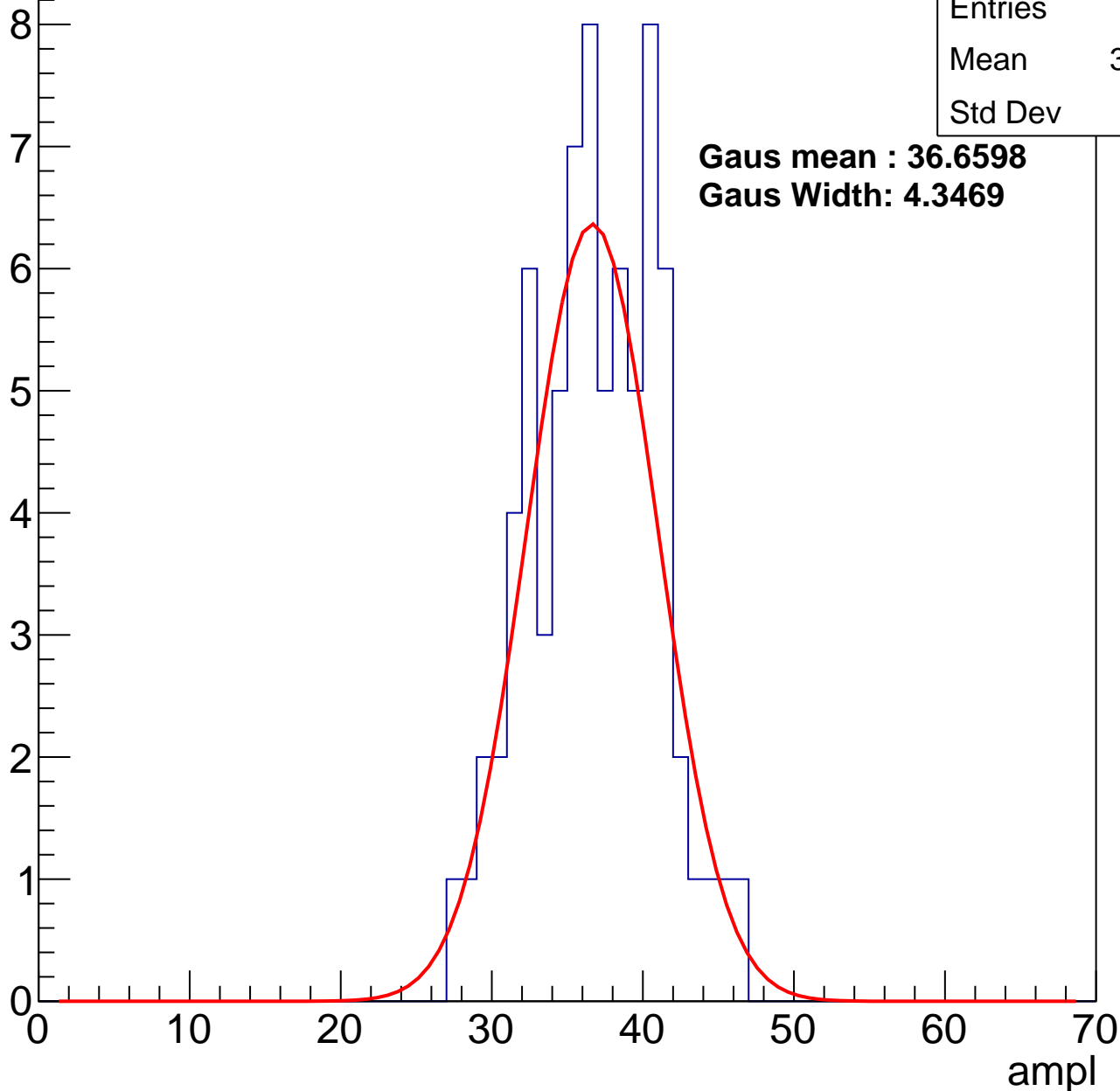
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.36
Std Dev	4.12

Gaus mean : 36.6598

Gaus Width: 4.3469



B1L101S, U12-ch126, adc2

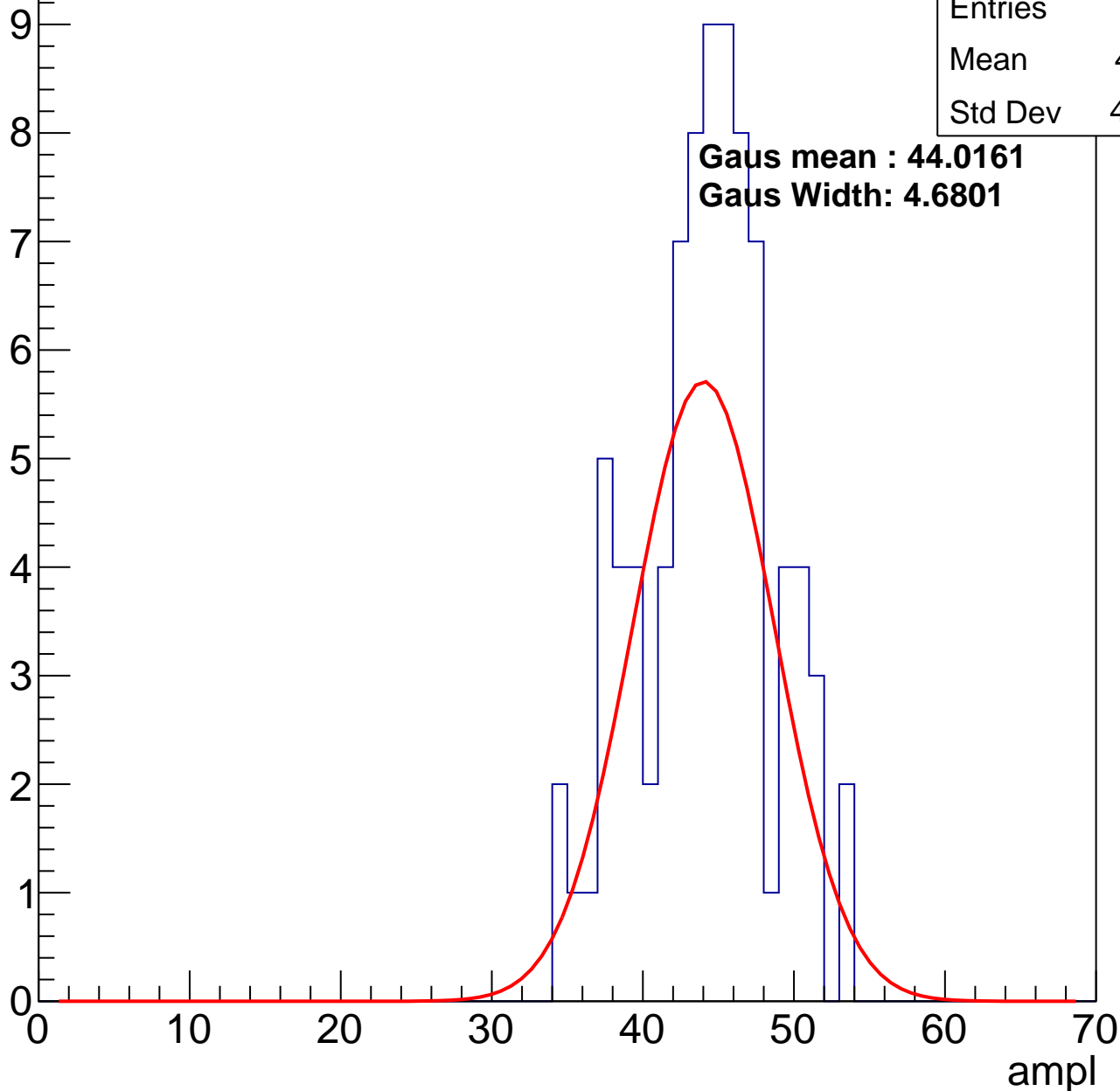
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	43.71
Std Dev	4.344

Gaus mean : 44.0161

Gaus Width: 4.6801

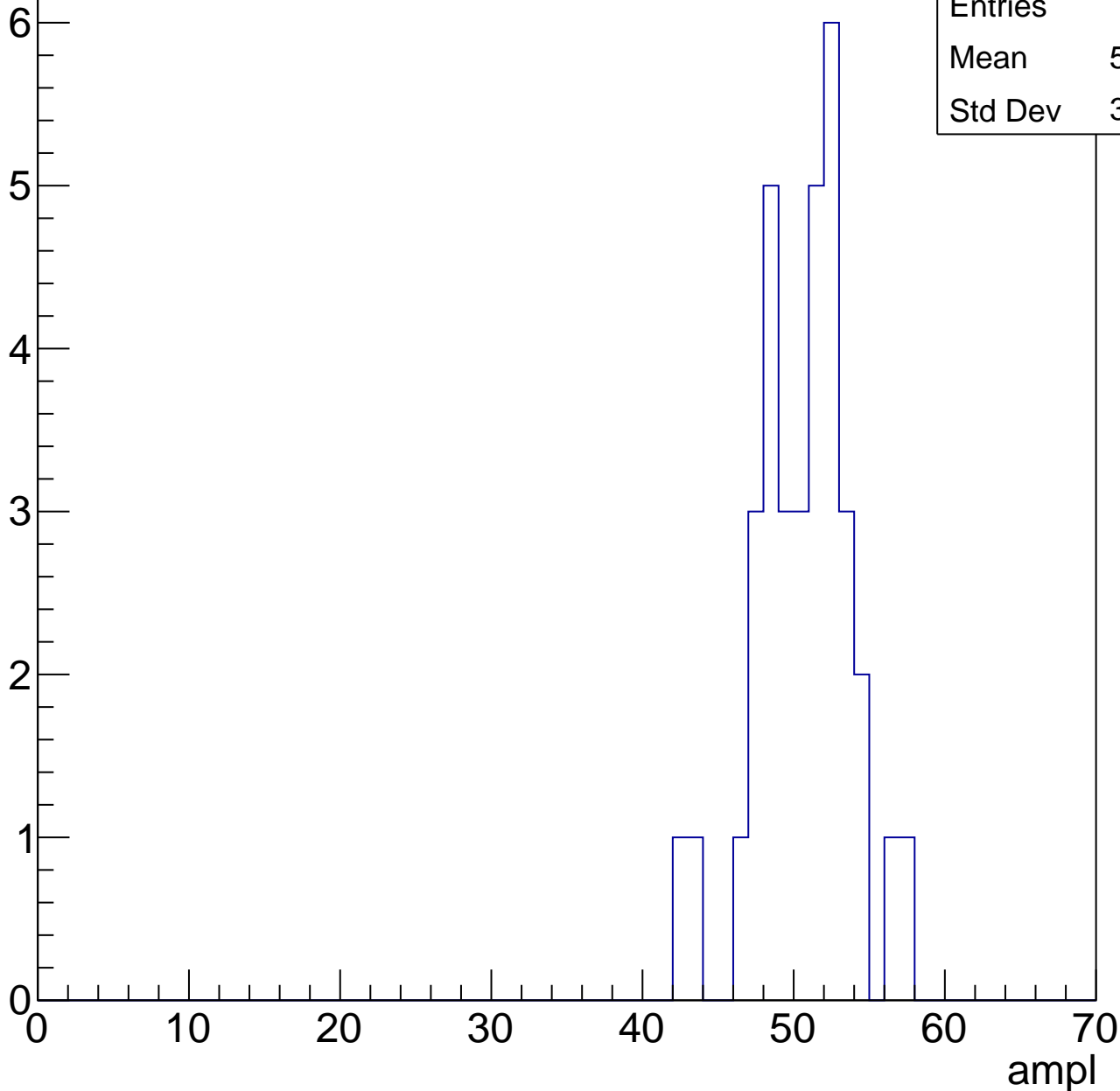


B1L101S, U12-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	50.17
Std Dev	3.167

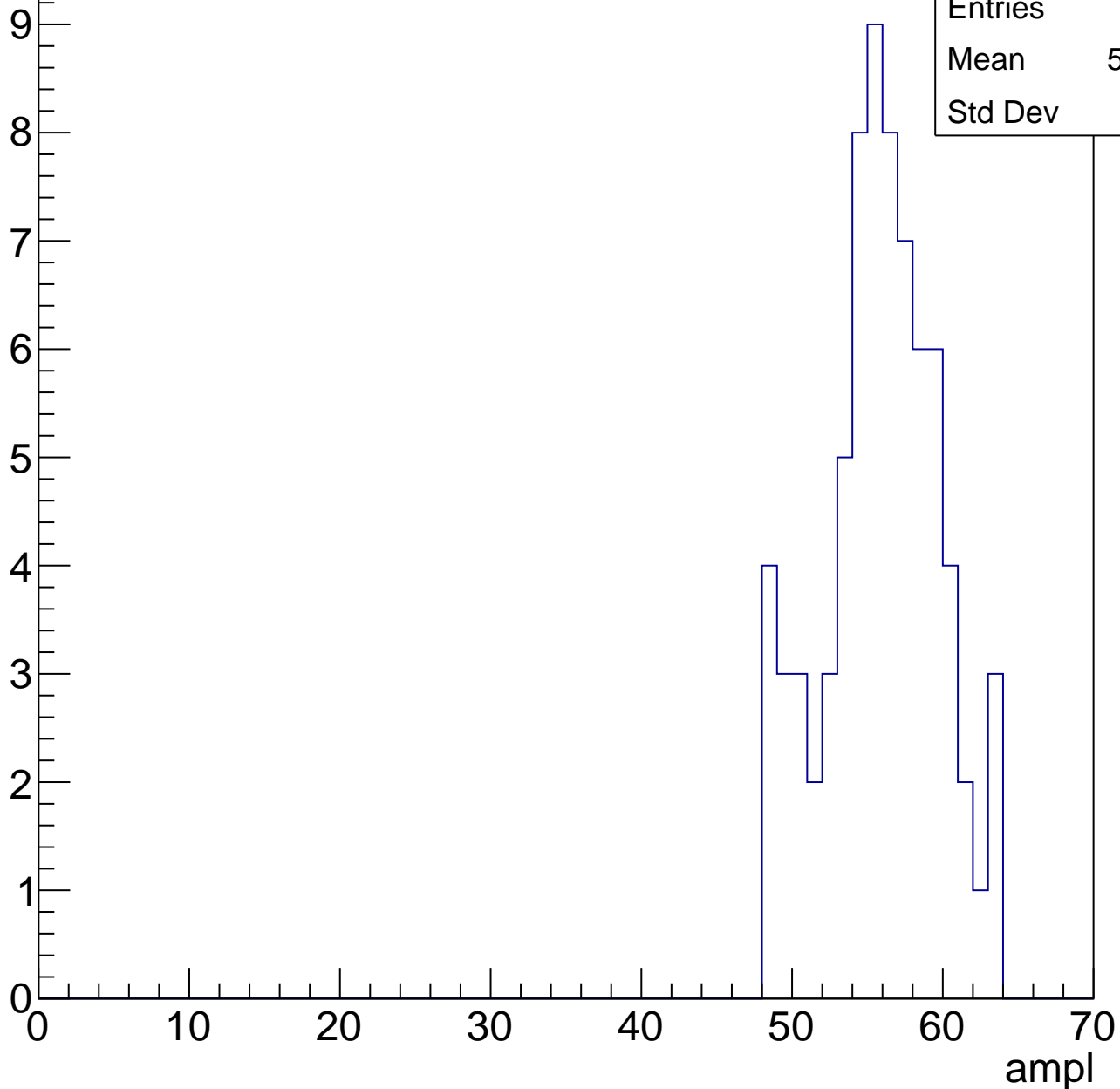


B1L101S, U12-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	55.42
Std Dev	3.77

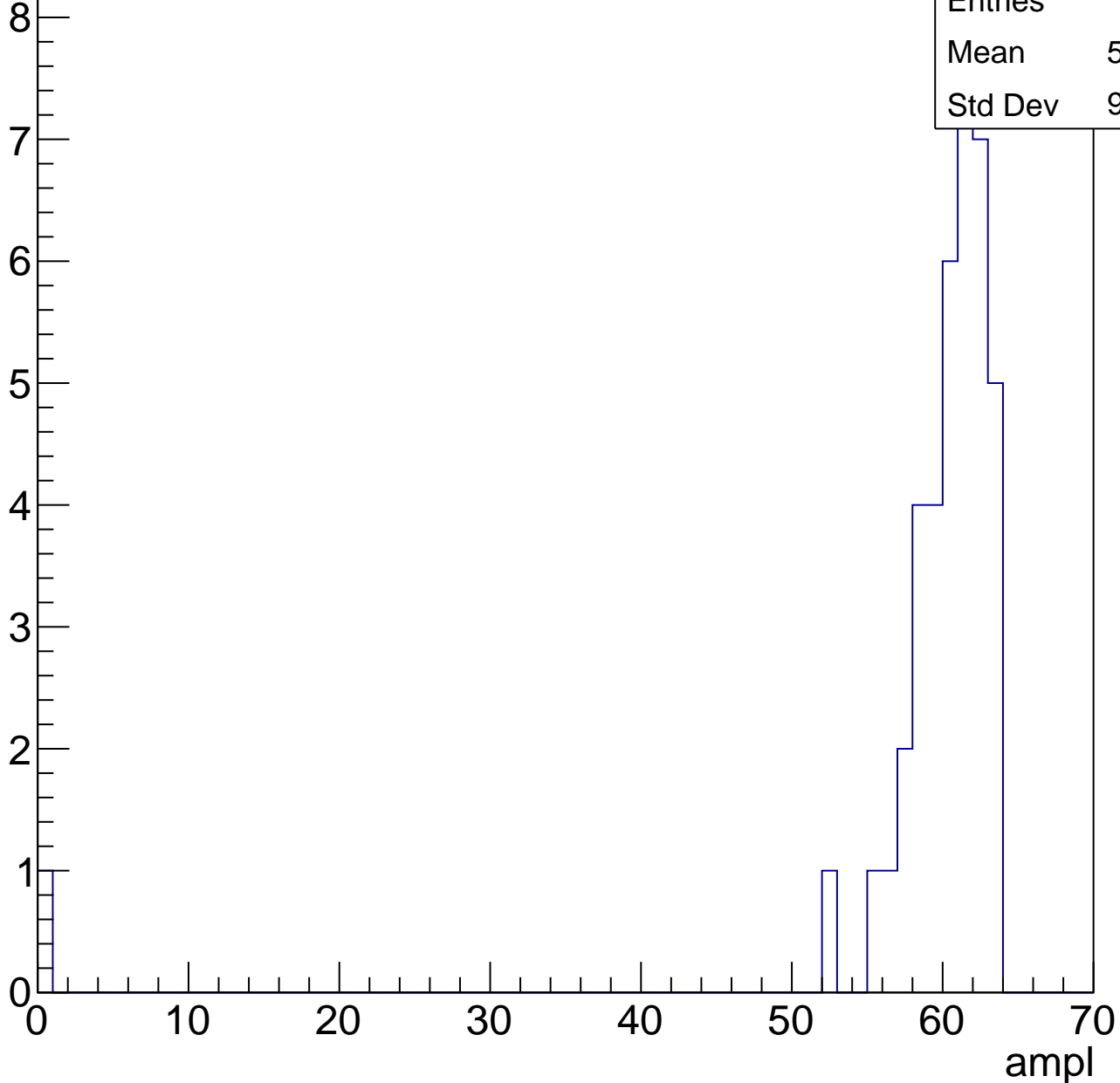


B1L101S, U12-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.55
Std Dev	9.669



B1L101S, U12-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U12-ch127, adc0

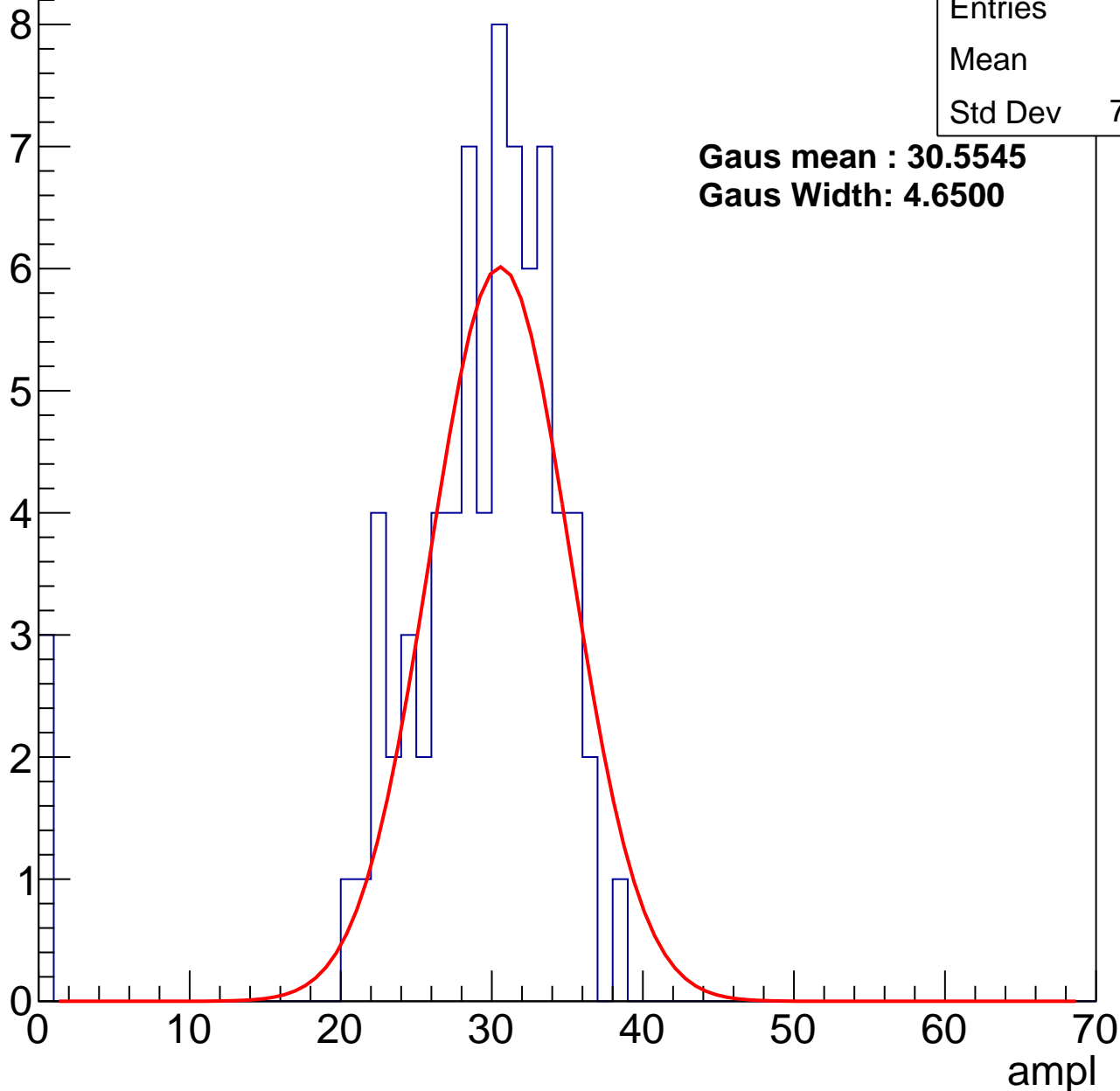
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.2
Std Dev	7.052

Gaus mean : 30.5545

Gaus Width: 4.6500



B1L101S, U12-ch127, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	36.85
Std Dev	3.881

Gaus mean : 37.3076

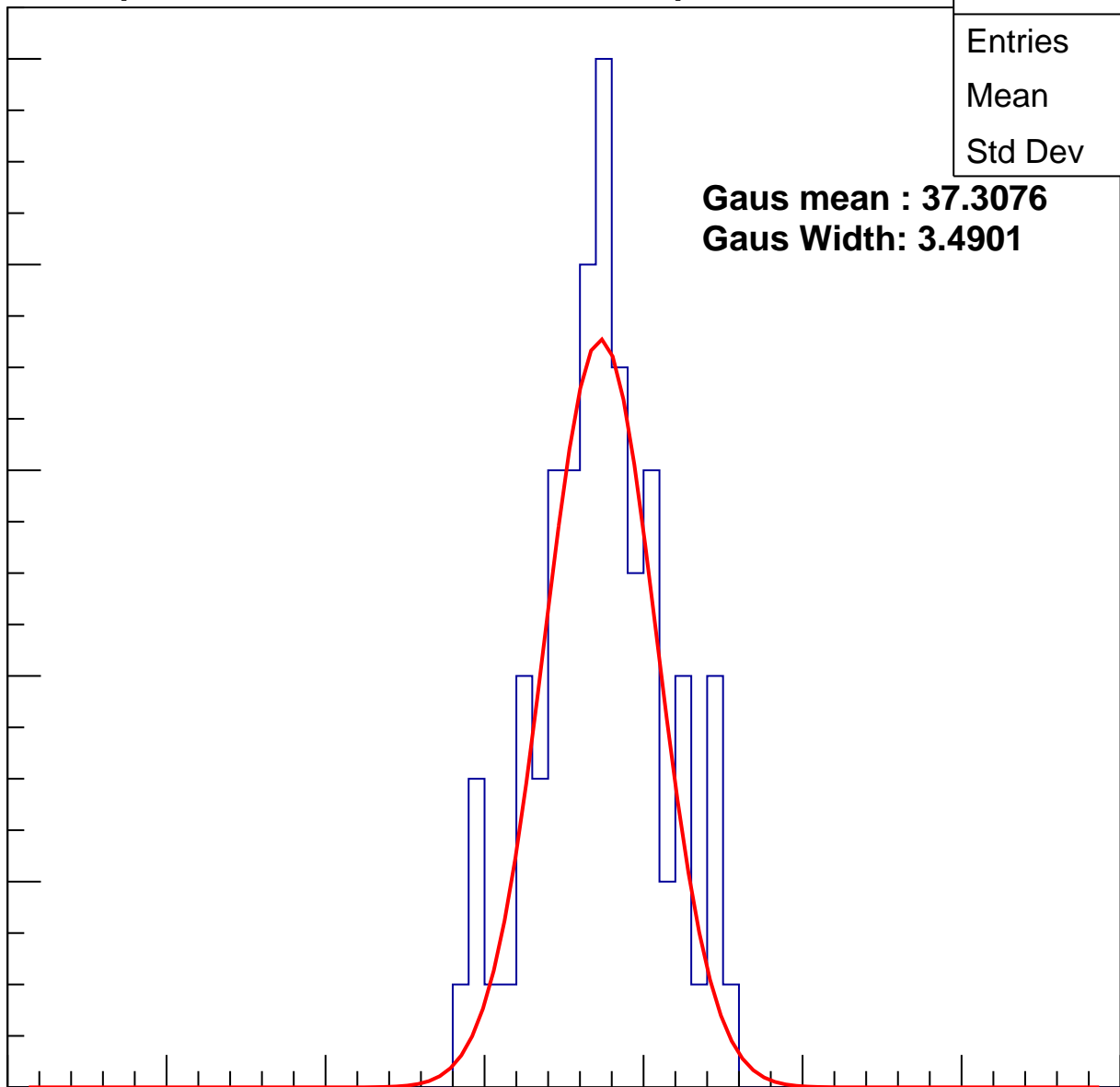
Gaus Width: 3.4901

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U12-ch127, adc2

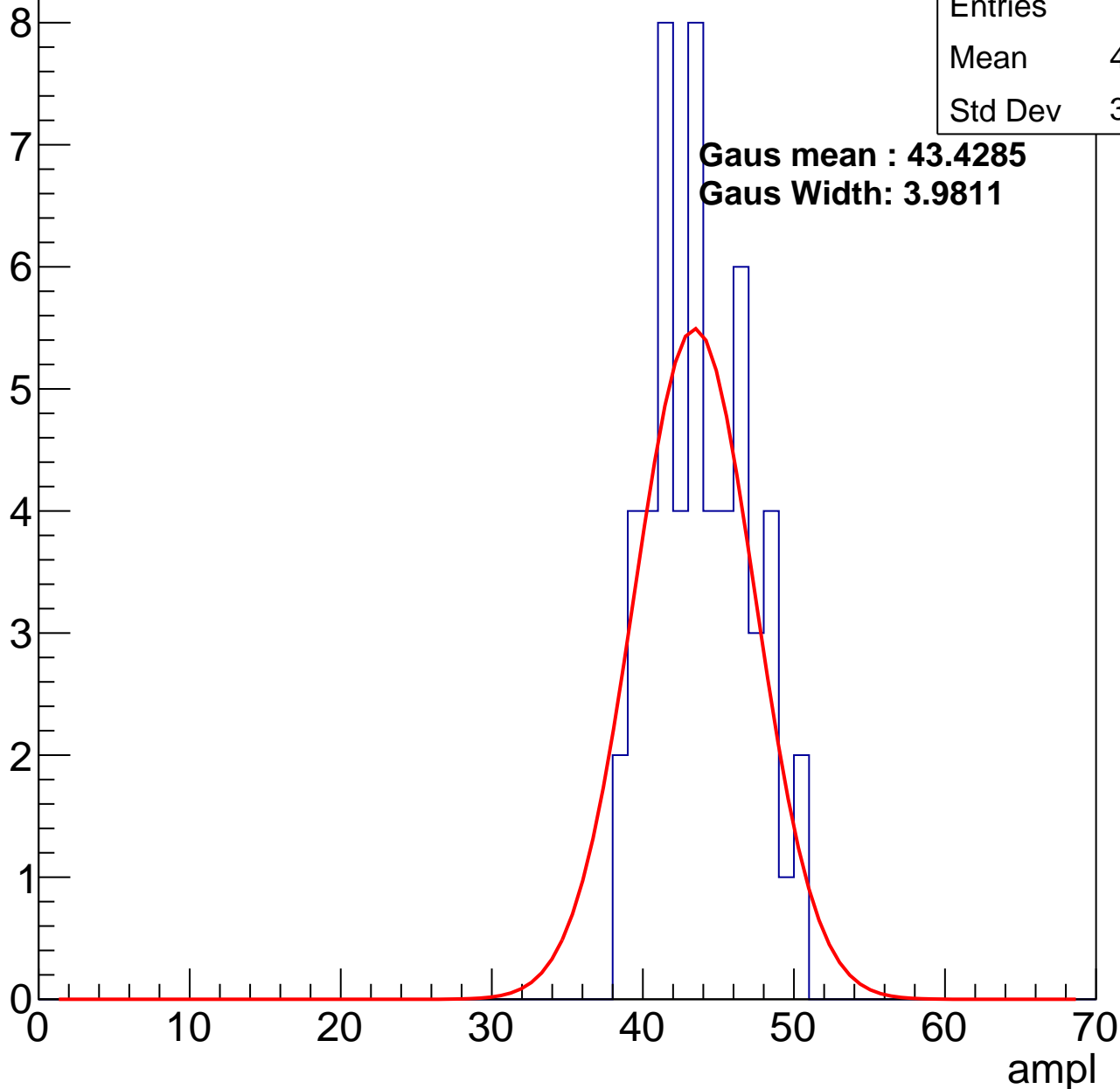
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.44
Std Dev	3.137

Gaus mean : 43.4285

Gaus Width: 3.9811

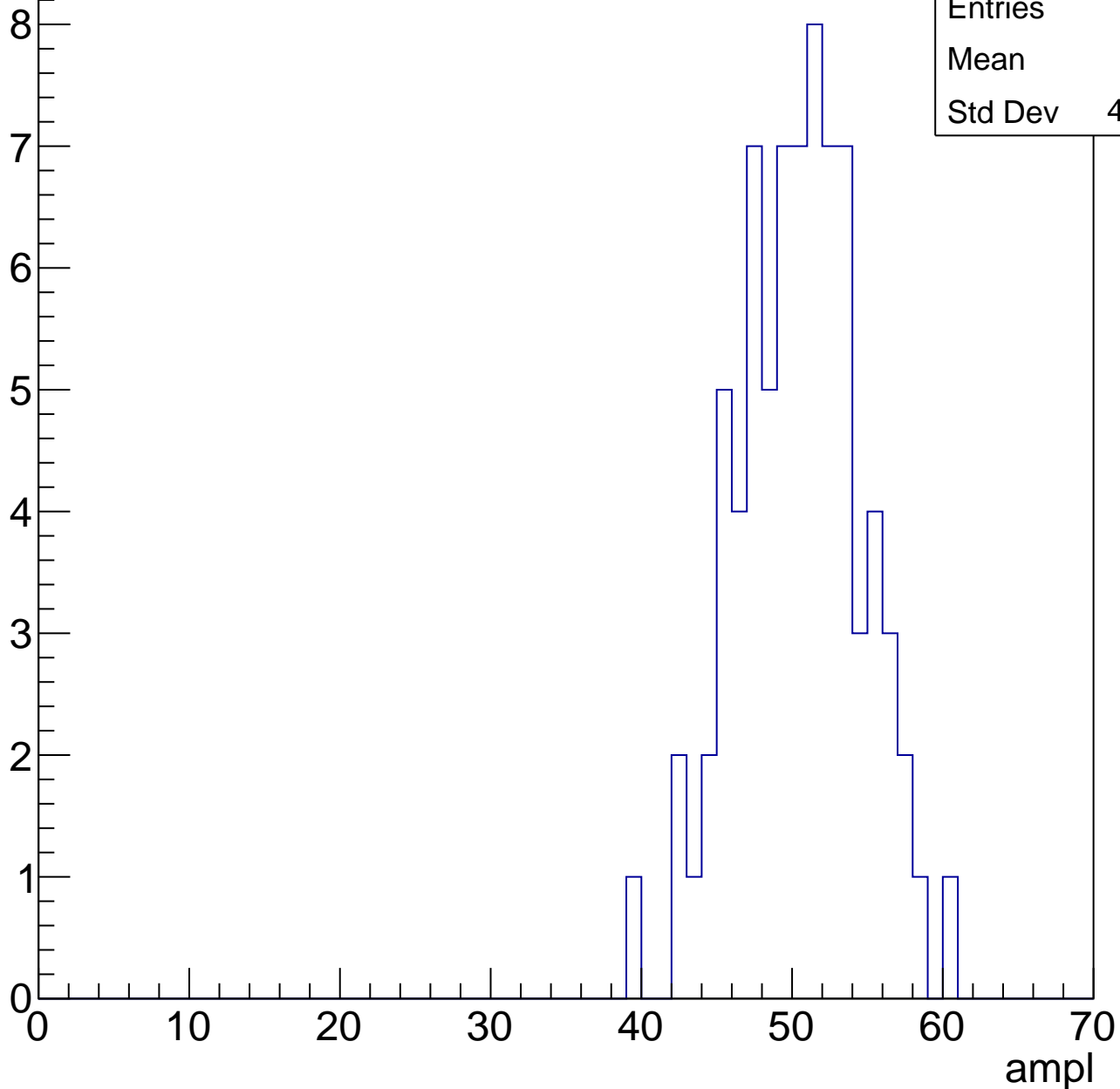


B1L101S, U12-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50
Std Dev	4.055

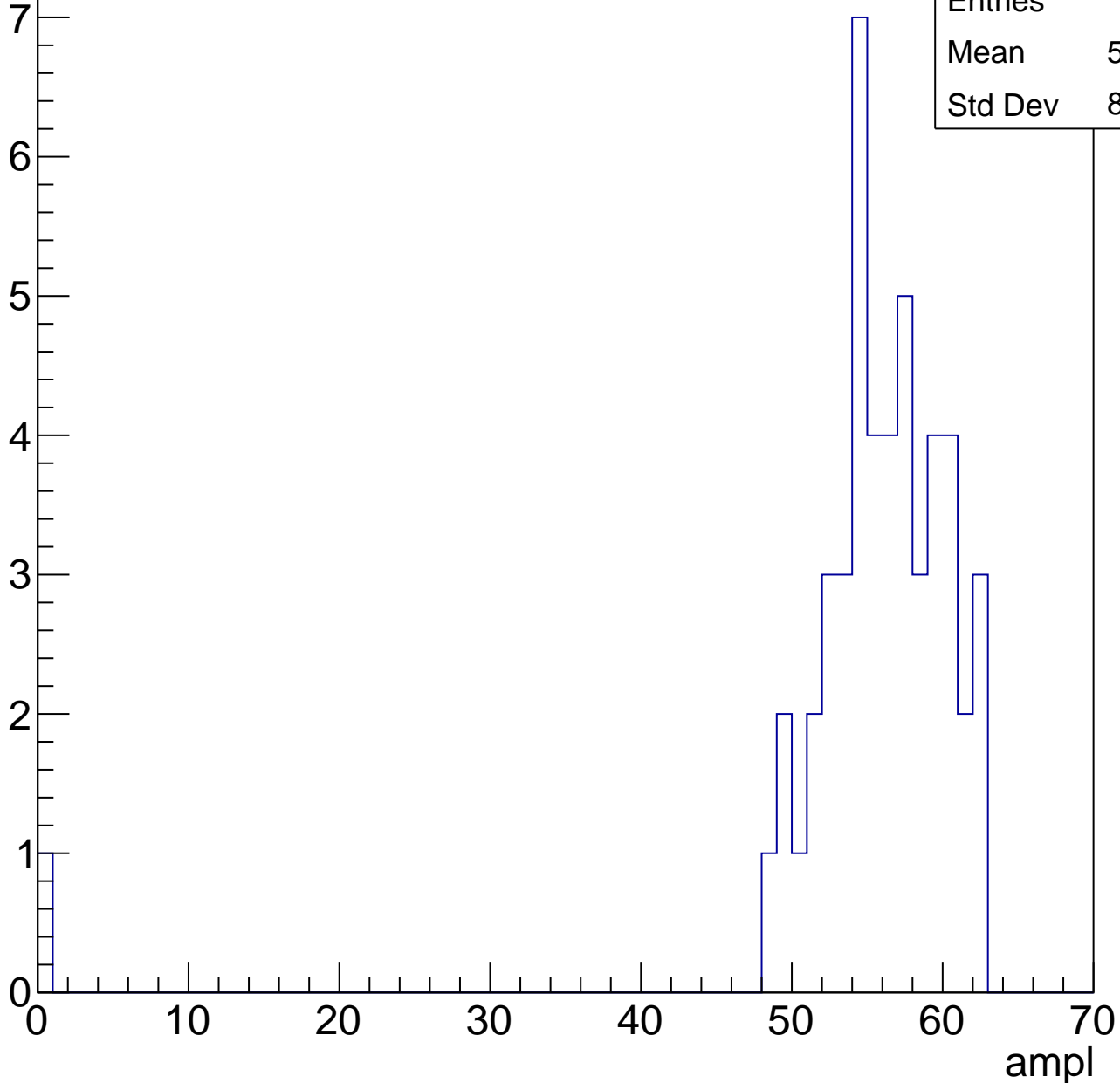


B1L101S, U12-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	54.65
Std Dev	8.663

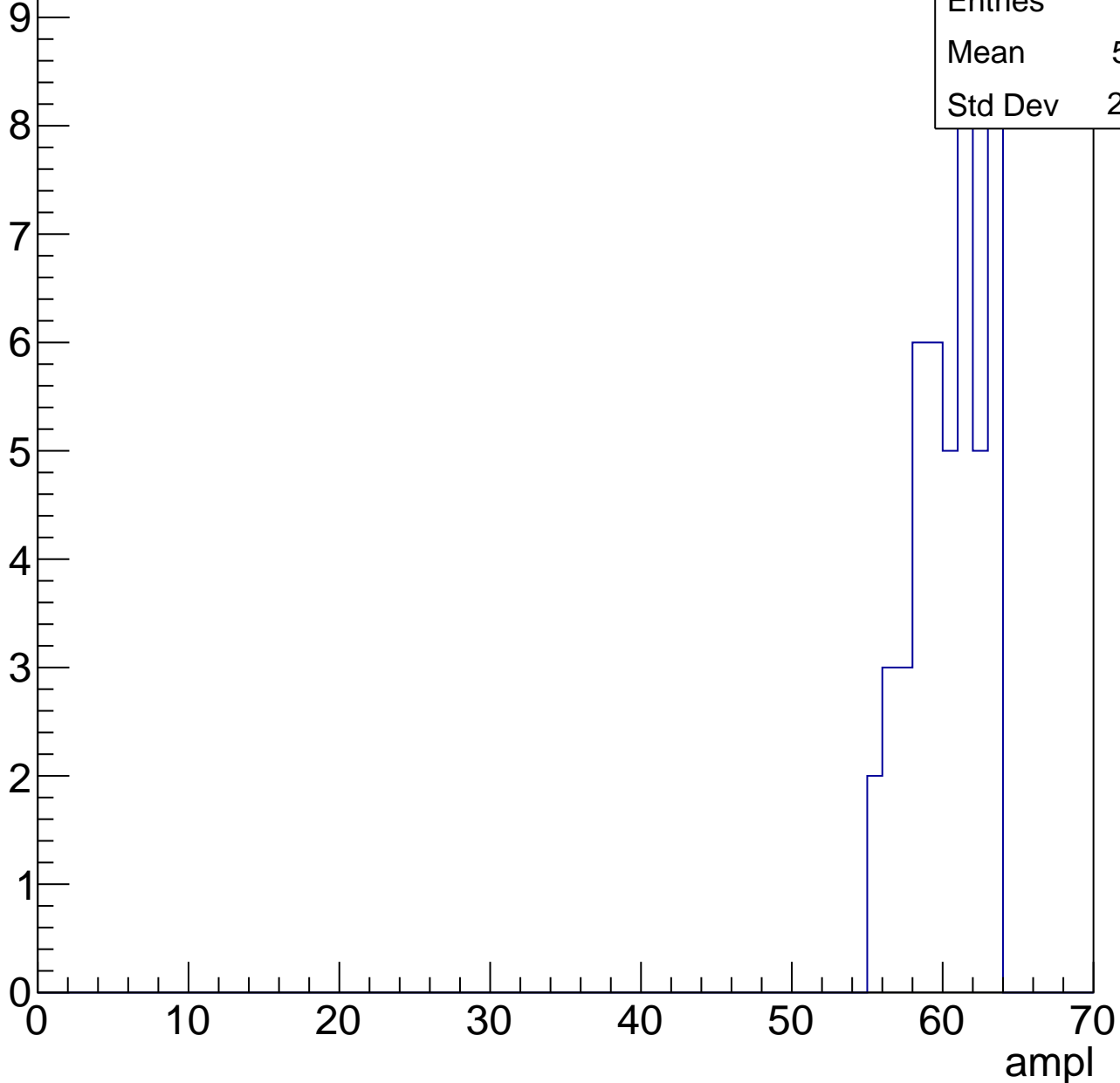


B1L101S, U12-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

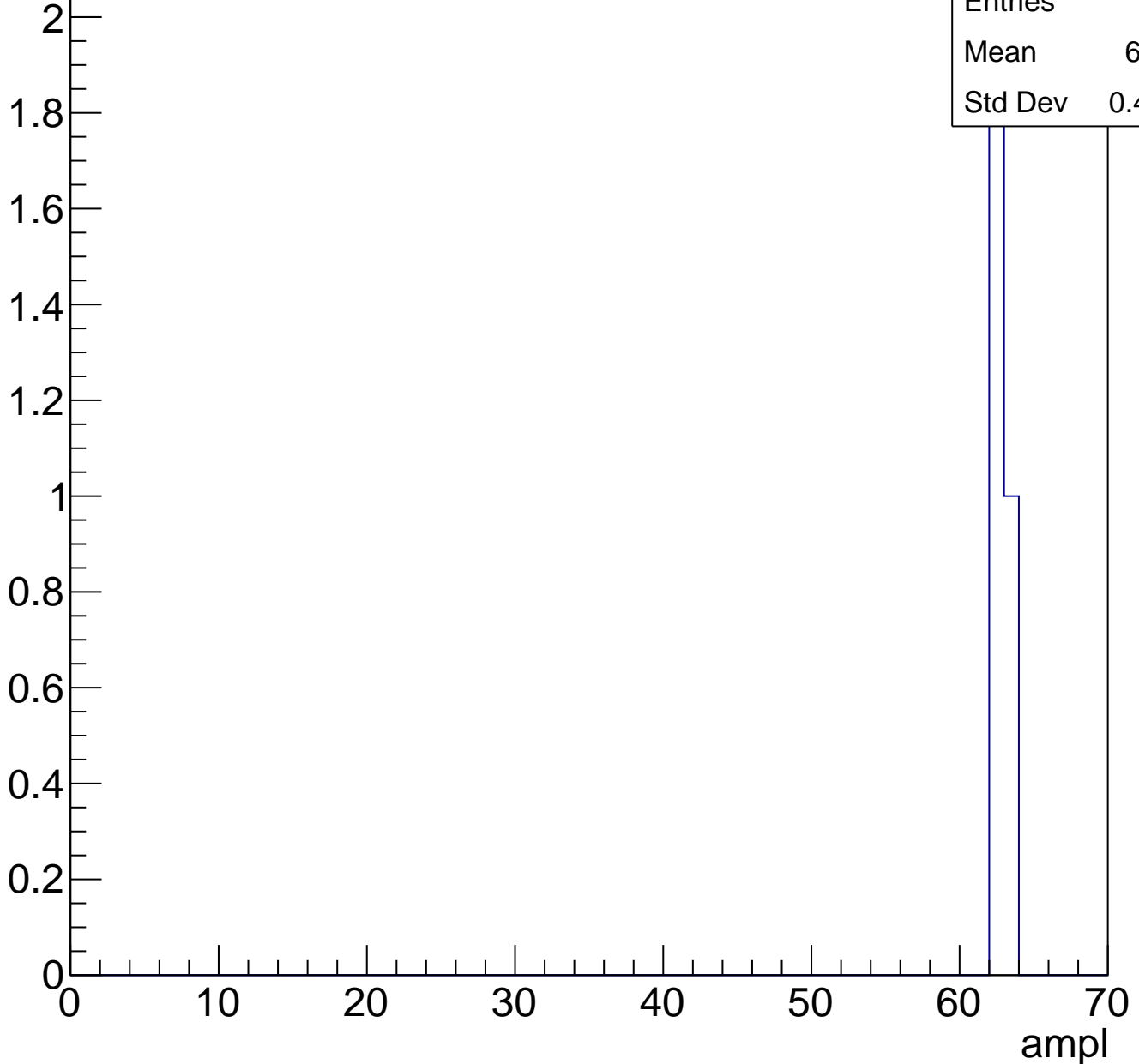
Entries	47
Mean	59.91
Std Dev	2.368



B1L101S, U12-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0