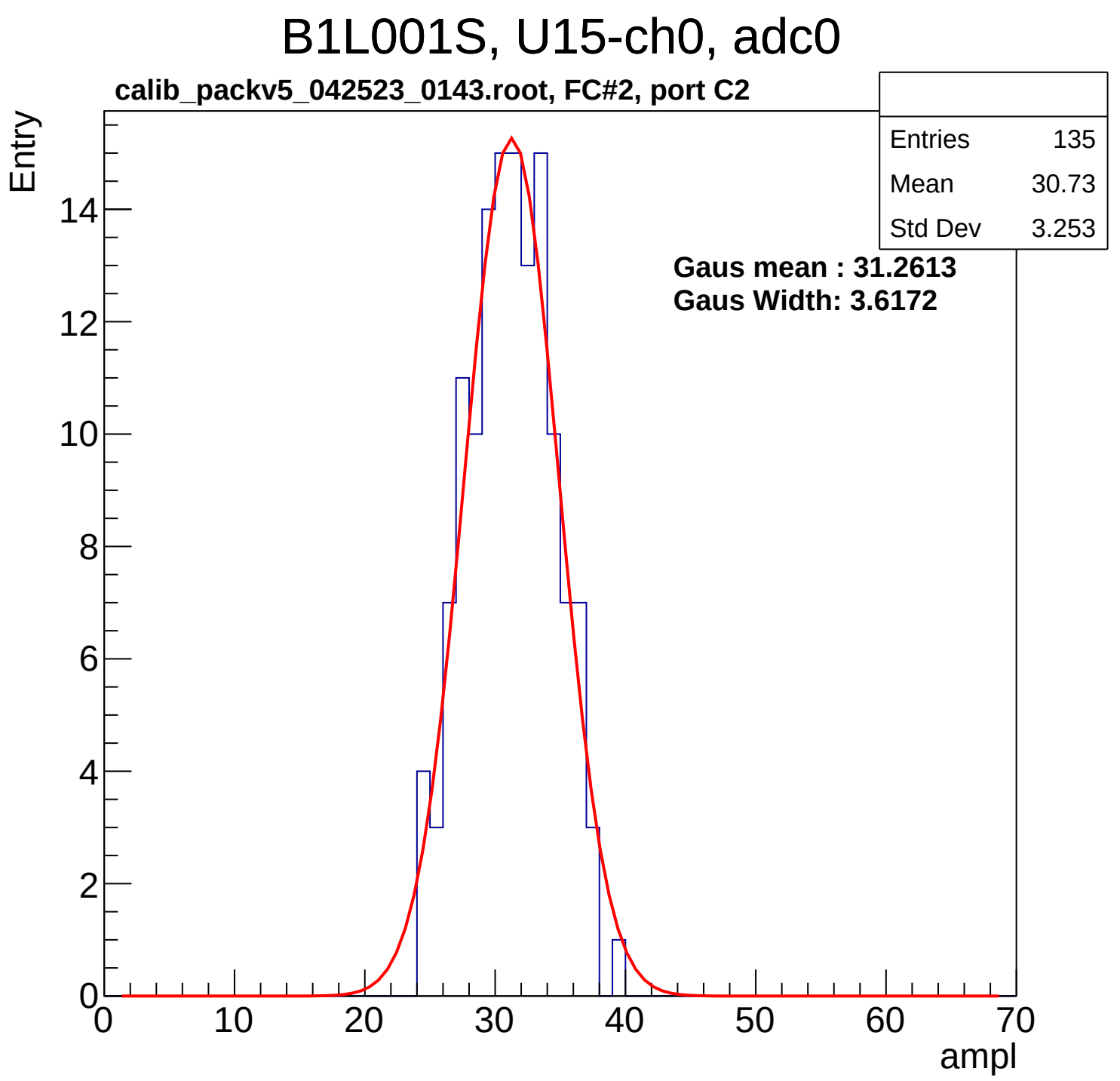
B1L001S, U15-ch0, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 30.73
Std Dev 3.253
Gaus mean : 31.2613
Gaus Width: 3.6172
Entry
ampl
0
2
4
6
8
10
12
14
0
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40
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60
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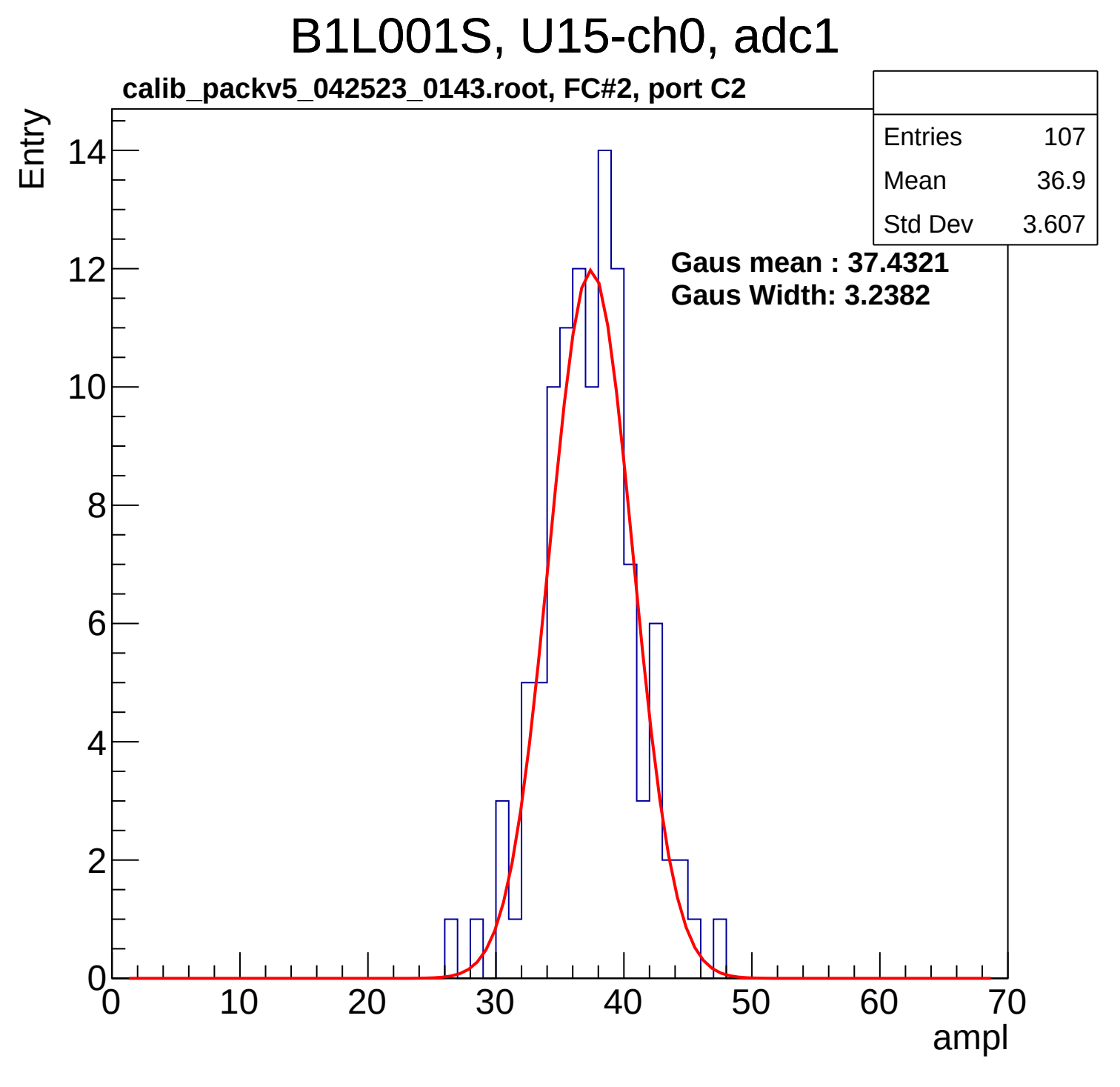
B1L001S, U15-ch0, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 36.9
Std Dev 3.607
Gaus mean : 37.4321
Gaus Width: 3.2382
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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60
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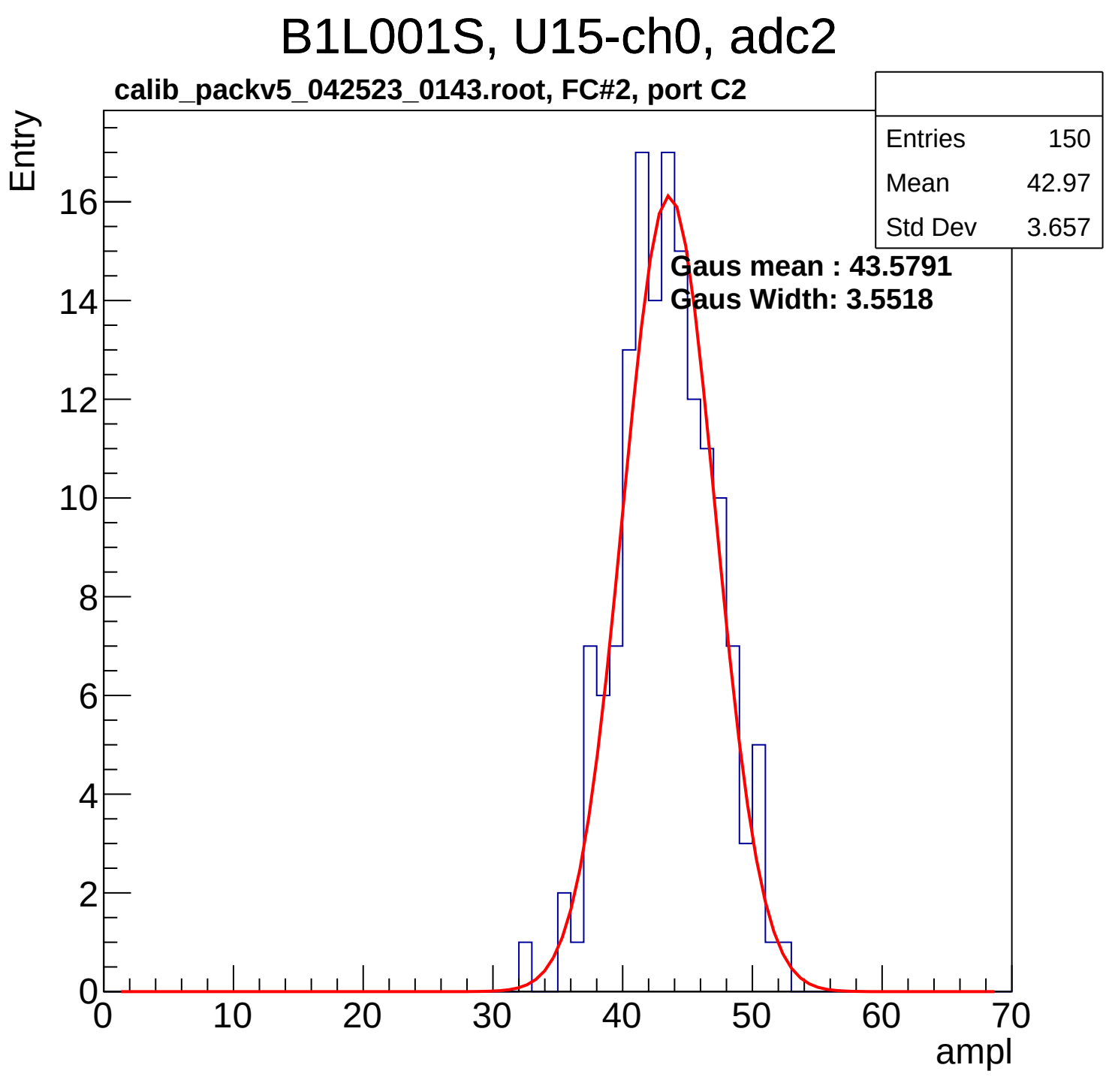
B1L001S, U15-ch0, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 42.97
Std Dev 3.657
Gaus mean : 43.5791
Gaus Width: 3.5518
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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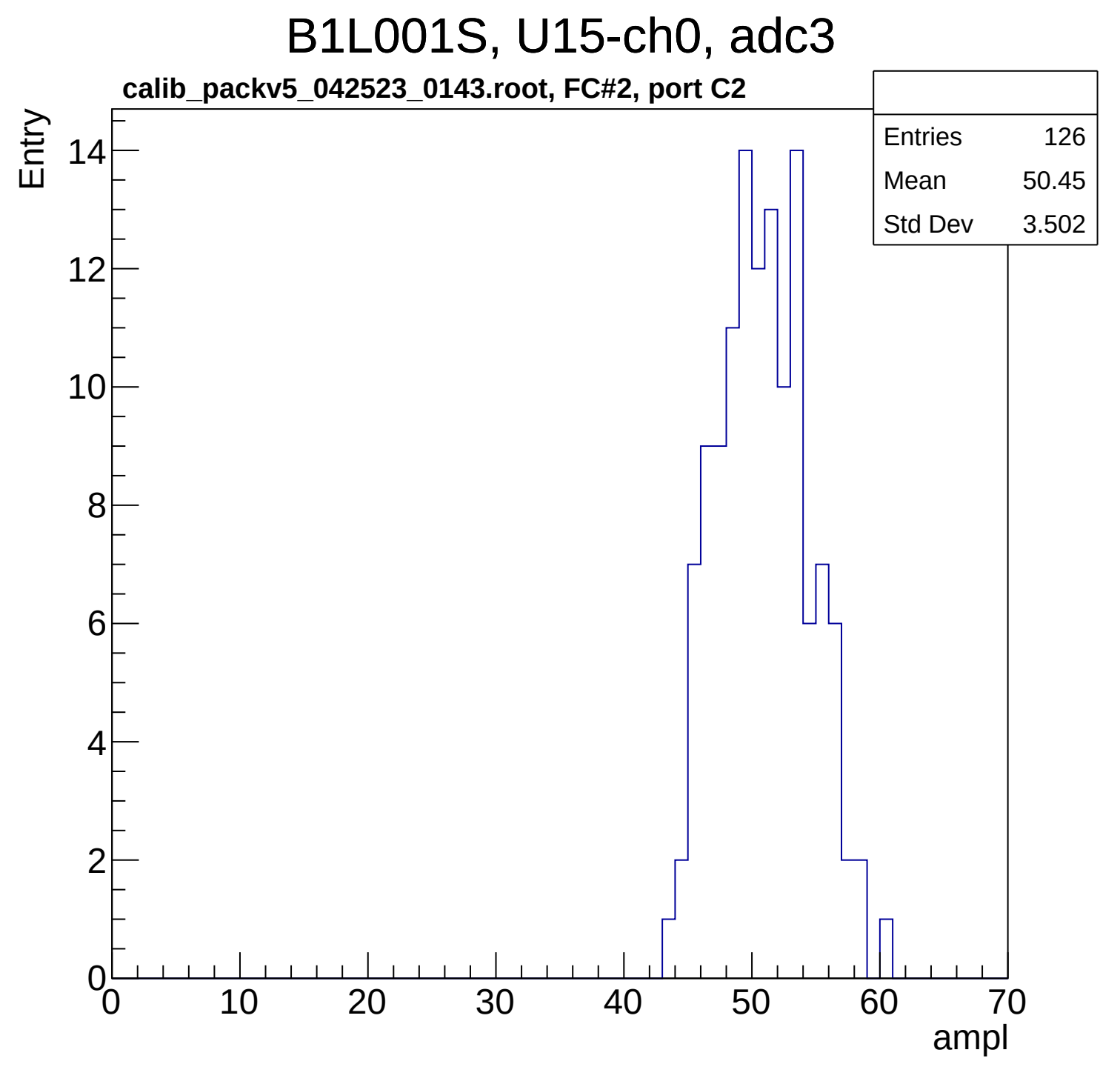
B1L001S, U15-ch0, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 50.45
Std Dev 3.502
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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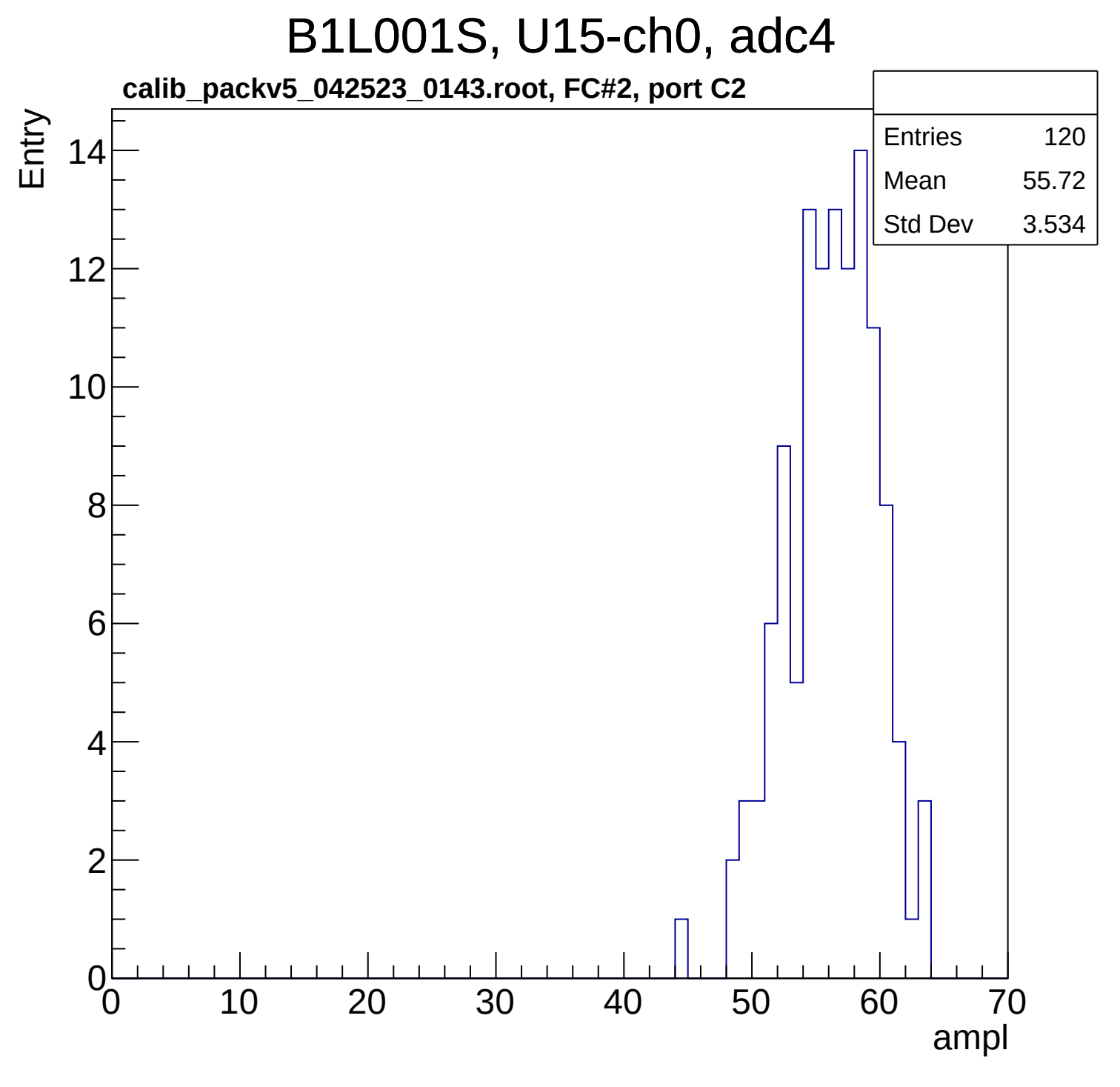
B1L001S, U15-ch0, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 55.72
Std Dev 3.534
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
50
60
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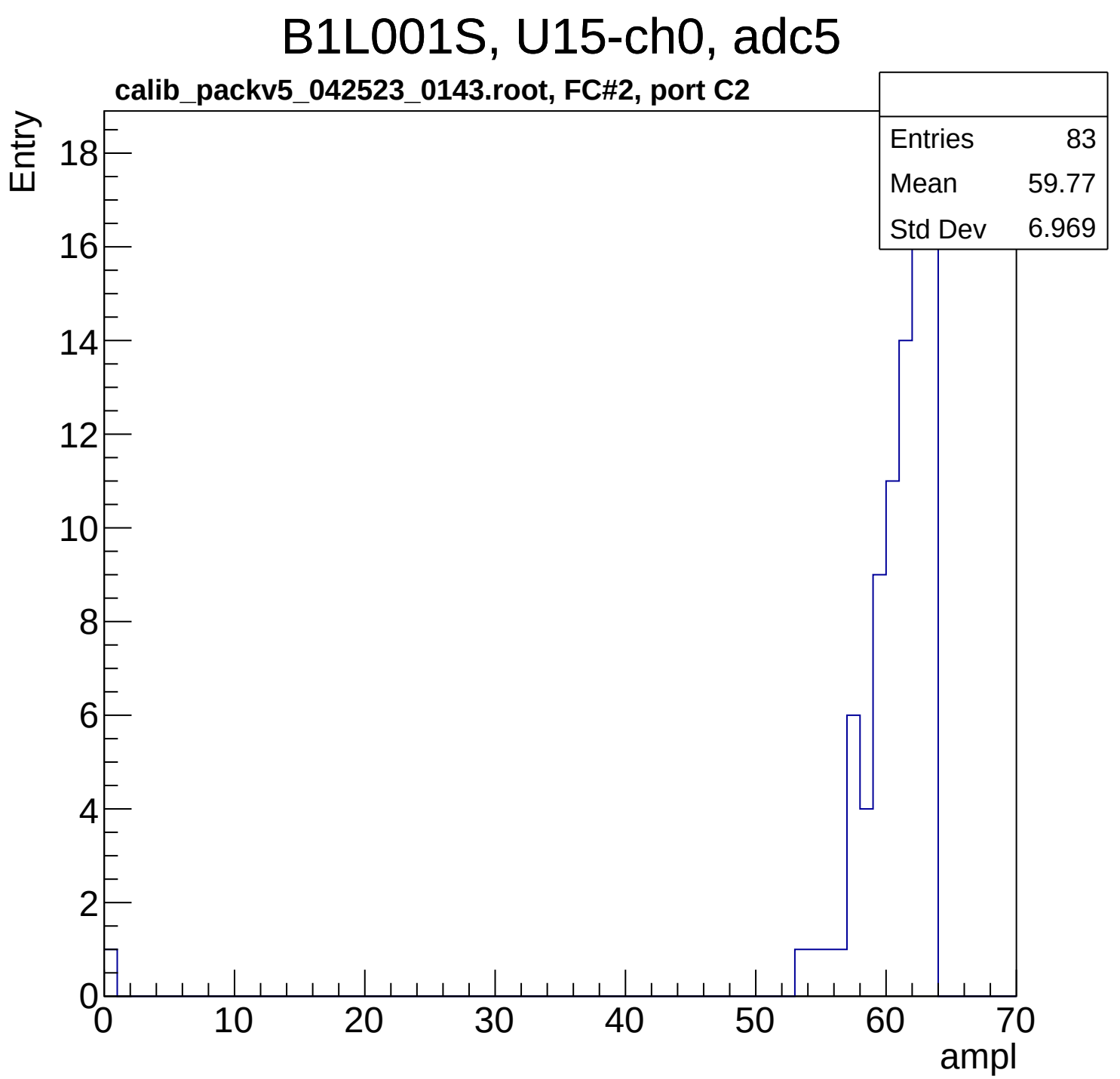
B1L001S, U15-ch0, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 59.77
Std Dev 6.969
Entry
ampl
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4
6
8
10
12
14
16
18
0
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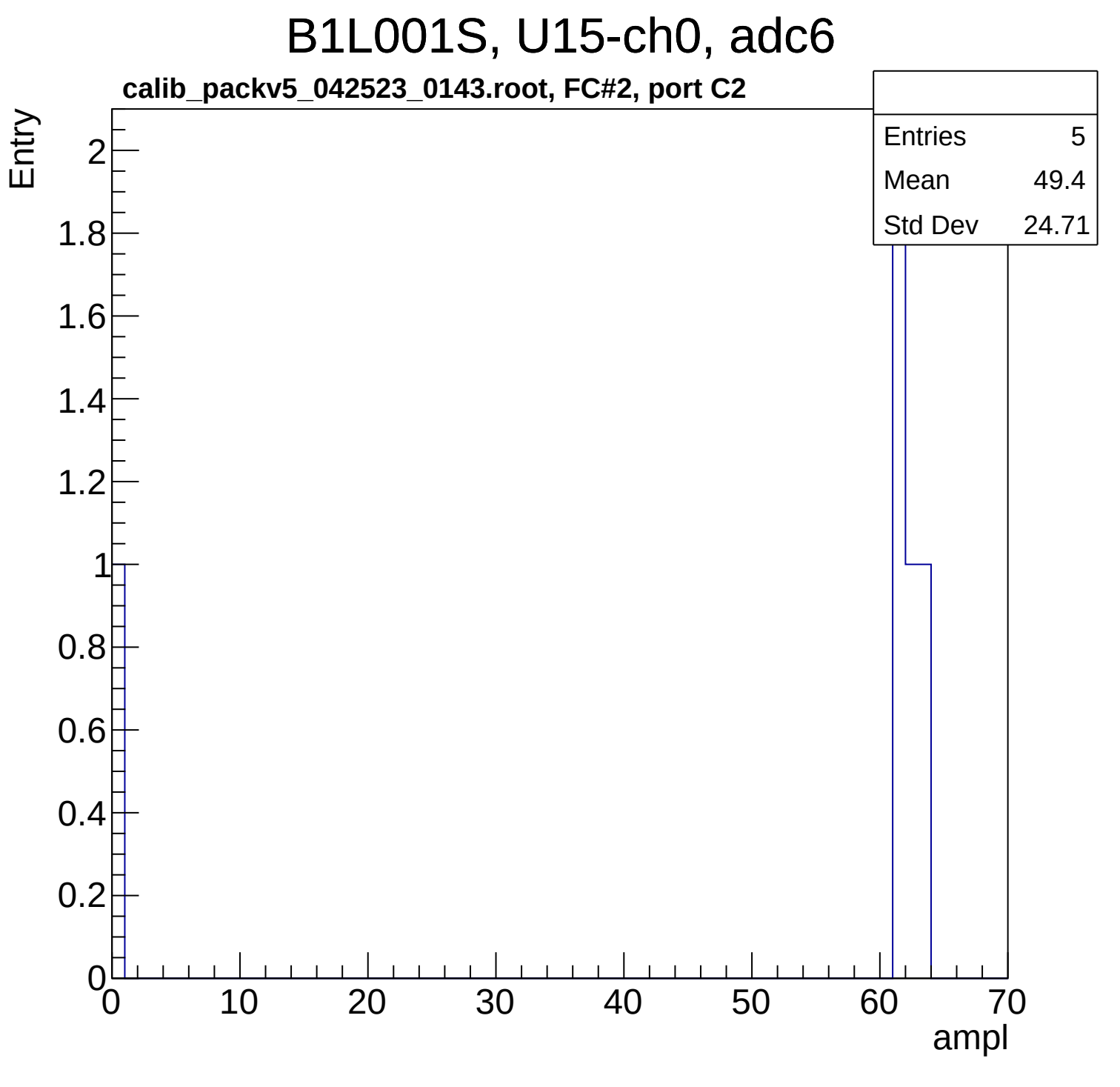
B1L001S, U15-ch0, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 49.4
Std Dev 24.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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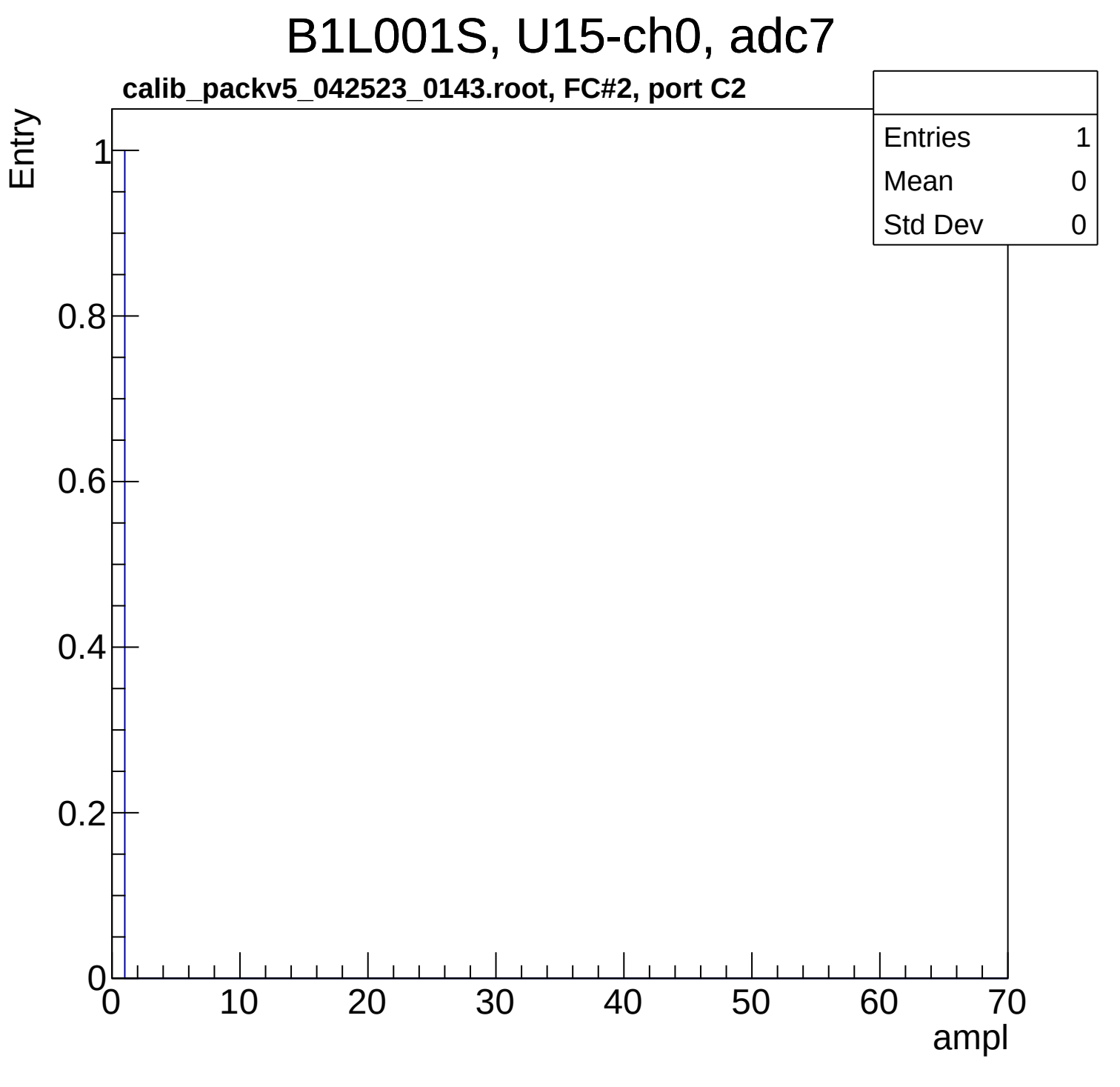
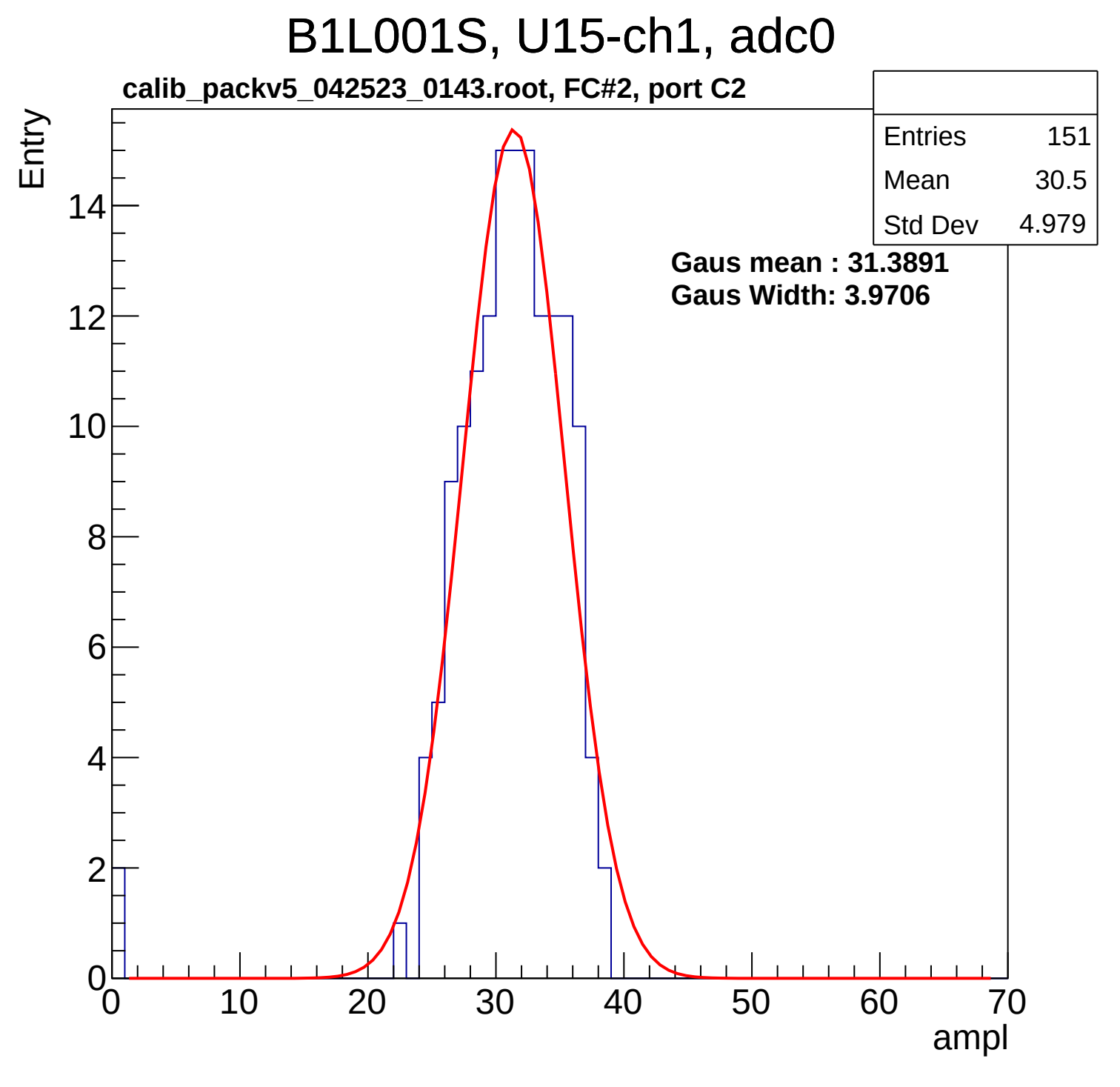

B1L001S, U15-ch0, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch1, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 30.5
Std Dev 4.979
Gaus mean : 31.3891
Gaus Width: 3.9706
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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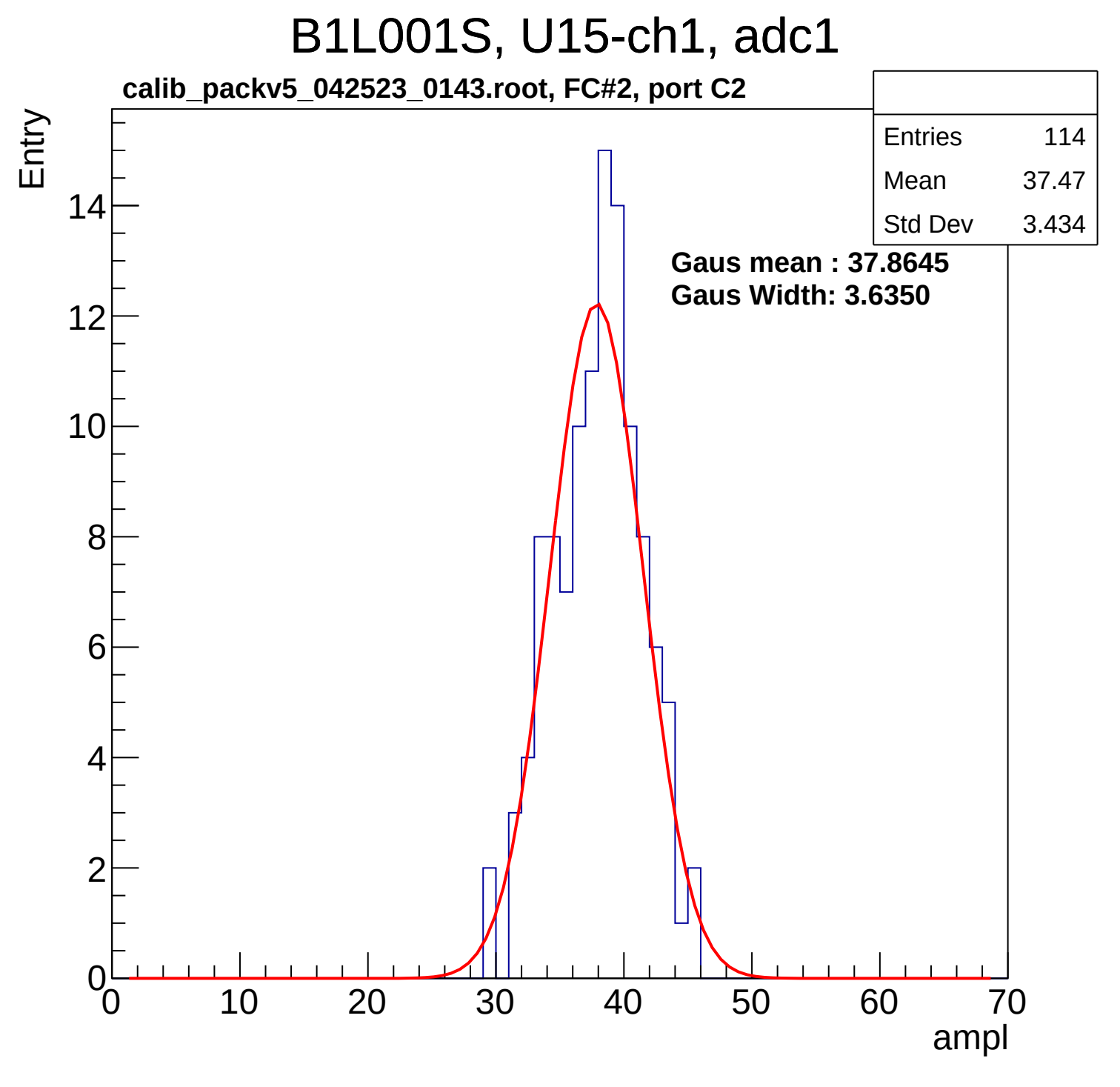
B1L001S, U15-ch1, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 37.47
Std Dev 3.434
Gaus mean : 37.8645
Gaus Width: 3.6350
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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60
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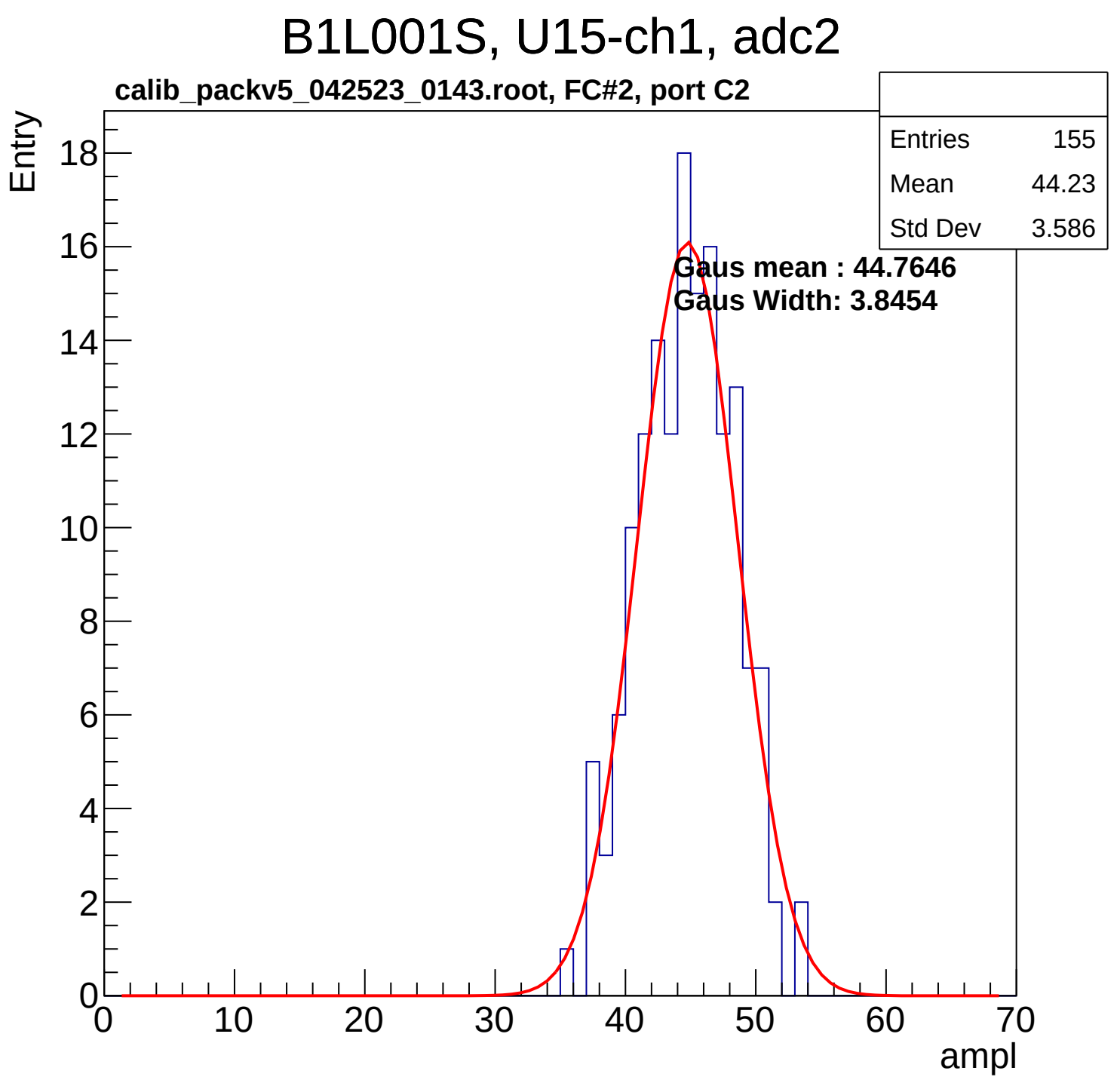
B1L001S, U15-ch1, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 44.23
Std Dev 3.586
Gaus mean : 44.7646
Gaus Width: 3.8454
Entry
ampl
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4
6
8
10
12
14
16
18
0
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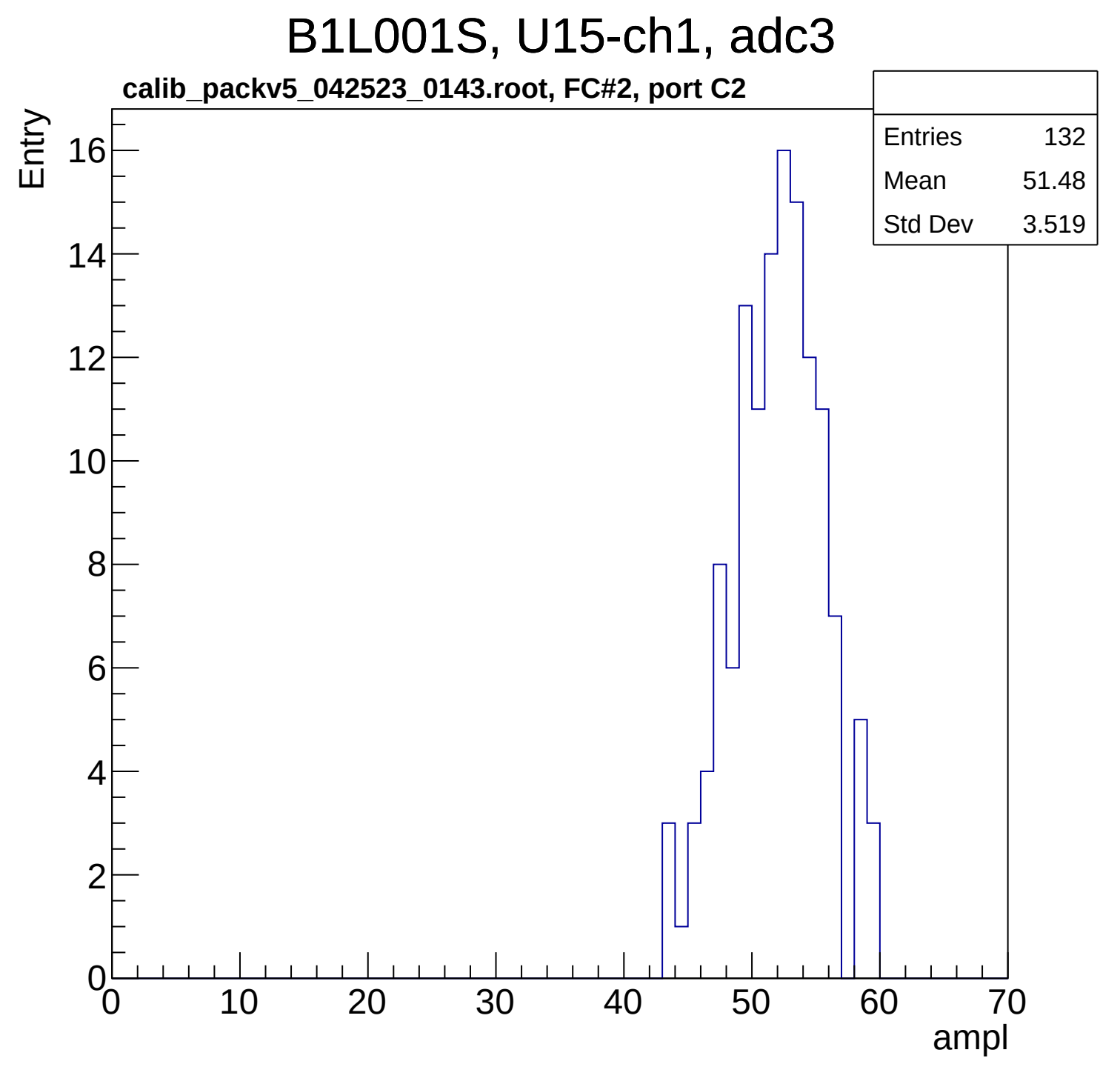
B1L001S, U15-ch1, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 51.48
Std Dev 3.519
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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60
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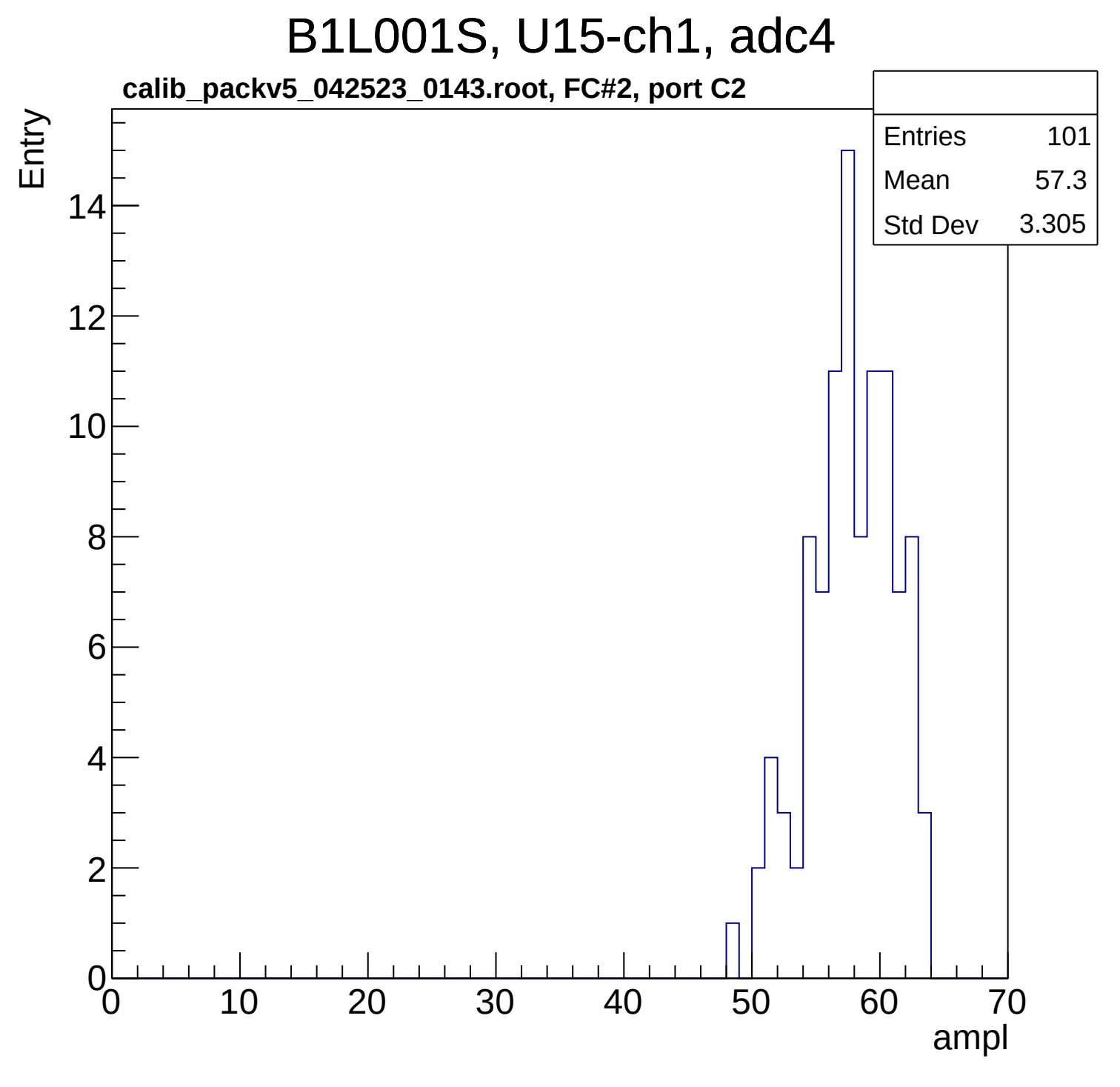
B1L001S, U15-ch1, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 57.3
Std Dev 3.305
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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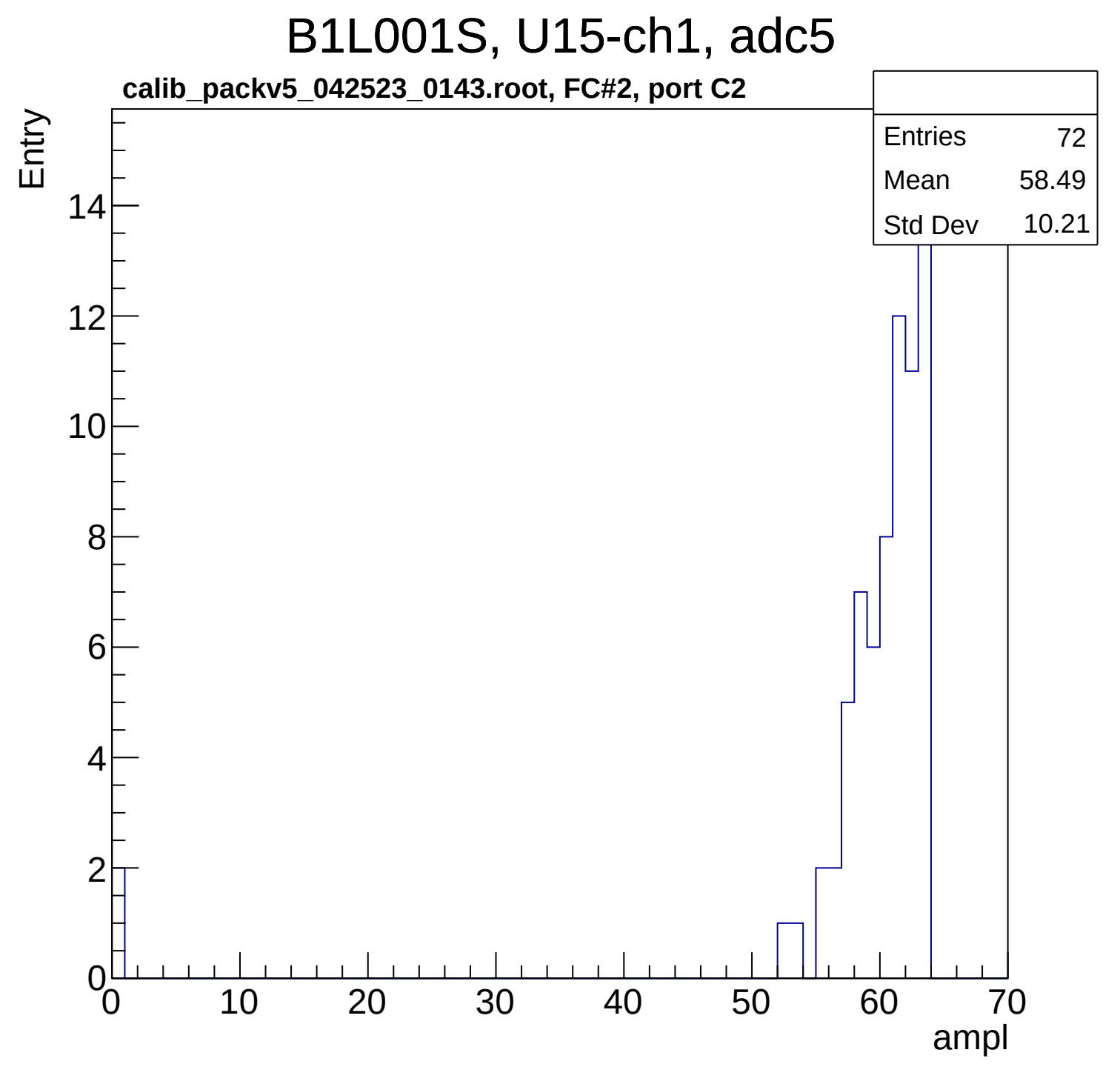
B1L001S, U15-ch1, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 58.49
Std Dev 10.21
Entry
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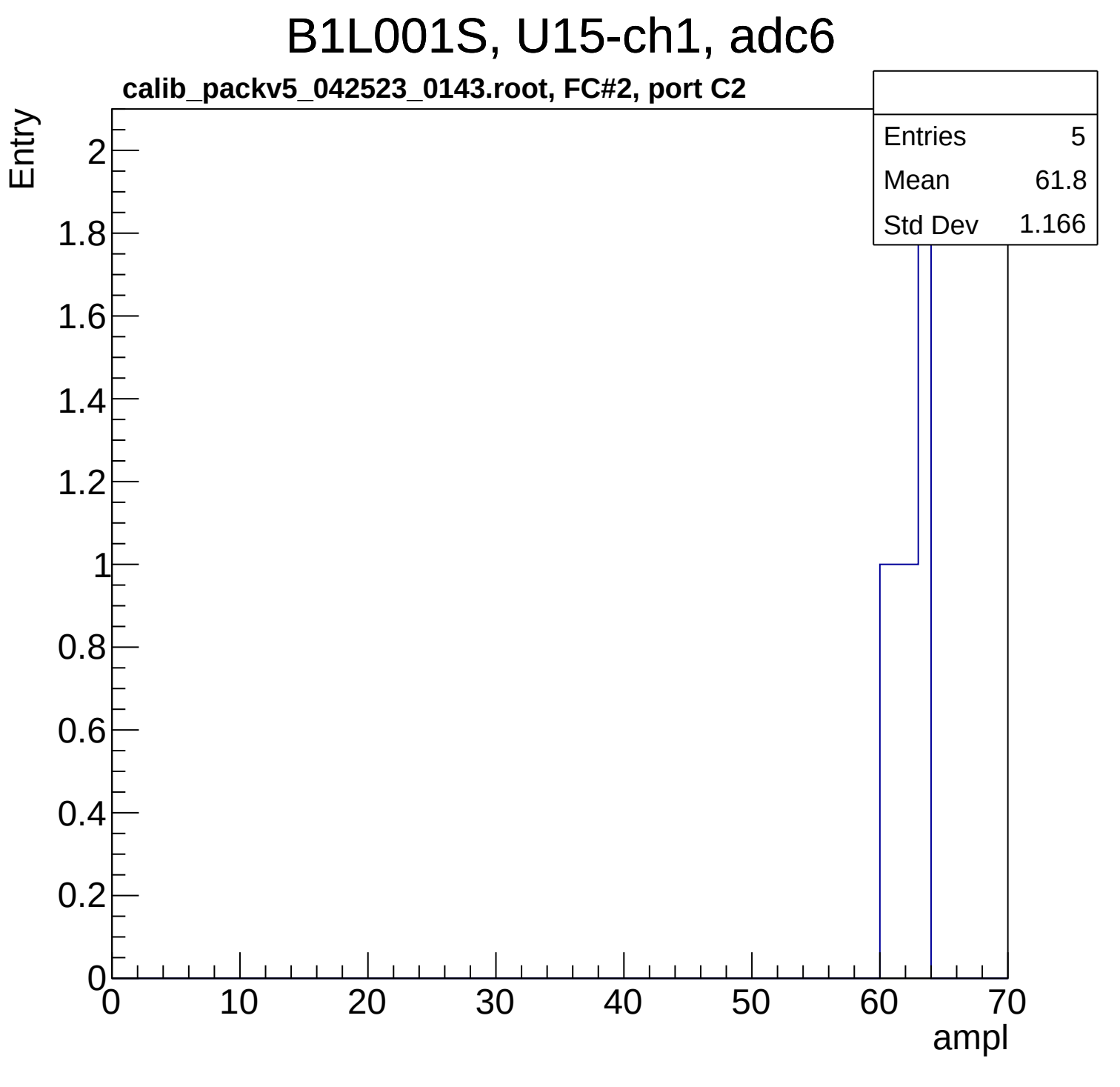
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch1, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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30
40
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60
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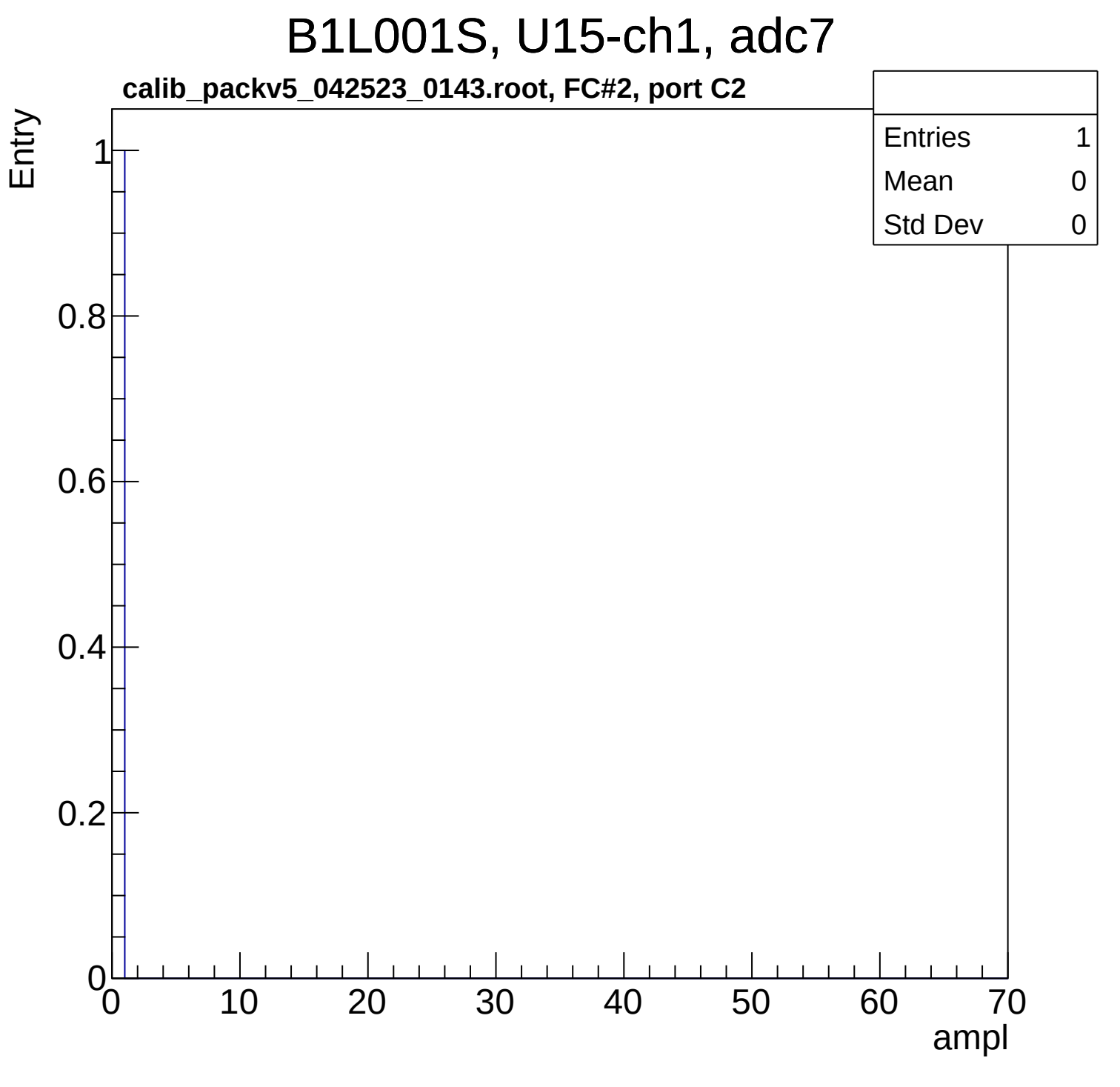
B1L001S, U15-ch1, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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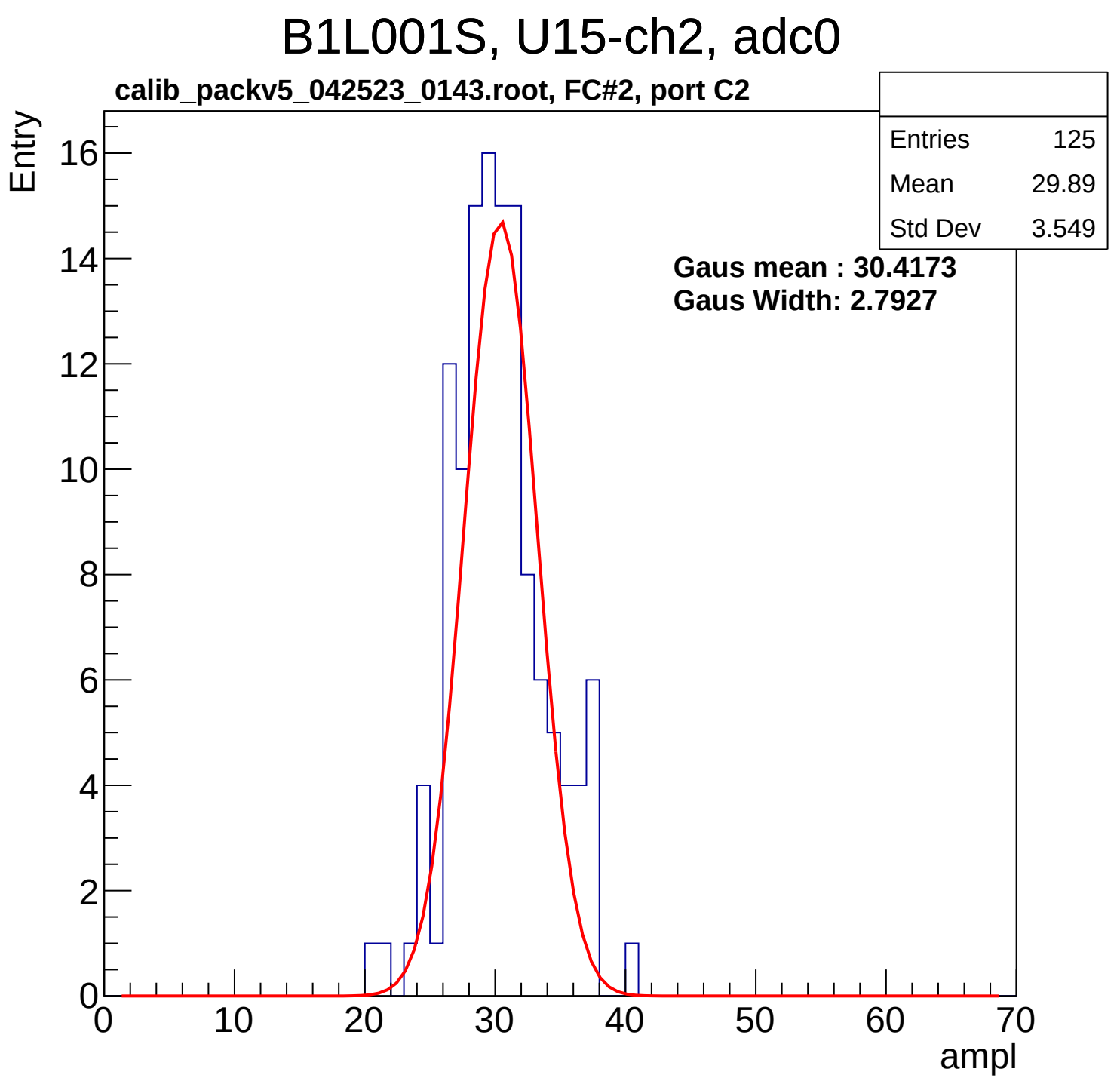
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch2, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 29.89
Std Dev 3.549
Gaus mean : 30.4173
Gaus Width: 2.7927
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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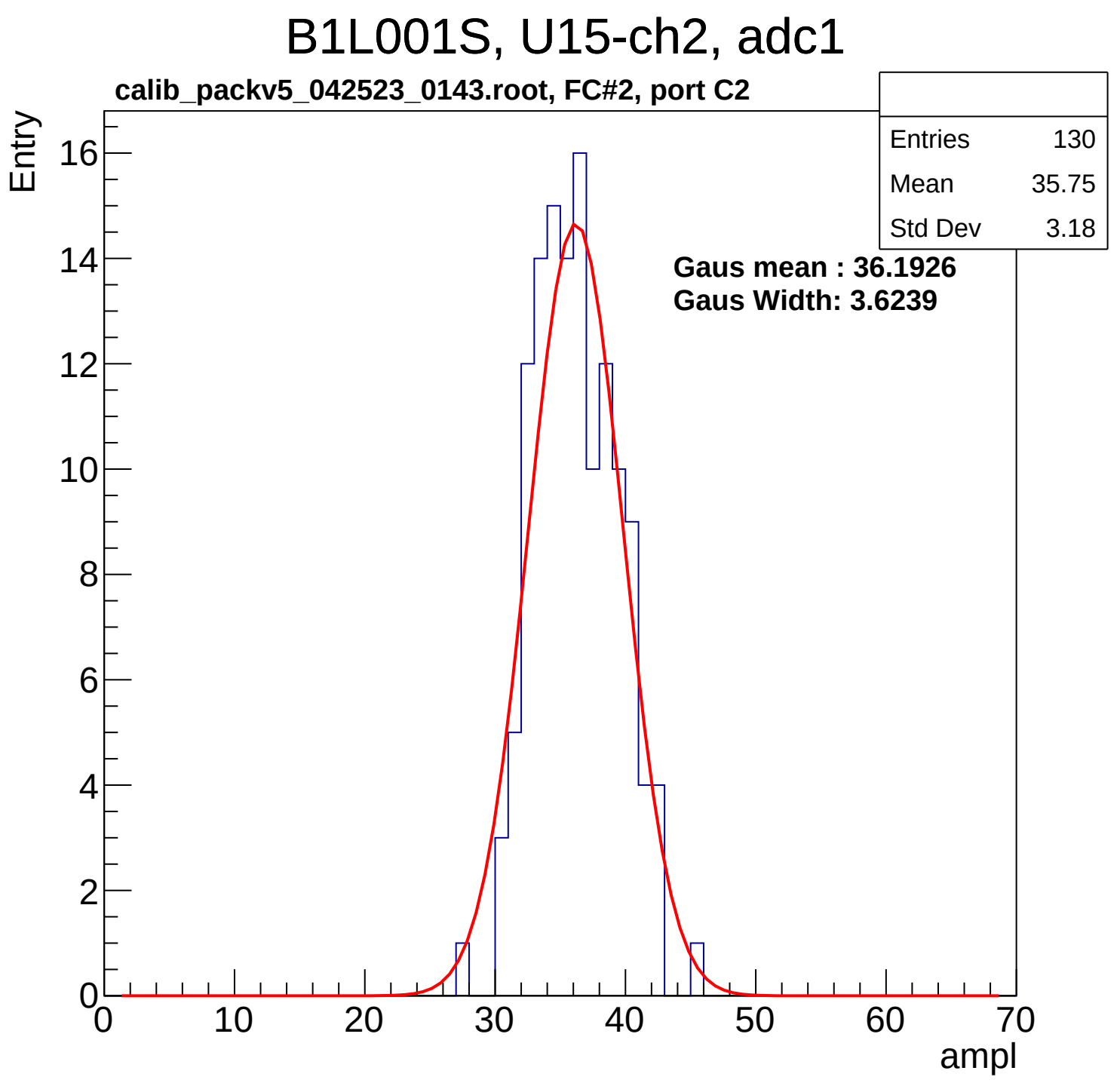
B1L001S, U15-ch2, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 35.75
Std Dev 3.18
Gaus mean : 36.1926
Gaus Width: 3.6239
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
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60
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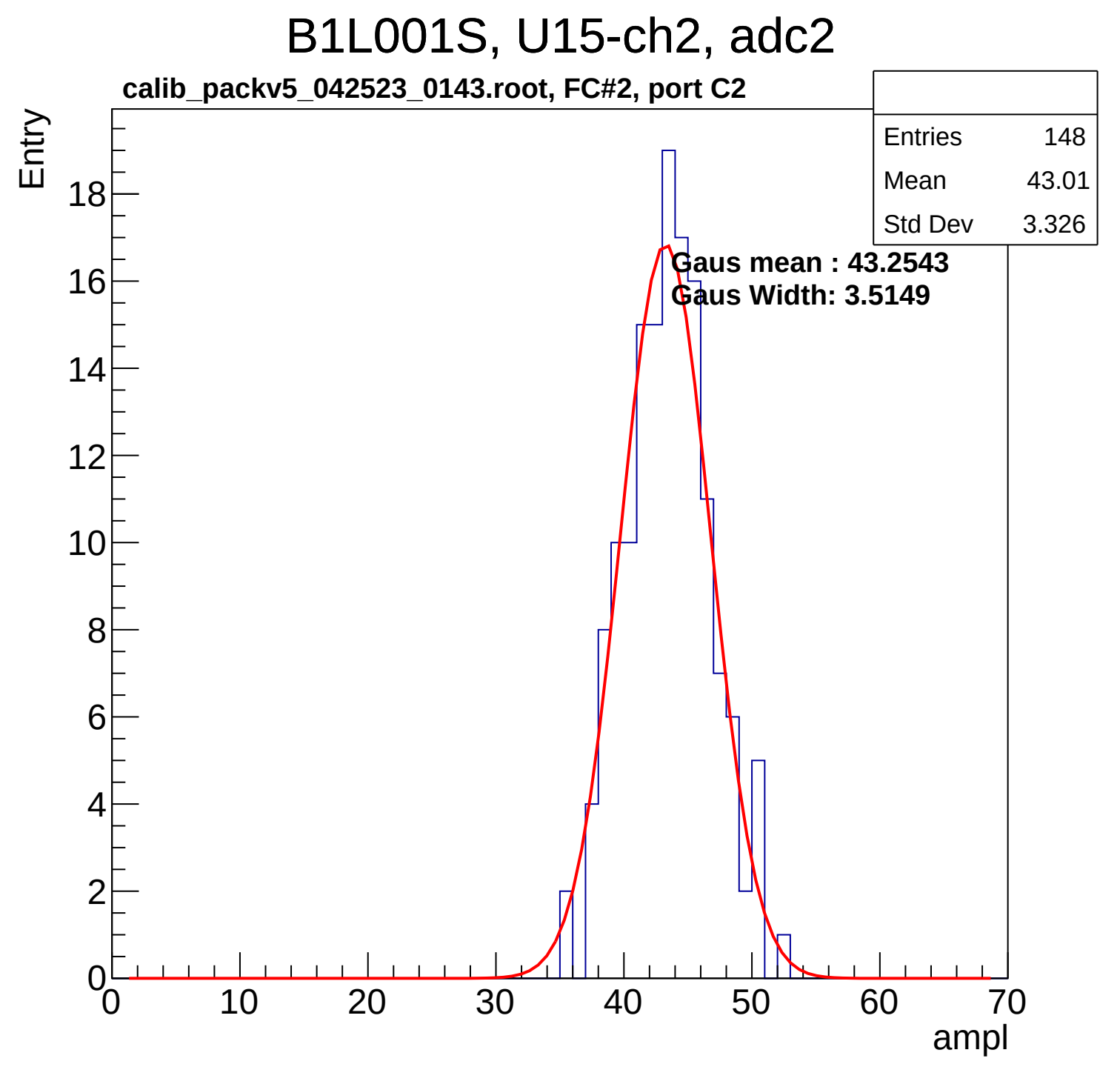
B1L001S, U15-ch2, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 43.01
Std Dev 3.326
Gaus mean : 43.2543
Gaus Width: 3.5149
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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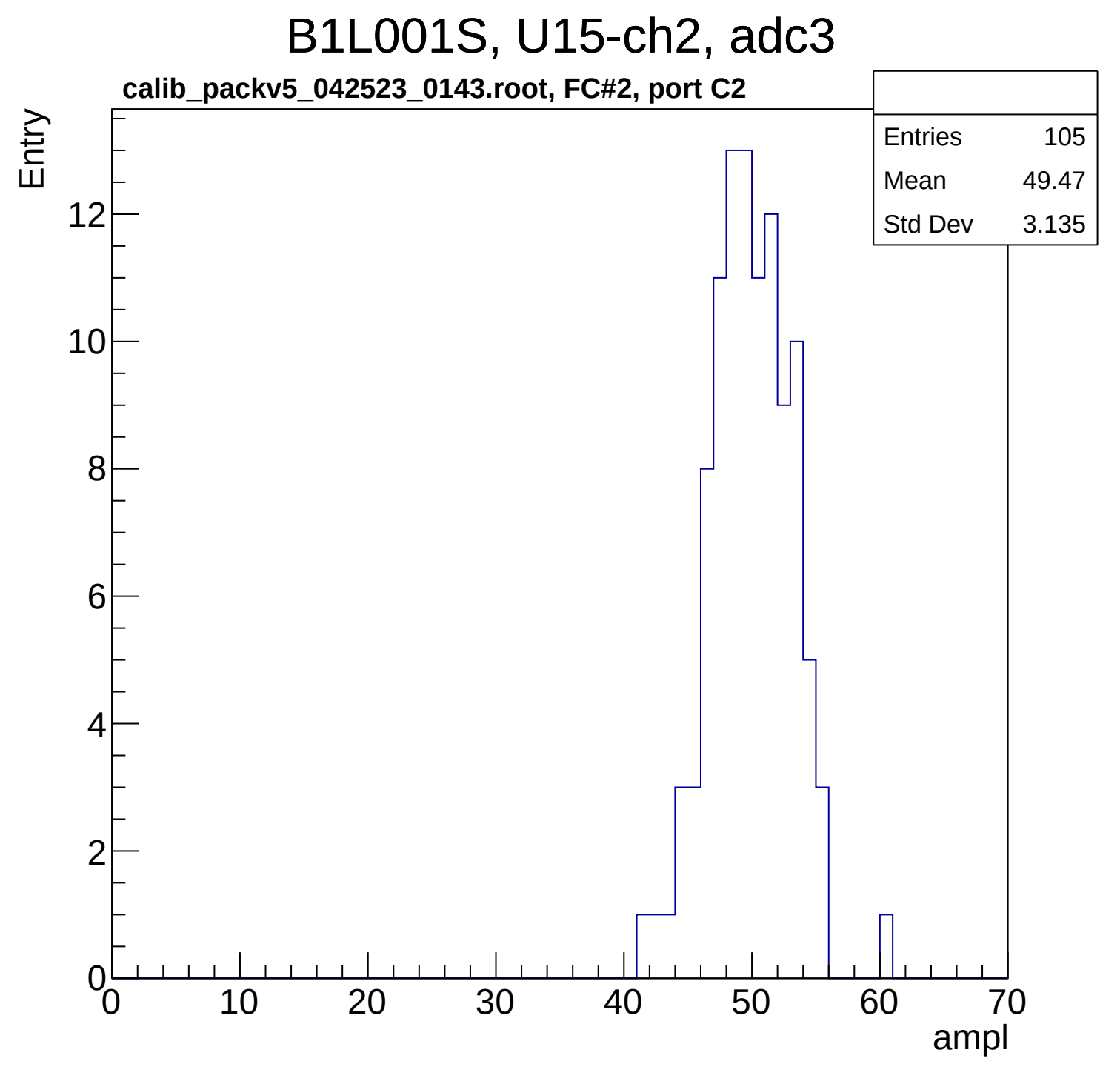
B1L001S, U15-ch2, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 49.47
Std Dev 3.135
Entry
ampl
0
2
4
6
8
10
12
0
10
20
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60
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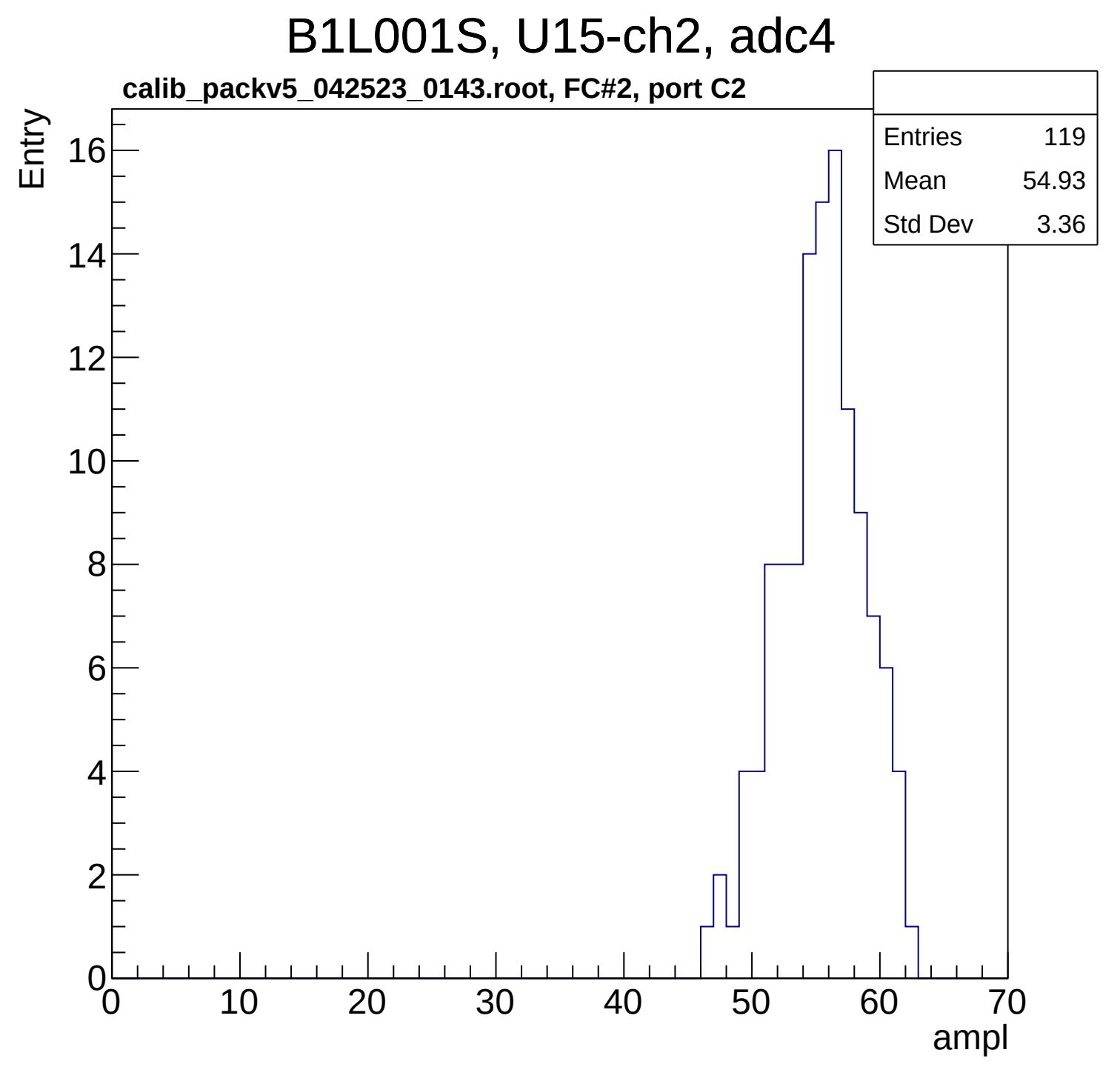
B1L001S, U15-ch2, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 54.93
Std Dev 3.36
Entry
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4
6
8
10
12
14
16
0
10
20
30
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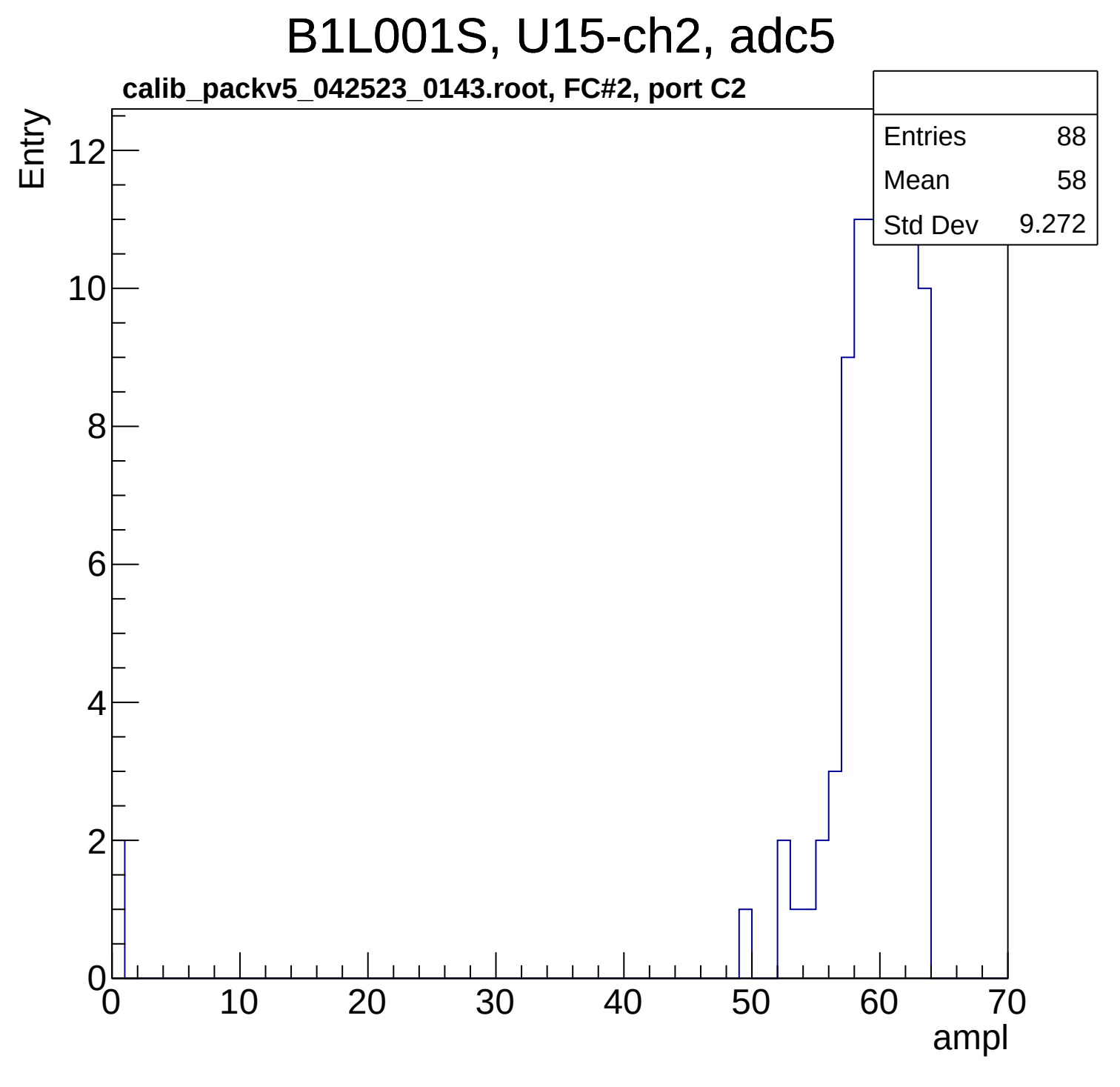
50
60
70
ampl


B1L001S, U15-ch2, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 58
Std Dev 9.272
Entry
ampl
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4
6
8
10
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0
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20
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60
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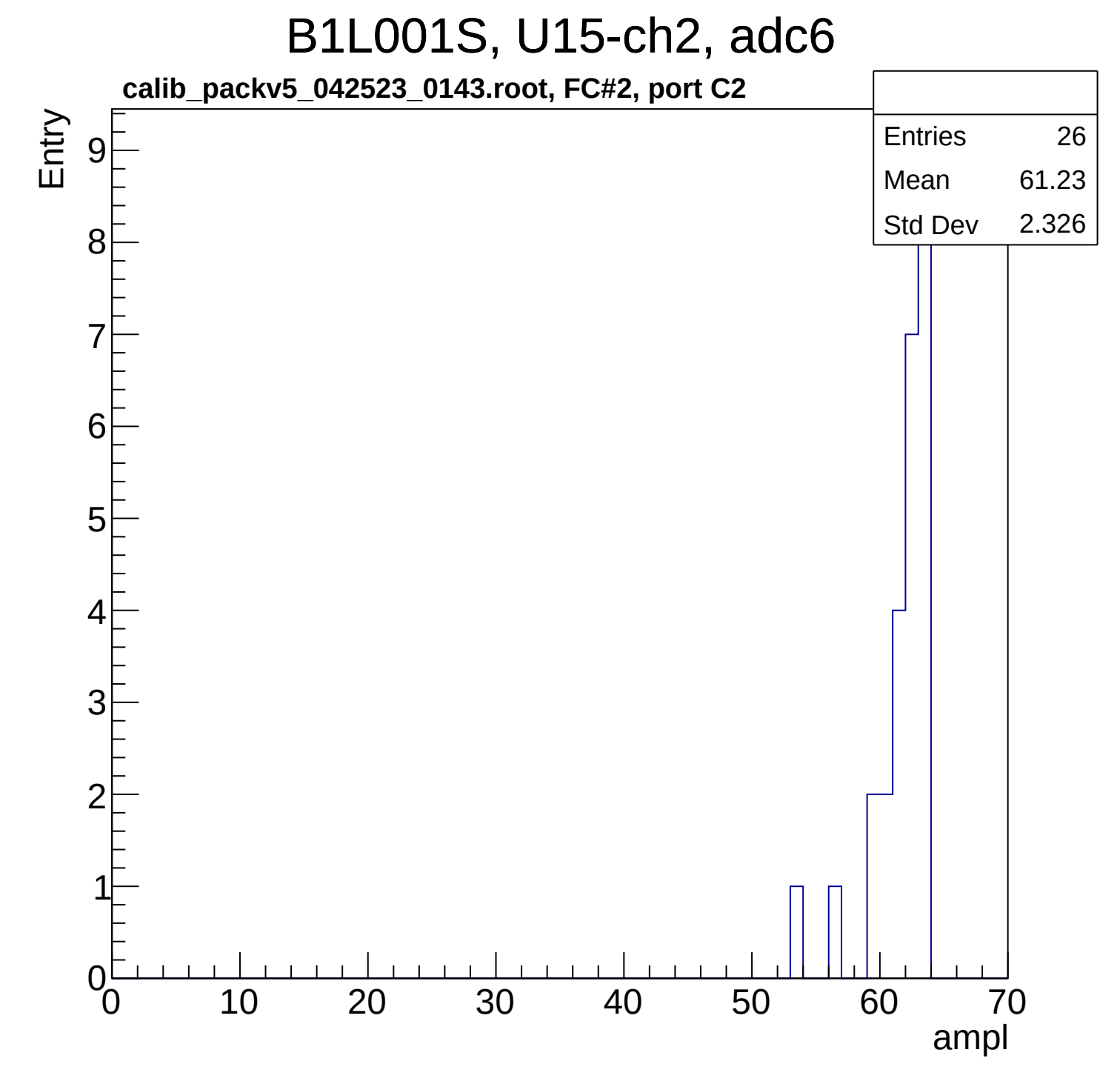
B1L001S, U15-ch2, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 61.23
Std Dev 2.326
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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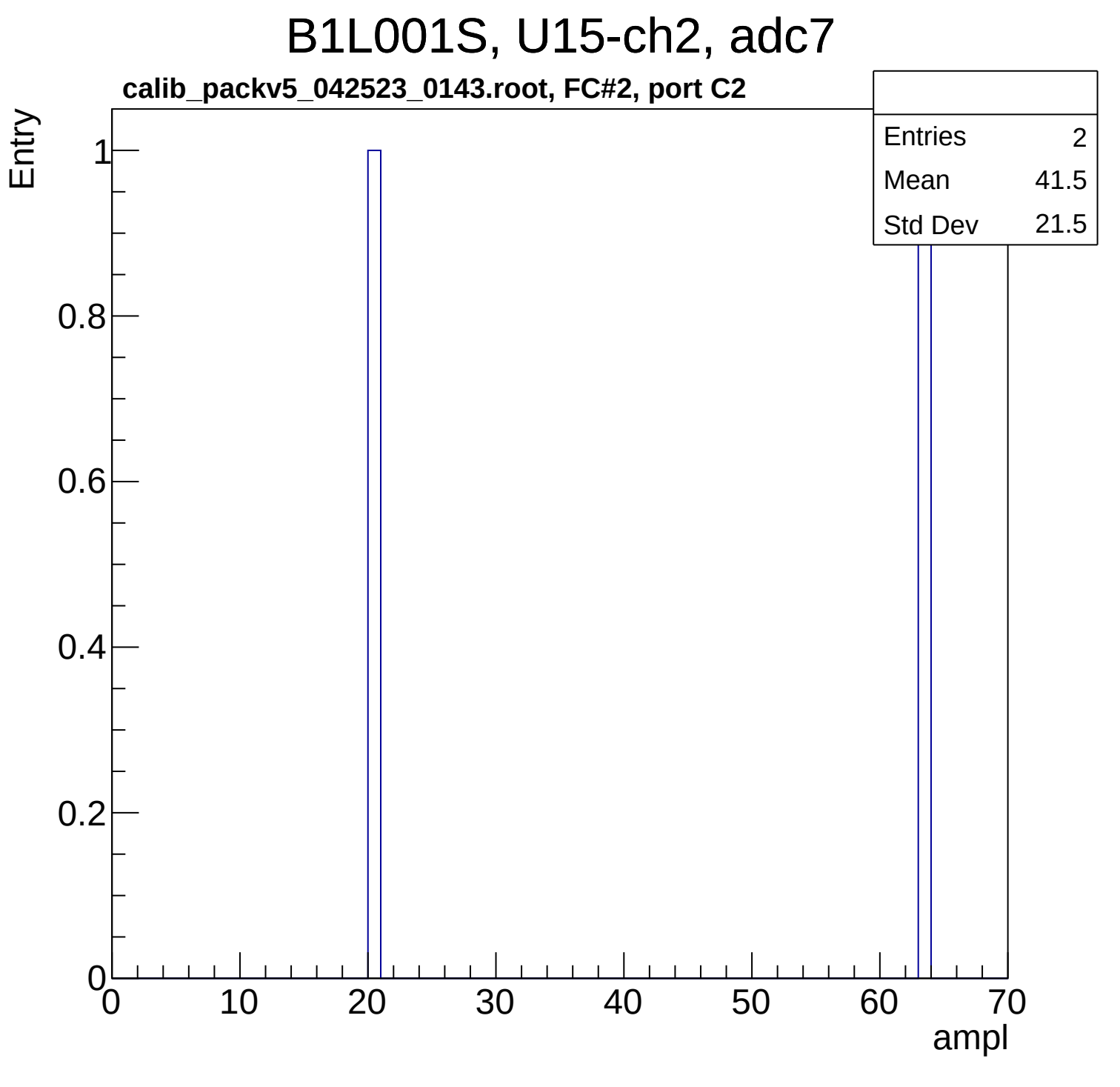
40
50
60
70


B1L001S, U15-ch2, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
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0.4
0.6
0.8
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0
10
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60
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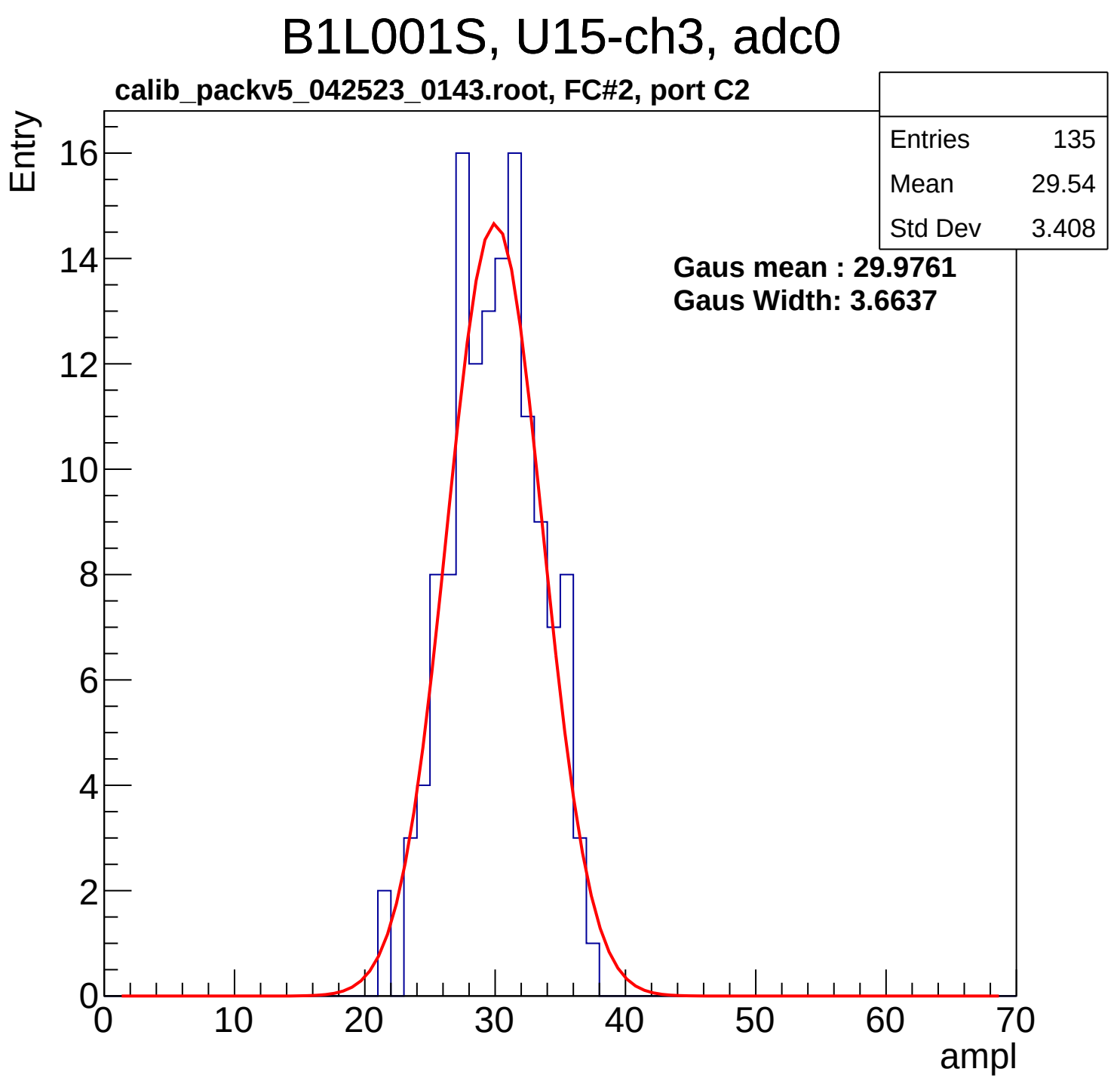
B1L001S, U15-ch3, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 29.54
Std Dev 3.408
Gaus mean : 29.9761
Gaus Width: 3.6637
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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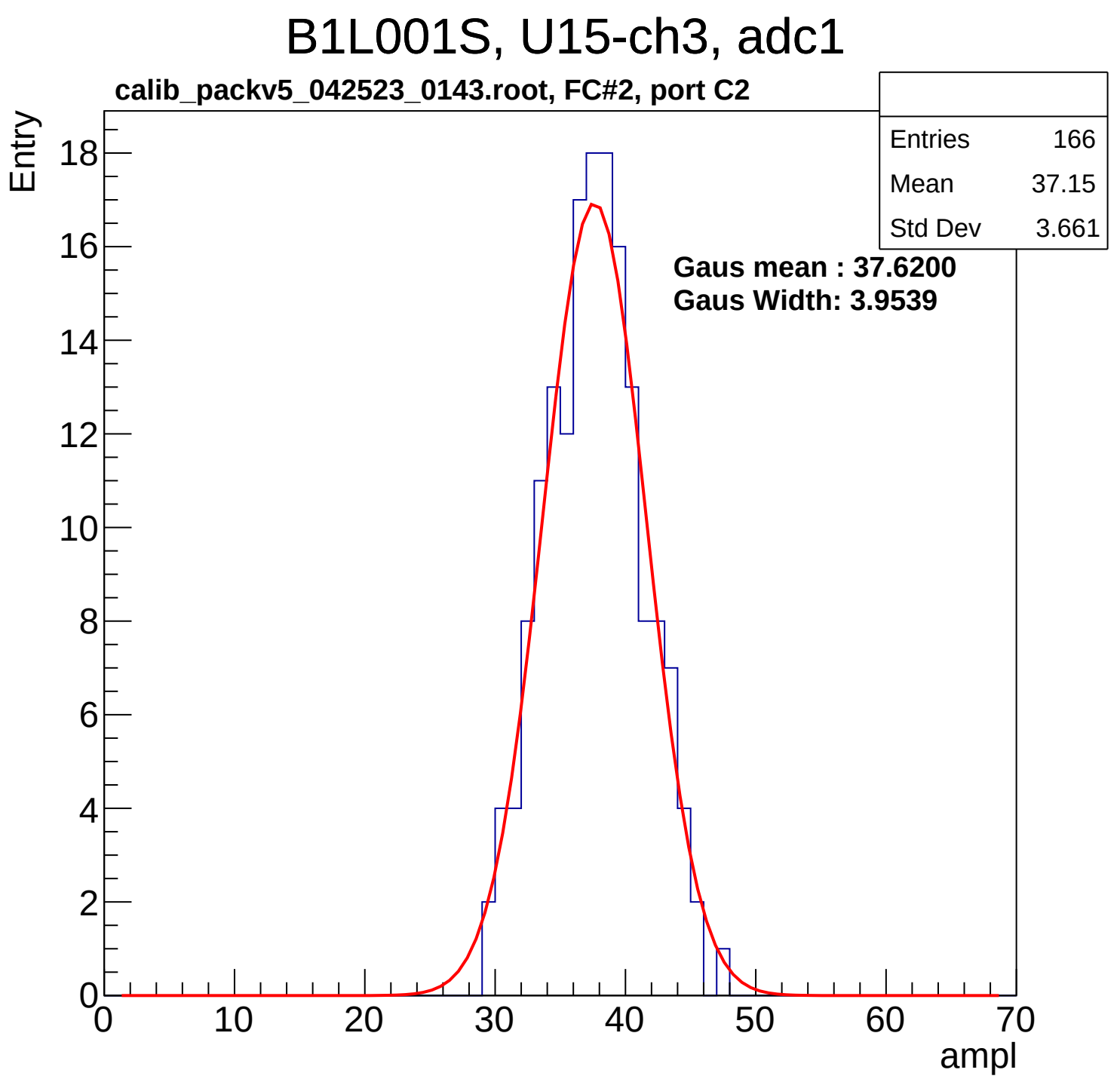
B1L001S, U15-ch3, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 166
Mean 37.15
Std Dev 3.661
Gaus mean : 37.6200
Gaus Width: 3.9539
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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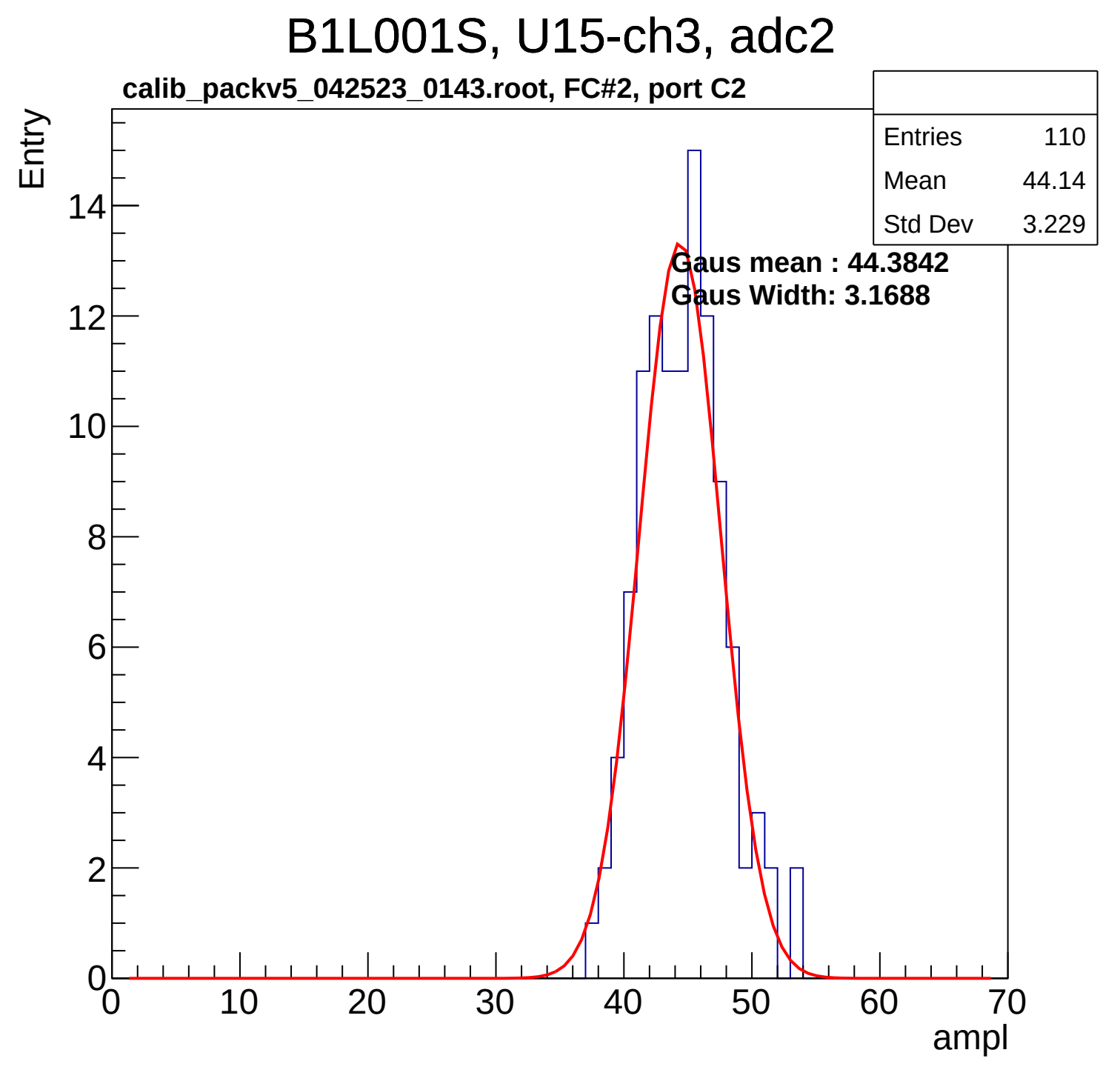
B1L001S, U15-ch3, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 44.14
Std Dev 3.229
Gaus mean : 44.3842
Gaus Width: 3.1688
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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60
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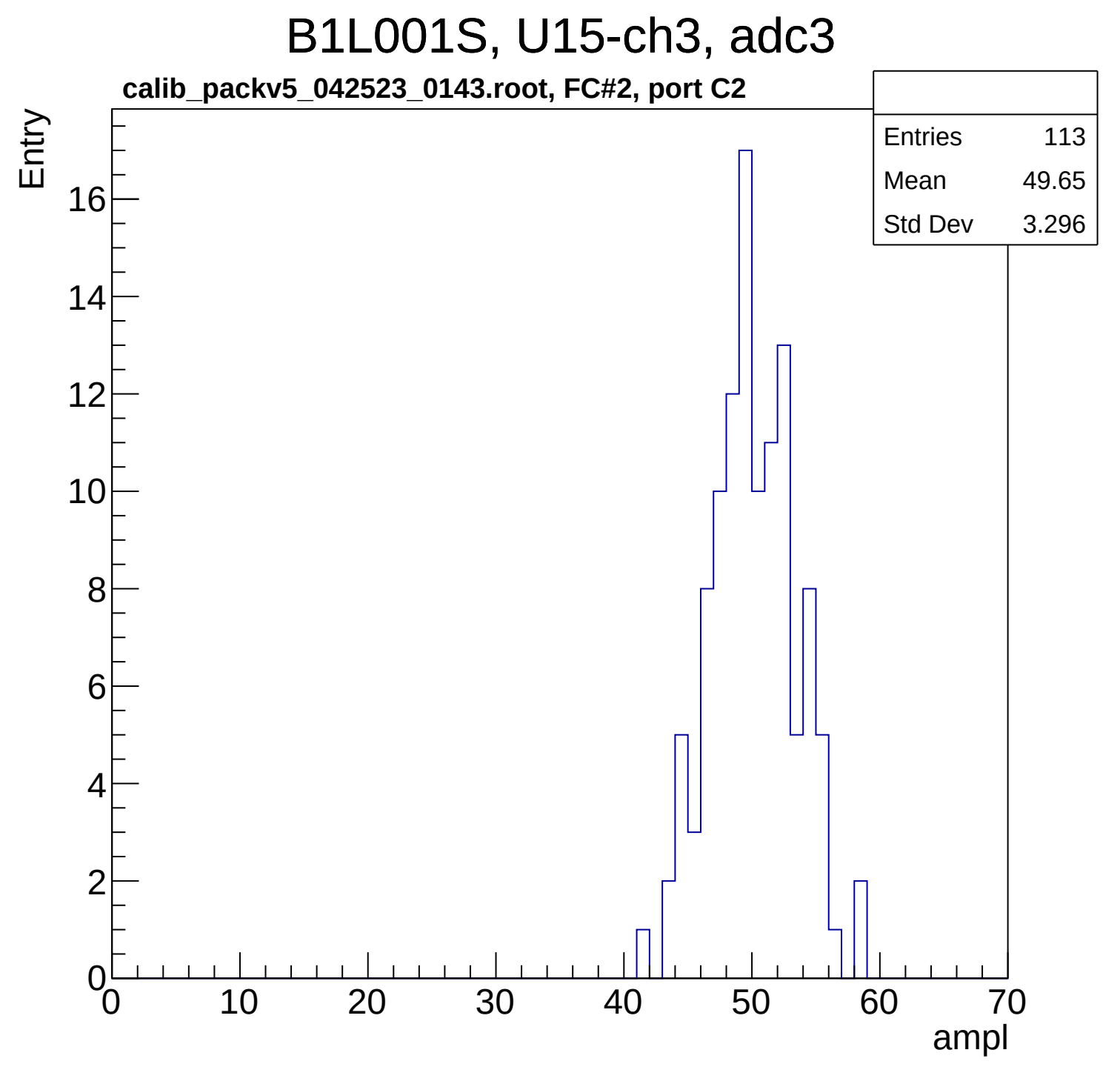
B1L001S, U15-ch3, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.65
Std Dev 3.296
Entry
ampl
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4
6
8
10
12
14
16
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20
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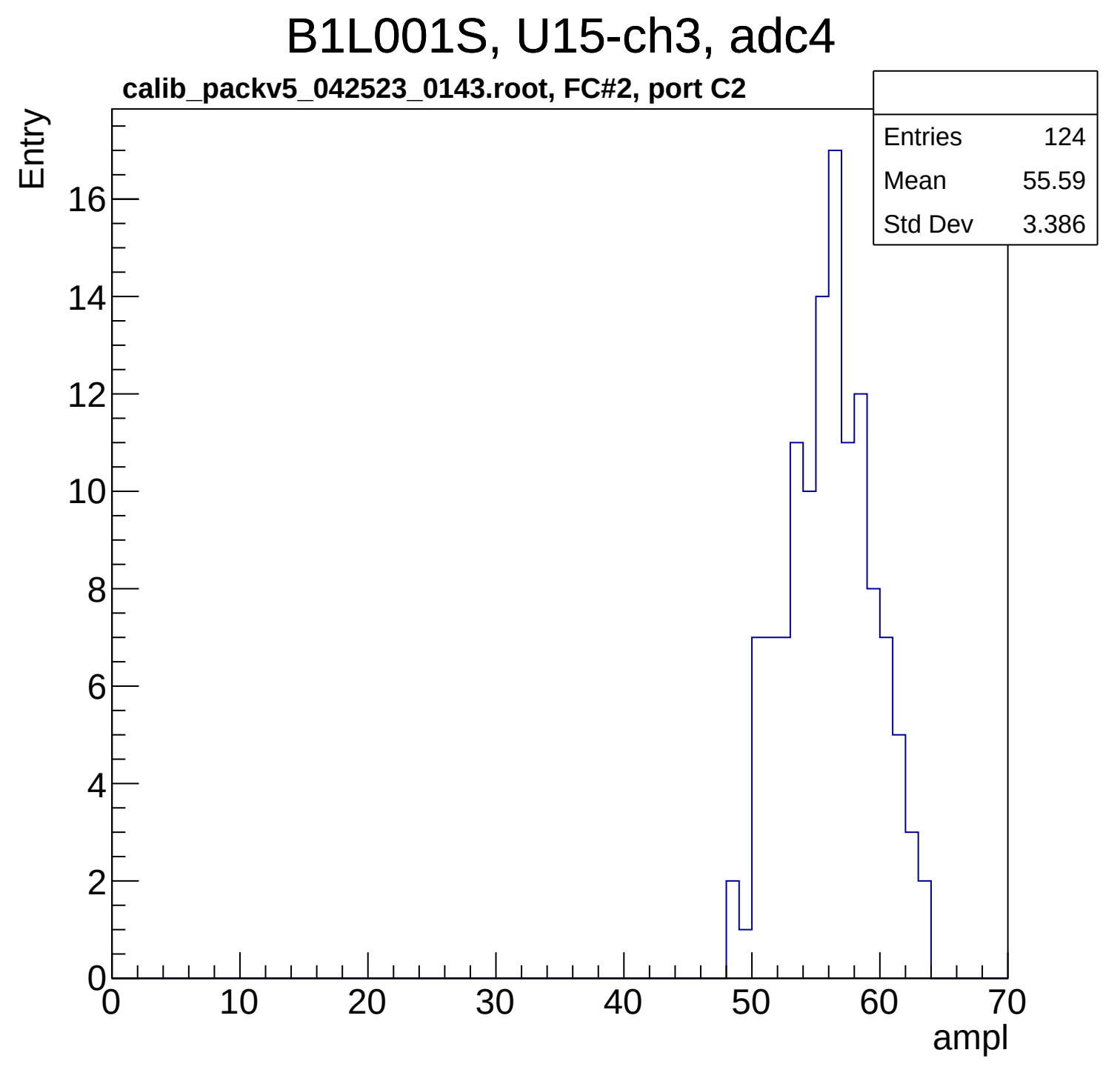
B1L001S, U15-ch3, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.59
Std Dev 3.386
Entry
ampl
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8
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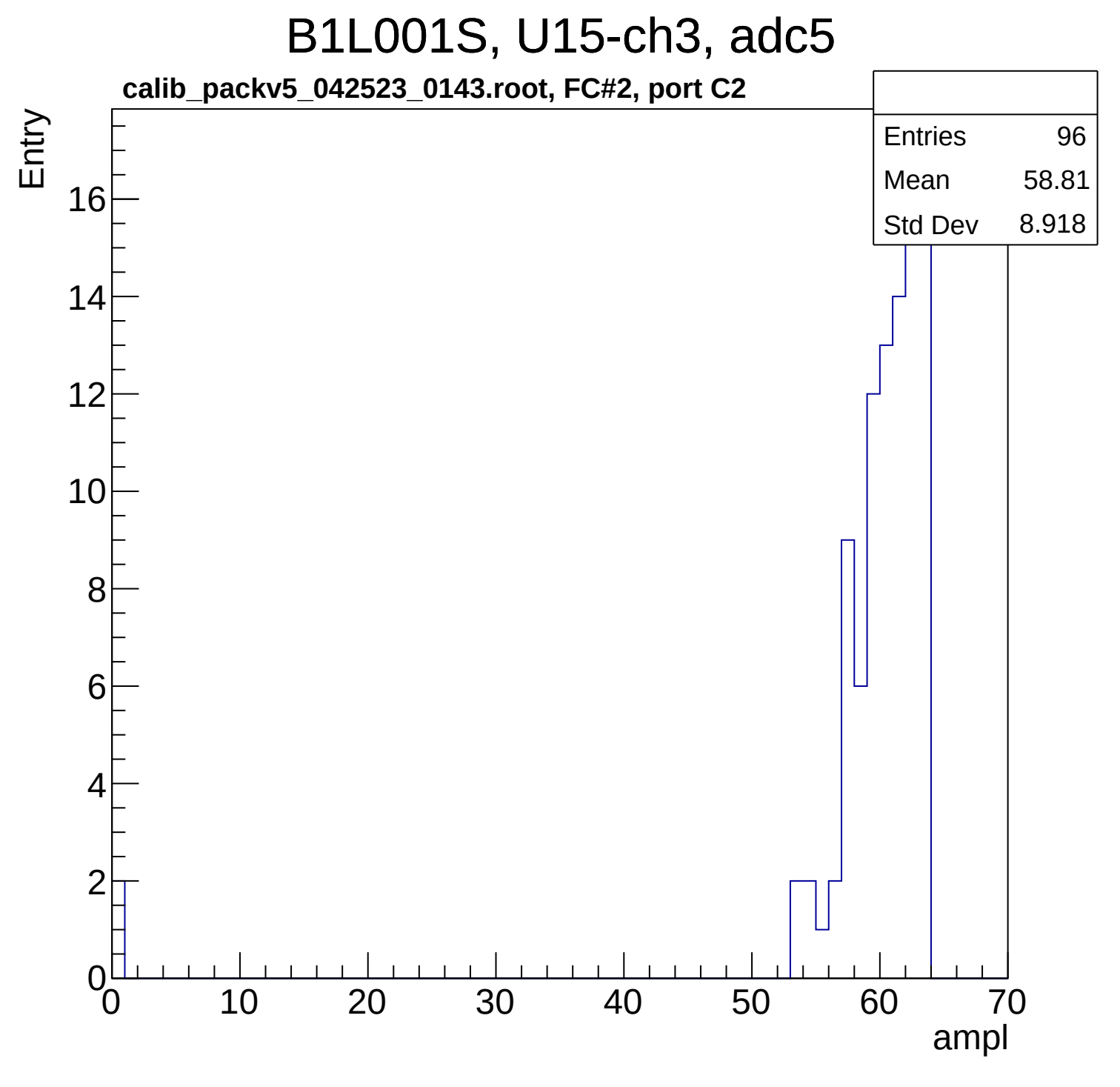
B1L001S, U15-ch3, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 58.81
Std Dev 8.918
Entry
ampl
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6
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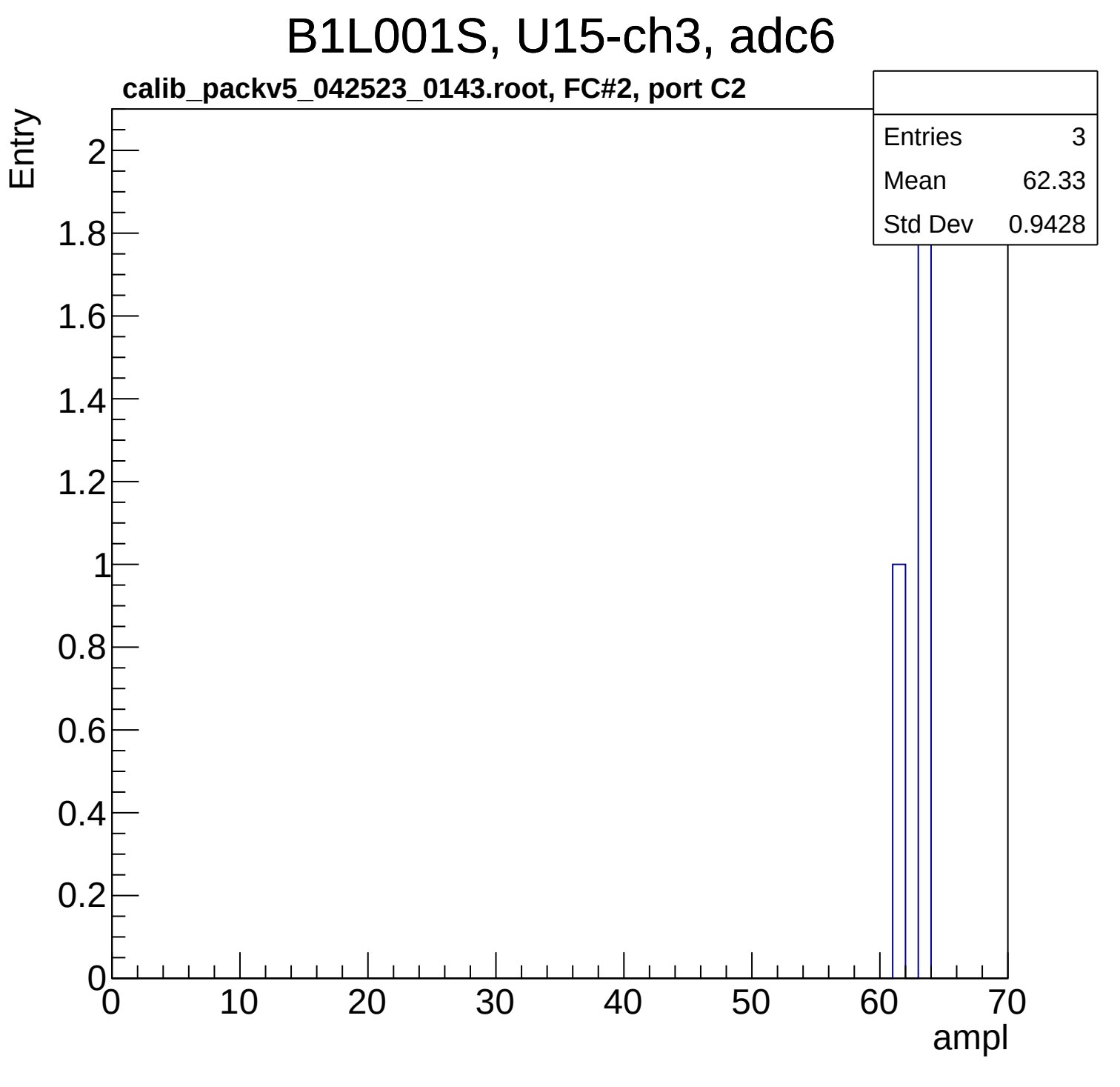
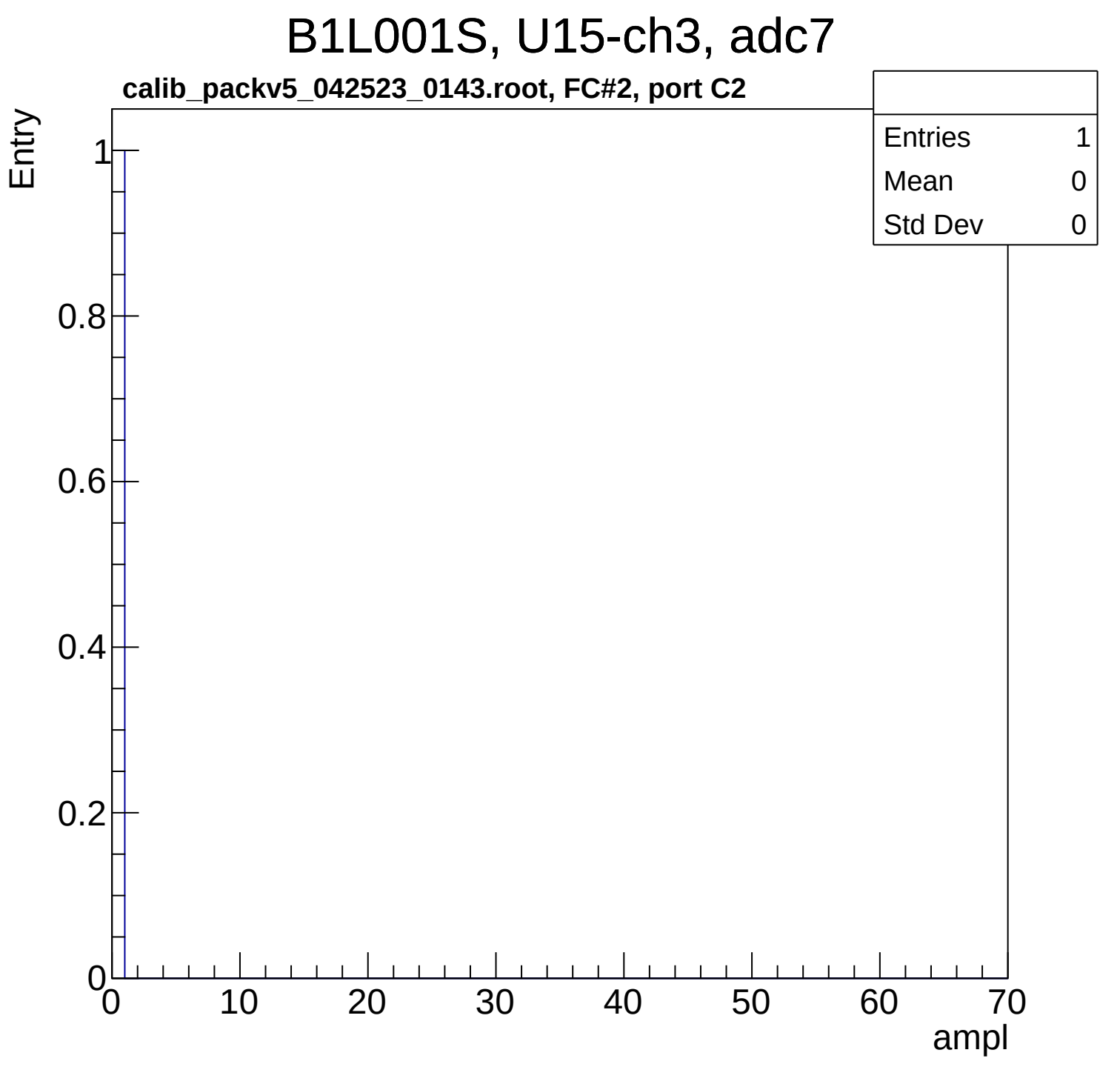

B1L001S, U15-ch3, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch3, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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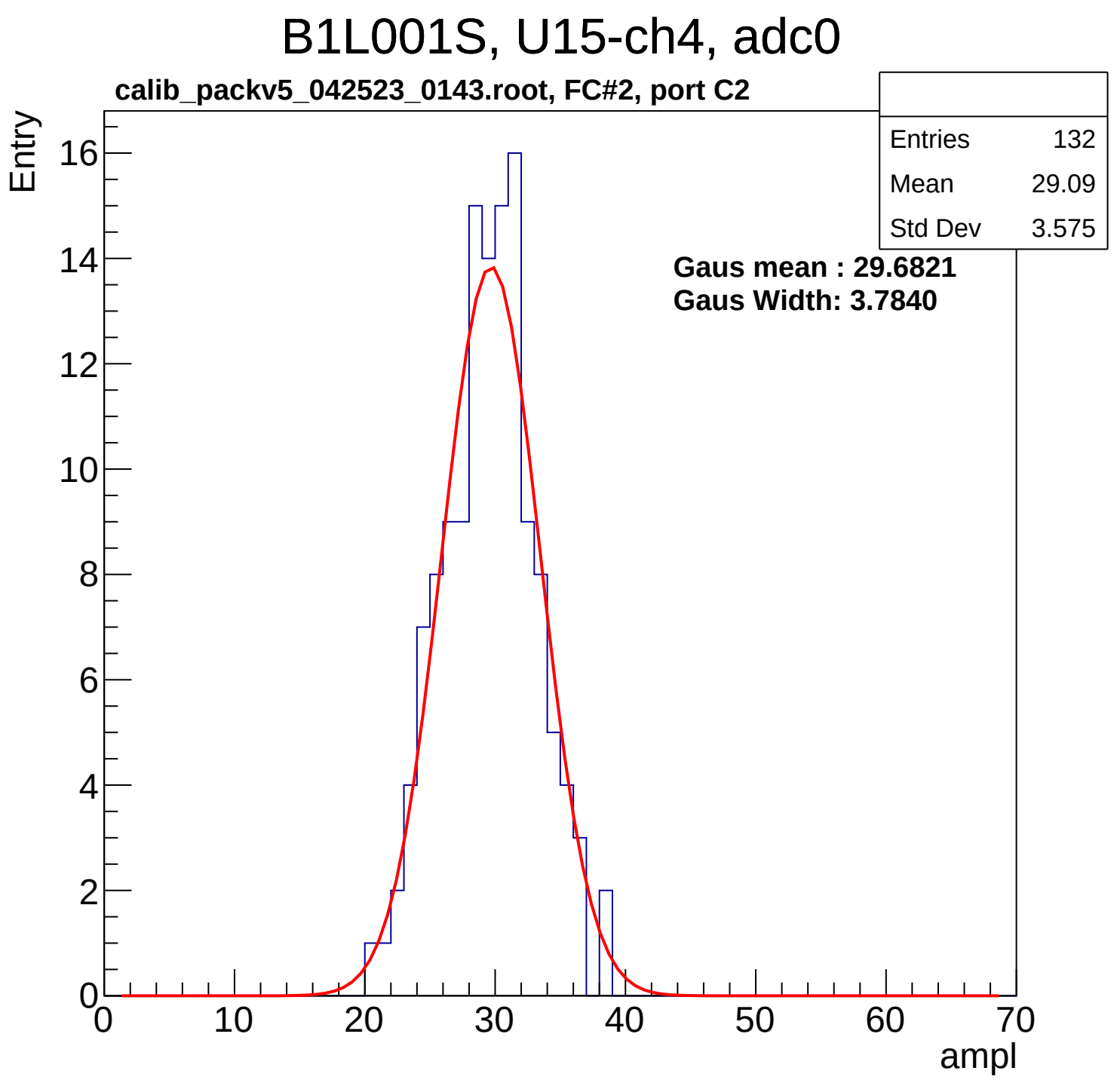
70
ampl


B1L001S, U15-ch4, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 29.09
Std Dev 3.575
Gaus mean : 29.6821
Gaus Width: 3.7840
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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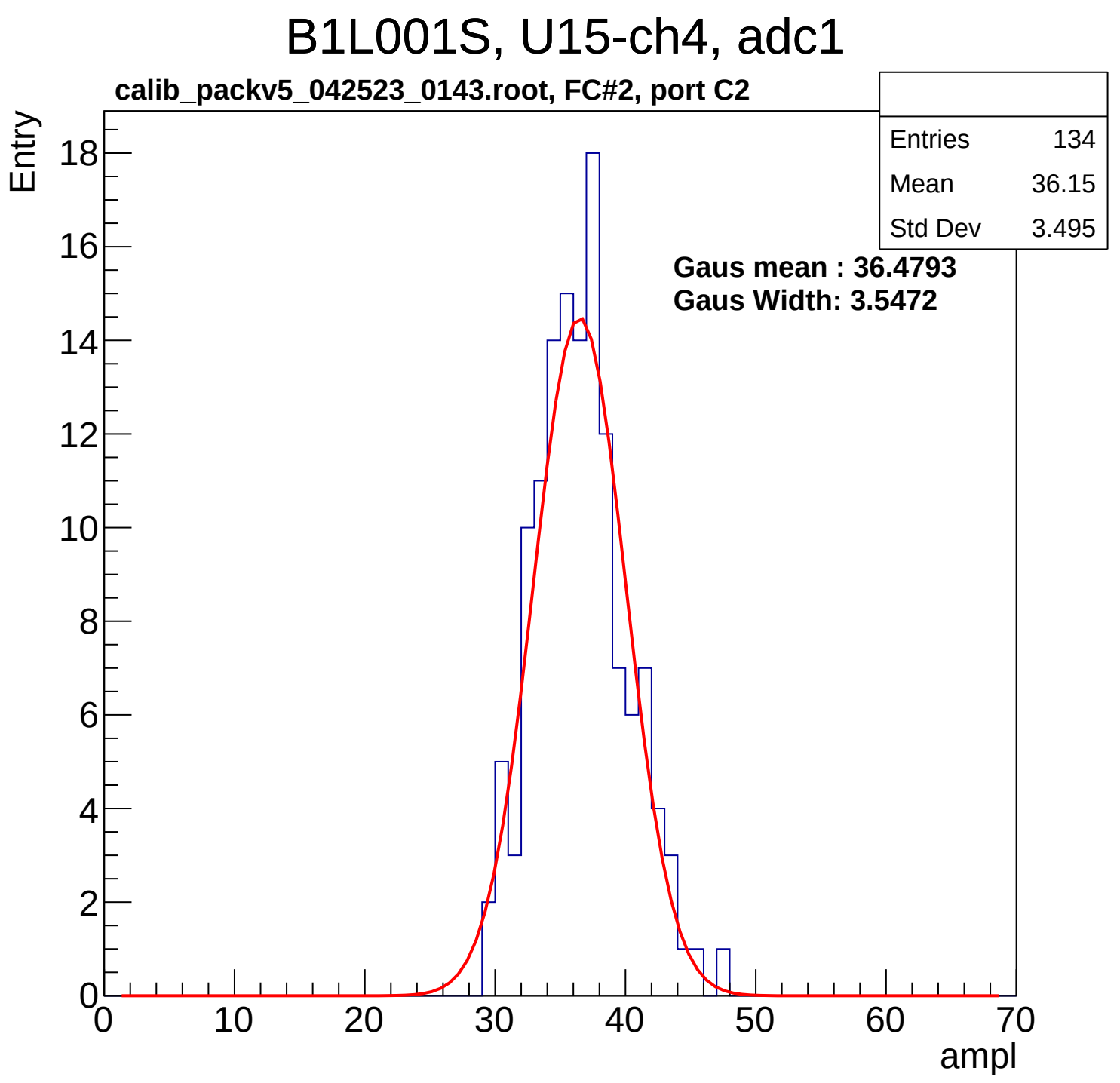
B1L001S, U15-ch4, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 36.15
Std Dev 3.495
Gaus mean : 36.4793
Gaus Width: 3.5472
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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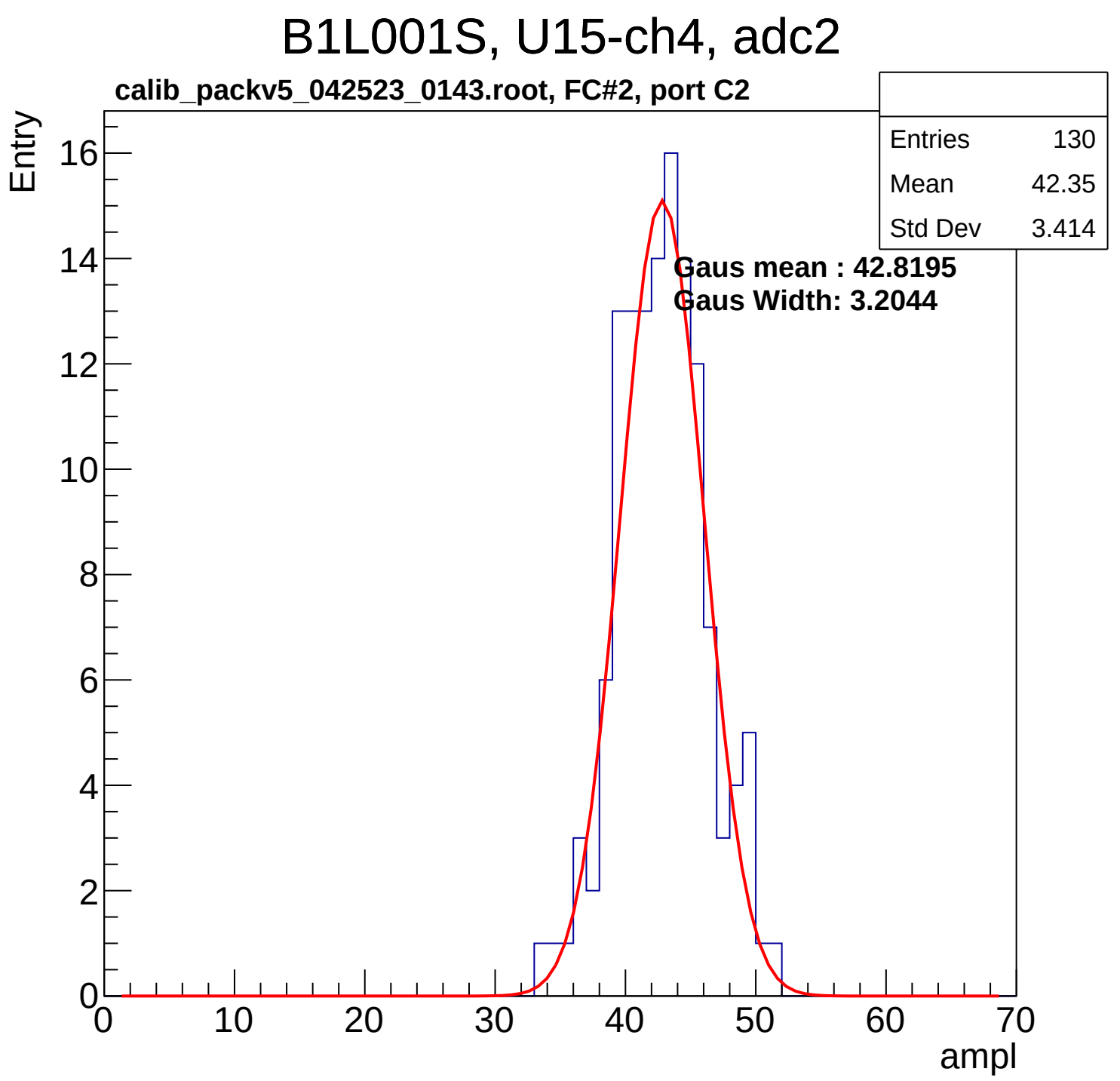
B1L001S, U15-ch4, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.35
Std Dev 3.414
Gaus mean : 42.8195
Gaus Width: 3.2044
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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60
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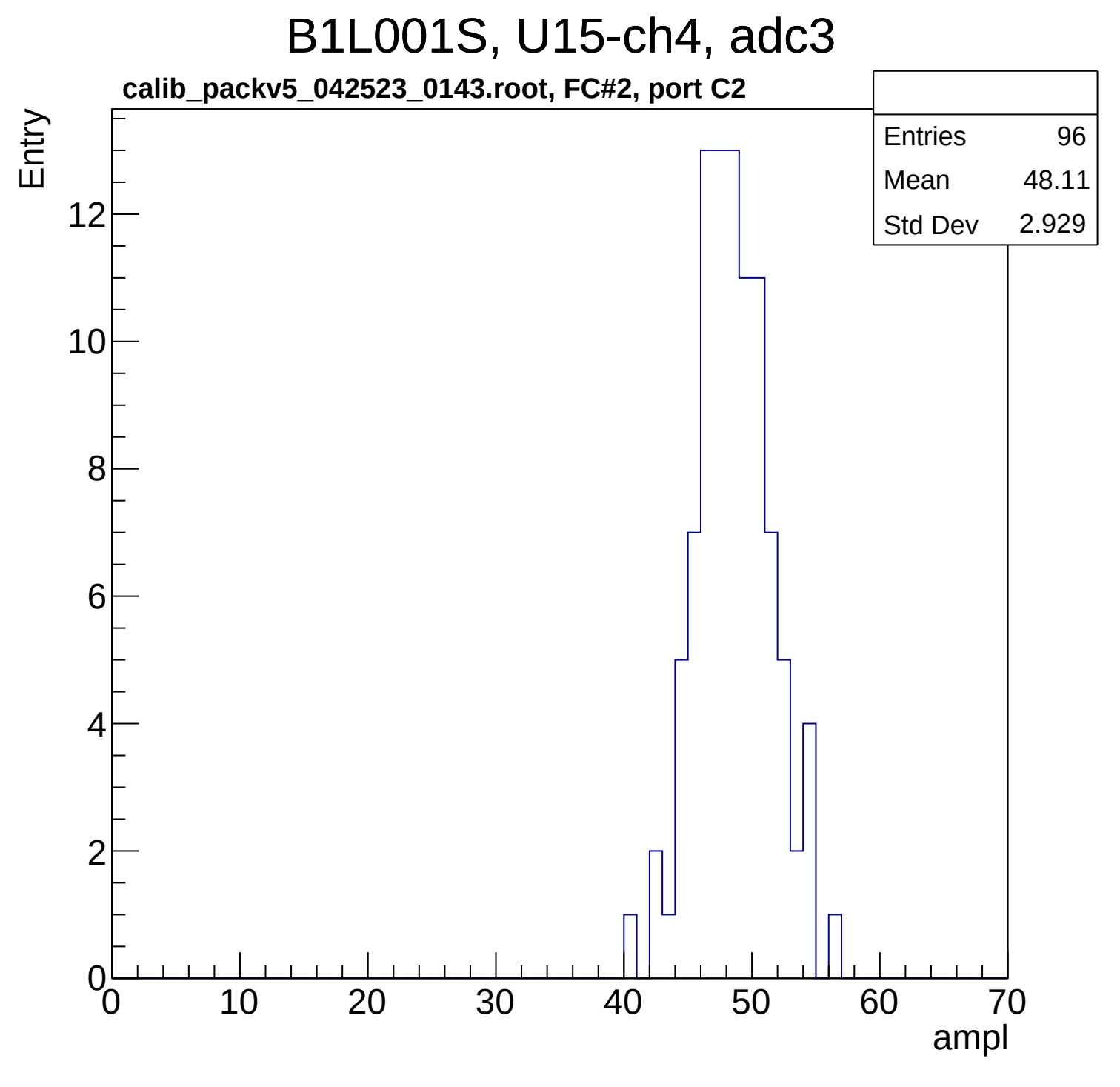
B1L001S, U15-ch4, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 48.11
Std Dev 2.929
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
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60
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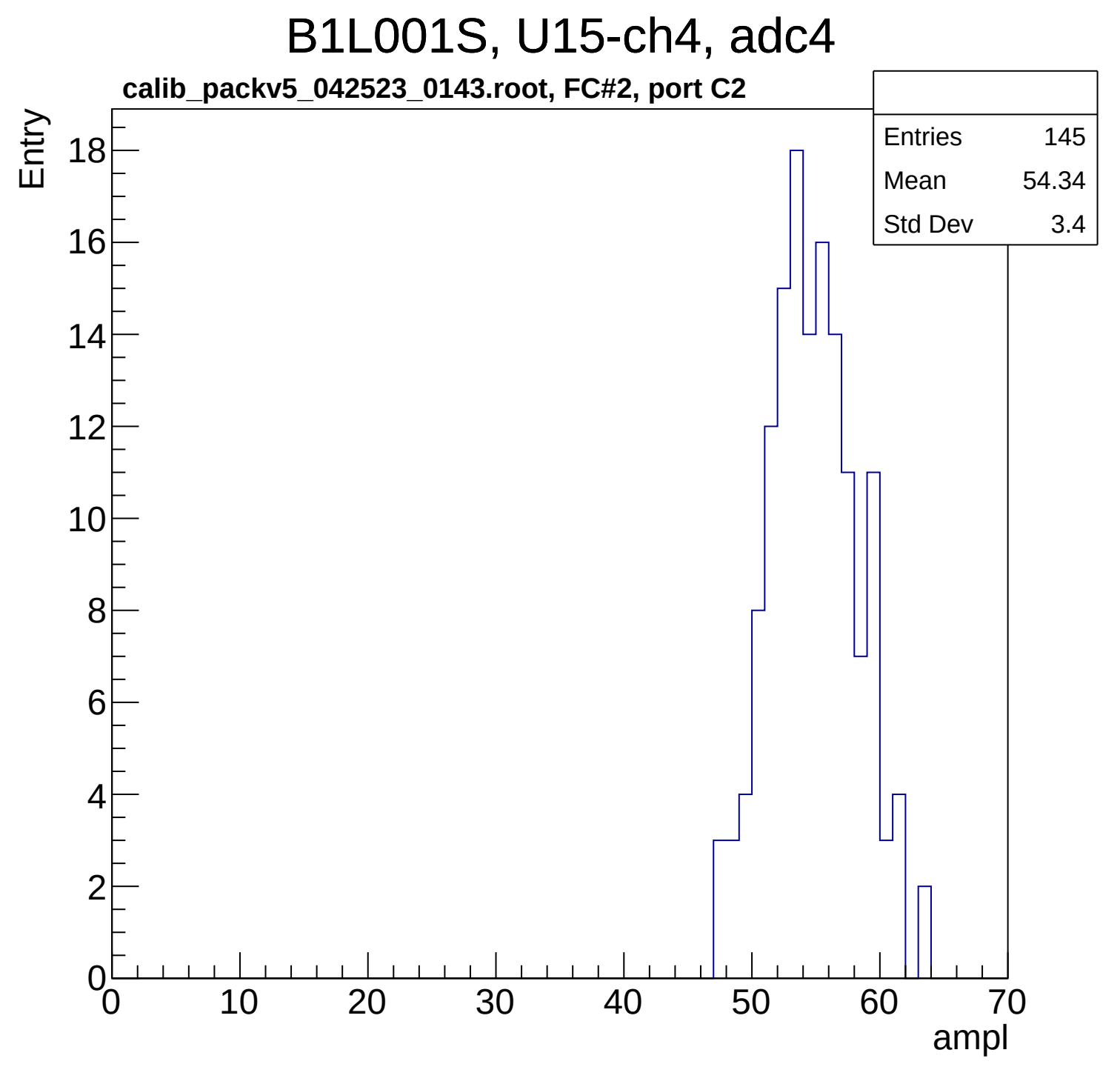
B1L001S, U15-ch4, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 54.34
Std Dev 3.4
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
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40
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60
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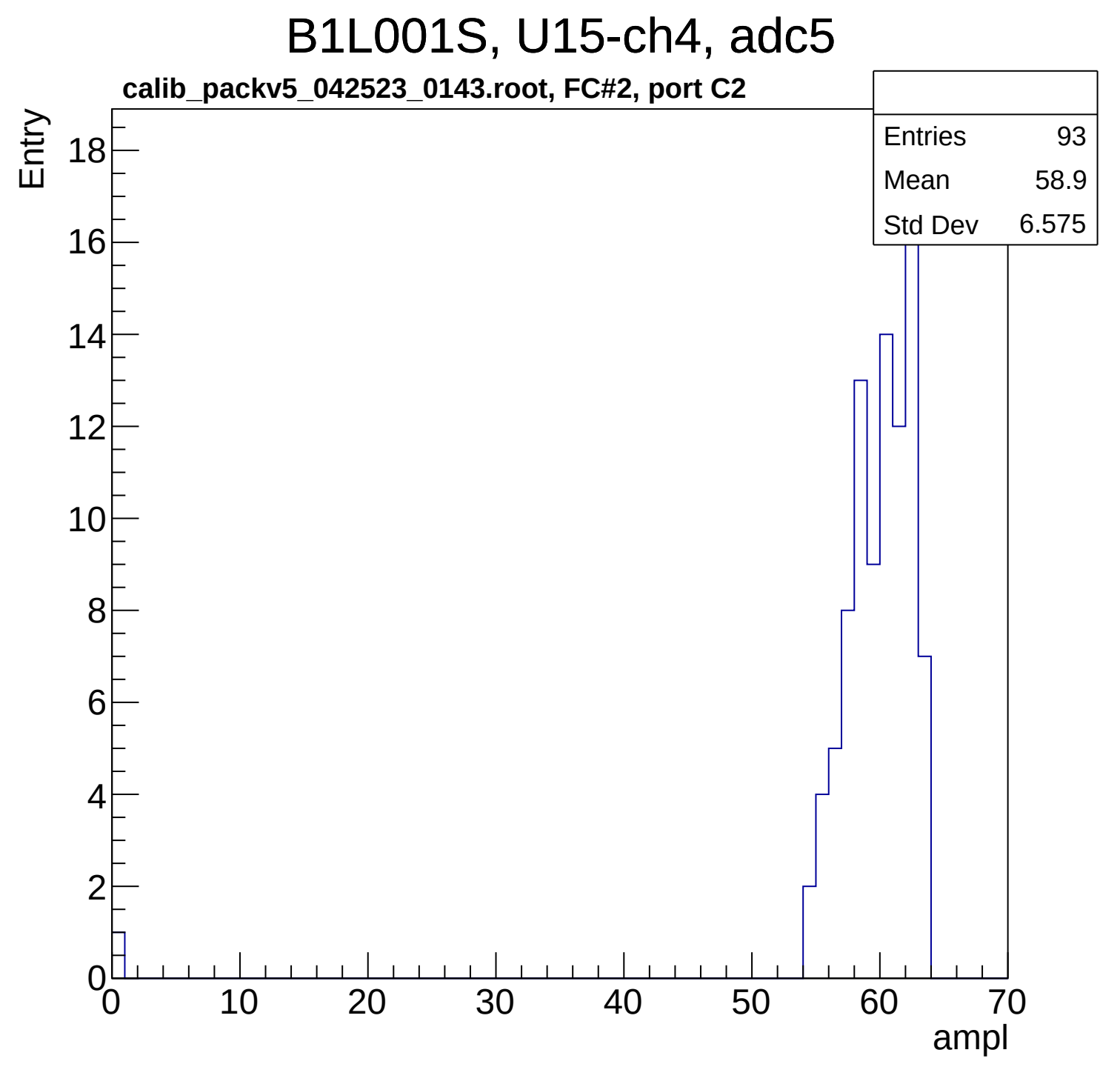
B1L001S, U15-ch4, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 58.9
Std Dev 6.575
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
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60
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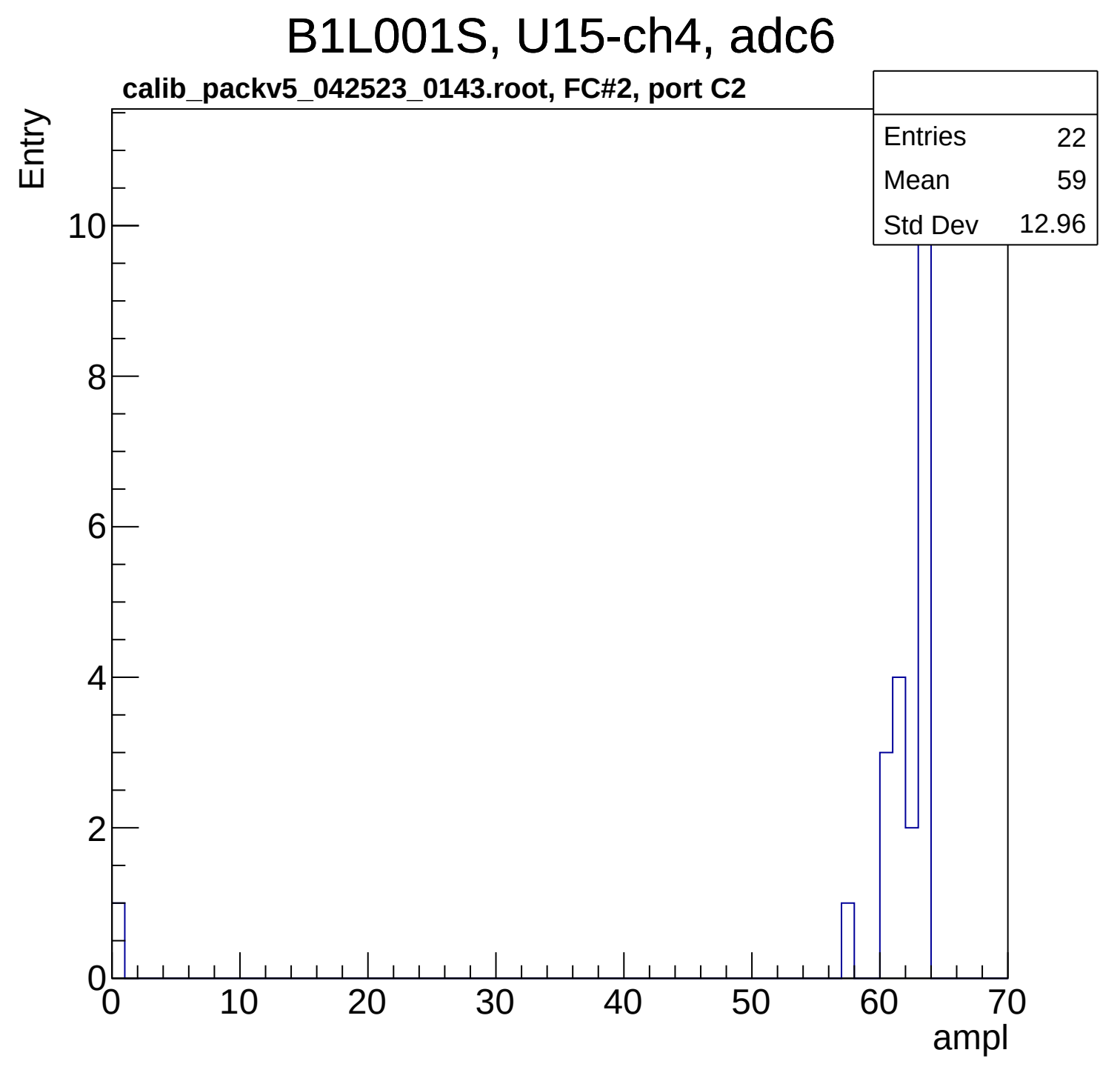
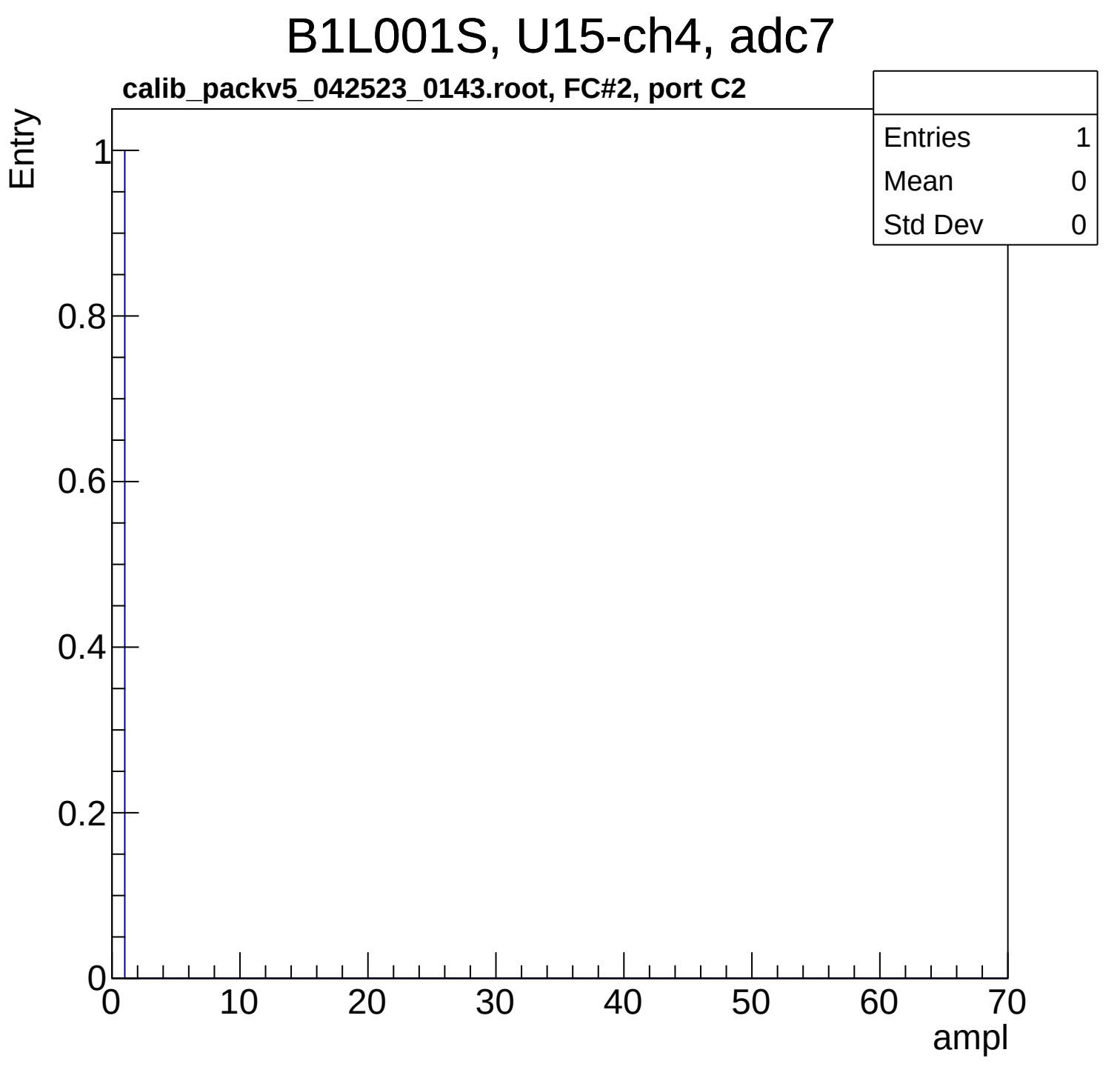
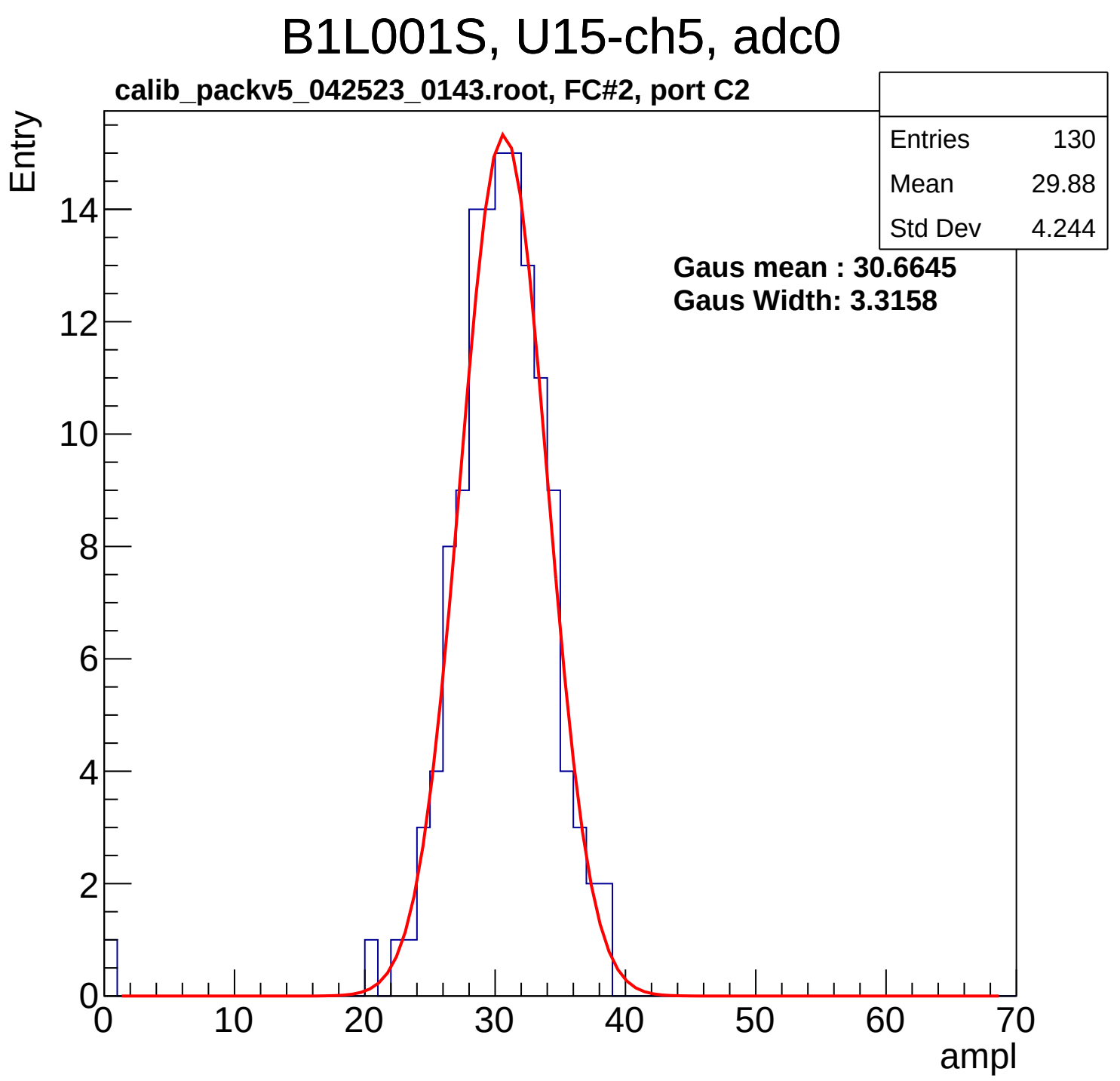

B1L001S, U15-ch4, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 22
Mean 59
Std Dev 12.96
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch4, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch5, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 29.88
Std Dev 4.244
Gaus mean : 30.6645
Gaus Width: 3.3158
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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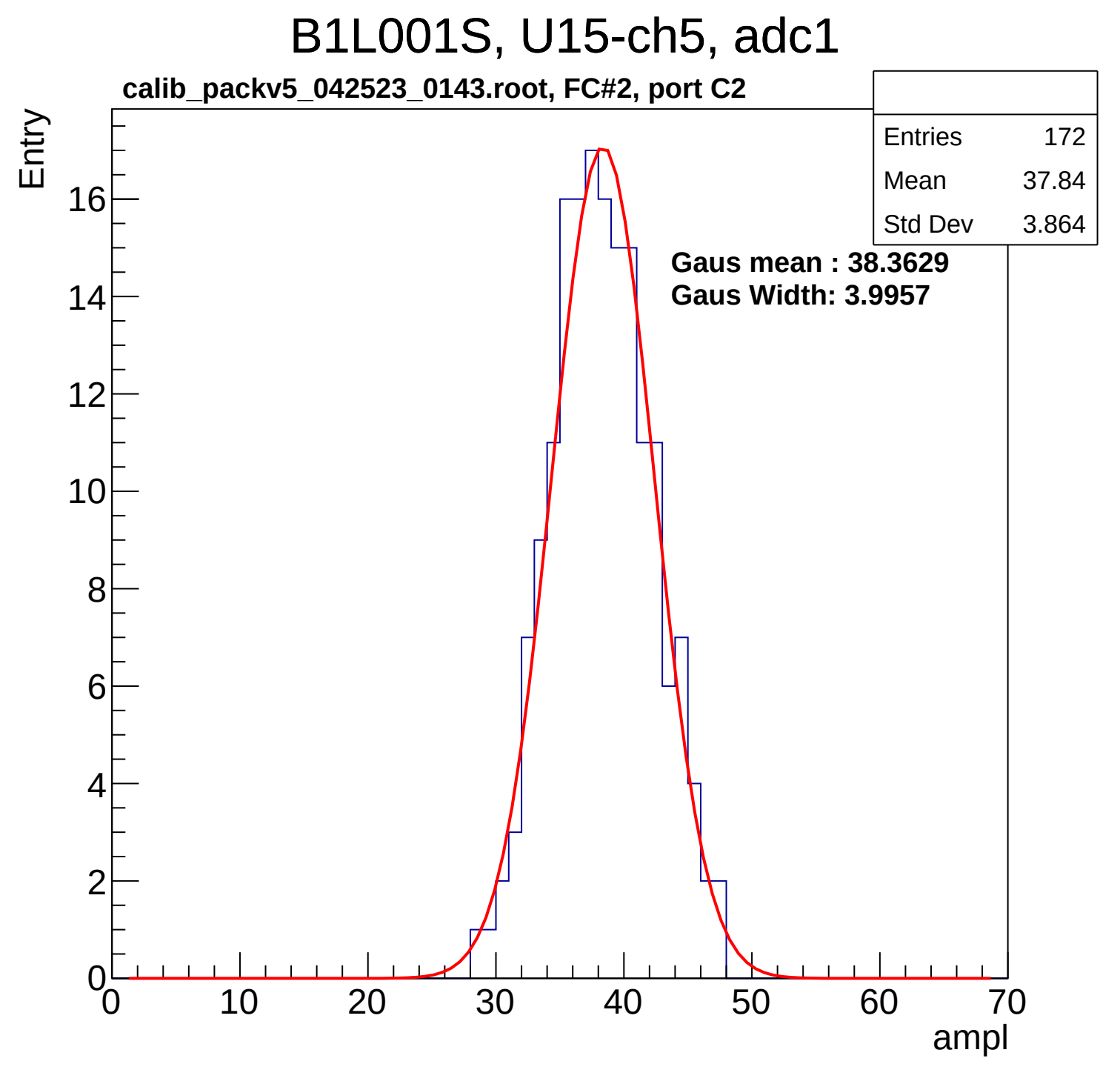
B1L001S, U15-ch5, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 172
Mean 37.84
Std Dev 3.864
Gaus mean : 38.3629
Gaus Width: 3.9957
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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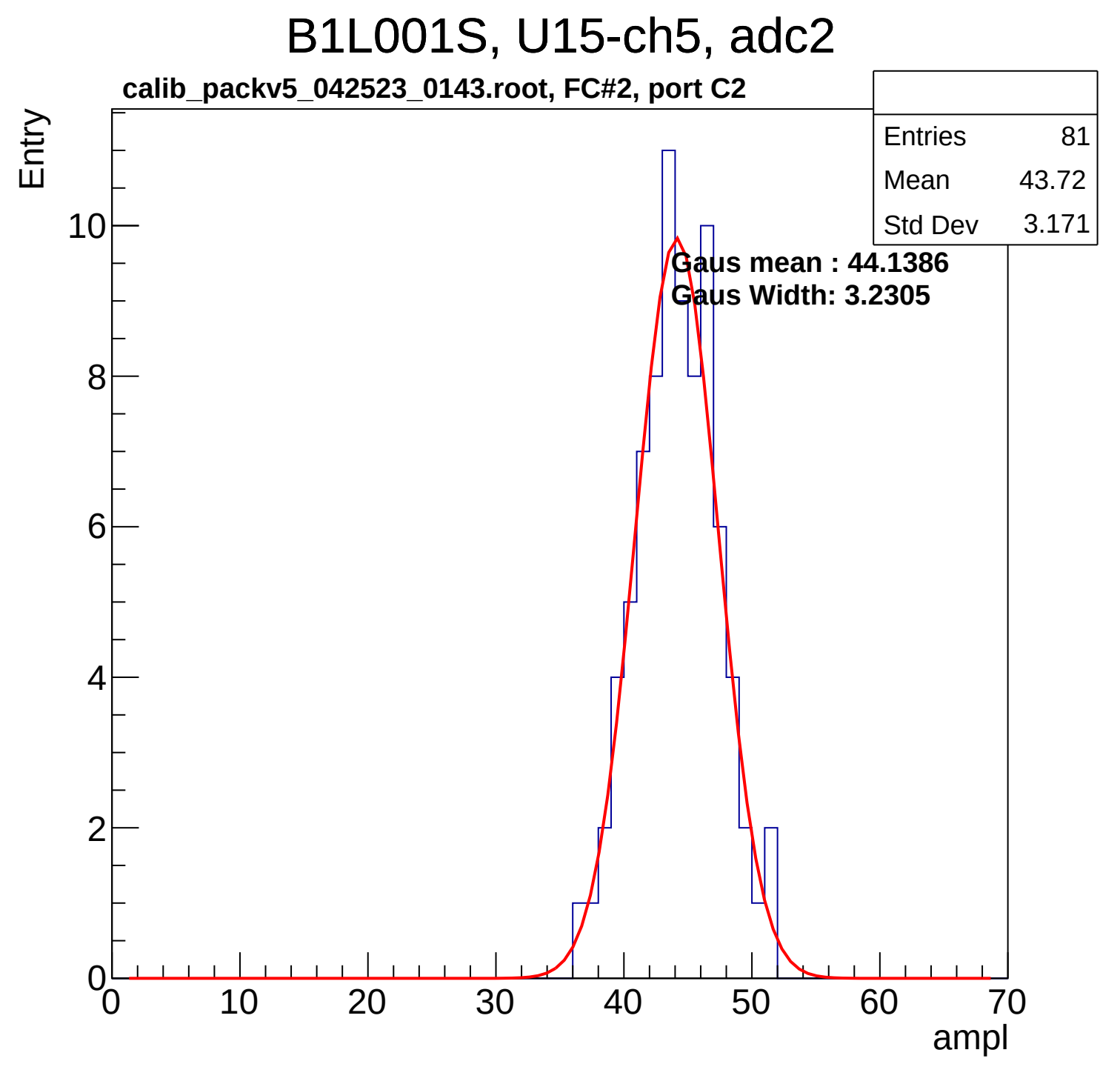
B1L001S, U15-ch5, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 43.72
Std Dev 3.171
Gaus mean : 44.1386
Gaus Width: 3.2305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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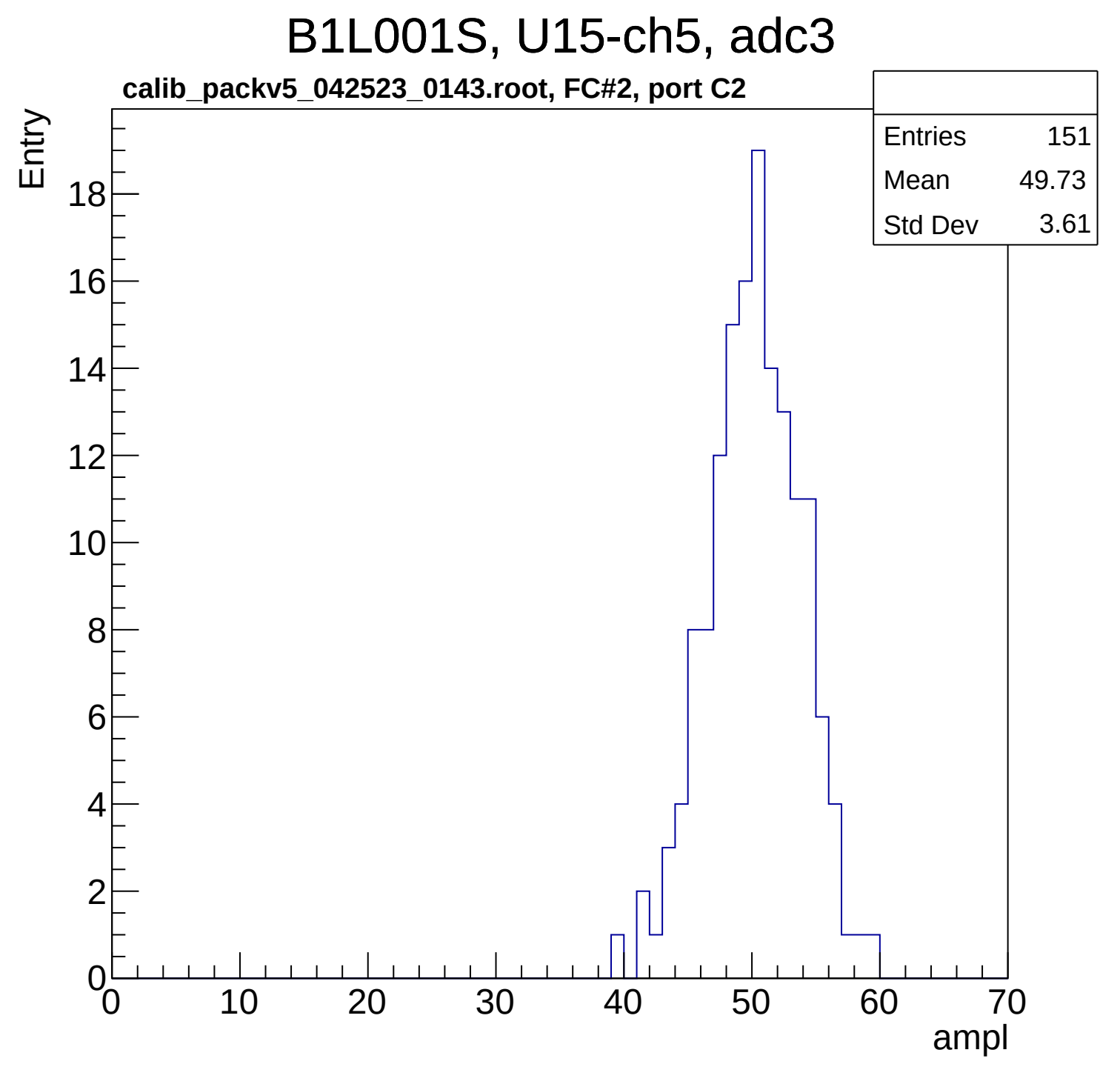
B1L001S, U15-ch5, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 49.73
Std Dev 3.61
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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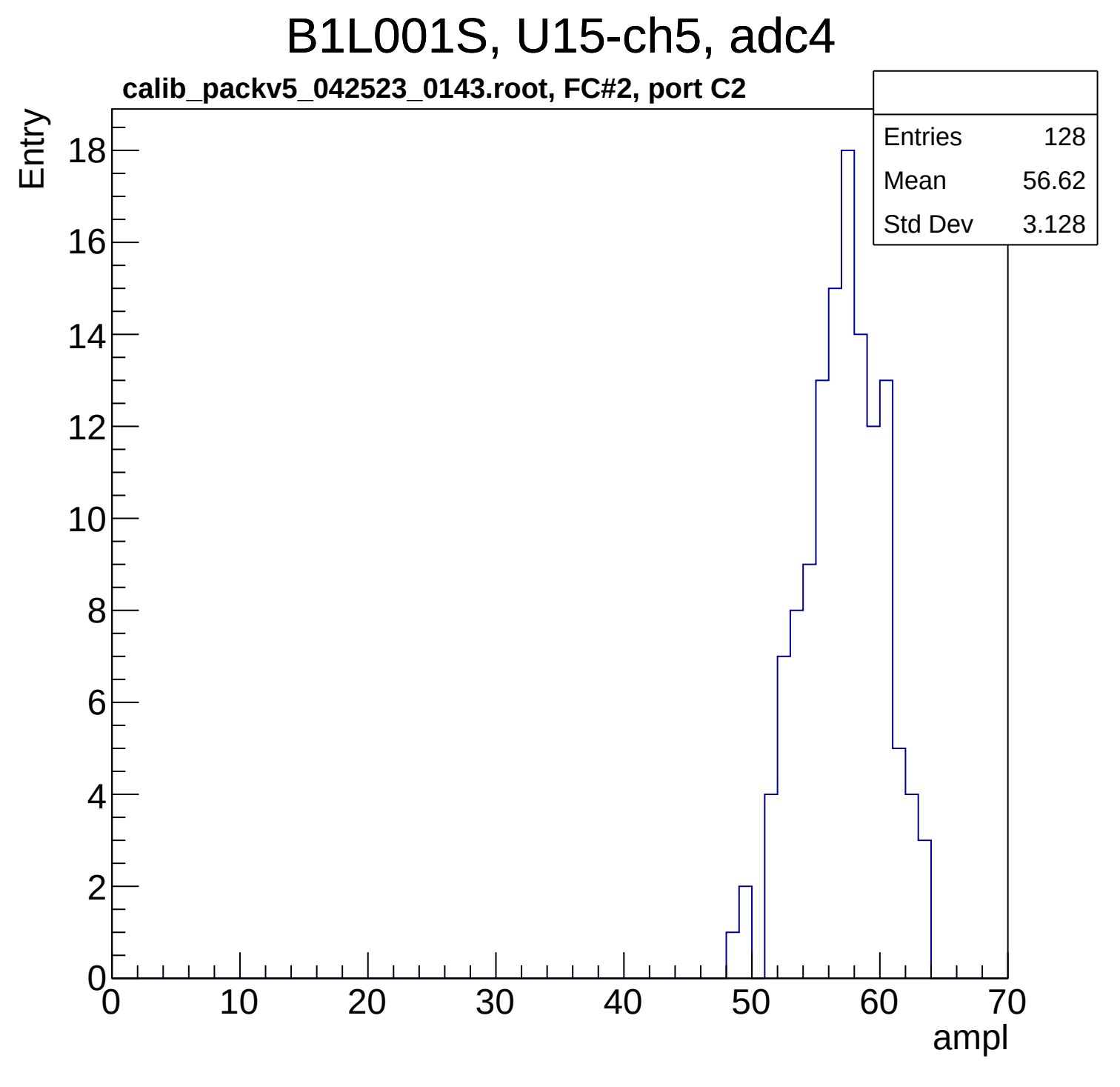
B1L001S, U15-ch5, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 56.62
Std Dev 3.128
Entry
ampl
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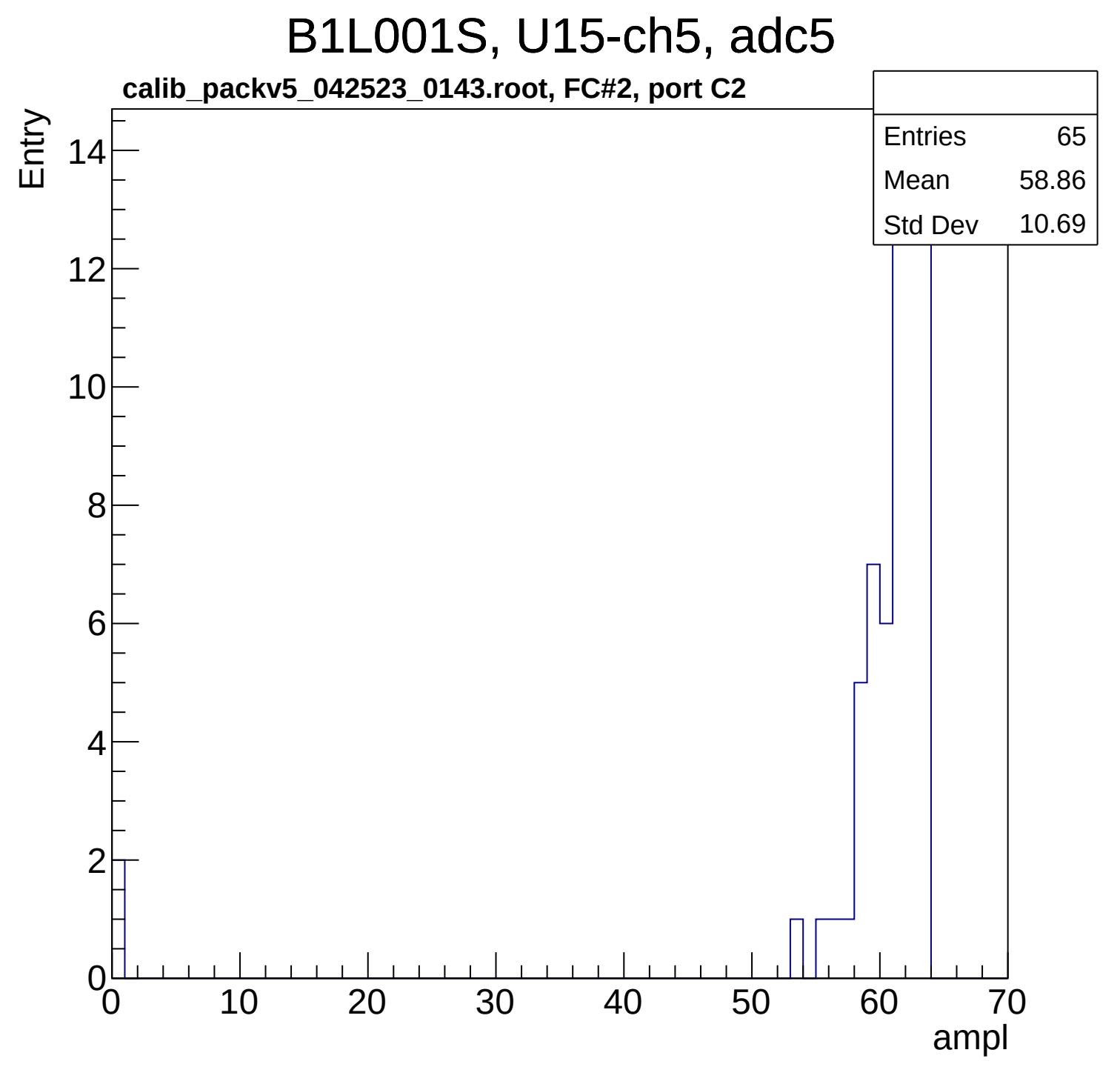
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch5, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 65
Mean 58.86
Std Dev 10.69
Entry
0
2
4
6
8
10
12
14
0
10
20
30
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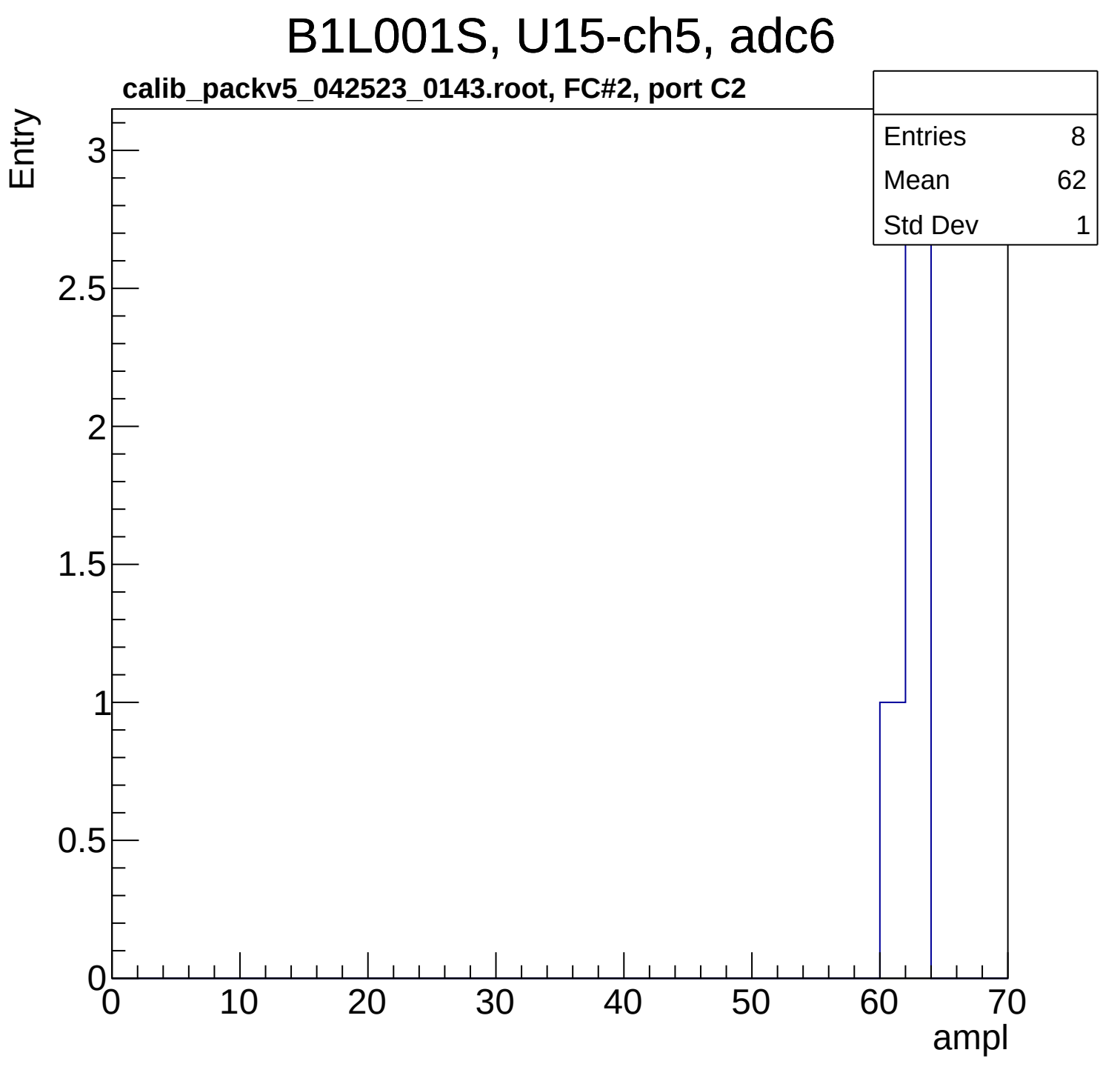
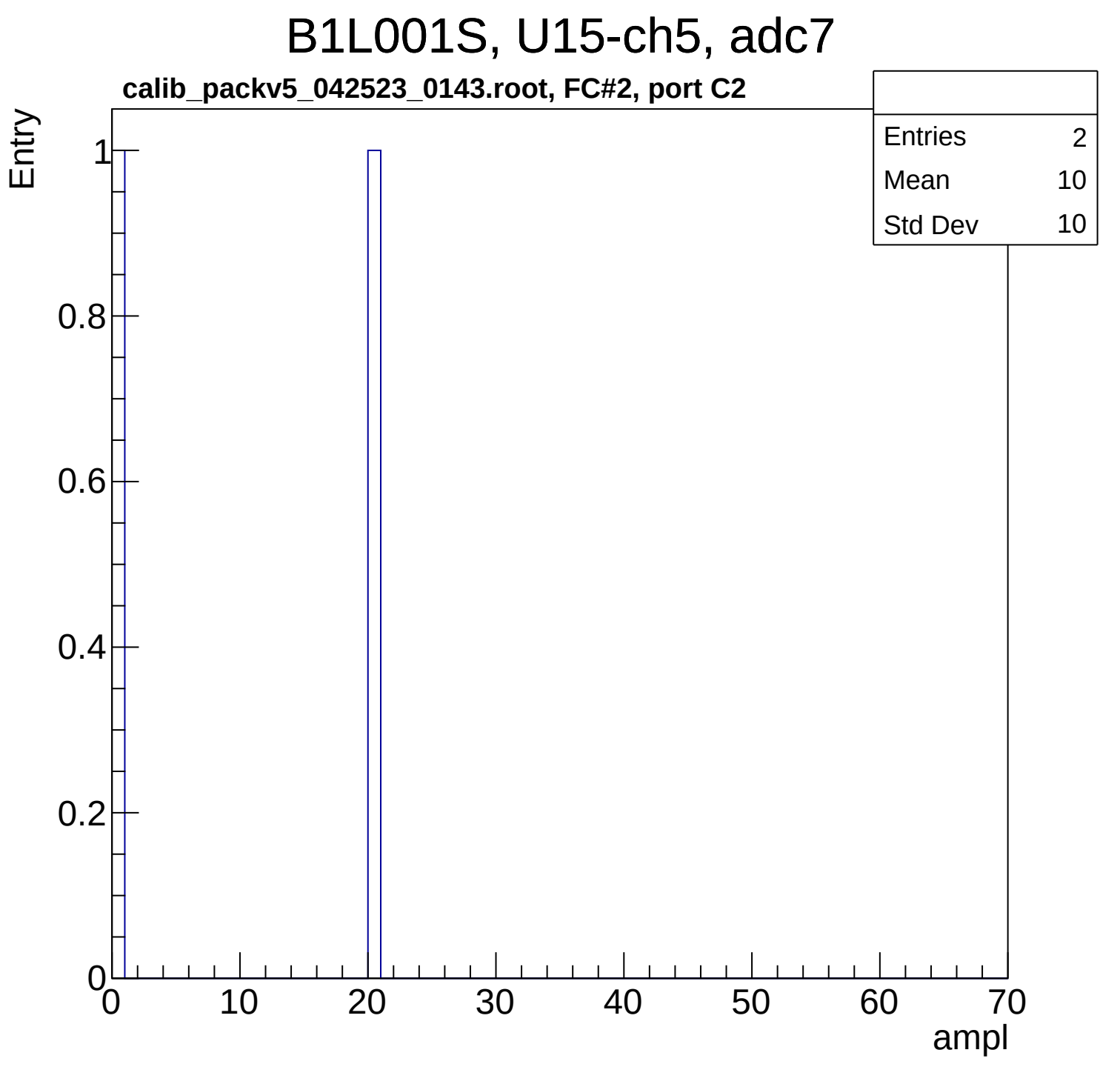
50
60
70
ampl


B1L001S, U15-ch5, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch5, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10
Std Dev 10
Entry
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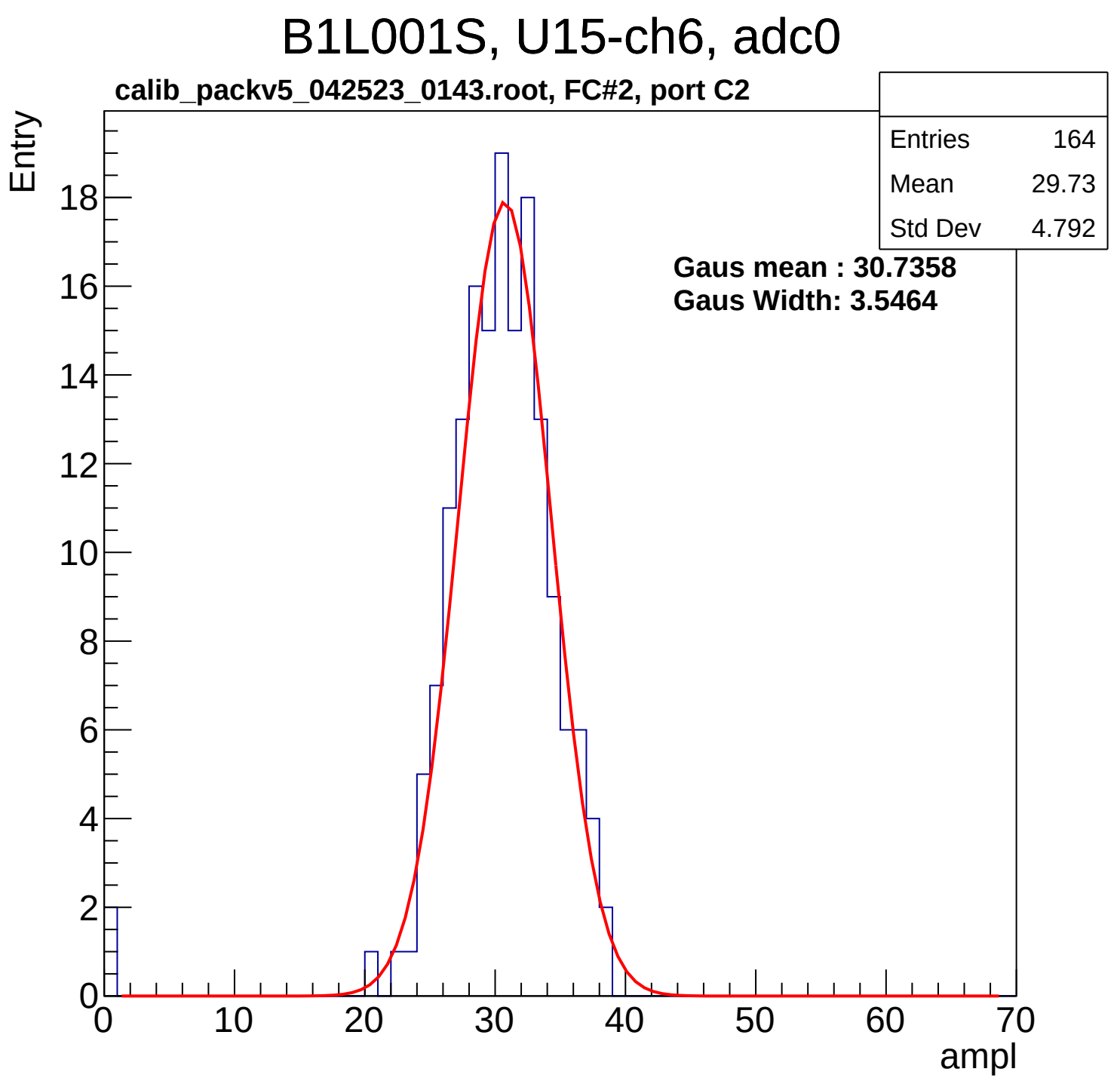
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch6, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 164
Mean 29.73
Std Dev 4.792
Gaus mean : 30.7358
Gaus Width: 3.5464
Entry
ampl
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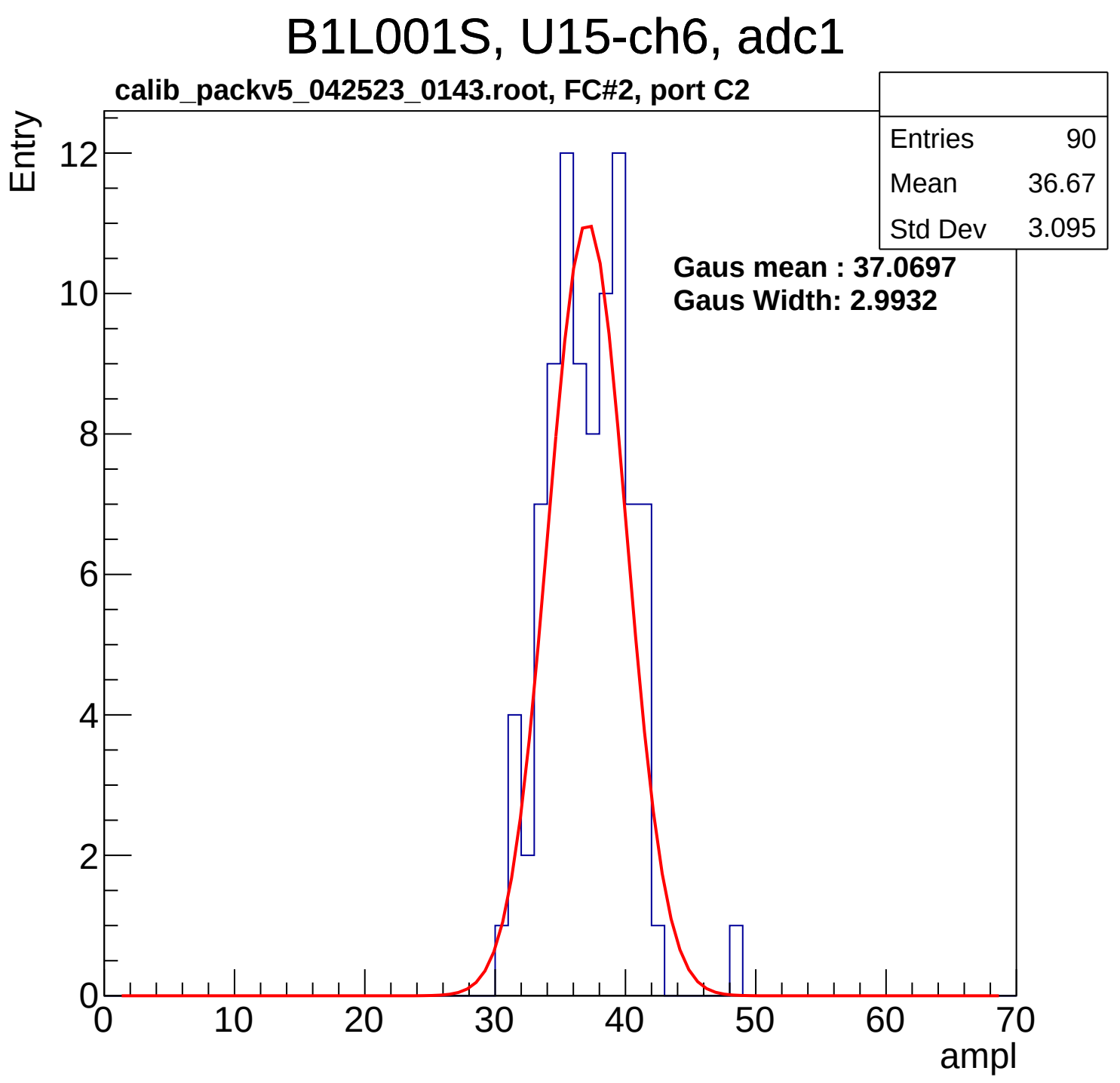
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch6, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 36.67
Std Dev 3.095
Gaus mean : 37.0697
Gaus Width: 2.9932
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
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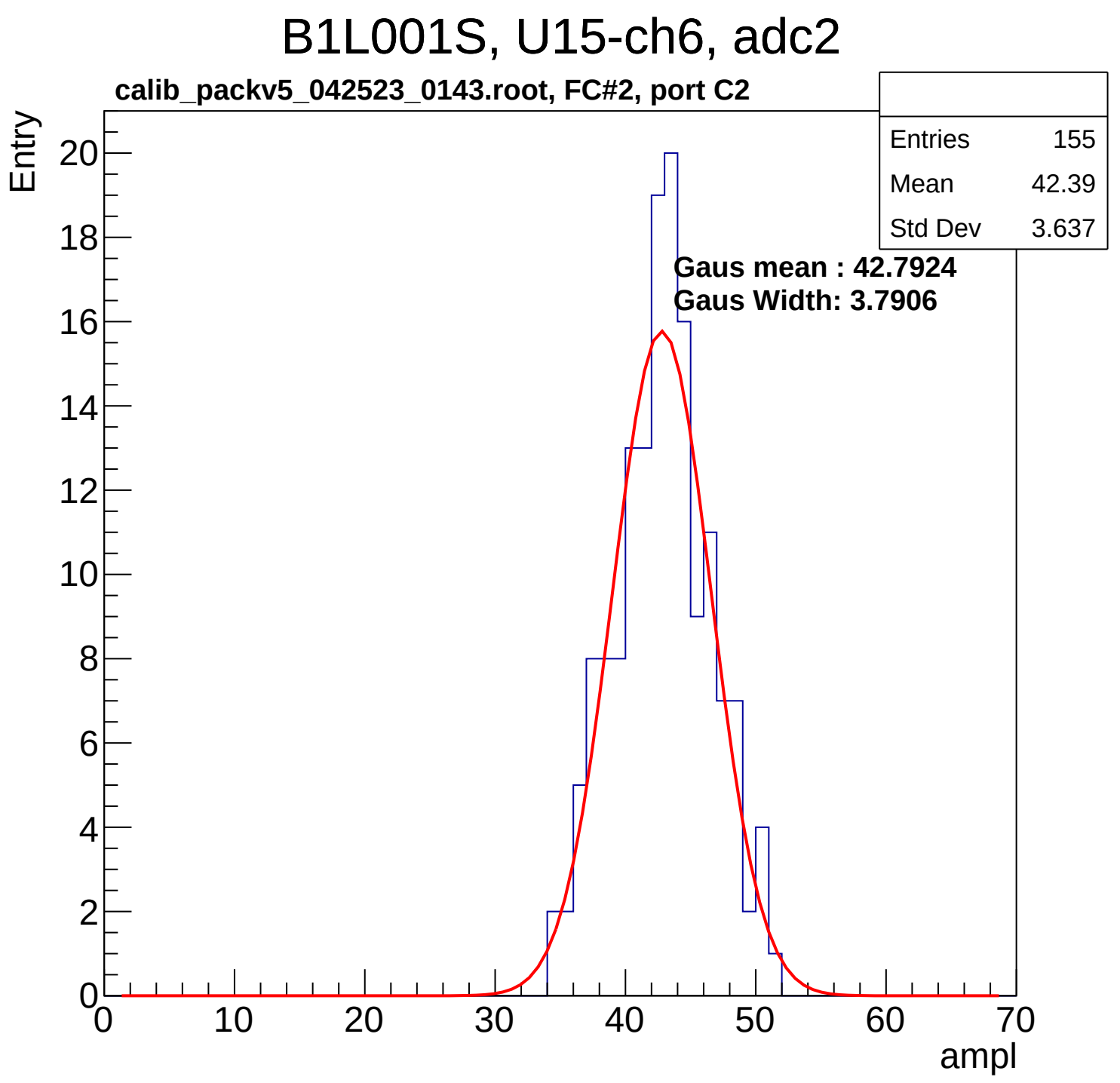
60
70


B1L001S, U15-ch6, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 42.39
Std Dev 3.637
Gaus mean : 42.7924
Gaus Width: 3.7906
Entry
ampl
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6
8
10
12
14
16
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0
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20
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60
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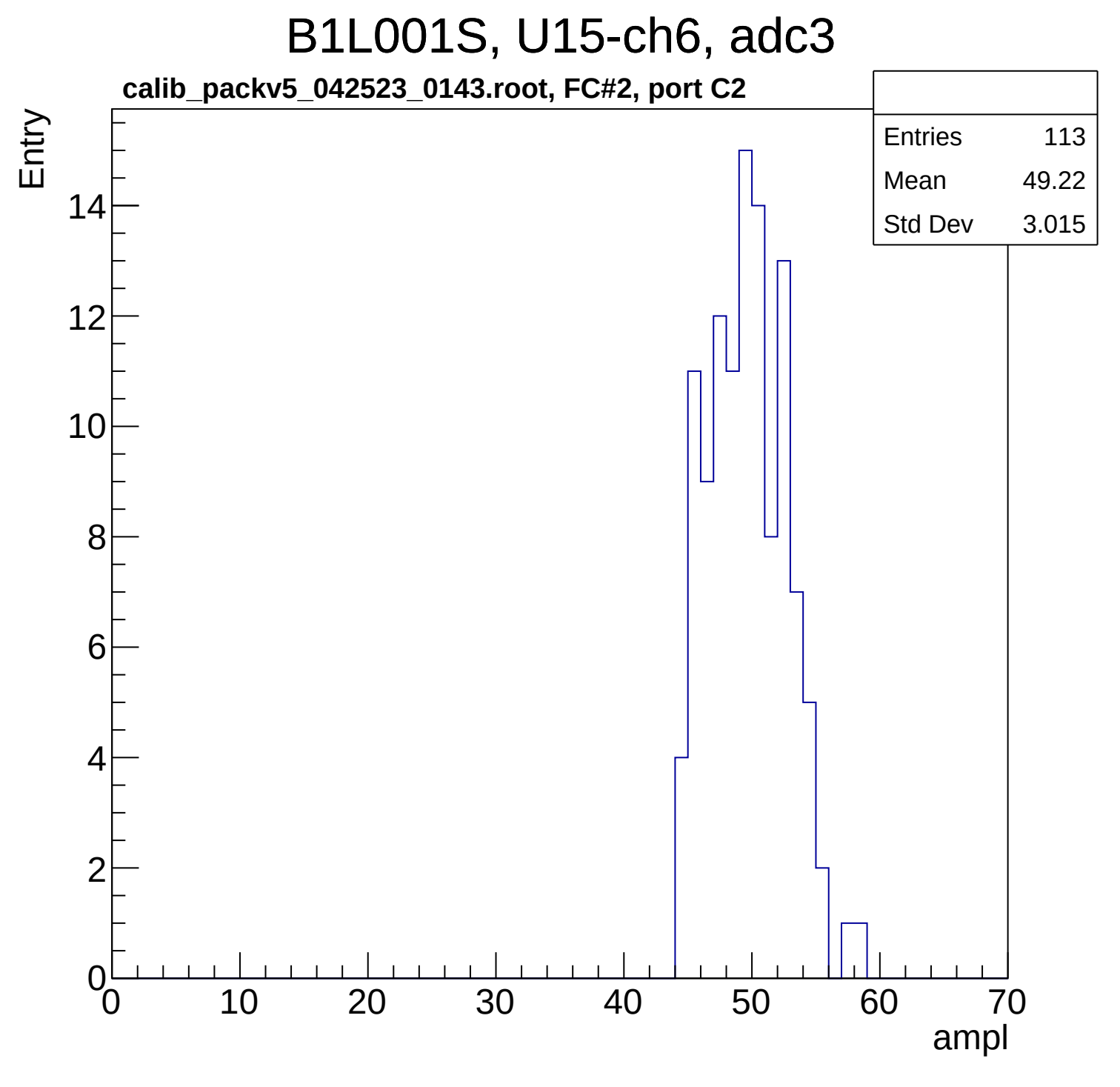
B1L001S, U15-ch6, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.22
Std Dev 3.015
Entry
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6
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14
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10
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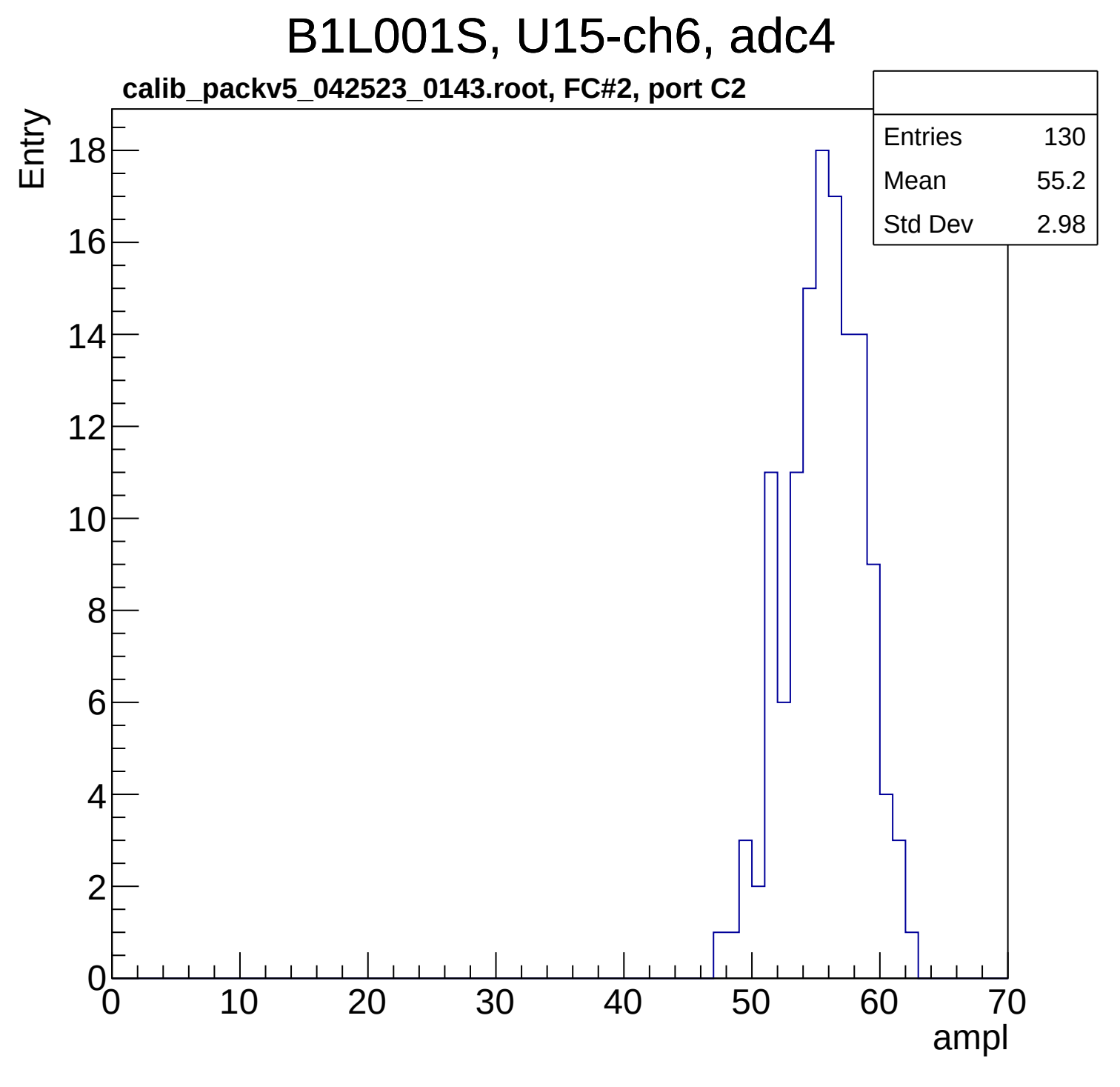
50
60
70
ampl


B1L001S, U15-ch6, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 55.2
Std Dev 2.98
Entry
ampl
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4
6
8
10
12
14
16
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0
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60
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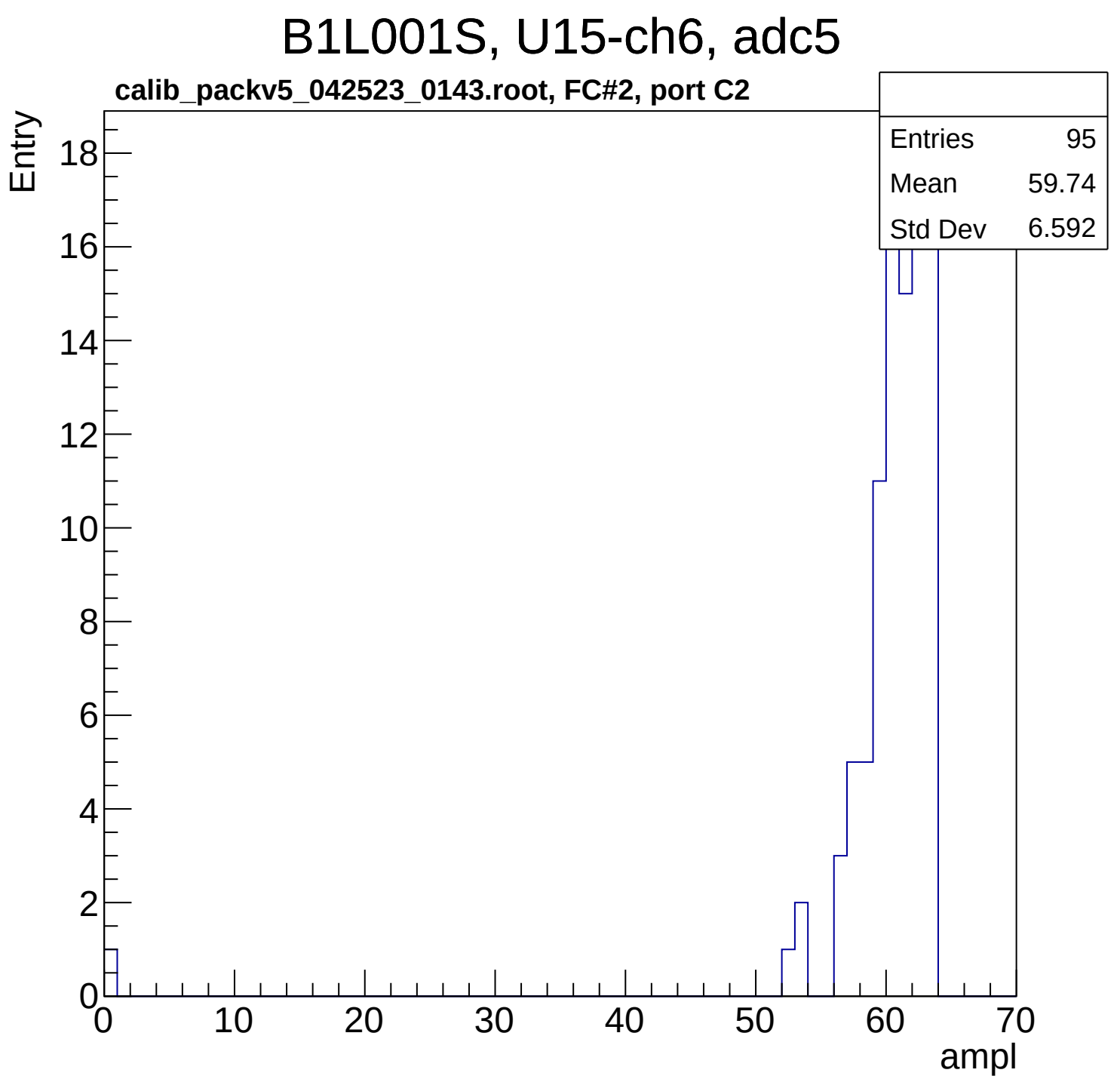
B1L001S, U15-ch6, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 59.74
Std Dev 6.592
Entry
ampl
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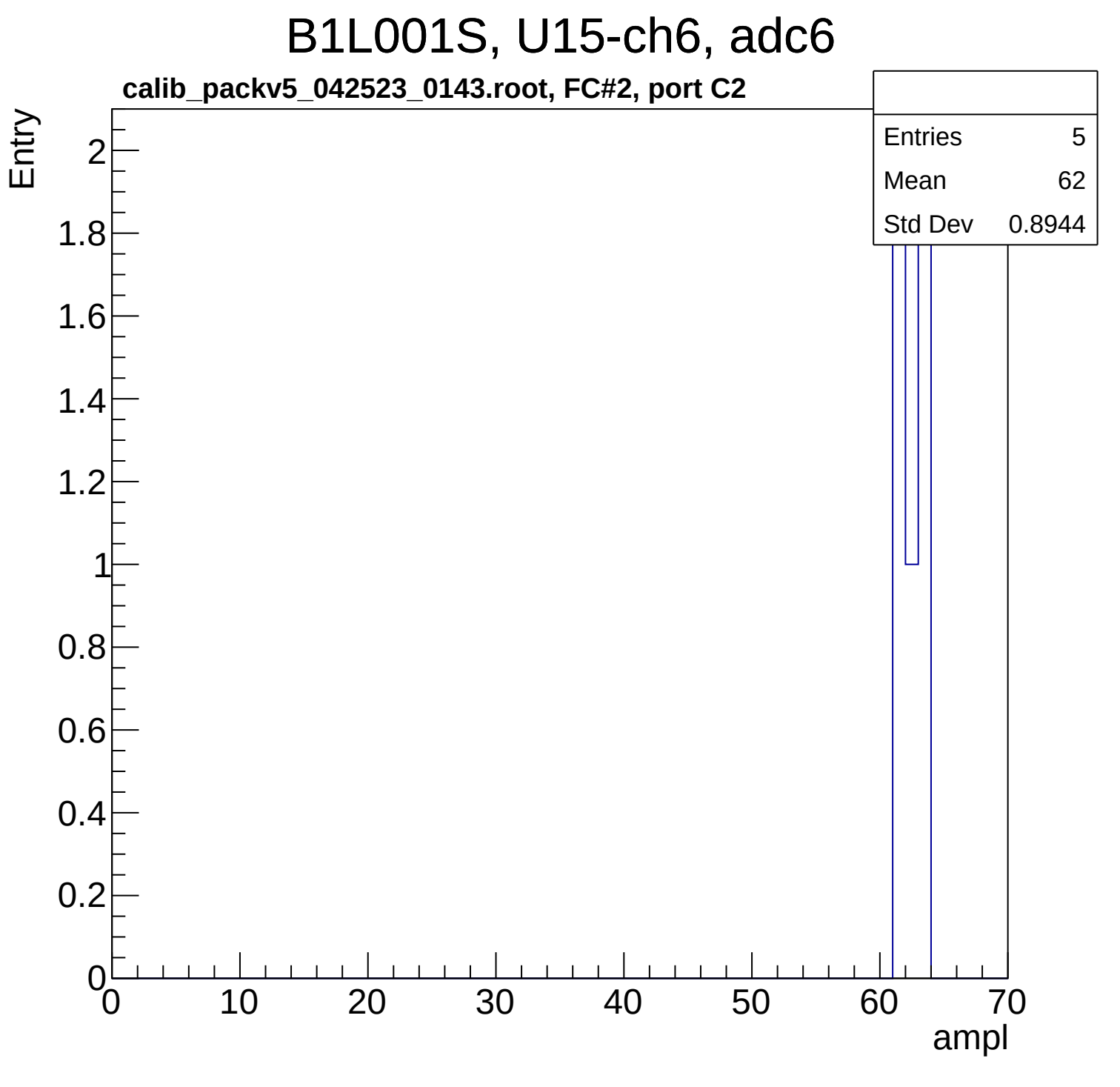
B1L001S, U15-ch6, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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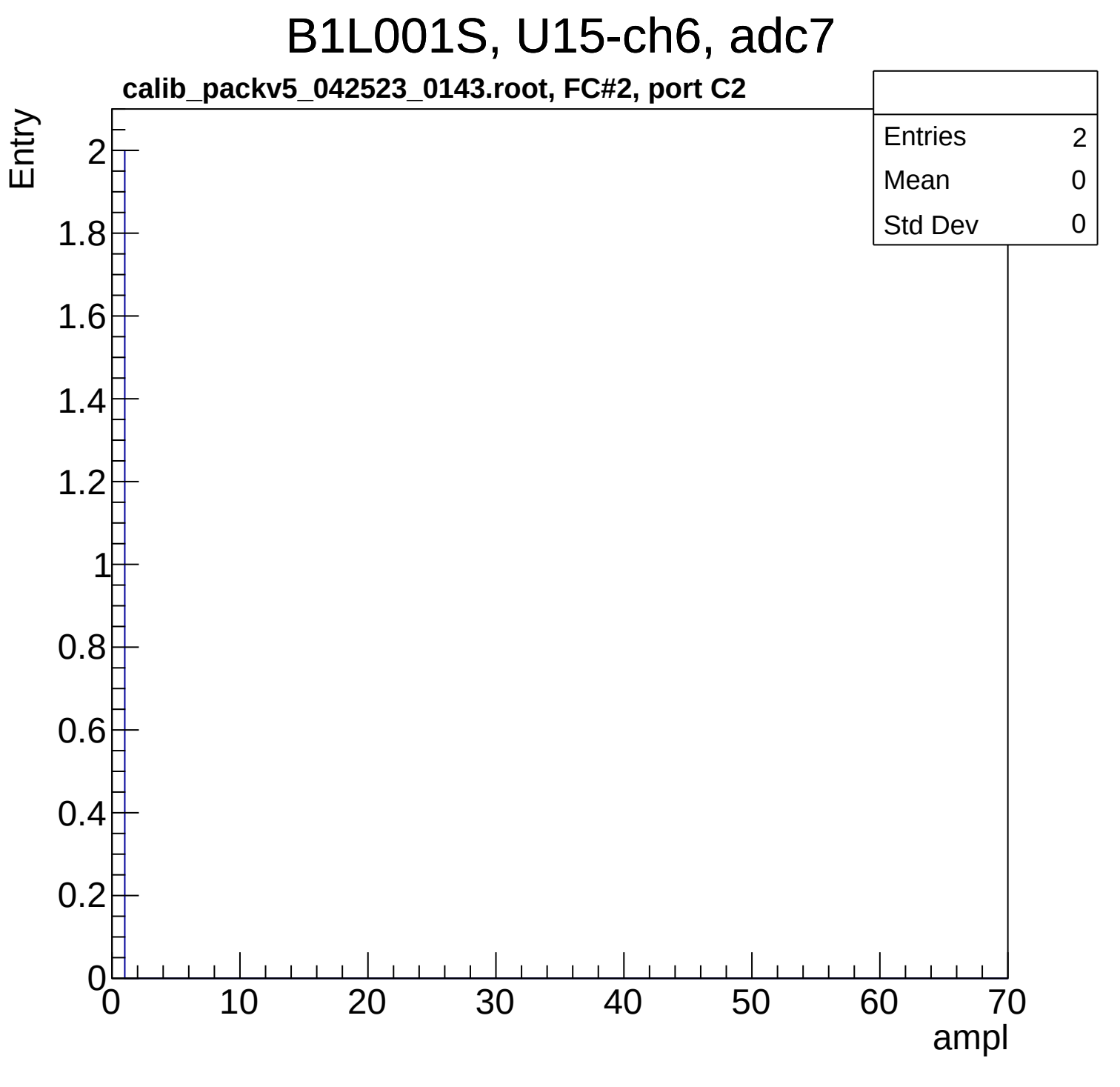
B1L001S, U15-ch6, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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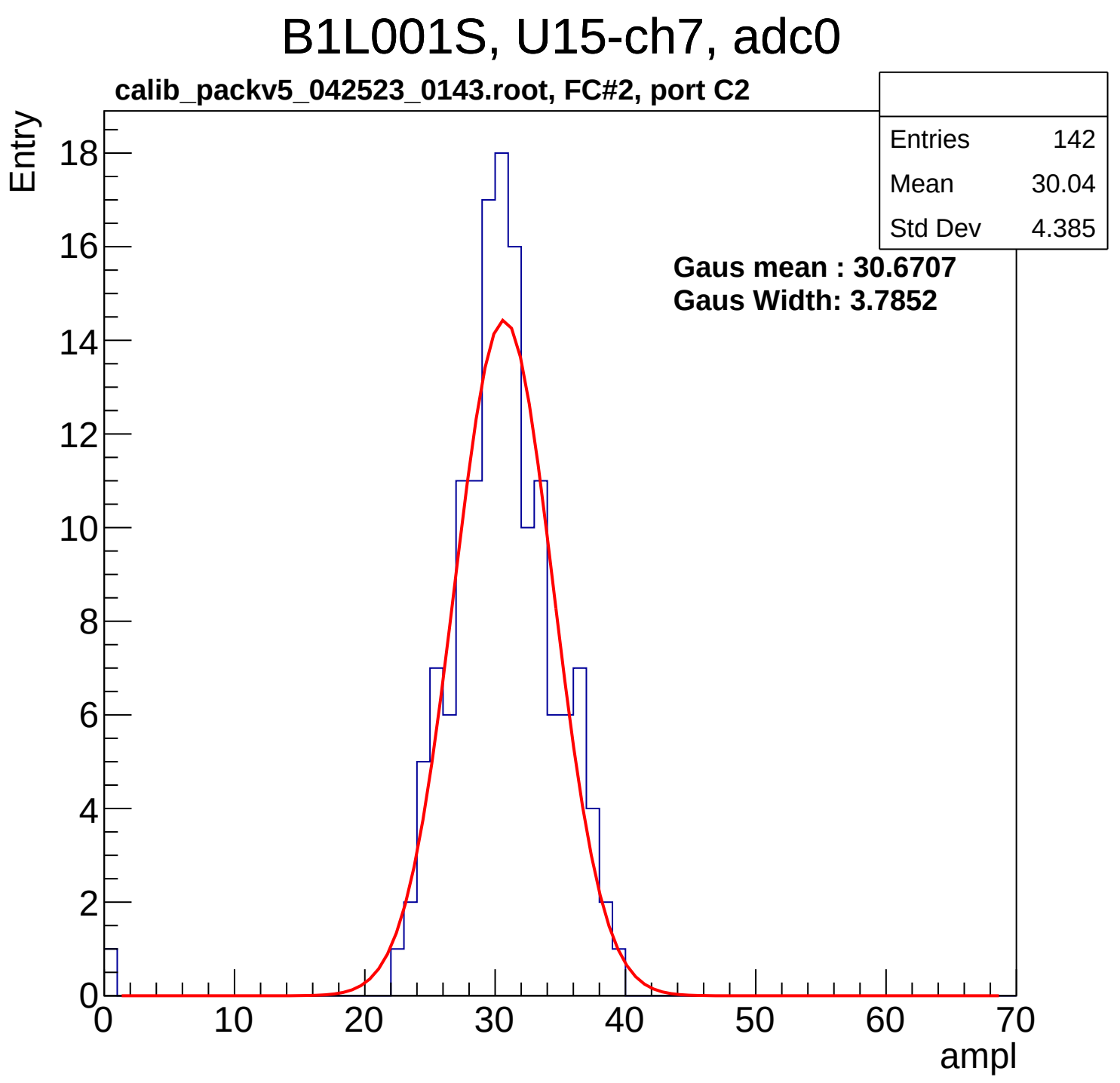
50
60
70
ampl


B1L001S, U15-ch7, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 30.04
Std Dev 4.385
Gaus mean : 30.6707
Gaus Width: 3.7852
Entry
ampl
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6
8
10
12
14
16
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0
10
20
30
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60
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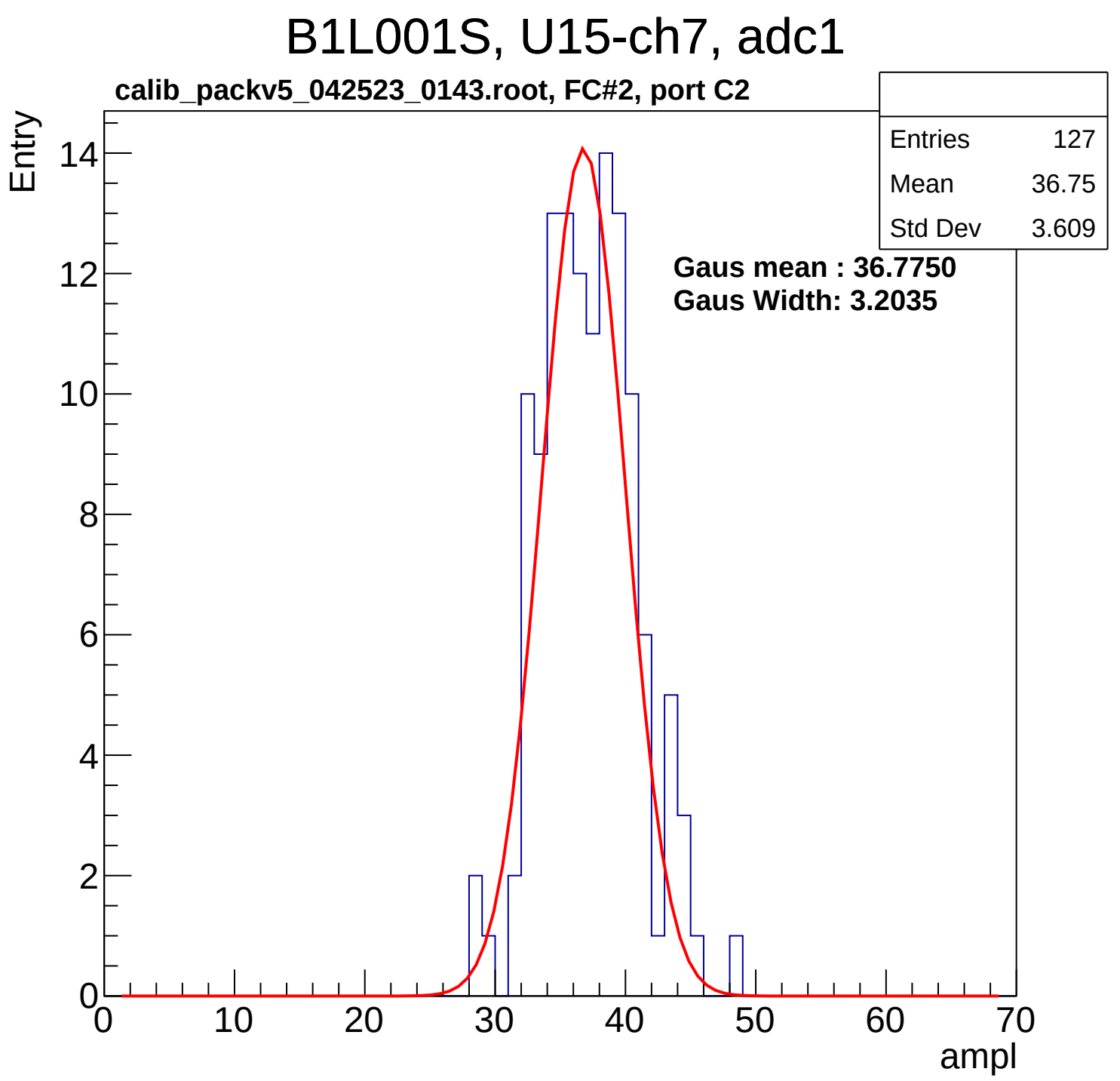
B1L001S, U15-ch7, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 36.75
Std Dev 3.609
Gaus mean : 36.7750
Gaus Width: 3.2035
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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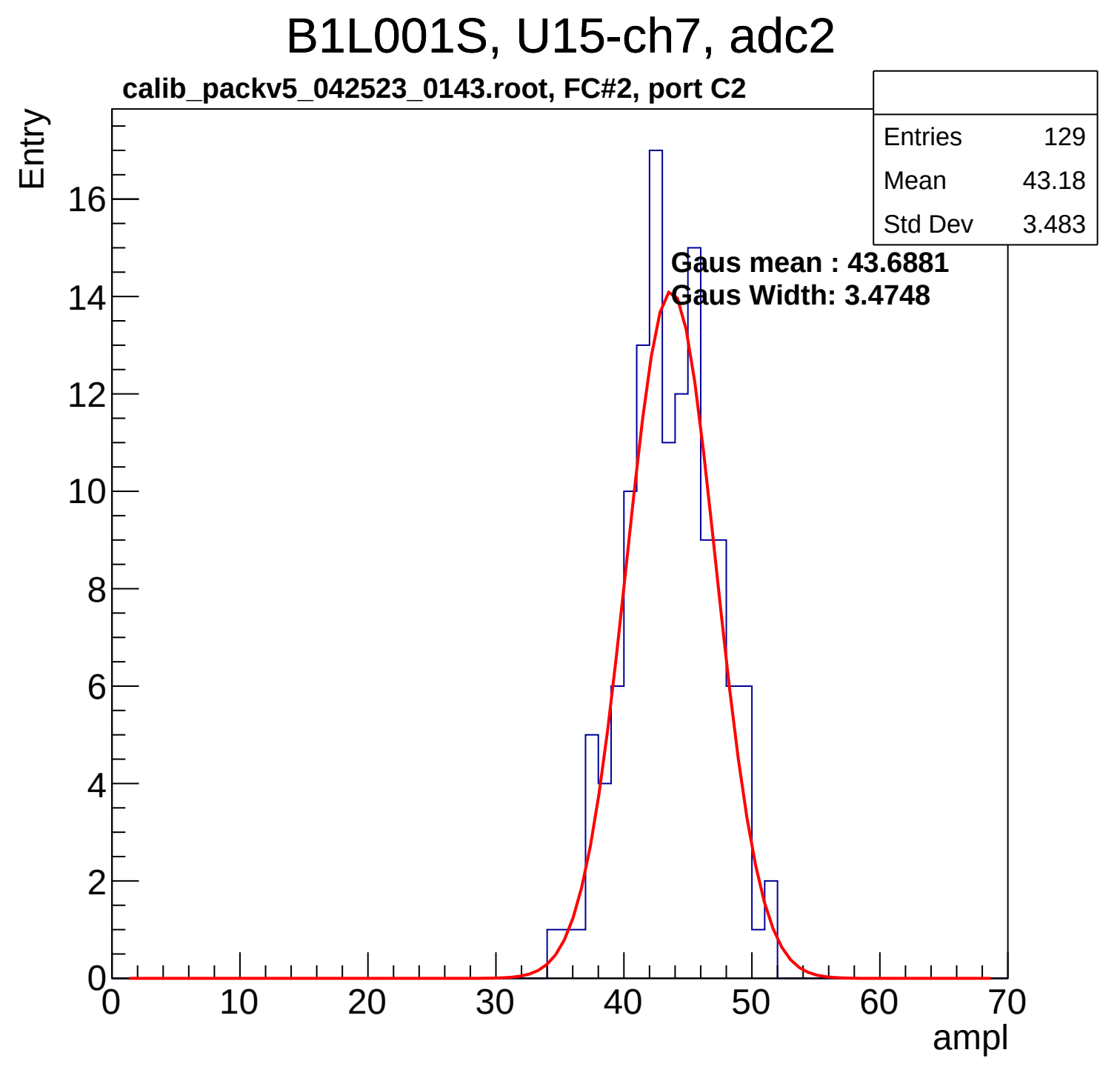
B1L001S, U15-ch7, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 43.18
Std Dev 3.483
Gaus mean : 43.6881
Gaus Width: 3.4748
Entry
ampl
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2
4
6
8
10
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0
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60
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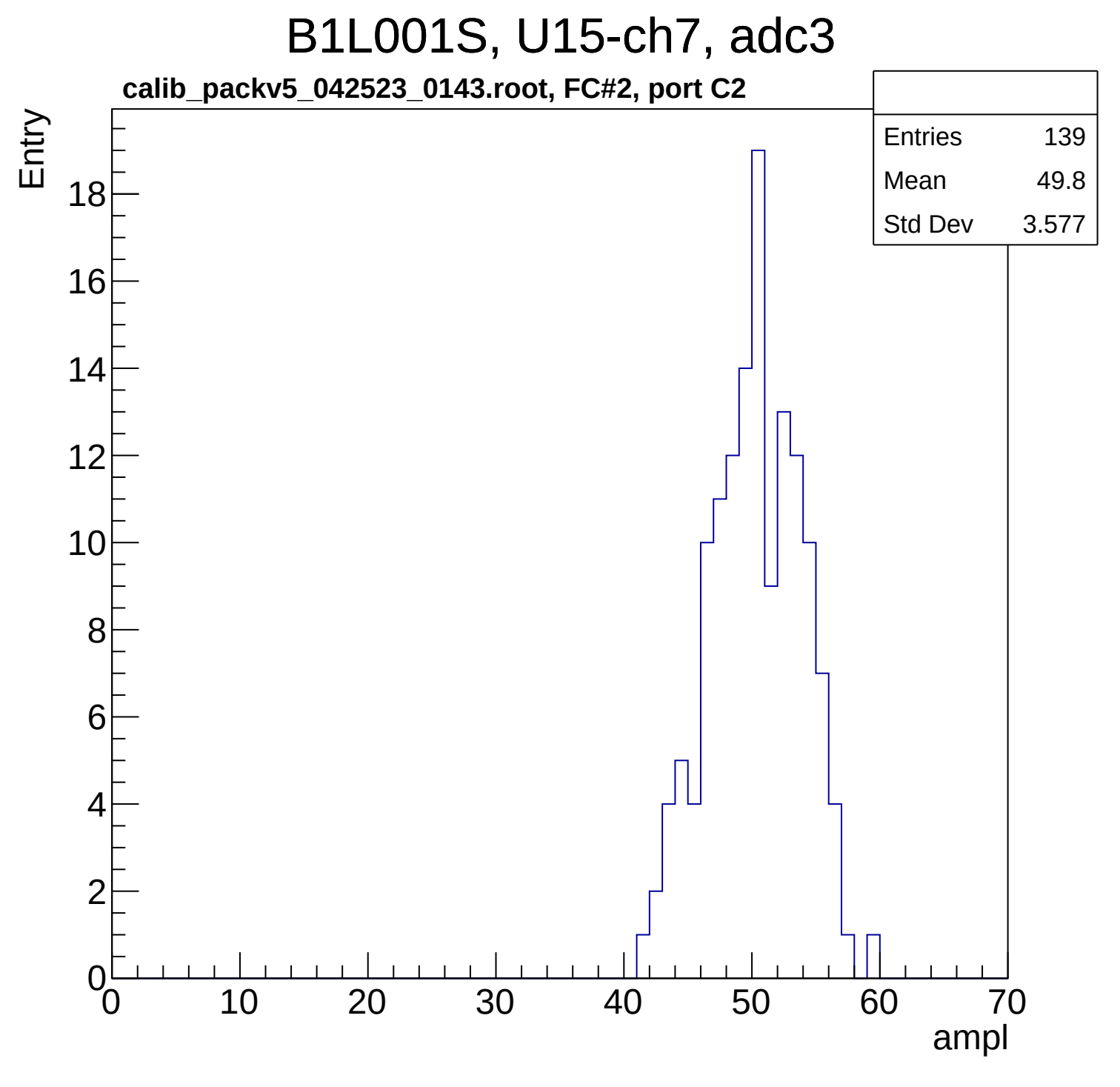
B1L001S, U15-ch7, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.8
Std Dev 3.577
Entry
ampl
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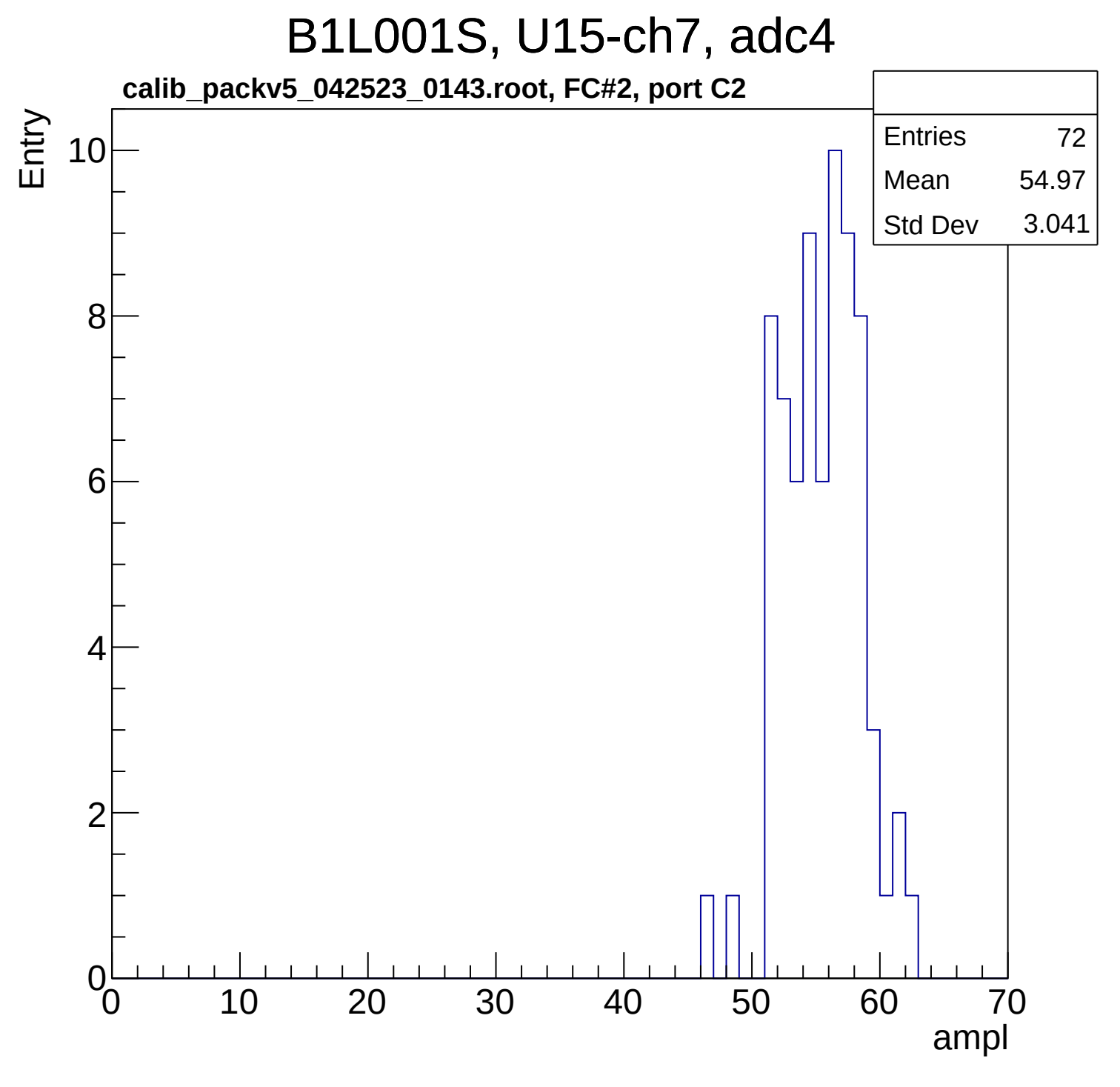
B1L001S, U15-ch7, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 54.97
Std Dev 3.041
Entry
ampl
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40
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60
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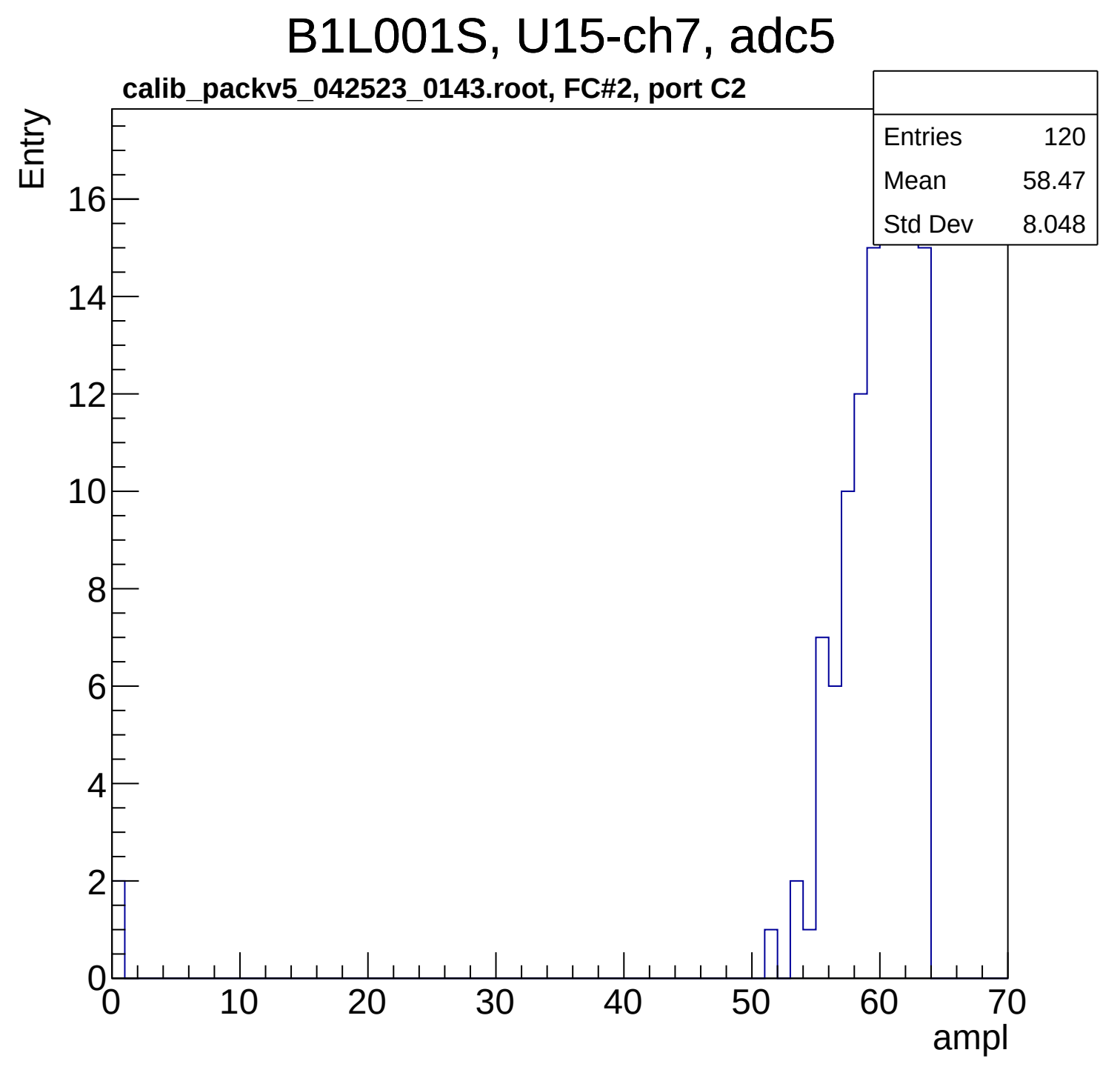
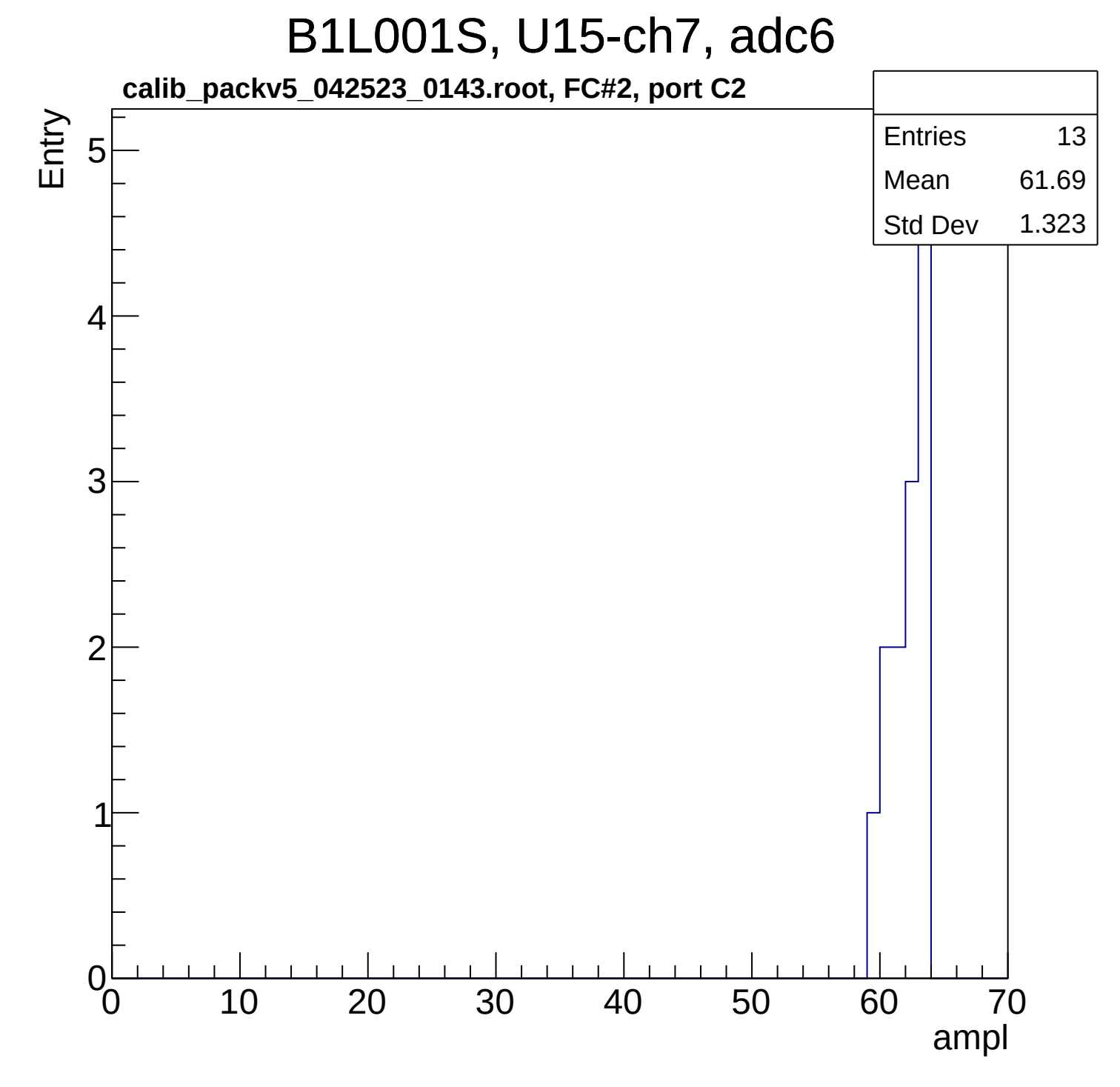
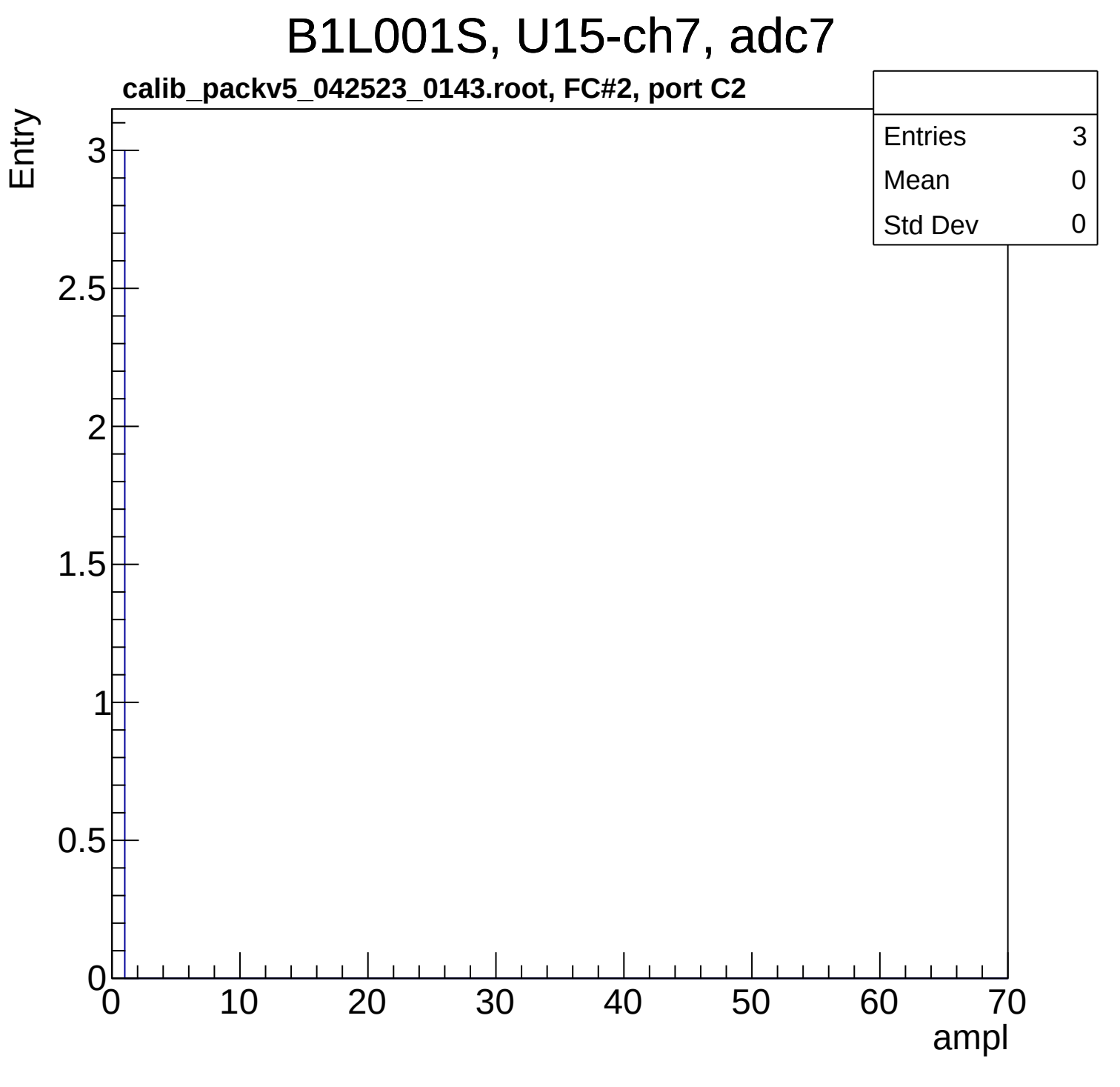

B1L001S, U15-ch7, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 58.47
Std Dev 8.048
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch7, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 61.69
Std Dev 1.323
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch7, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 0
Std Dev 0
Entry
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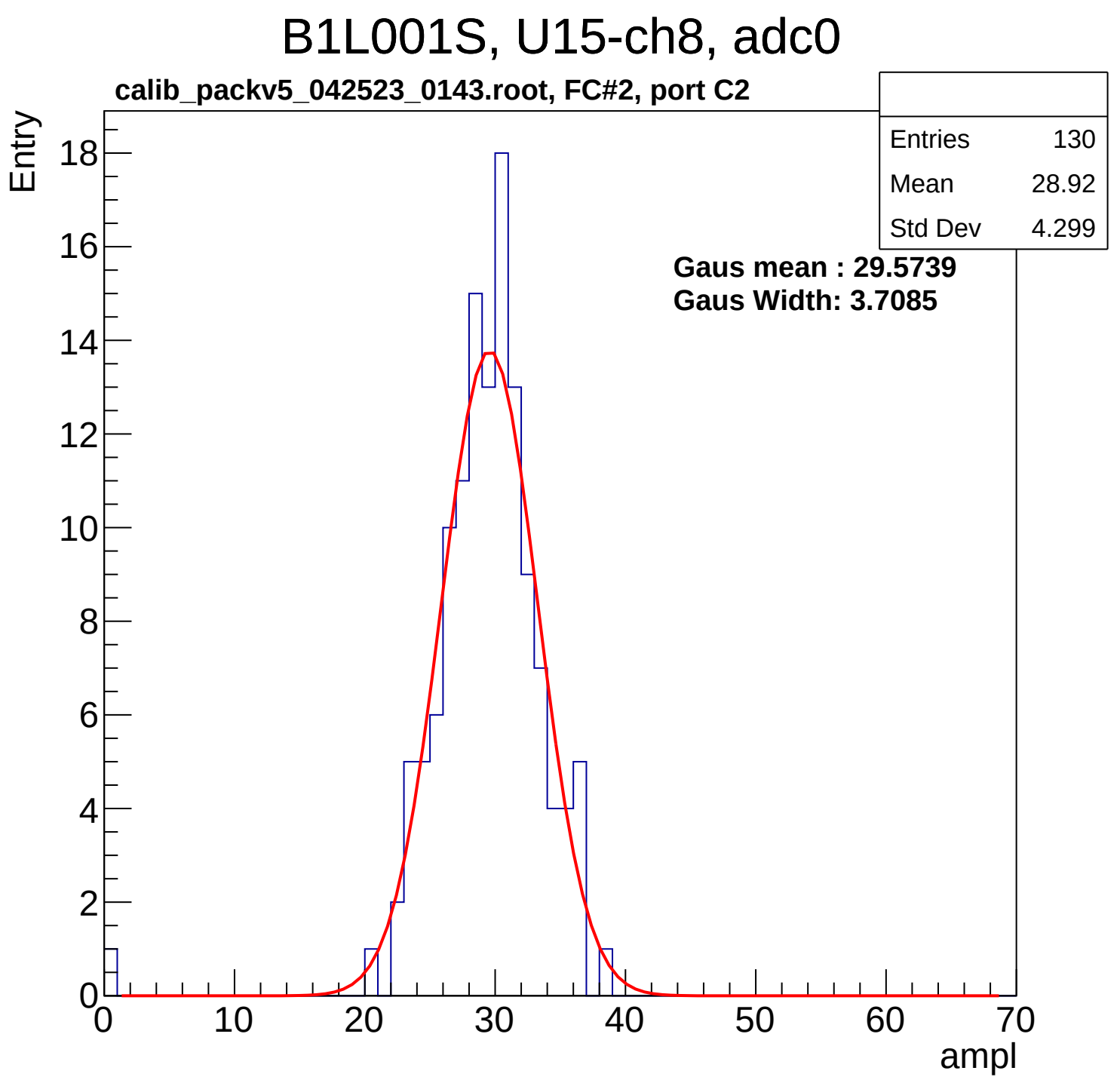
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch8, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 28.92
Std Dev 4.299
Gaus mean : 29.5739
Gaus Width: 3.7085
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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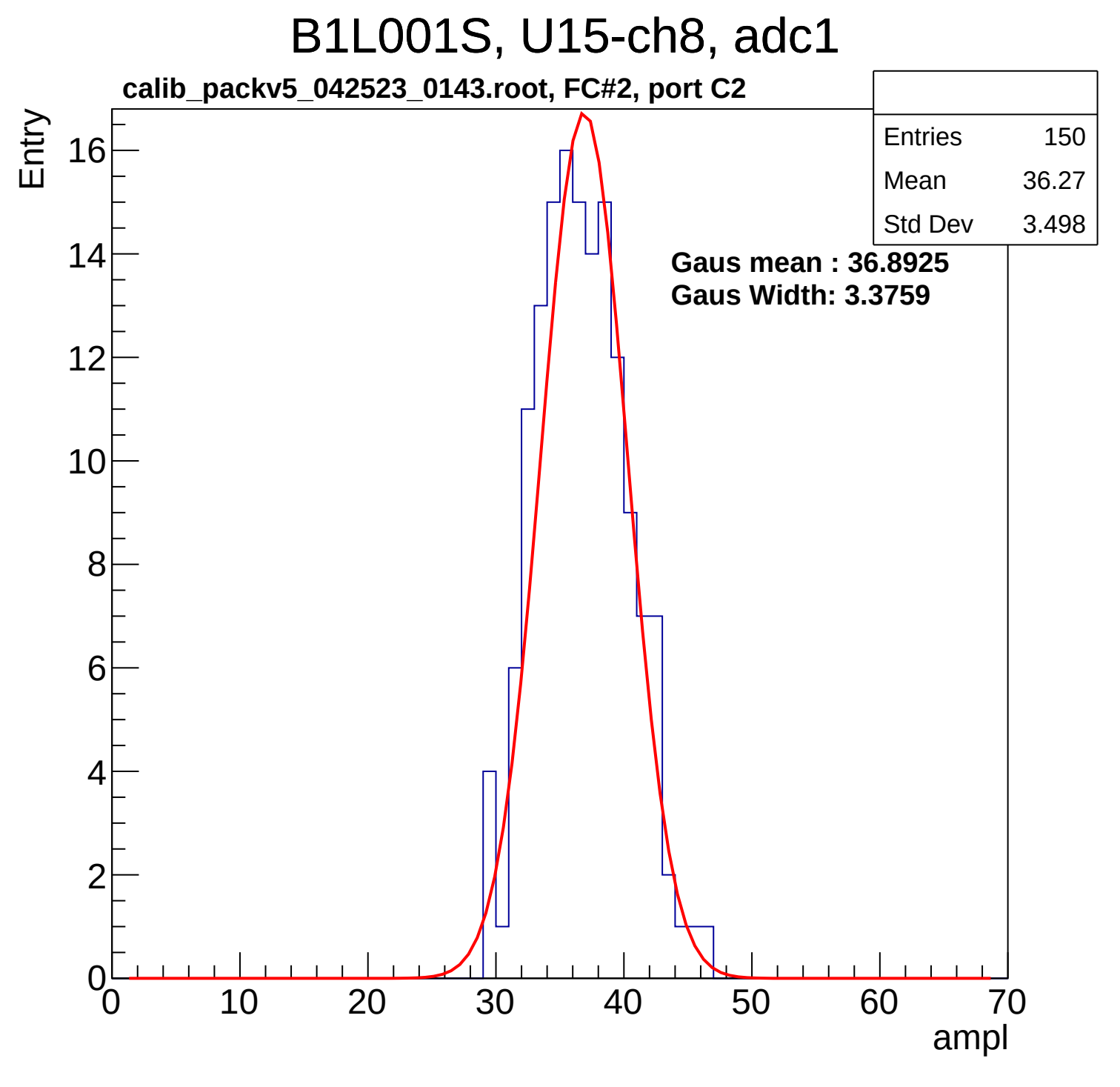
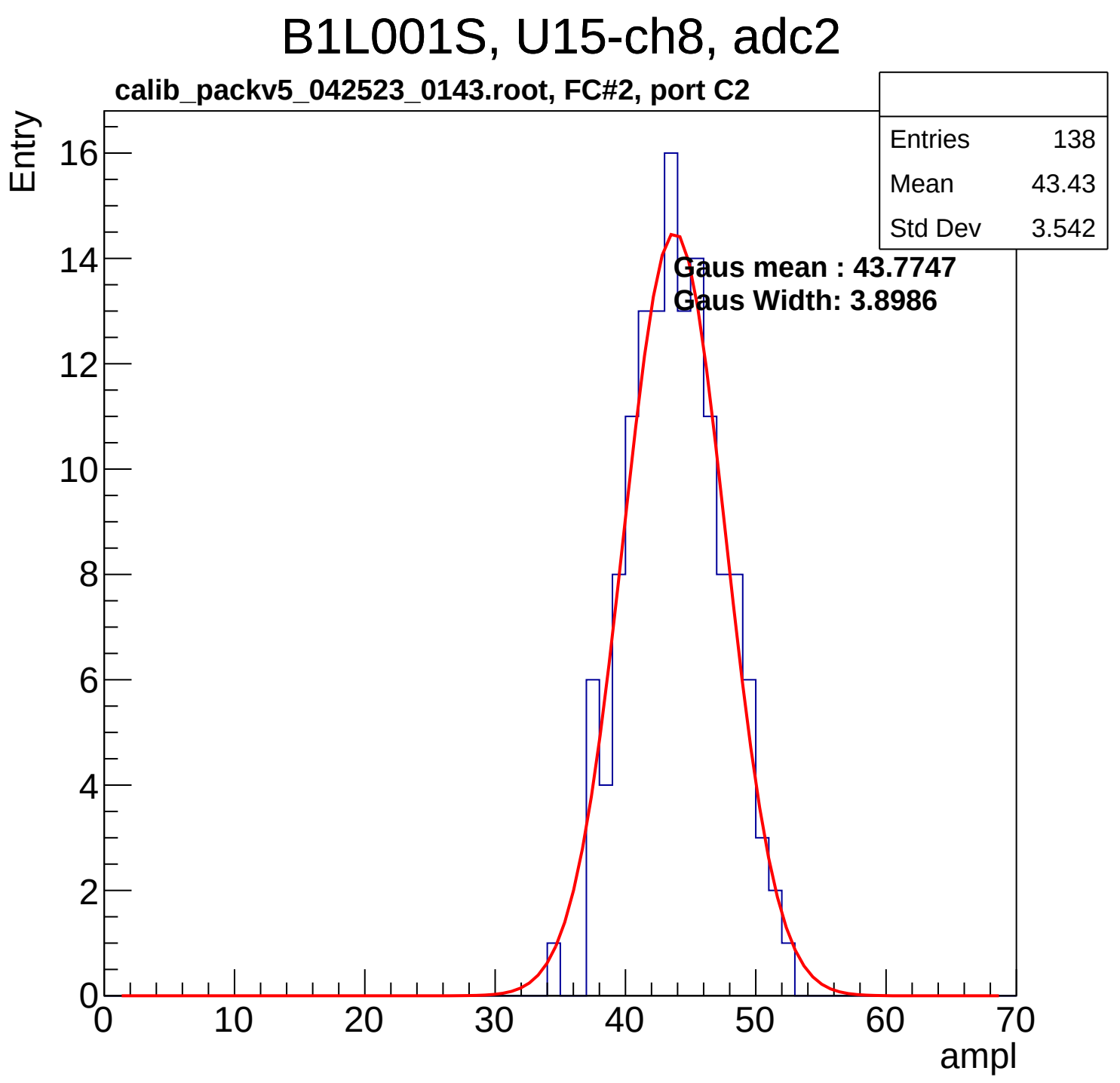

B1L001S, U15-ch8, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 36.27
Std Dev 3.498
Gaus mean : 36.8925
Gaus Width: 3.3759
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch8, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.43
Std Dev 3.542
Gaus mean : 43.7747
Gaus Width: 3.8986
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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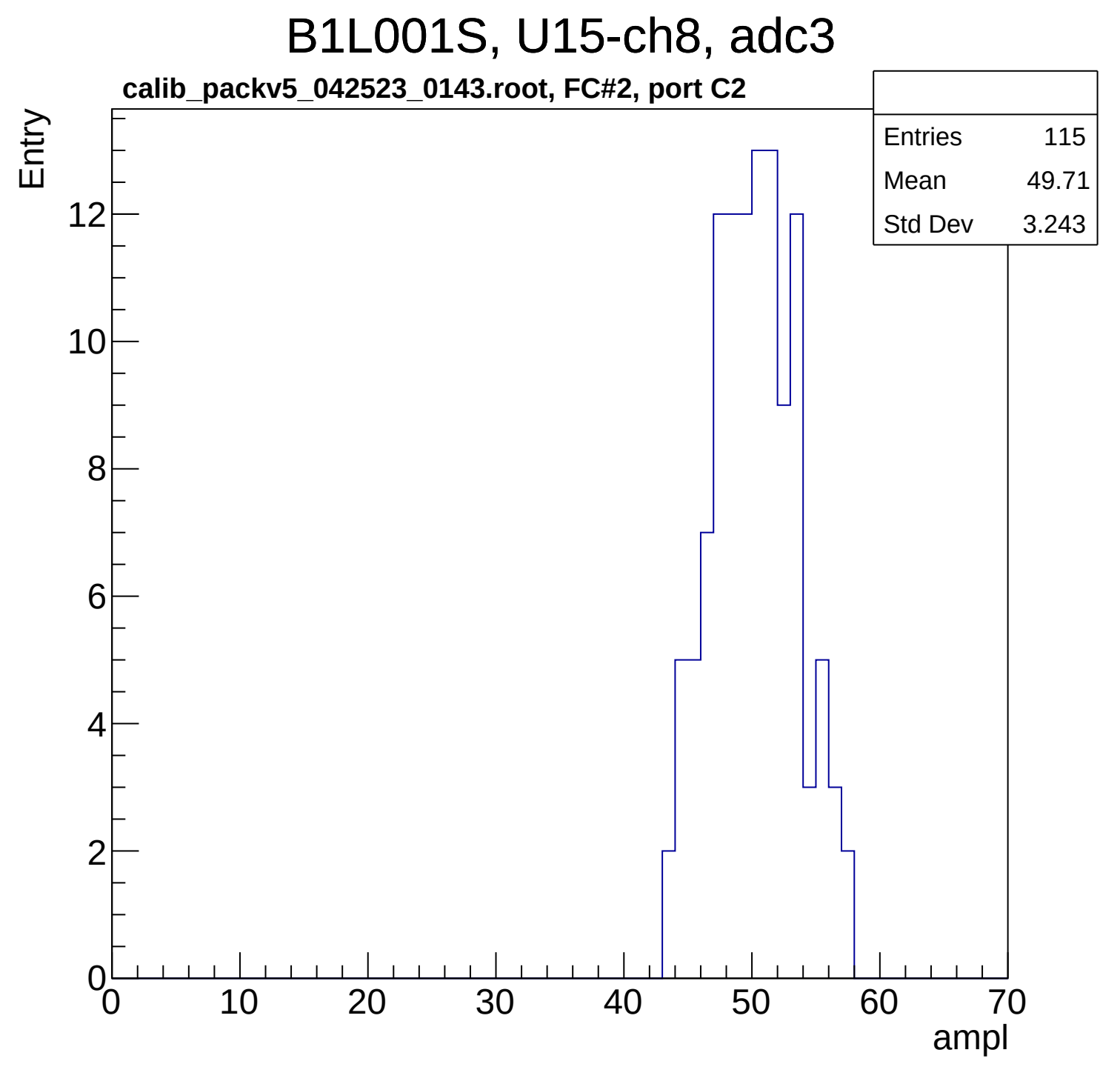
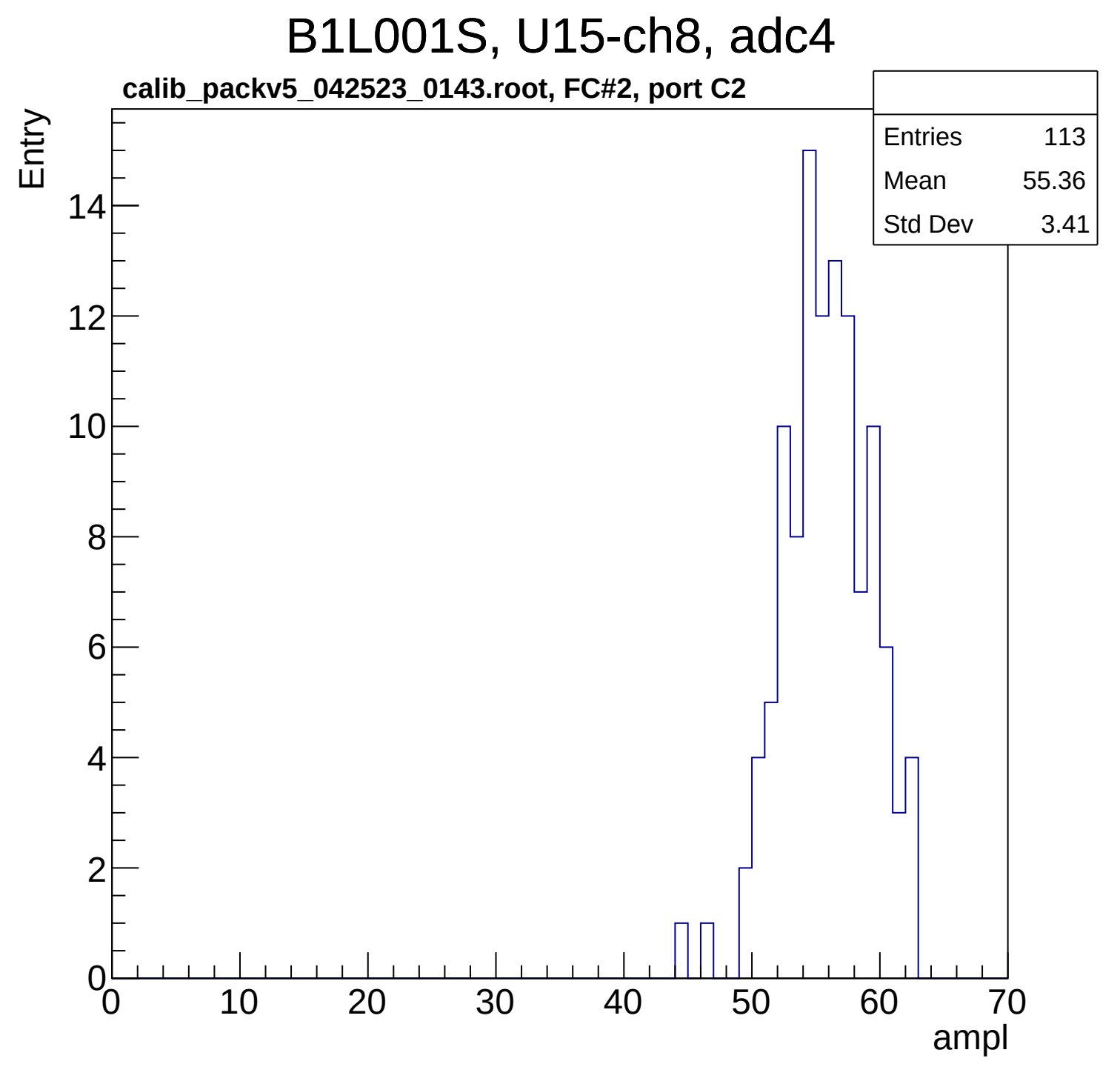
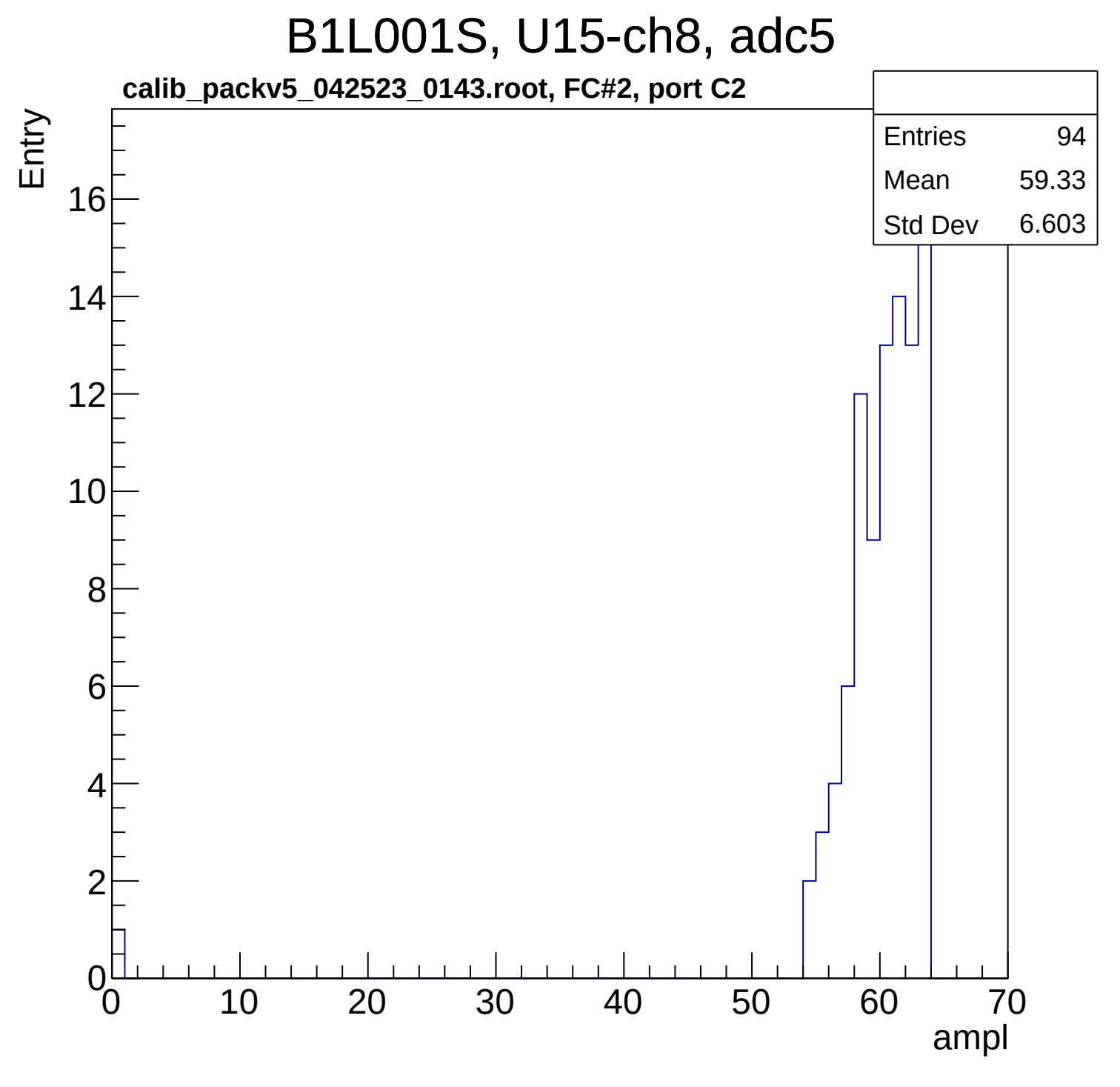

B1L001S, U15-ch8, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 49.71
Std Dev 3.243
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch8, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 55.36
Std Dev 3.41
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch8, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.33
Std Dev 6.603
Entry
ampl
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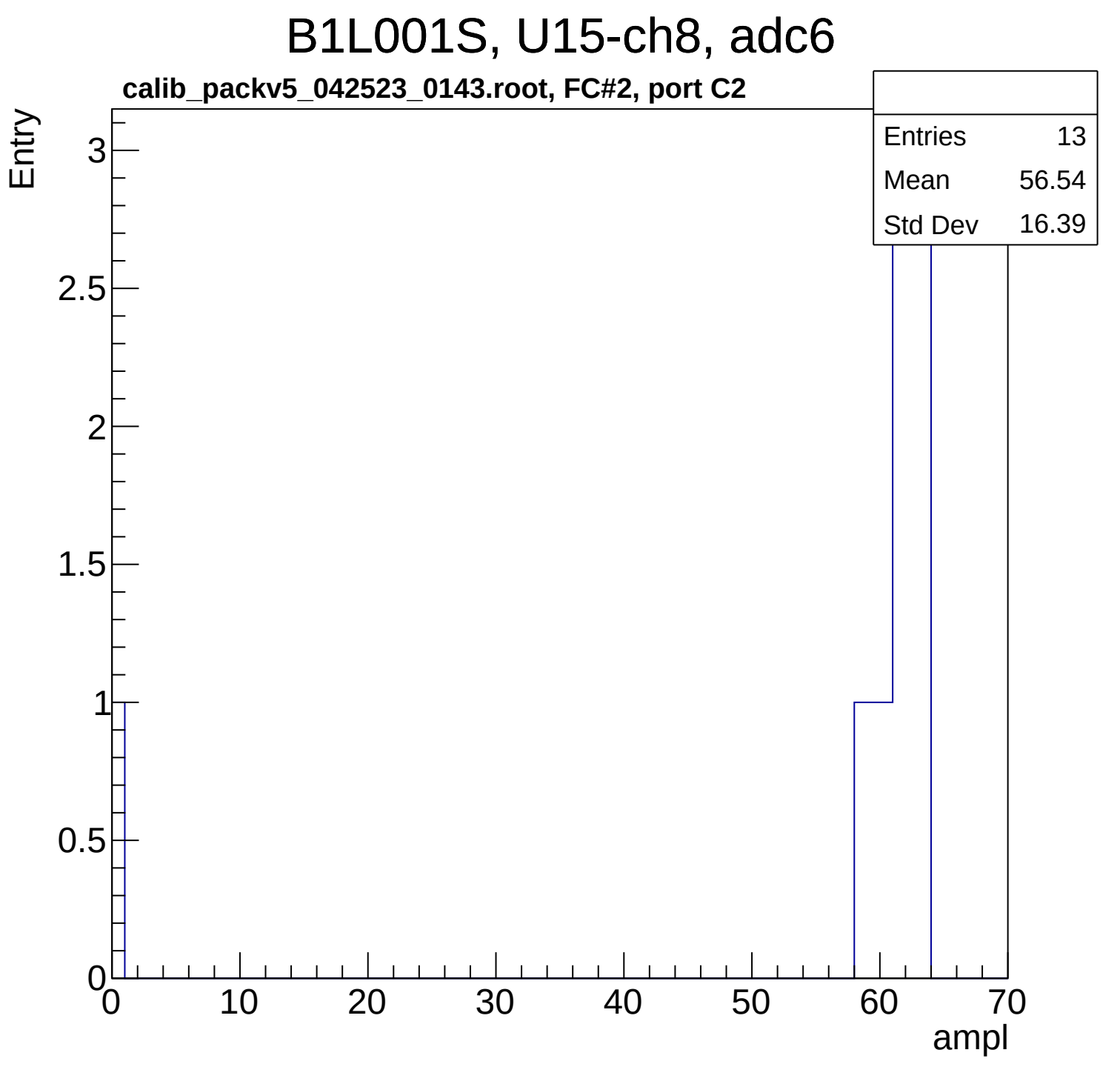
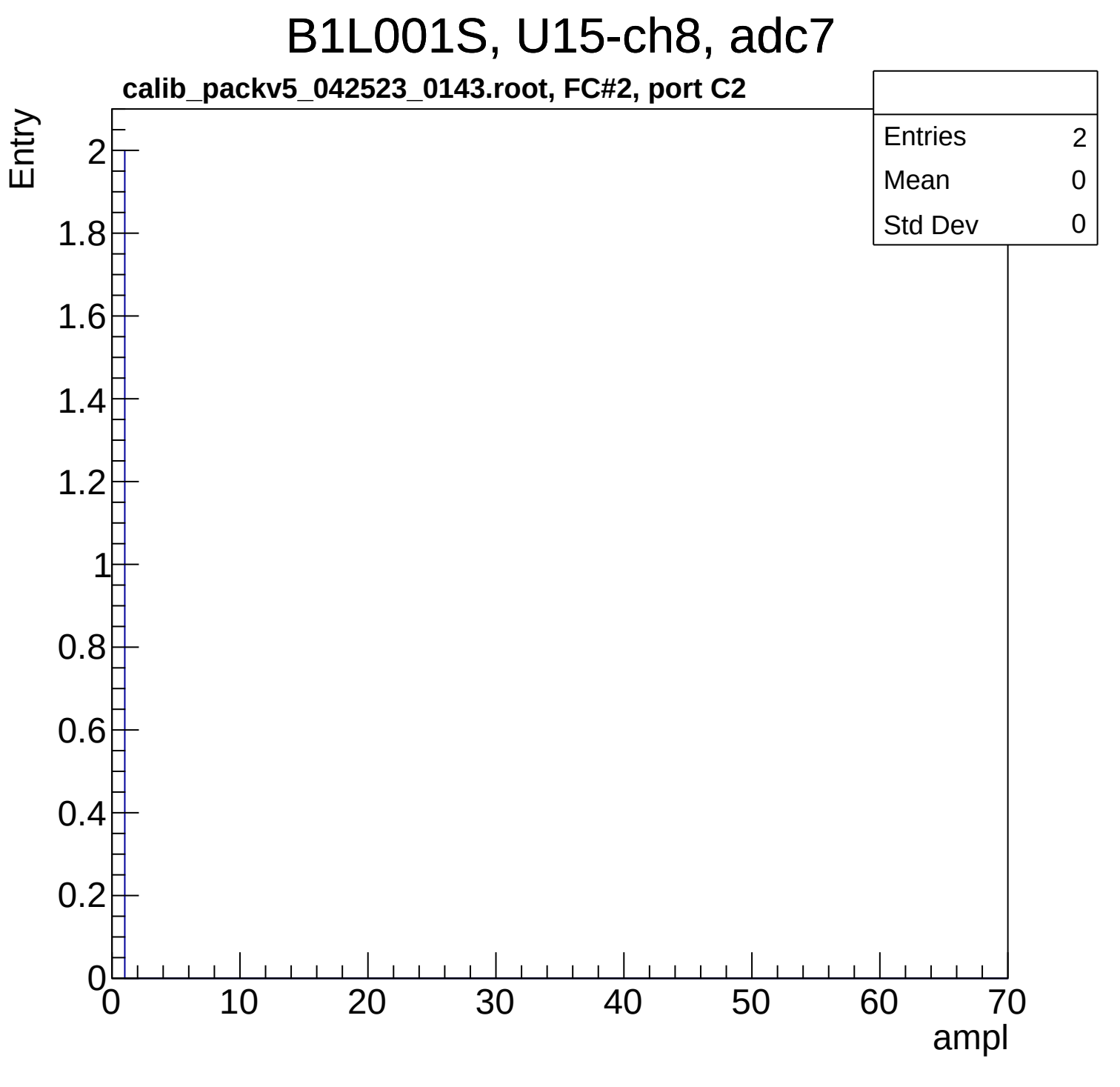
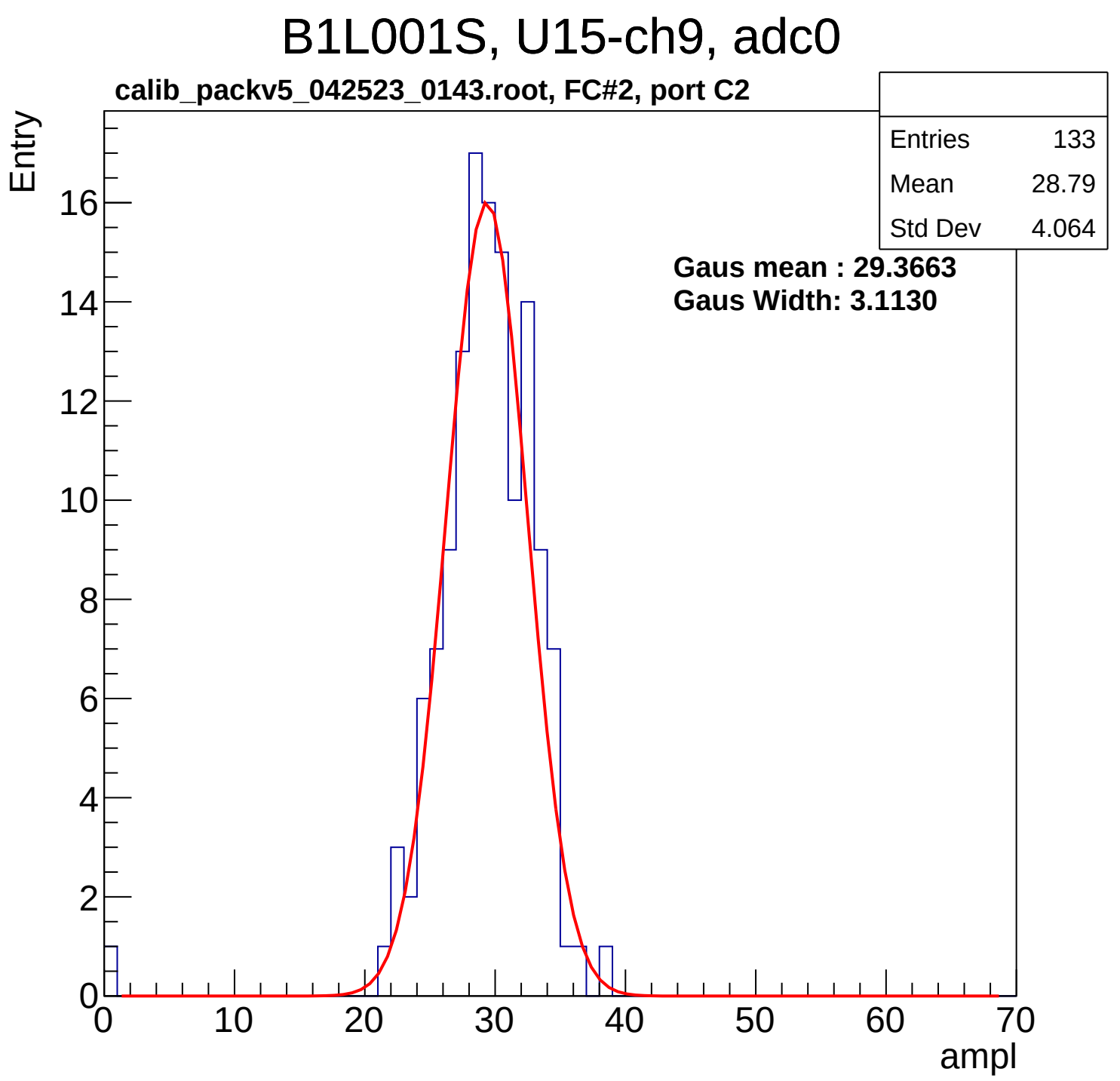
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch8, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 56.54
Std Dev 16.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch8, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch9, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 28.79
Std Dev 4.064
Gaus mean : 29.3663
Gaus Width: 3.1130
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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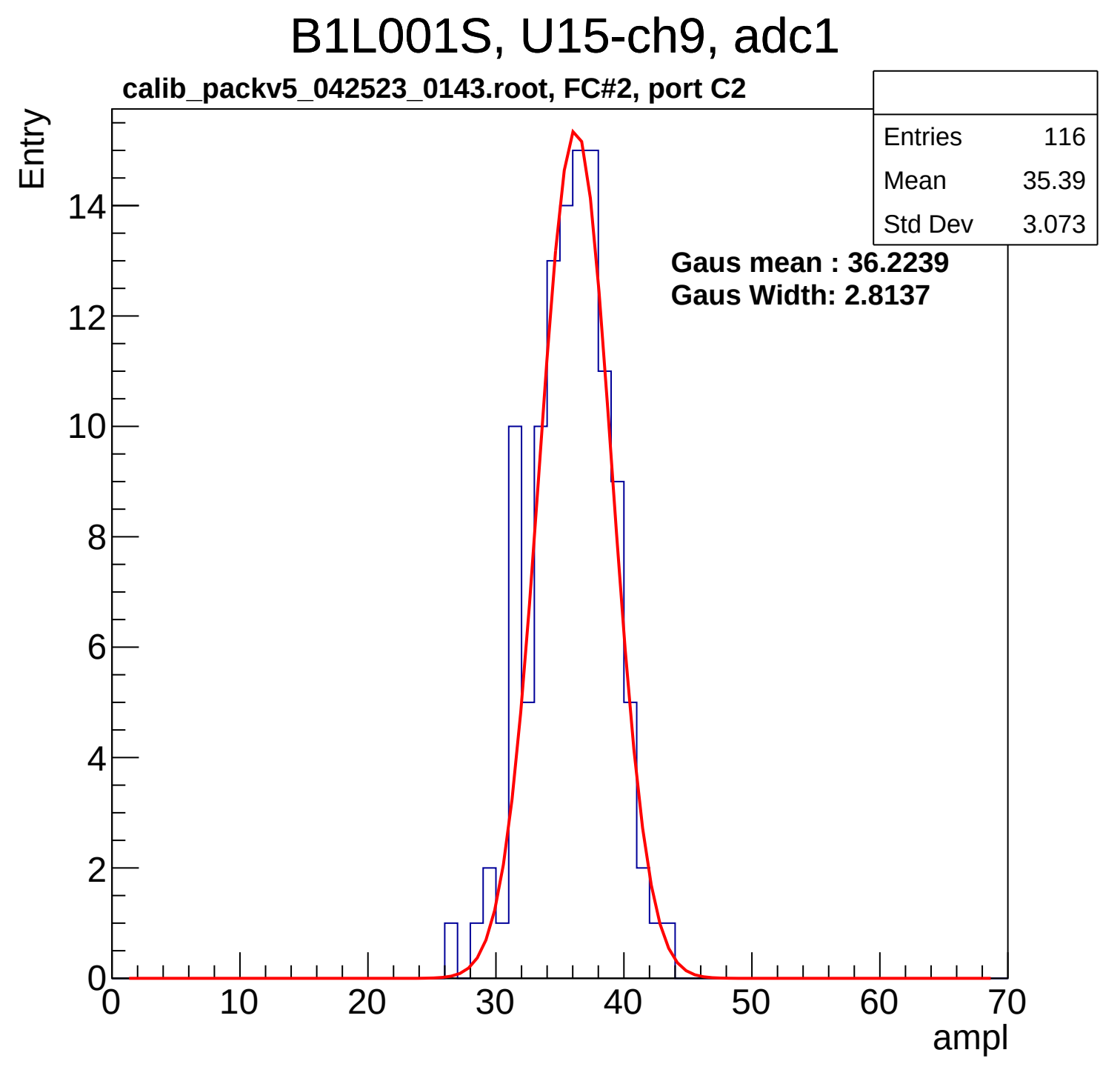
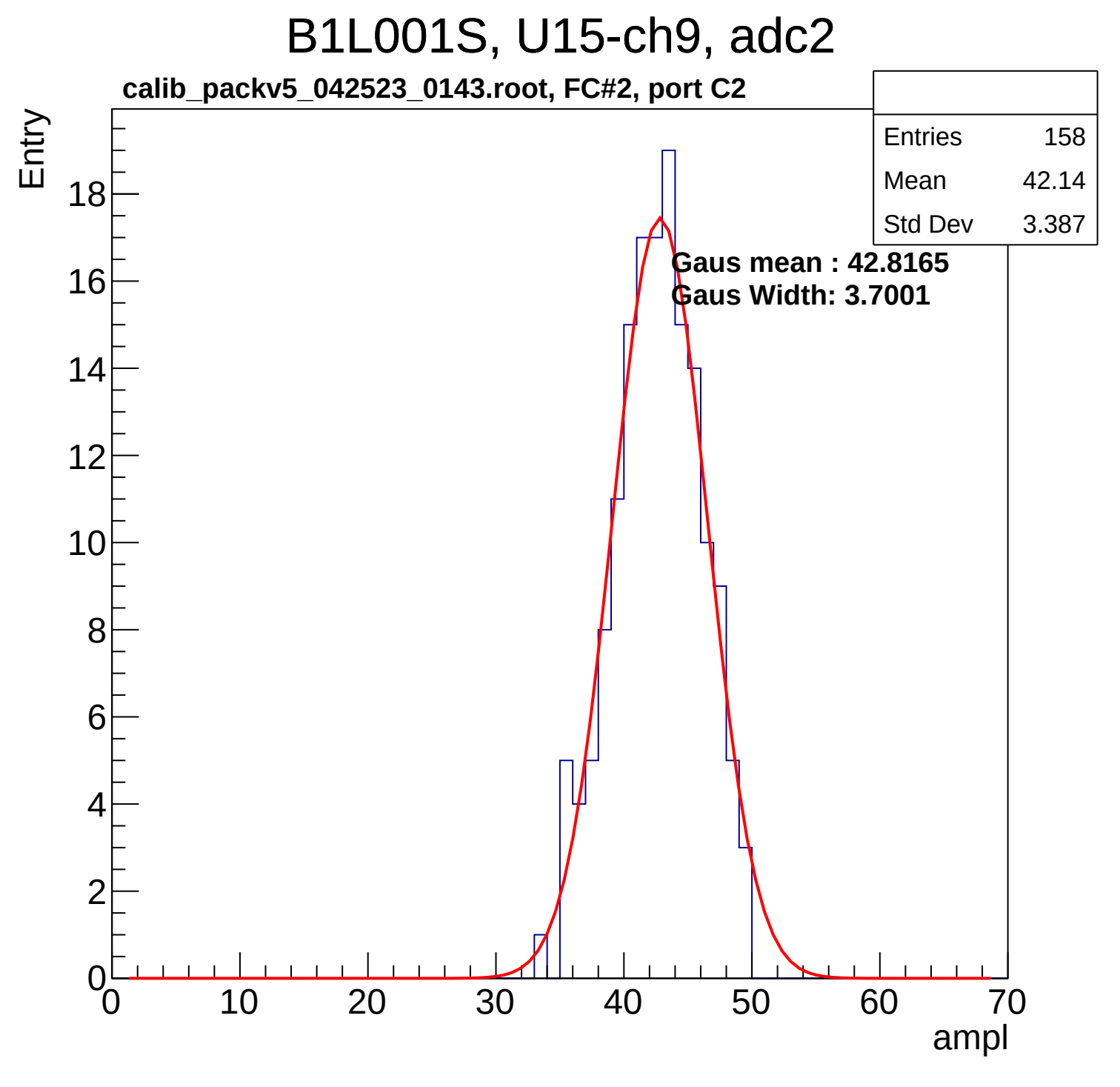
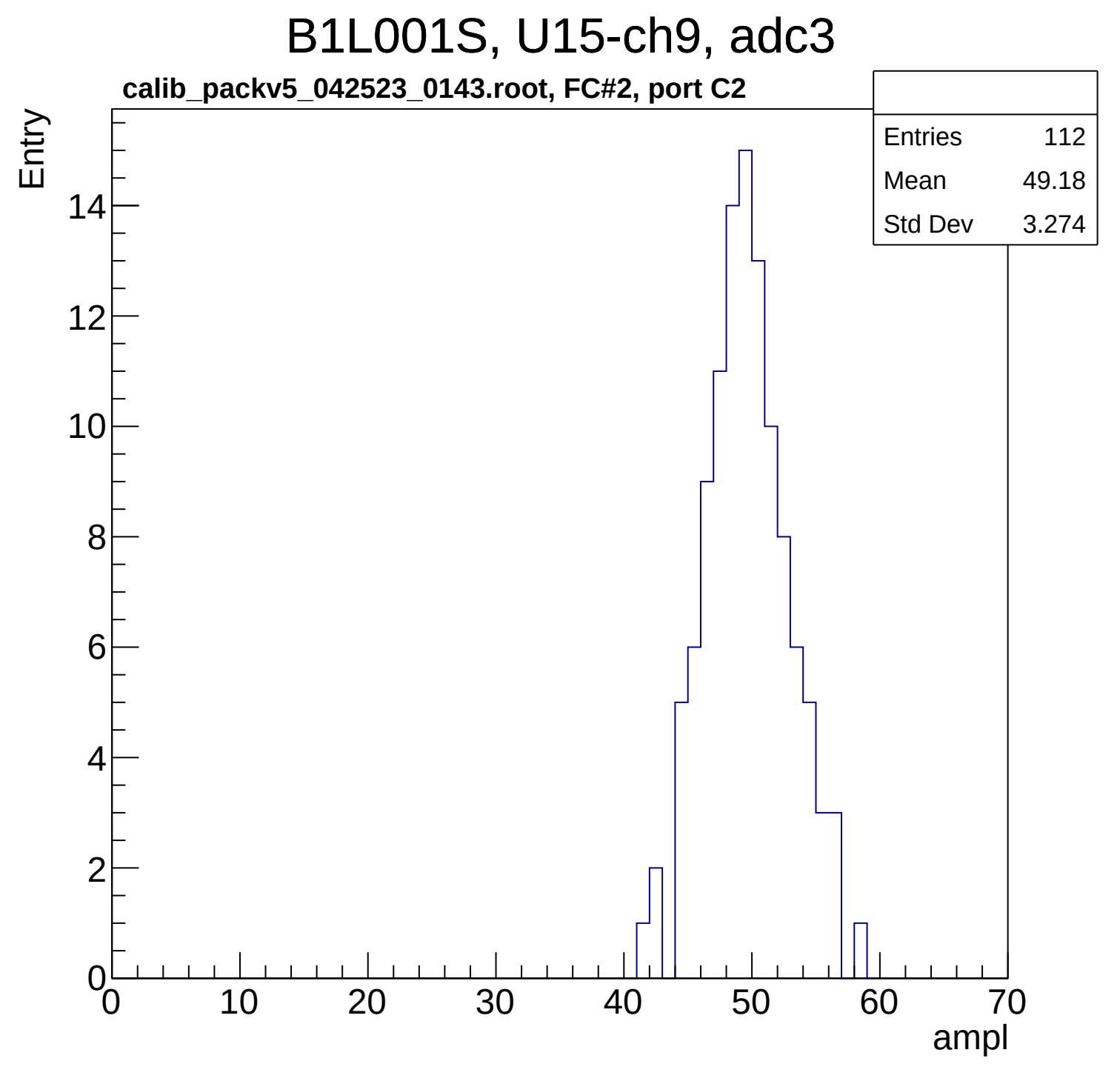

B1L001S, U15-ch9, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 35.39
Std Dev 3.073
Gaus mean : 36.2239
Gaus Width: 2.8137
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch9, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 42.14
Std Dev 3.387
Gaus mean : 42.8165
Gaus Width: 3.7001
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch9, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 49.18
Std Dev 3.274
Entry
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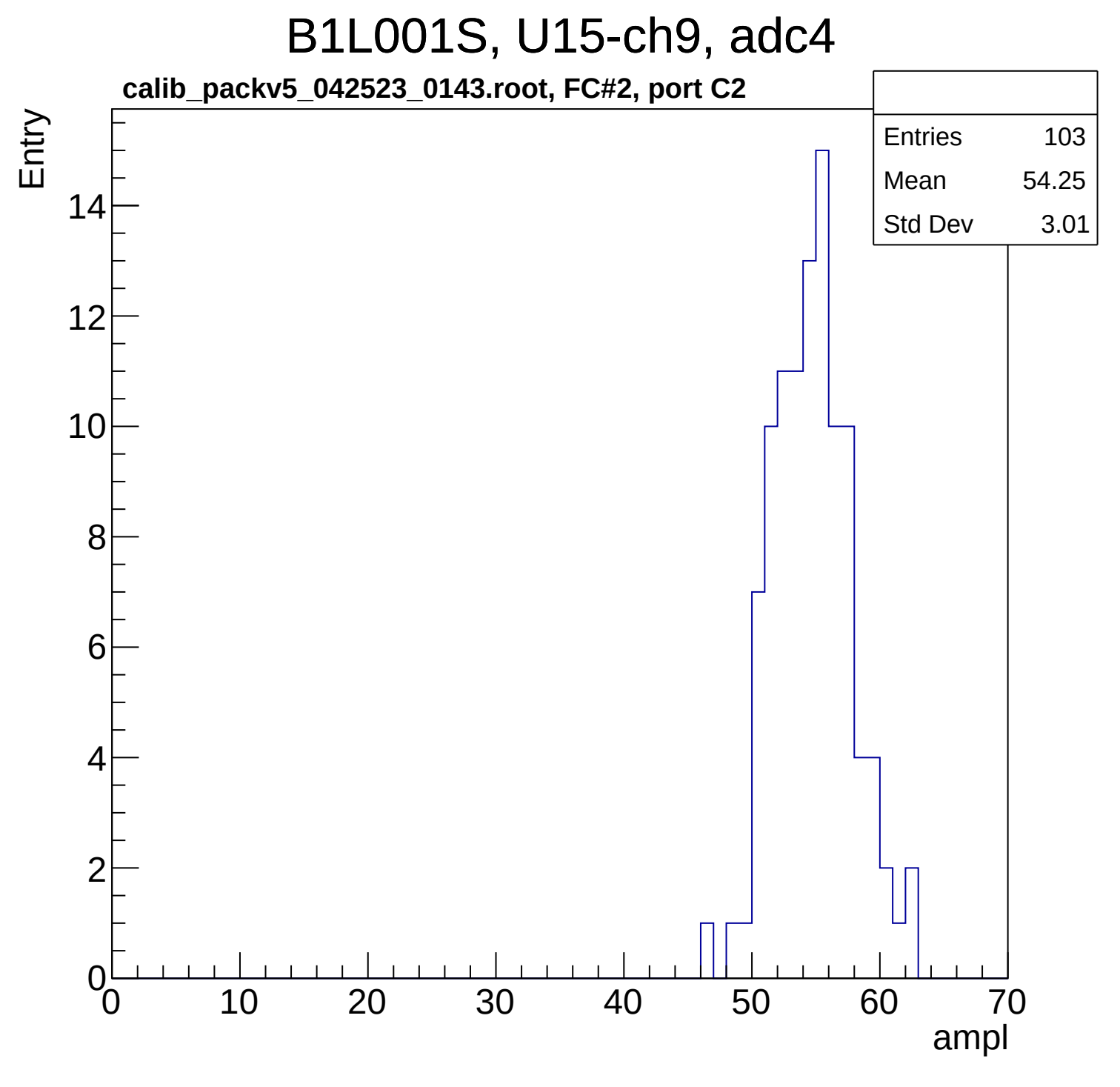
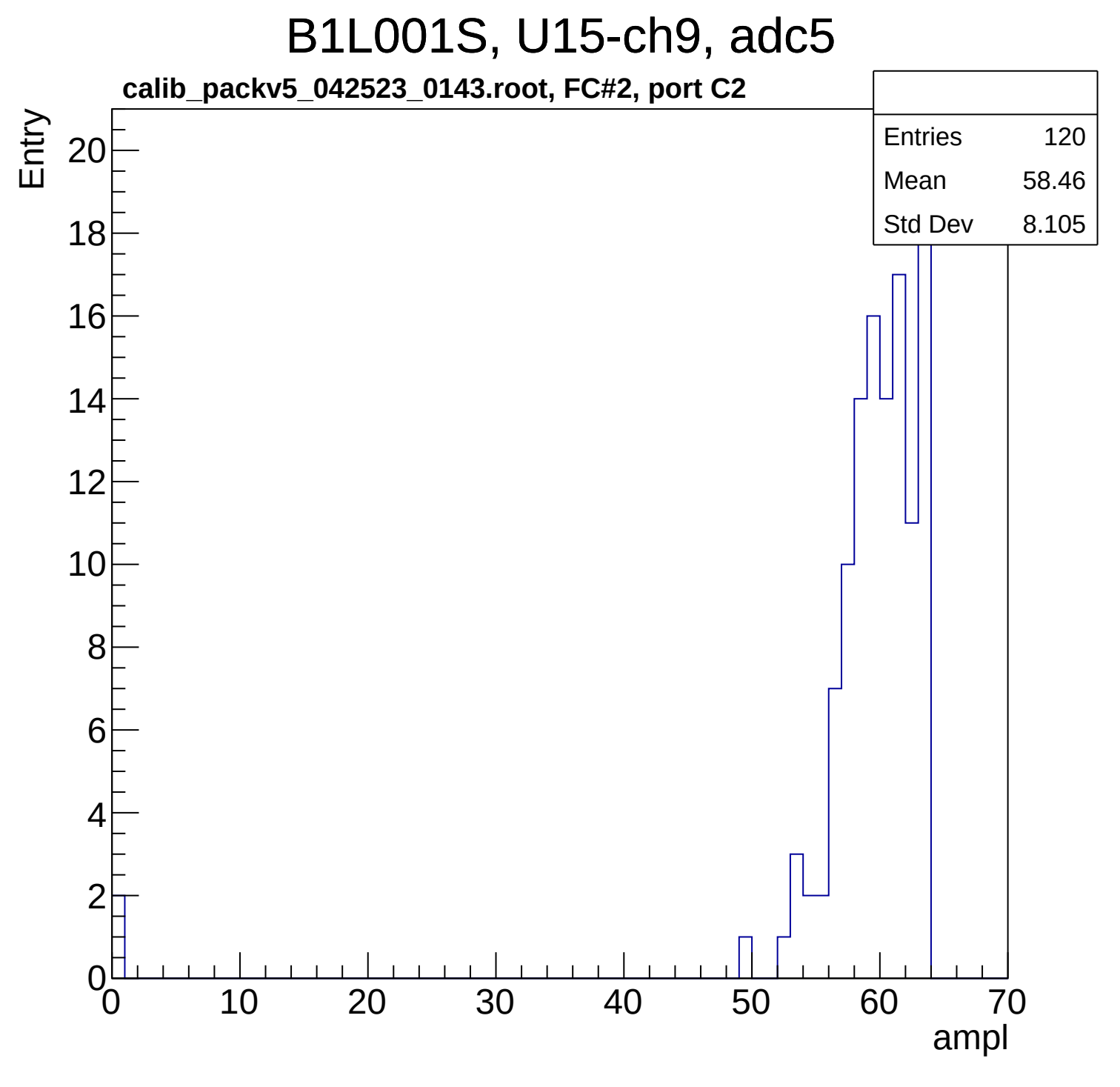
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch9, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 54.25
Std Dev 3.01
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch9, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 58.46
Std Dev 8.105
Entry
ampl
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6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
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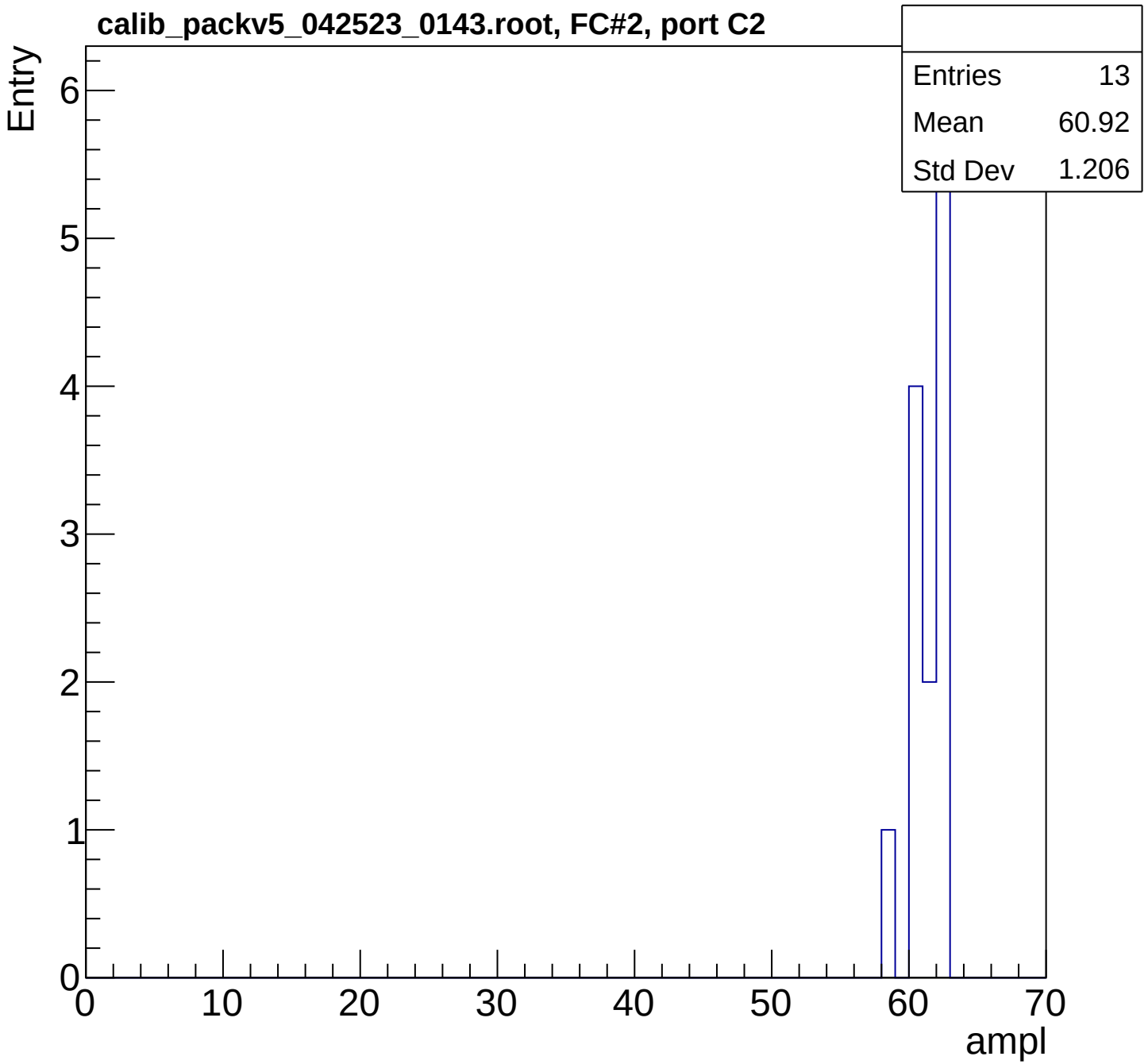
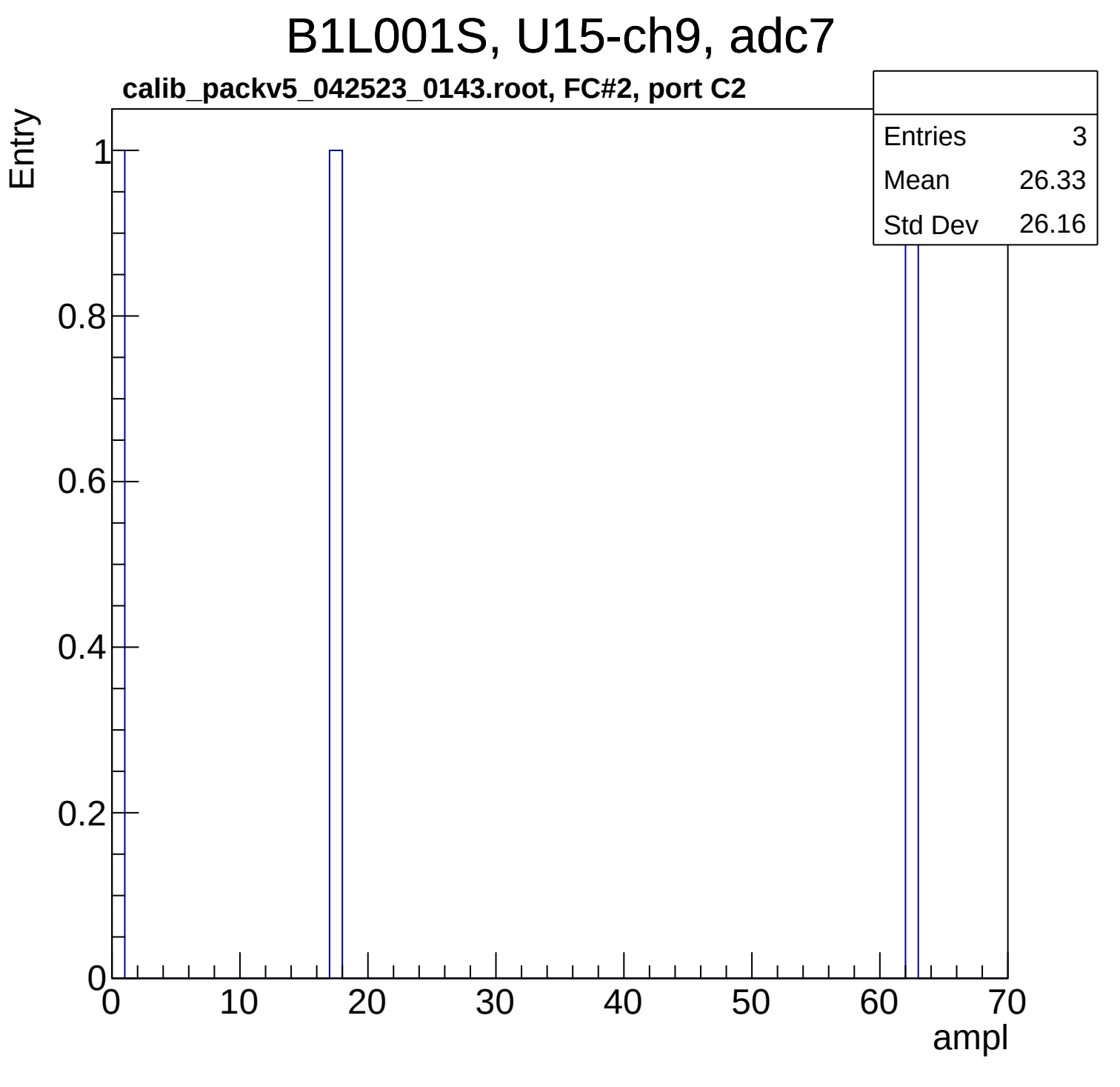

B1L001S, U15-ch9, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 60.92
Std Dev 1.206
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch9, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 26.33
Std Dev 26.16
Entry
ampl
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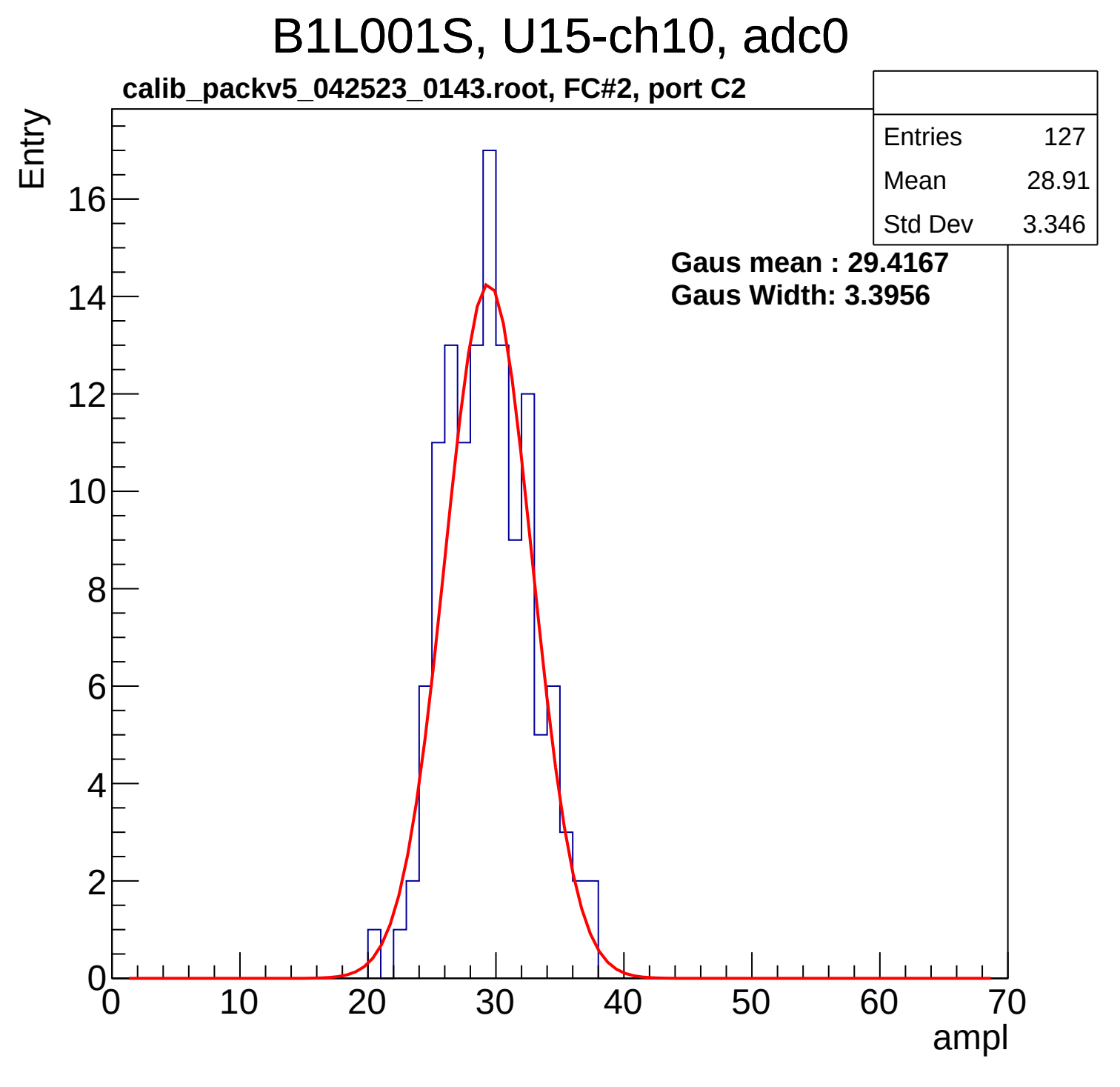
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U15-ch10, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 28.91
Std Dev 3.346
Gaus mean : 29.4167
Gaus Width: 3.3956
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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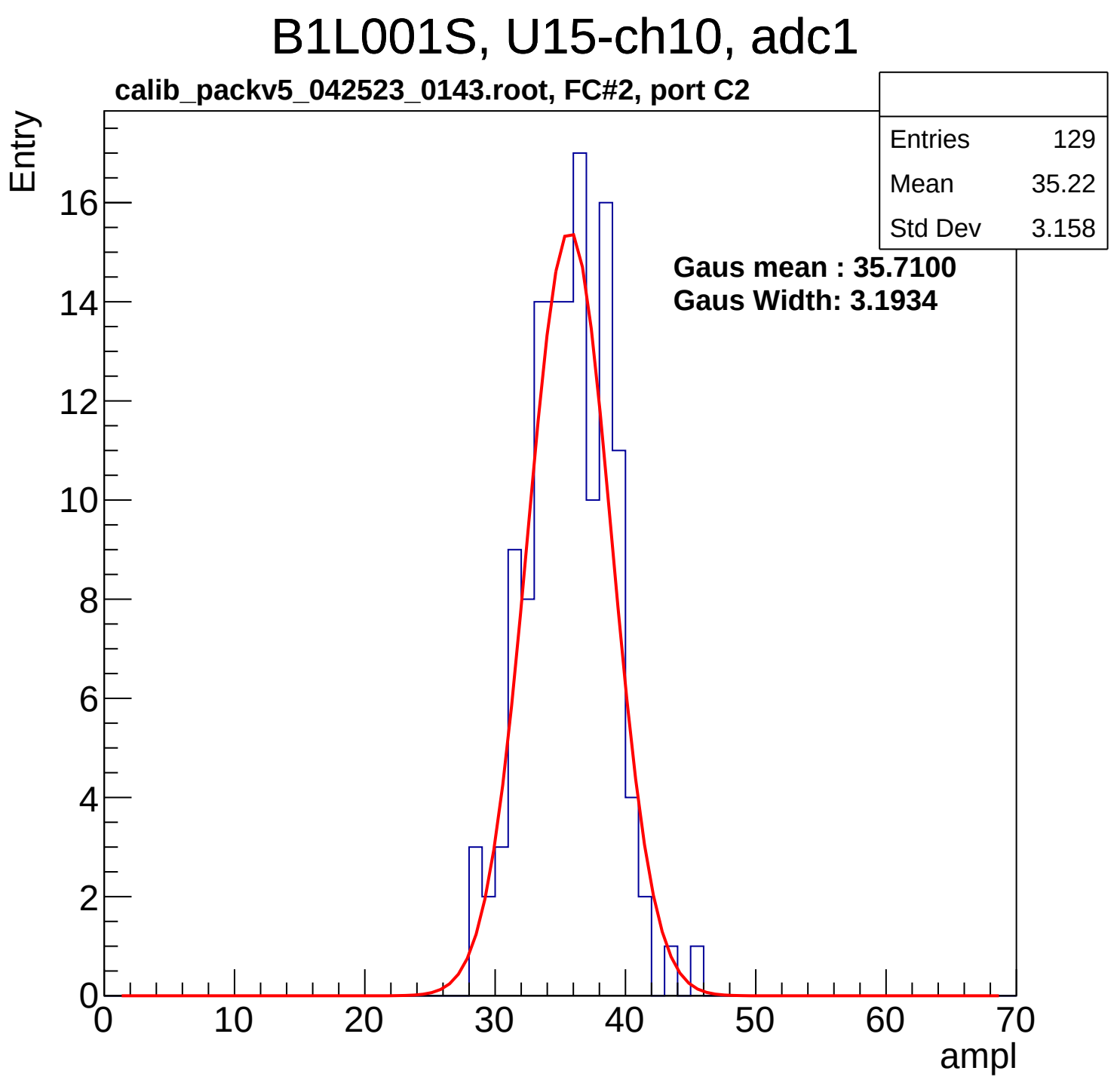
40
50
60
70


B1L001S, U15-ch10, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 35.22
Std Dev 3.158
Gaus mean : 35.7100
Gaus Width: 3.1934
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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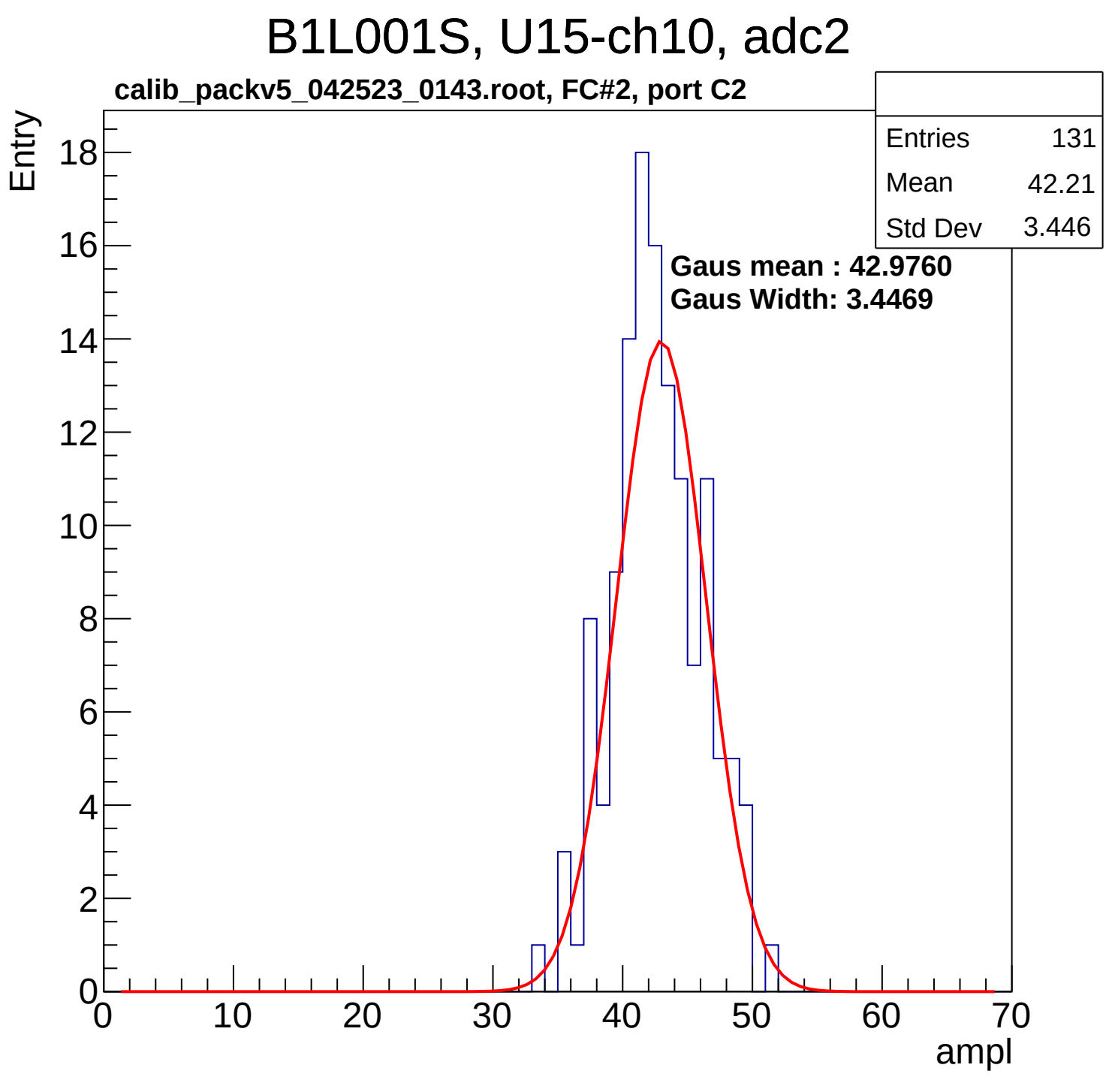
B1L001S, U15-ch10, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 42.21
Std Dev 3.446
Gaus mean : 42.9760
Gaus Width: 3.4469
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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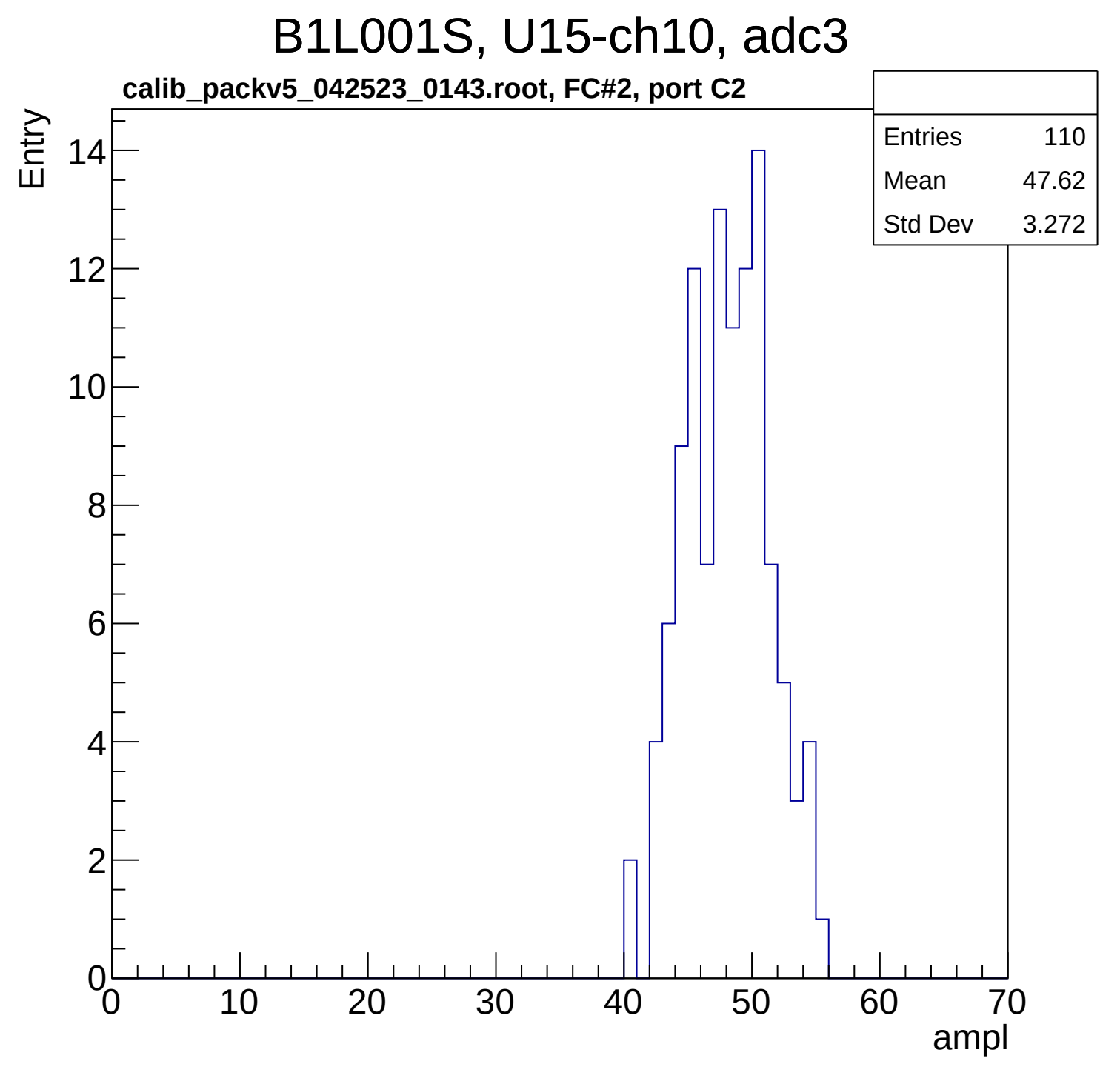
B1L001S, U15-ch10, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 47.62
Std Dev 3.272
Entry
ampl
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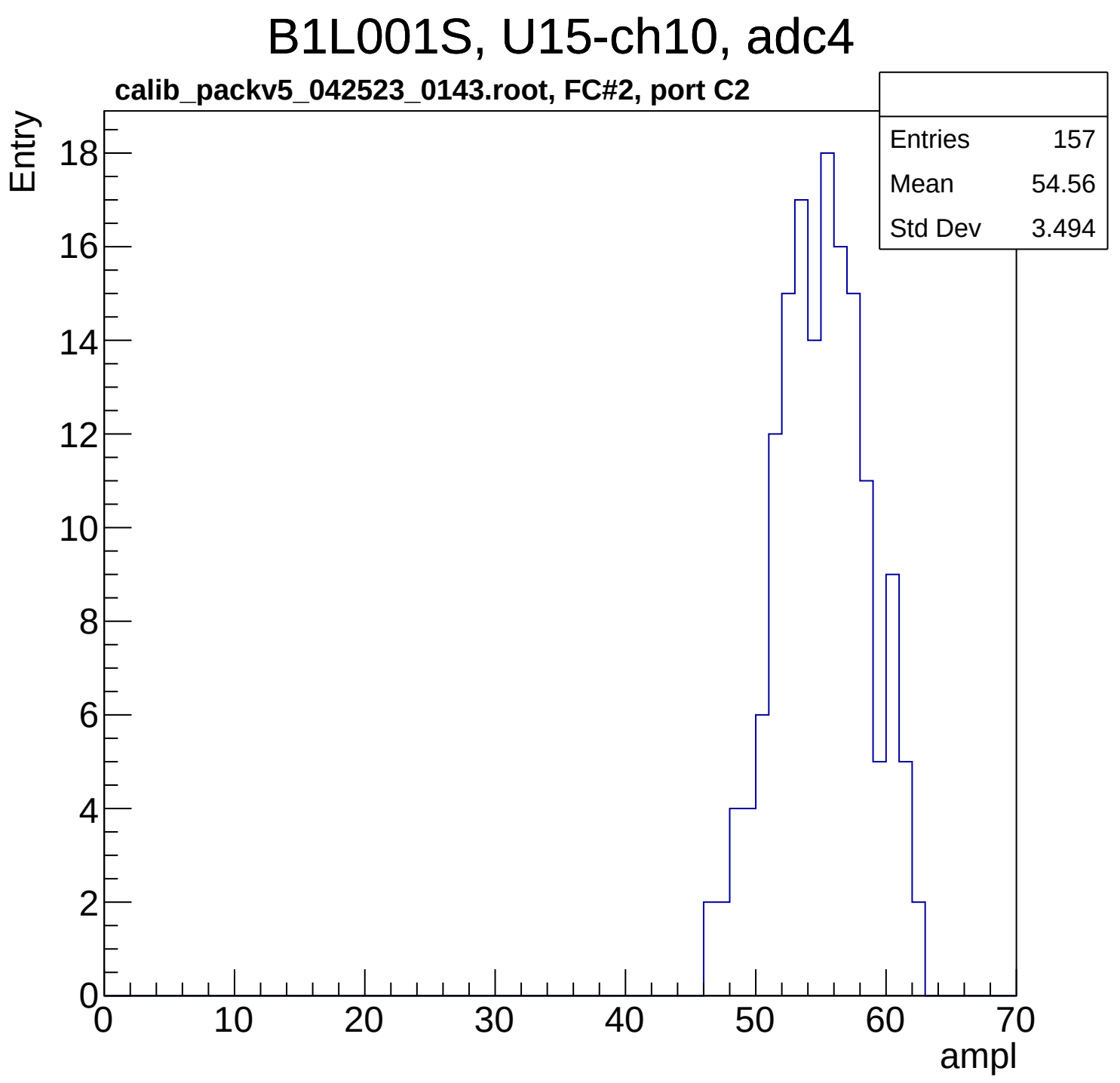
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch10, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 54.56
Std Dev 3.494
Entry
ampl
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6
8
10
12
14
16
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0
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40
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60
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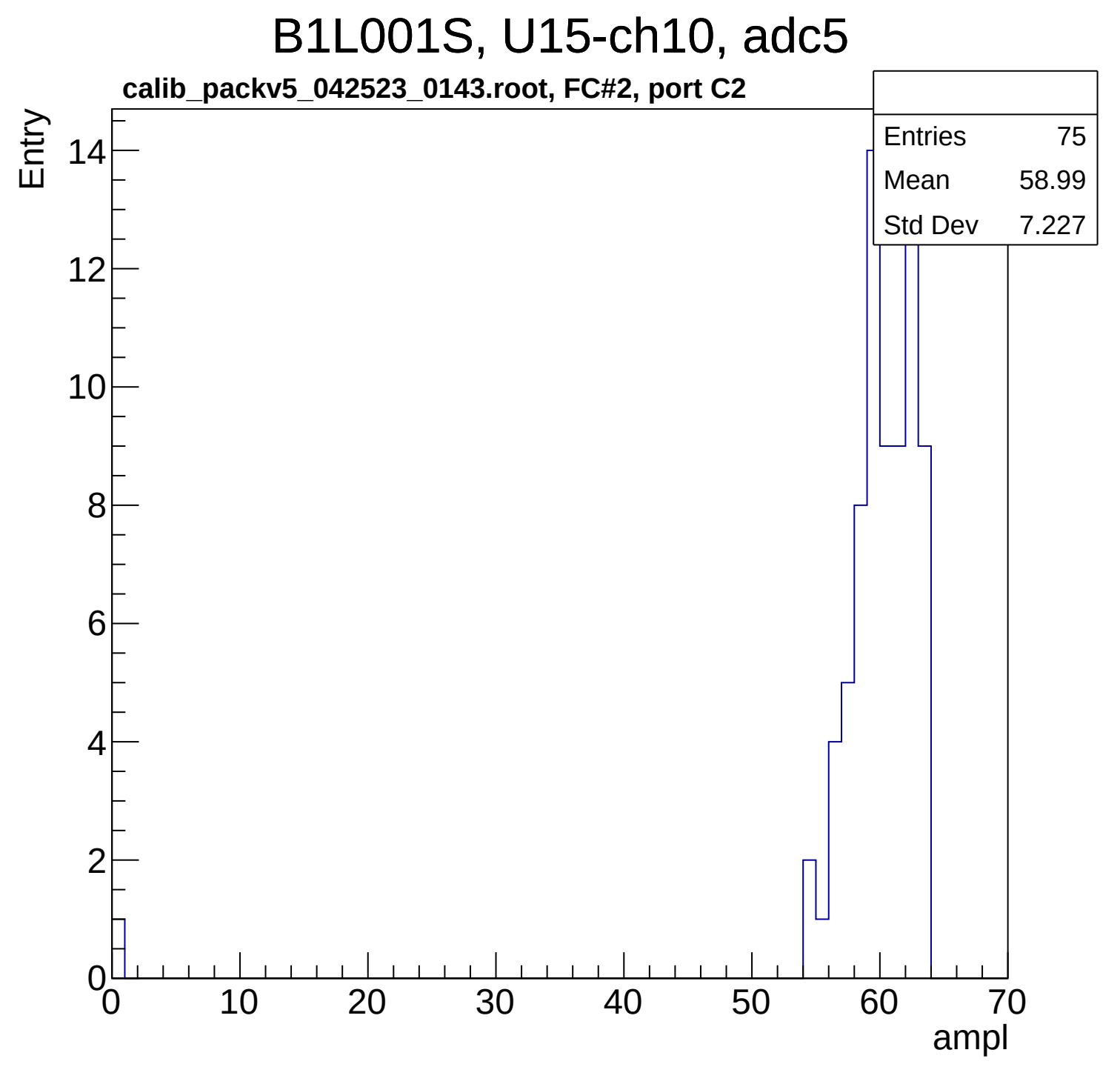
B1L001S, U15-ch10, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 58.99
Std Dev 7.227
Entry
ampl
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6
8
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60
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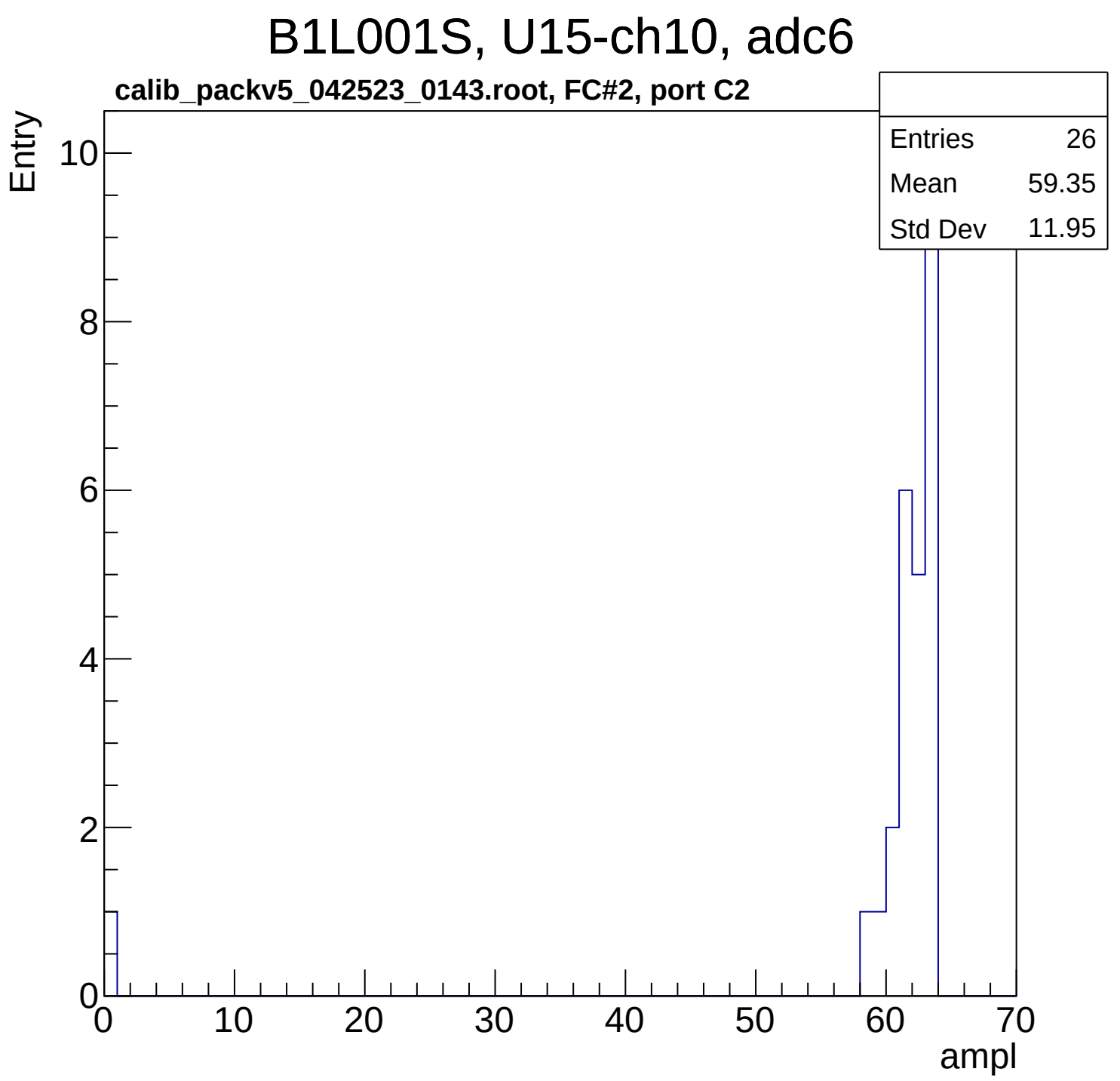
B1L001S, U15-ch10, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 59.35
Std Dev 11.95
Entry
0
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4
6
8
10
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10
20
30
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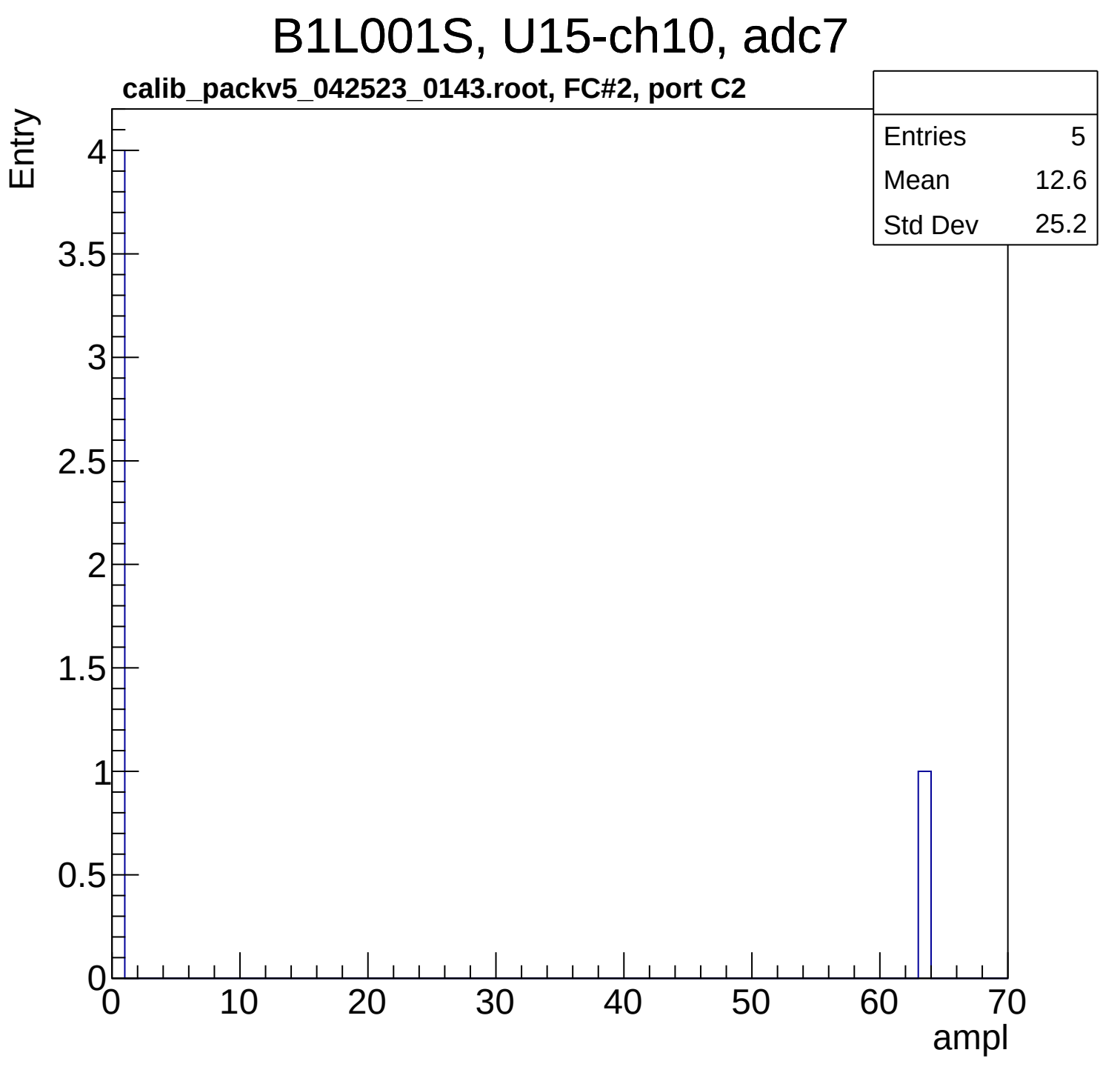
50
60
70
ampl


B1L001S, U15-ch10, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 12.6
Std Dev 25.2
Entry
ampl
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0.5
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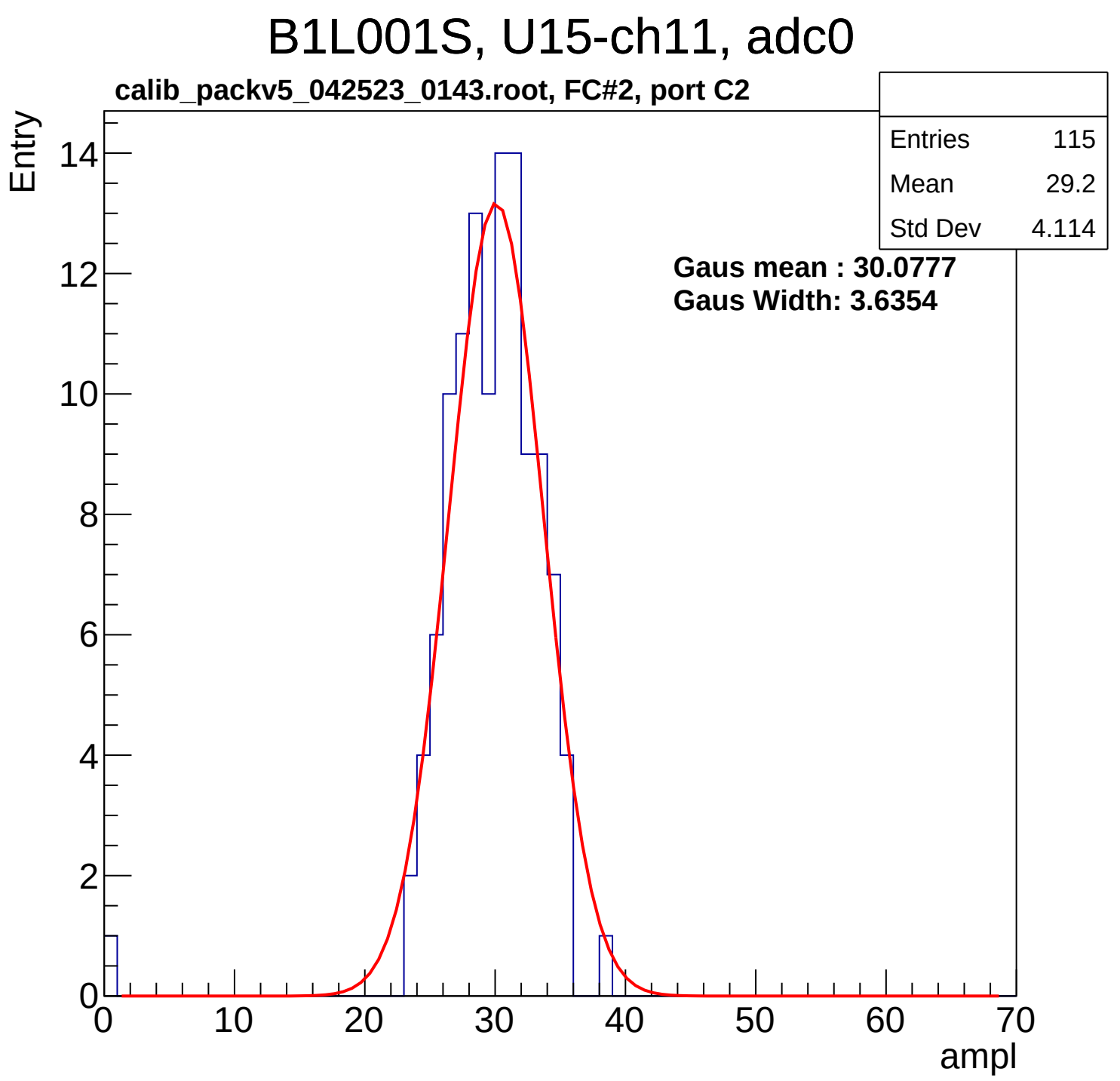
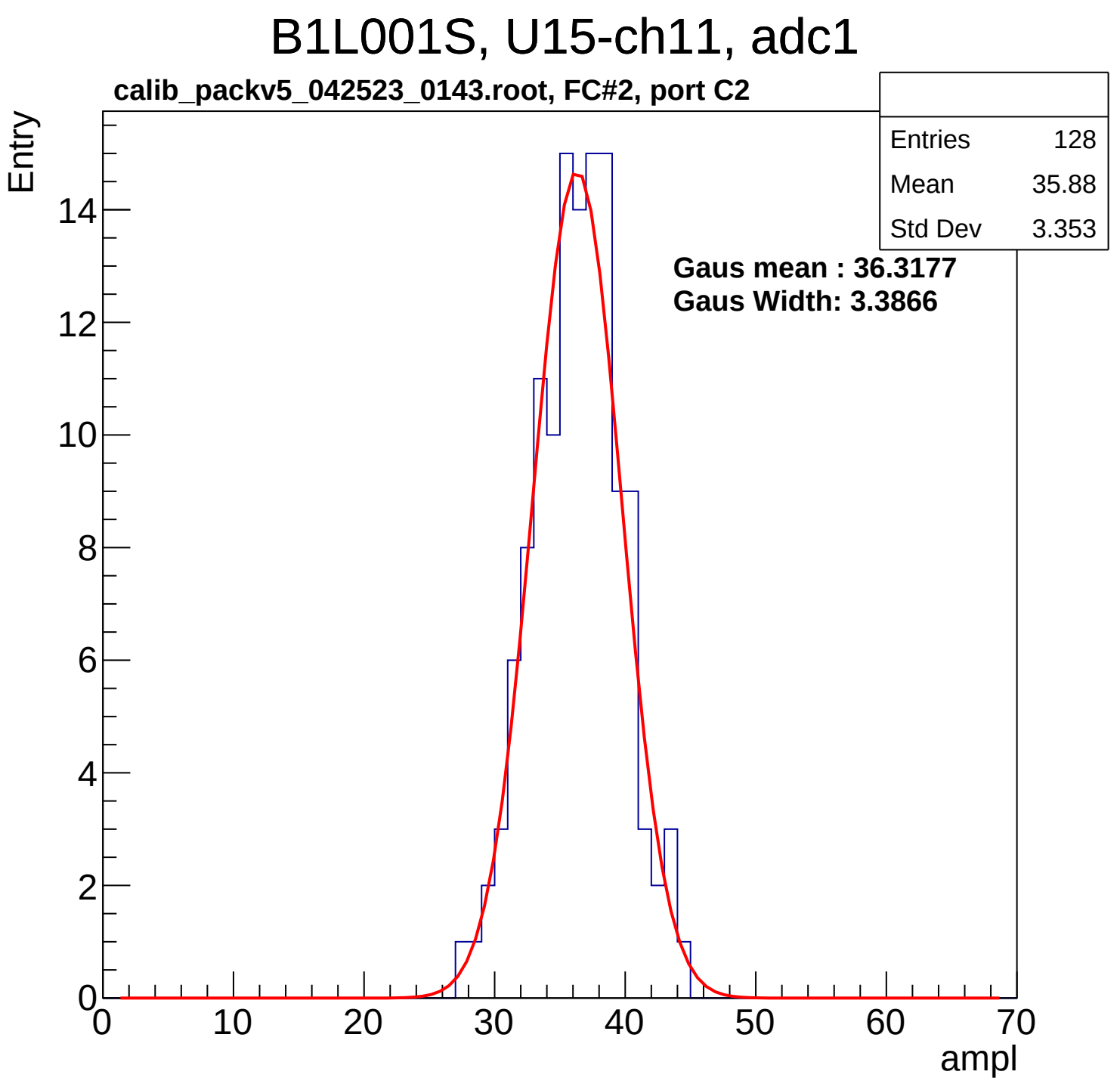
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch11, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 29.2
Std Dev 4.114
Gaus mean : 30.0777
Gaus Width: 3.6354
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch11, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 35.88
Std Dev 3.353
Gaus mean : 36.3177
Gaus Width: 3.3866
Entry
ampl
0
2
4
6
8
10
12
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20
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60
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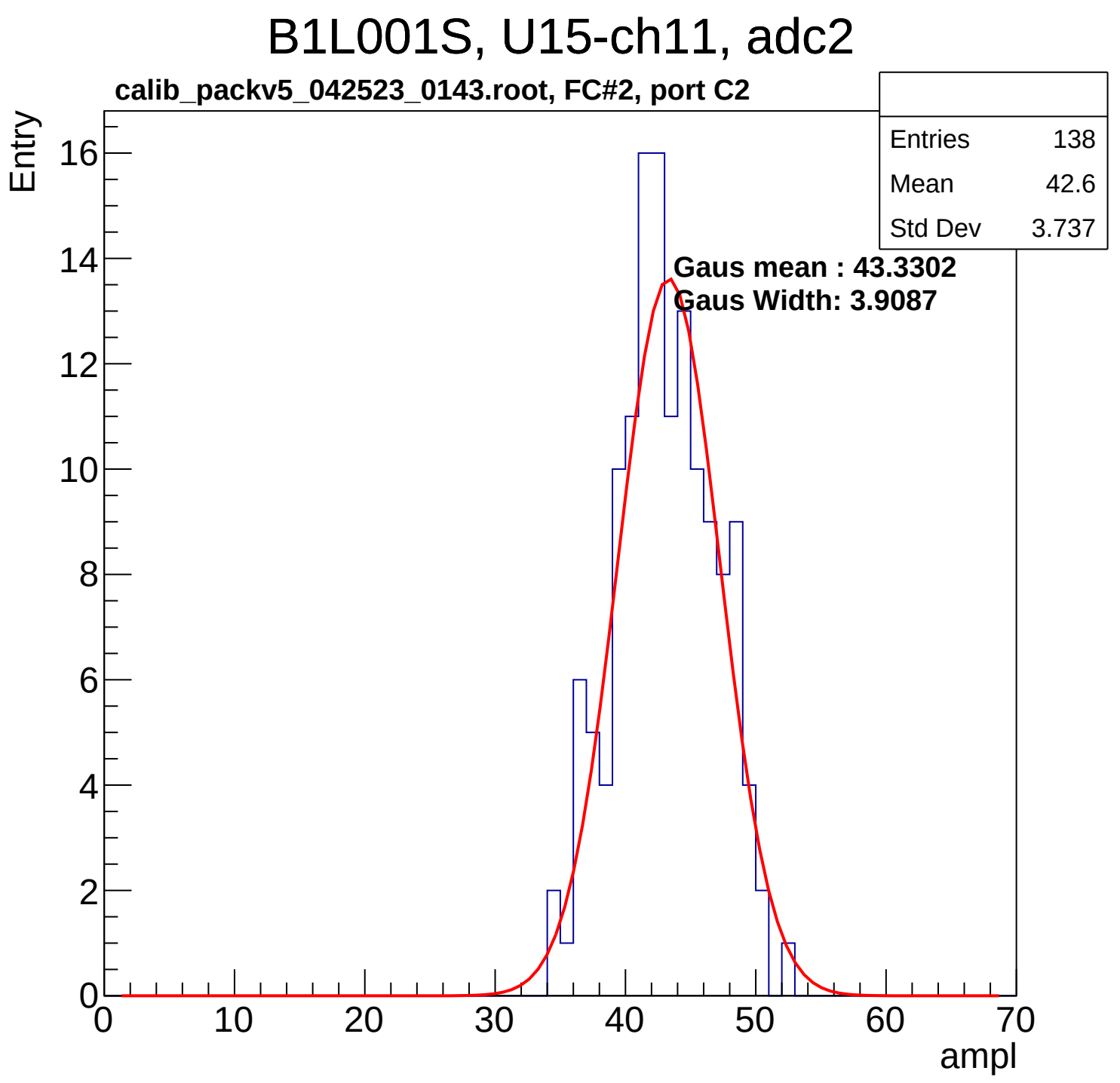
B1L001S, U15-ch11, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 42.6
Std Dev 3.737
Gaus mean : 43.3302
Gaus Width: 3.9087
Entry
ampl
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4
6
8
10
12
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16
0
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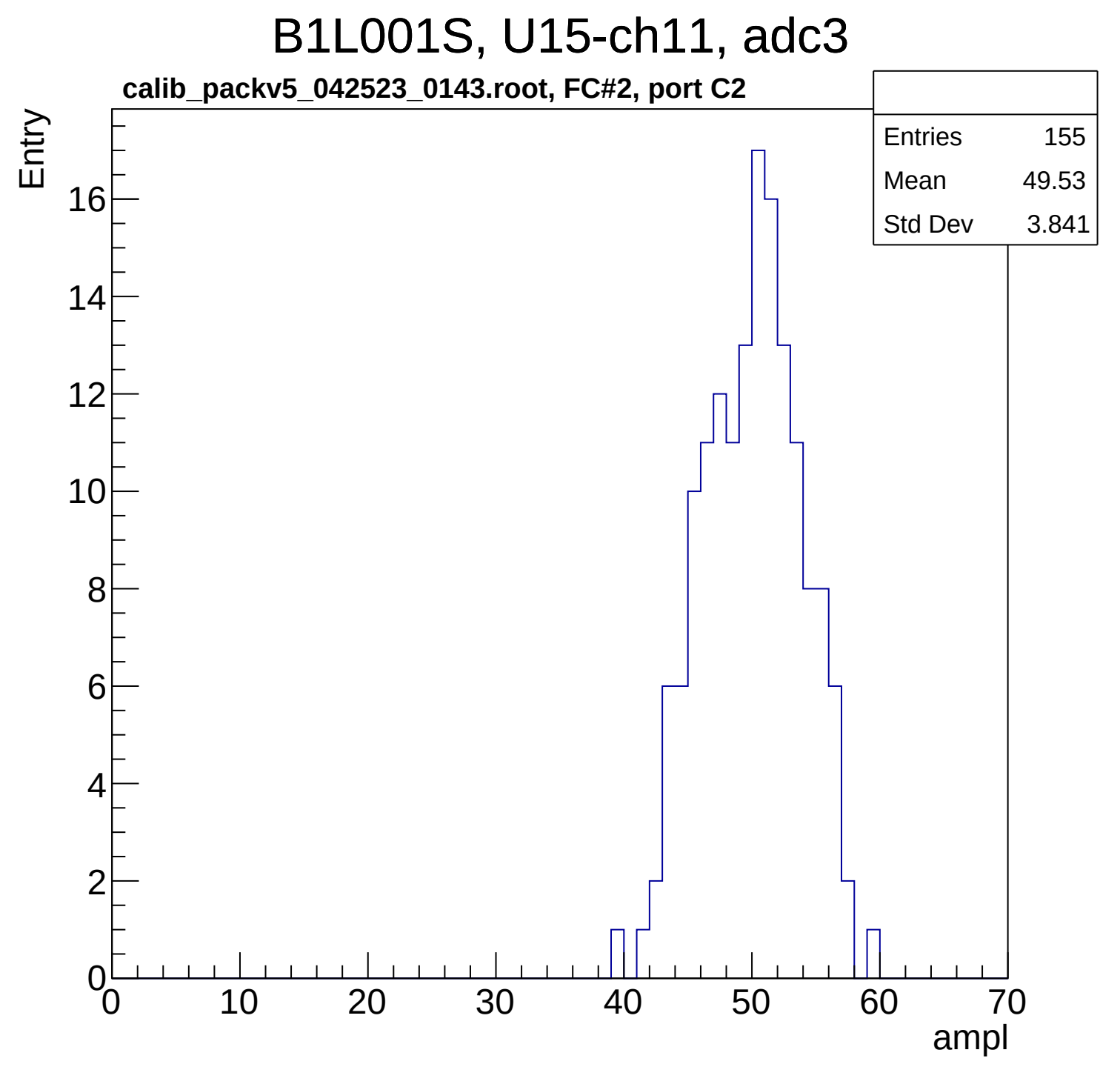
B1L001S, U15-ch11, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 49.53
Std Dev 3.841
Entry
ampl
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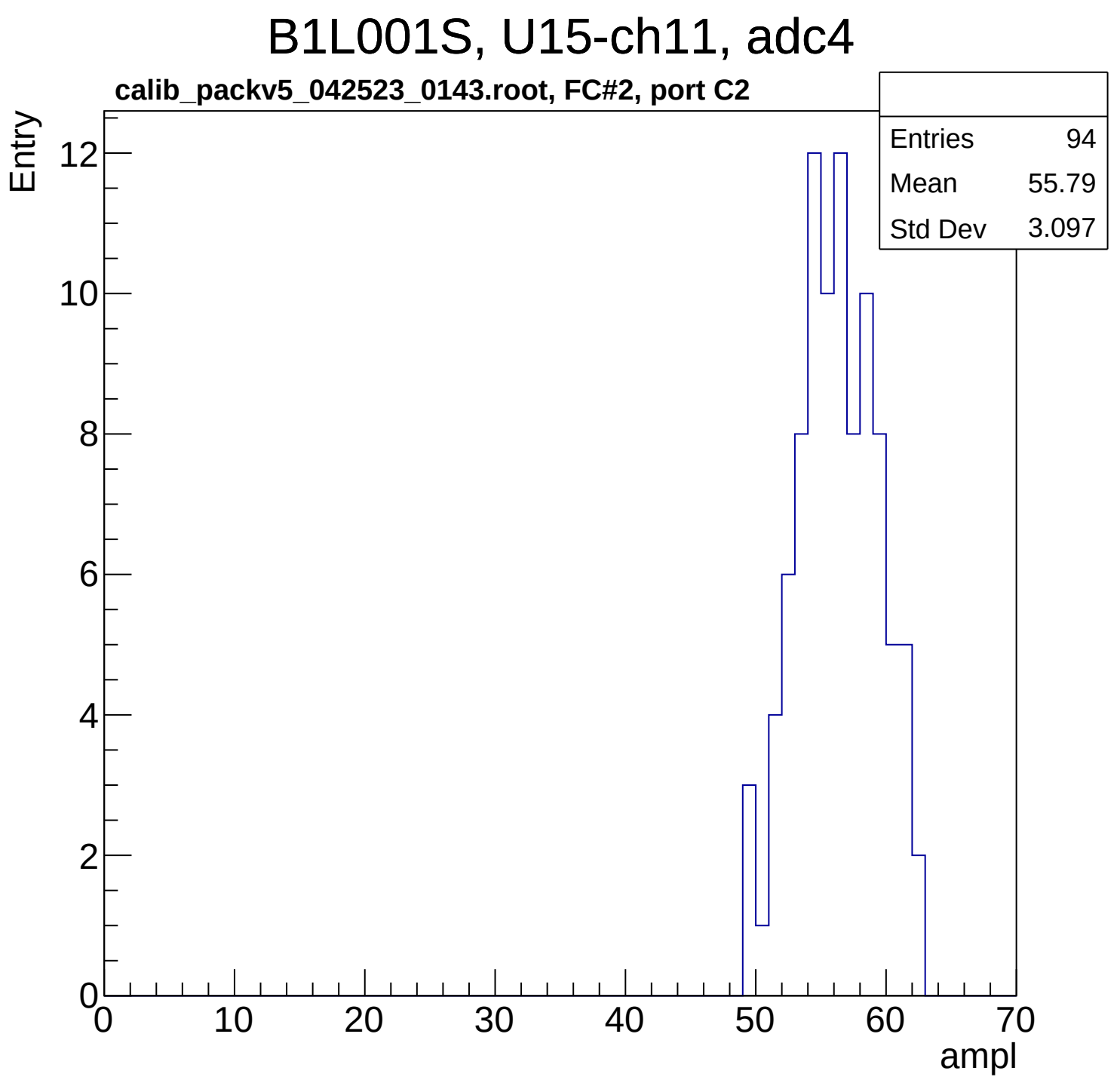
B1L001S, U15-ch11, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 55.79
Std Dev 3.097
Entry
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2
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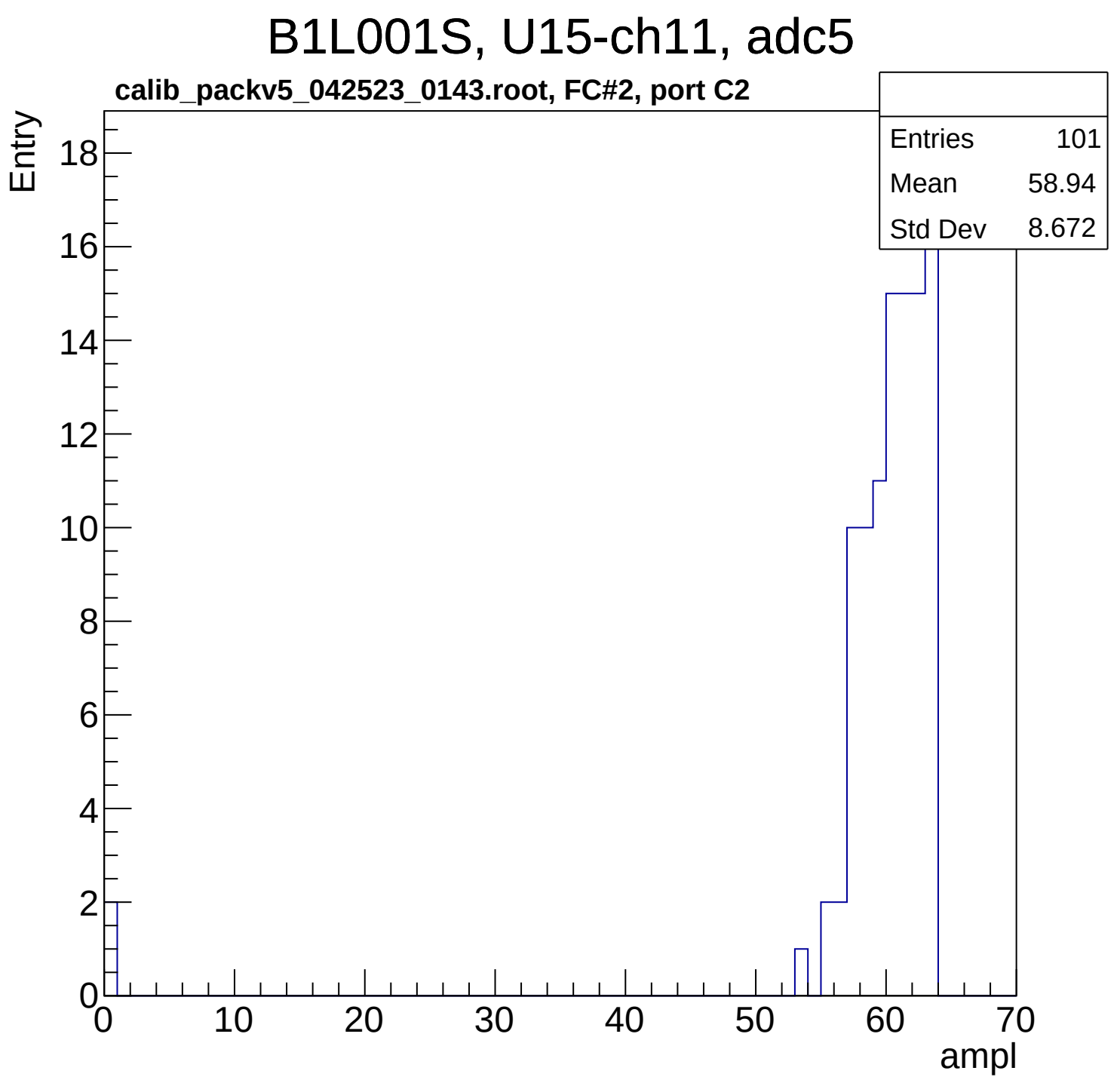
ampl


B1L001S, U15-ch11, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 58.94
Std Dev 8.672
Entry
ampl
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60
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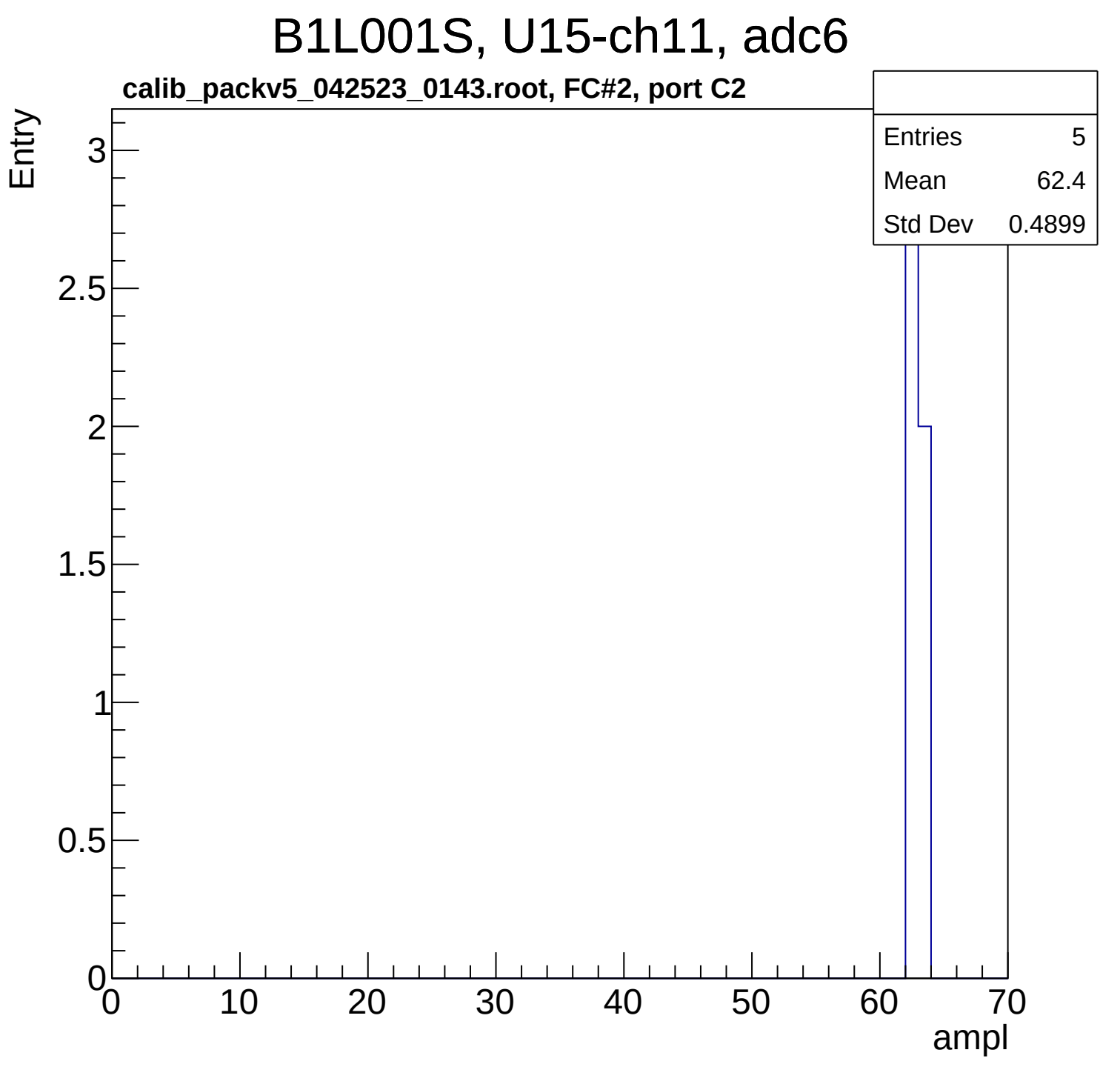
B1L001S, U15-ch11, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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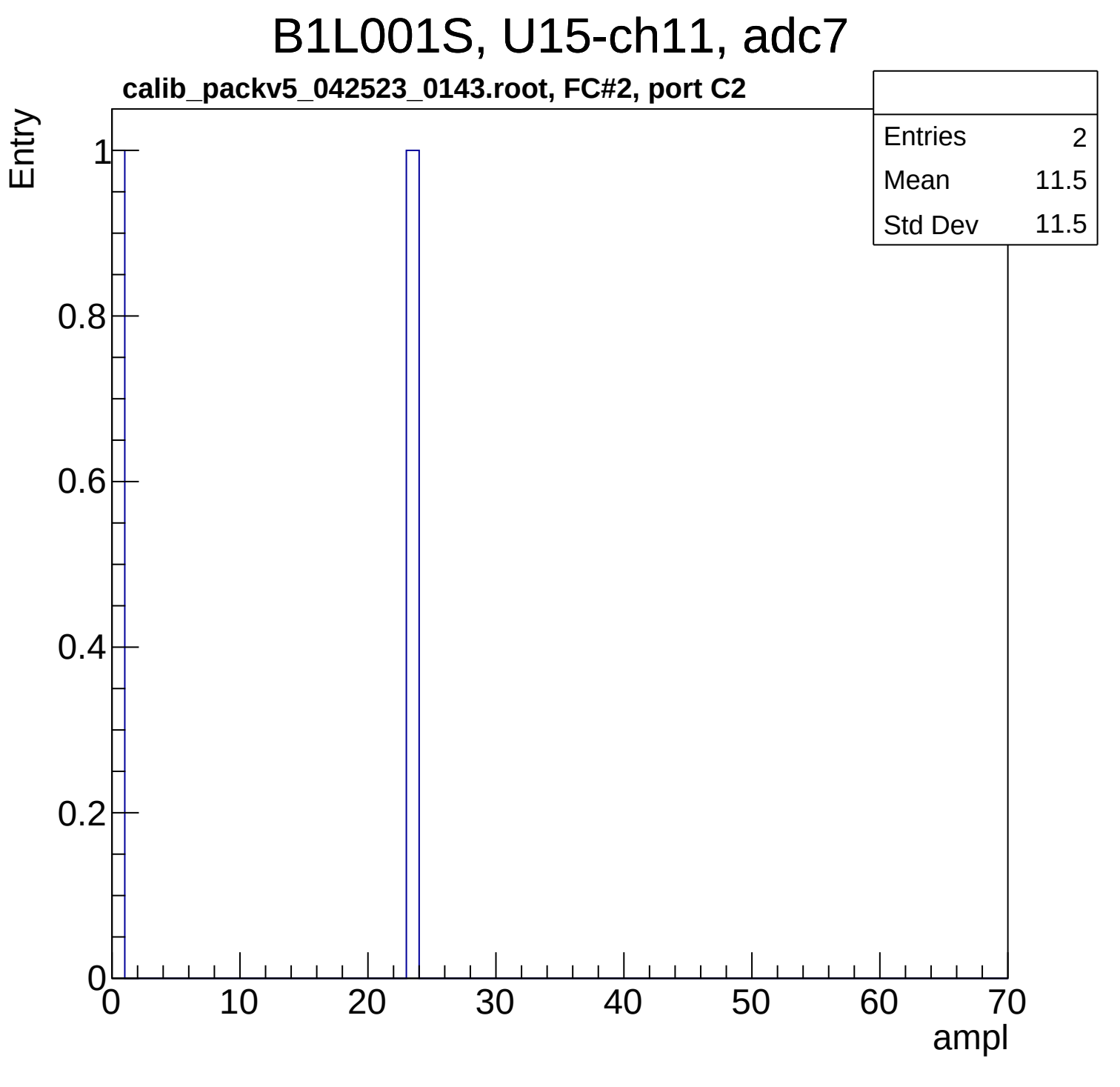
B1L001S, U15-ch11, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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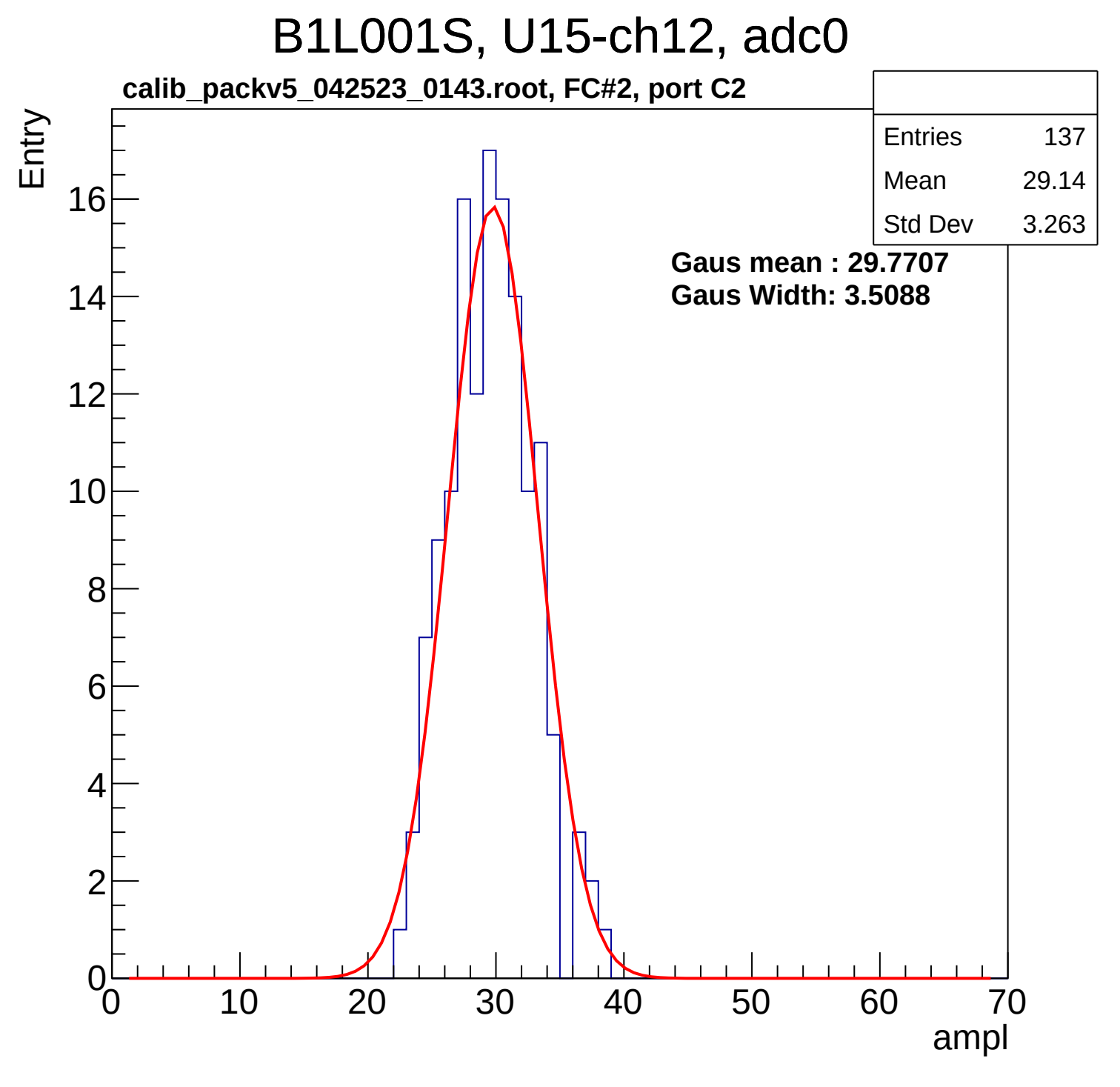
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch12, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 29.14
Std Dev 3.263
Gaus mean : 29.7707
Gaus Width: 3.5088
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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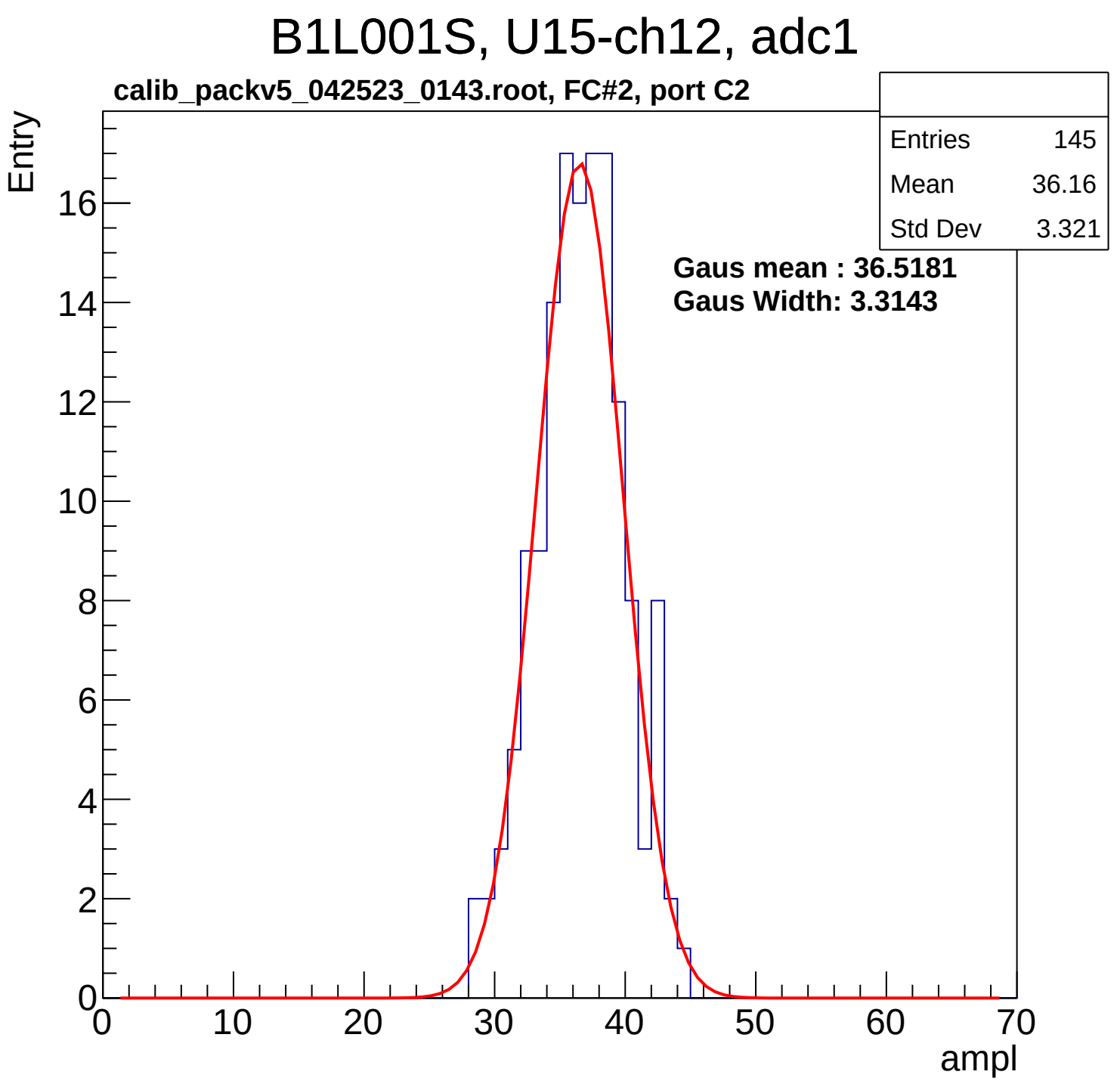
B1L001S, U15-ch12, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 36.16
Std Dev 3.321
Gaus mean : 36.5181
Gaus Width: 3.3143
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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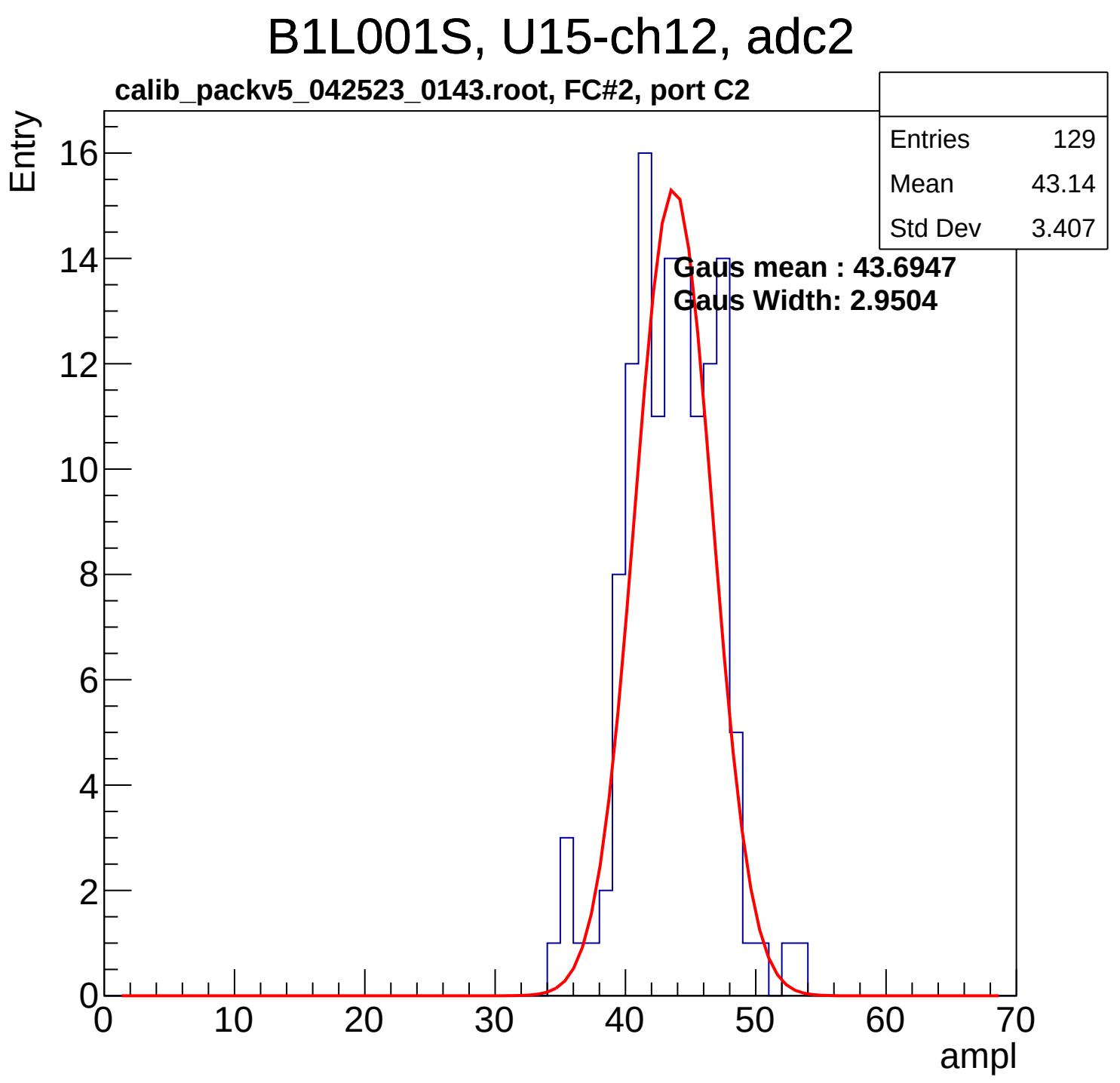
B1L001S, U15-ch12, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 43.14
Std Dev 3.407
Gaus mean : 43.6947
Gaus Width: 2.9504
Entry
ampl
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2
4
6
8
10
12
14
16
0
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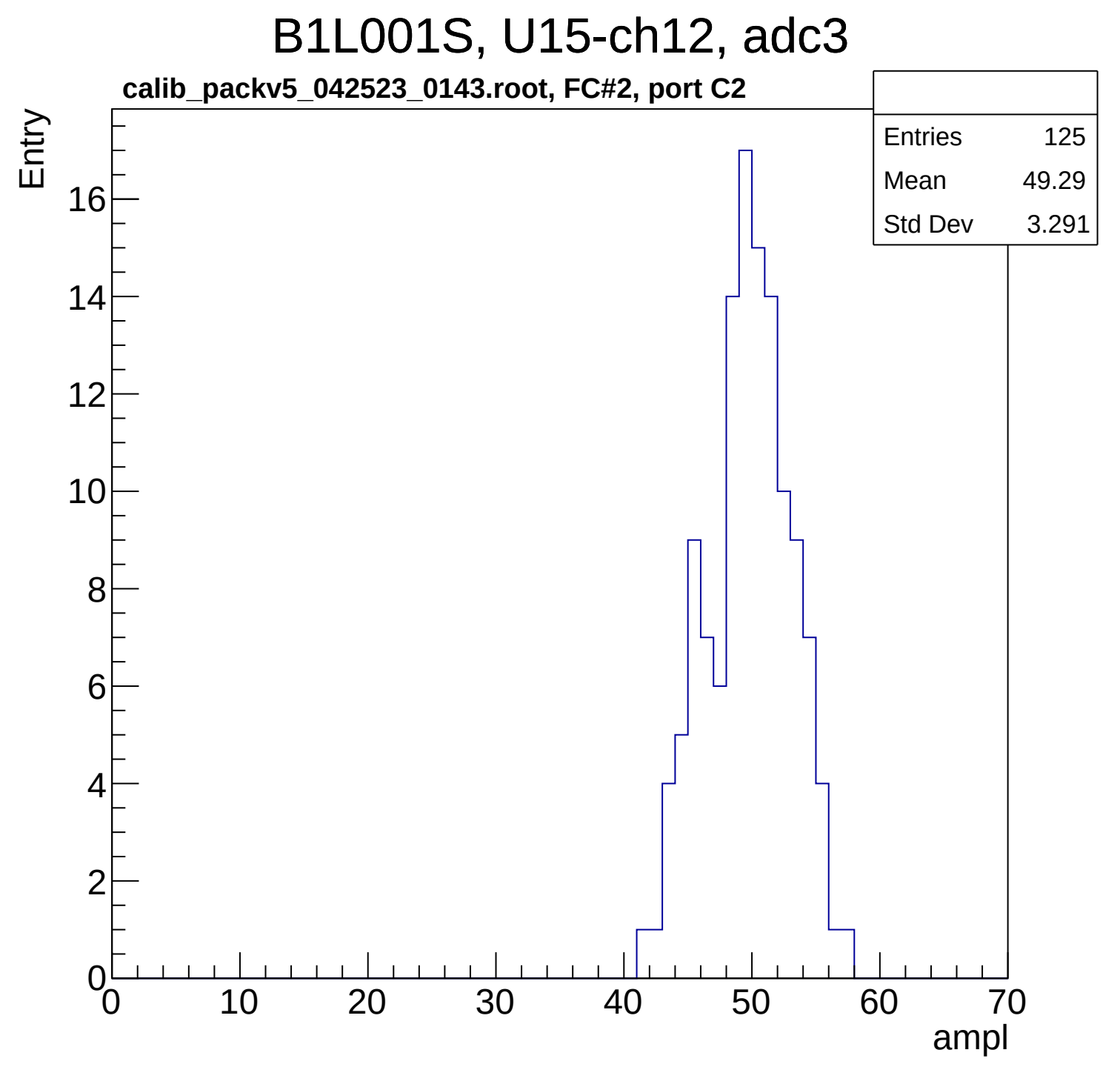
B1L001S, U15-ch12, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 49.29
Std Dev 3.291
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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60
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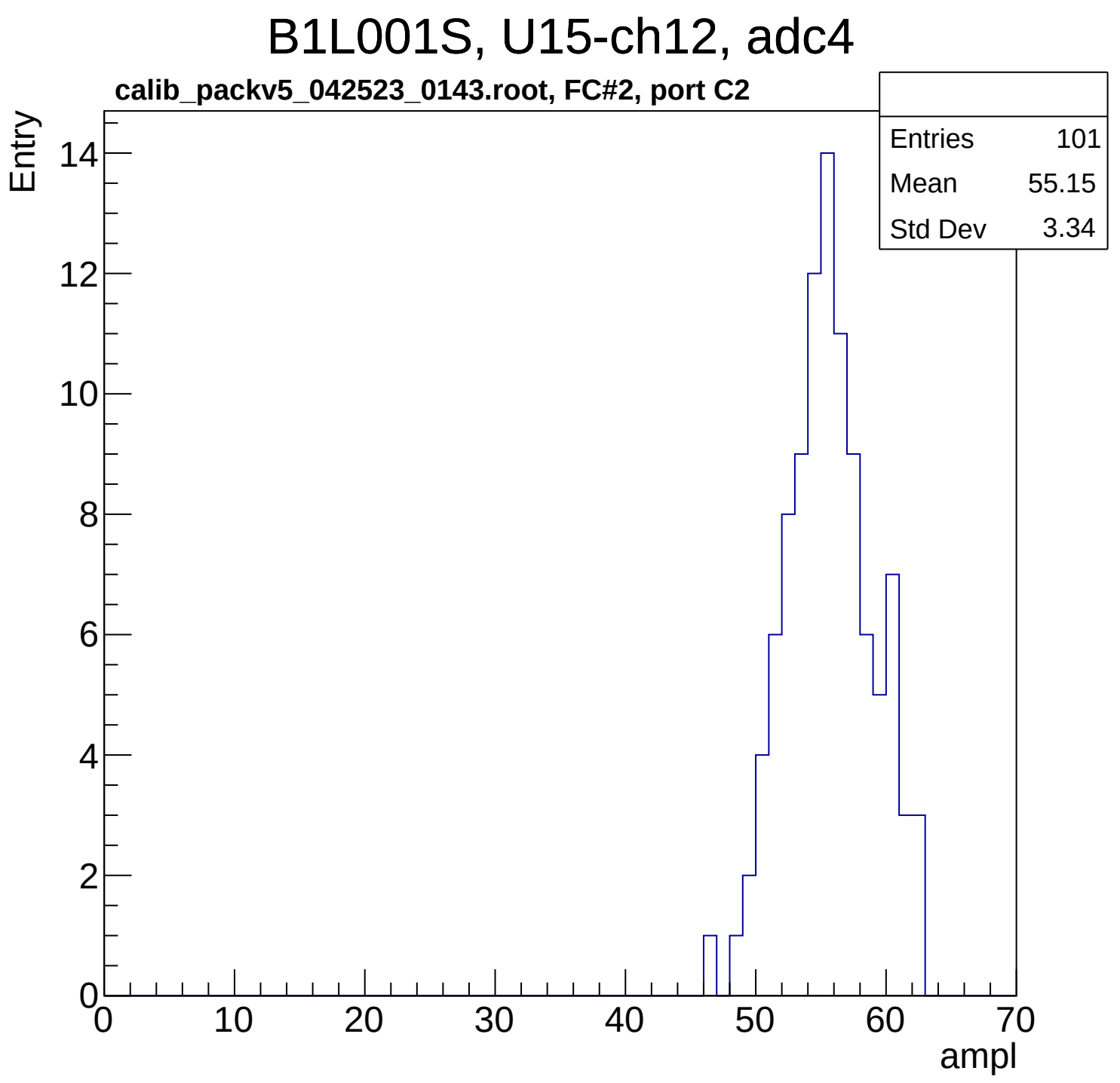
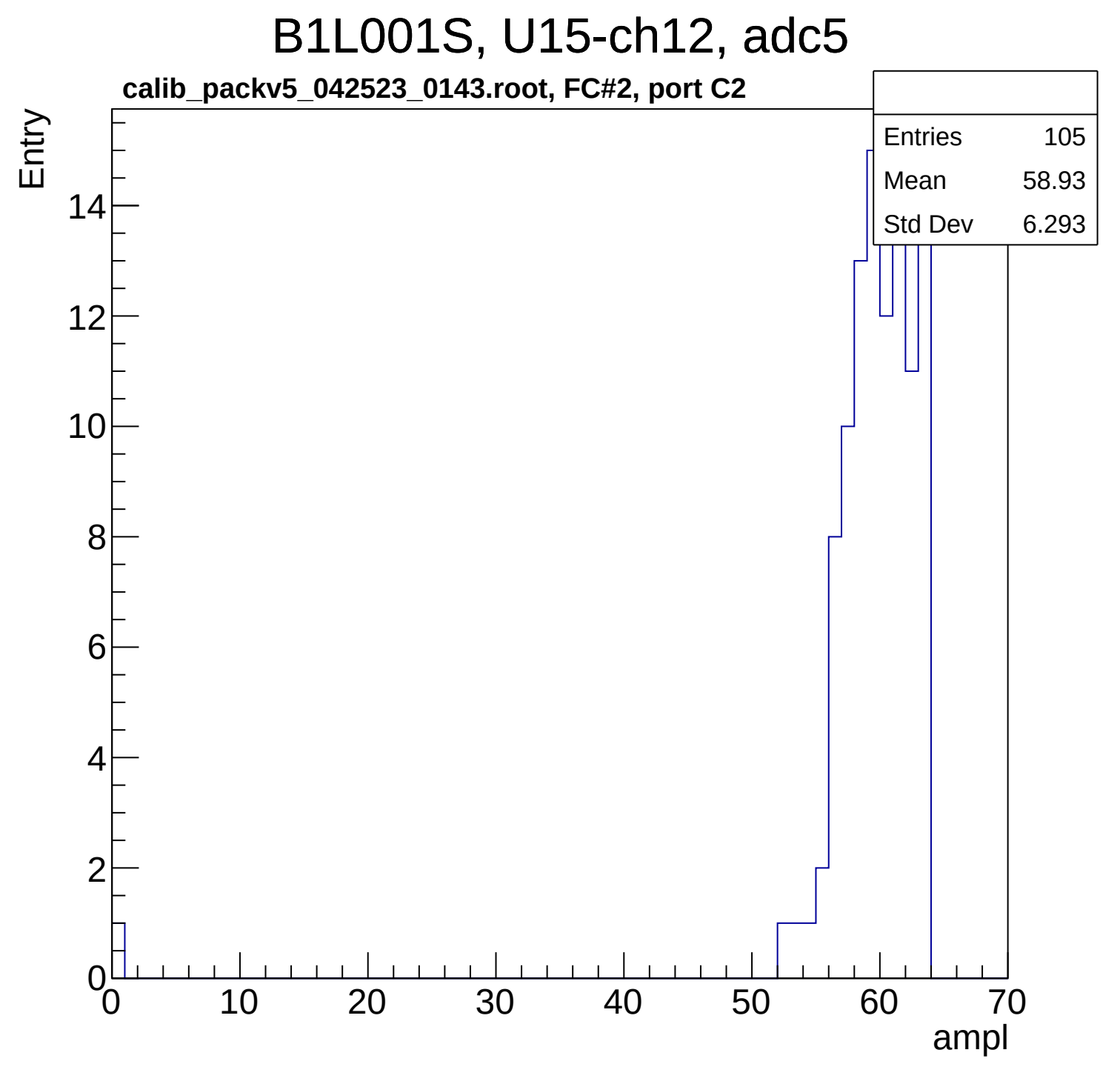

B1L001S, U15-ch12, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 55.15
Std Dev 3.34
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch12, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 58.93
Std Dev 6.293
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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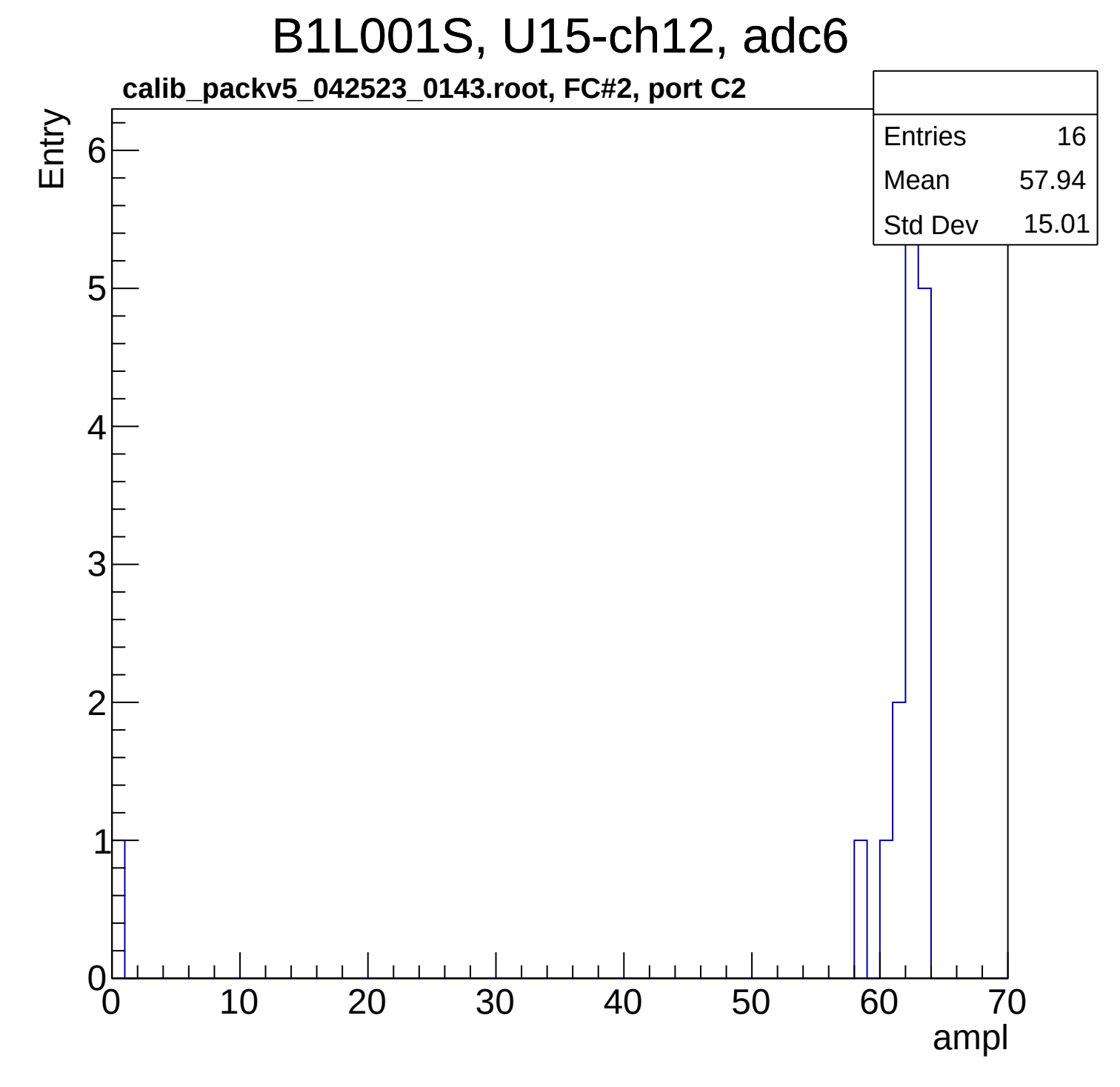
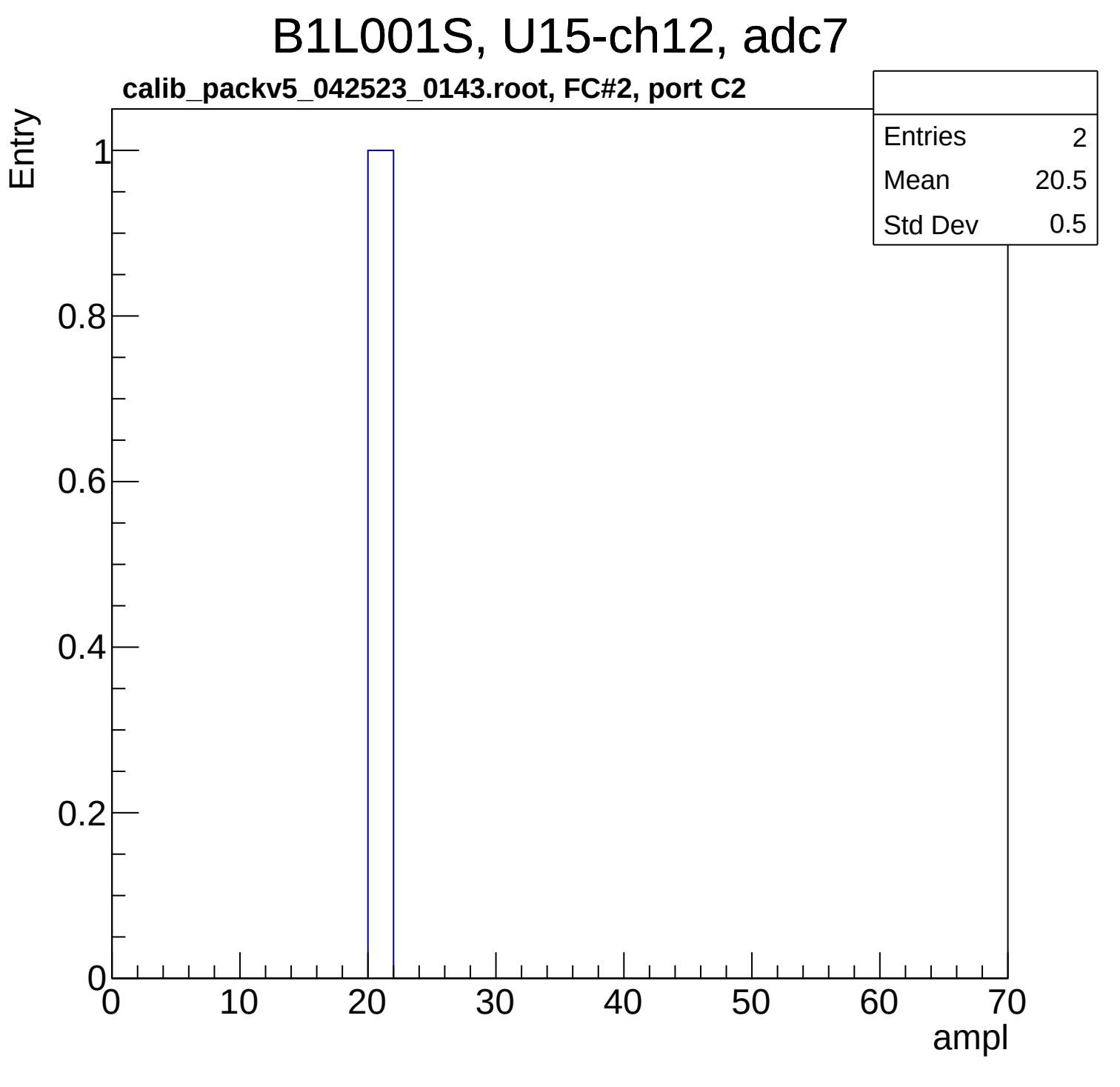
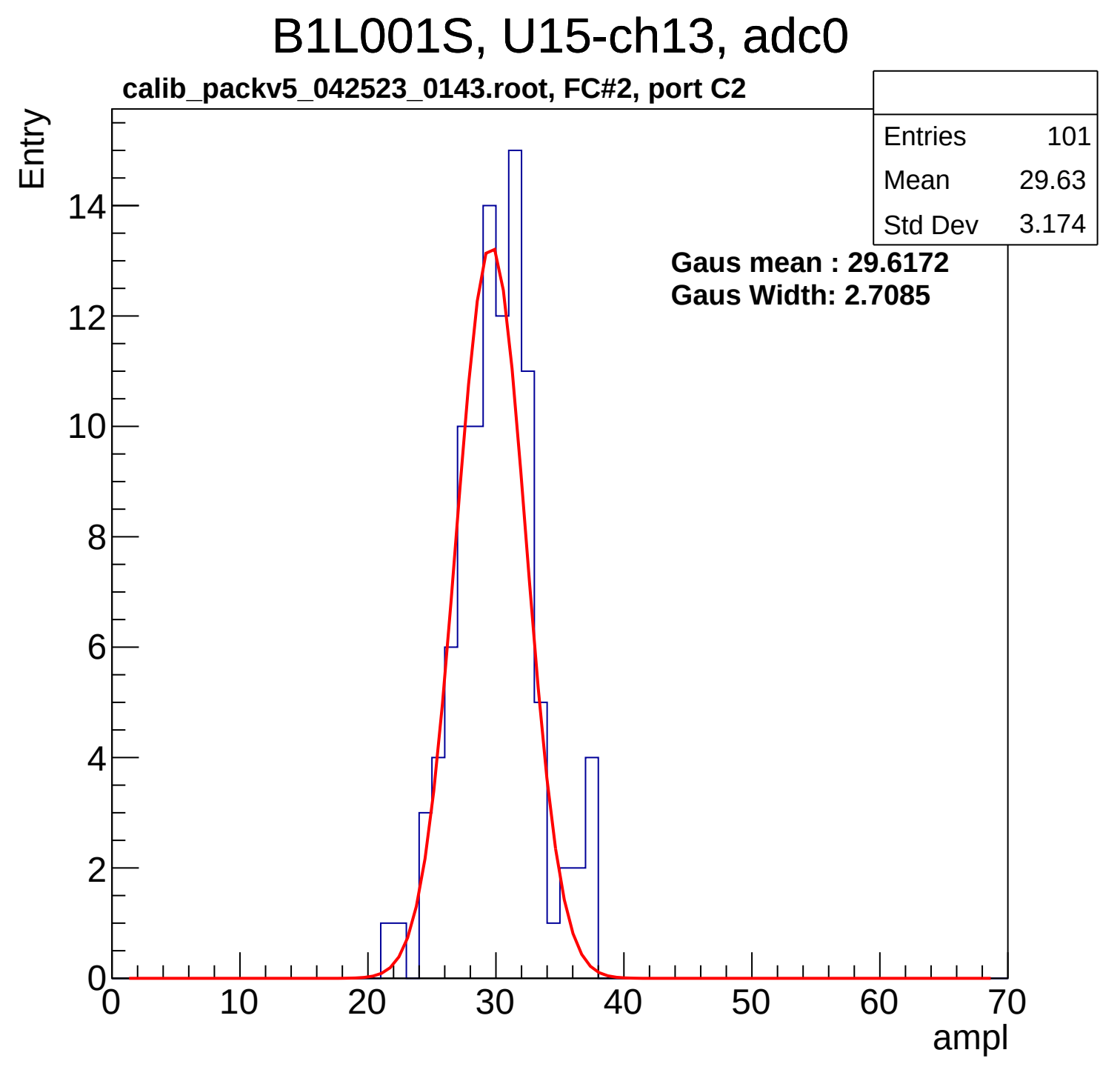

B1L001S, U15-ch12, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 57.94
Std Dev 15.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch12, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 20.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U15-ch13, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 29.63
Std Dev 3.174
Gaus mean : 29.6172
Gaus Width: 2.7085
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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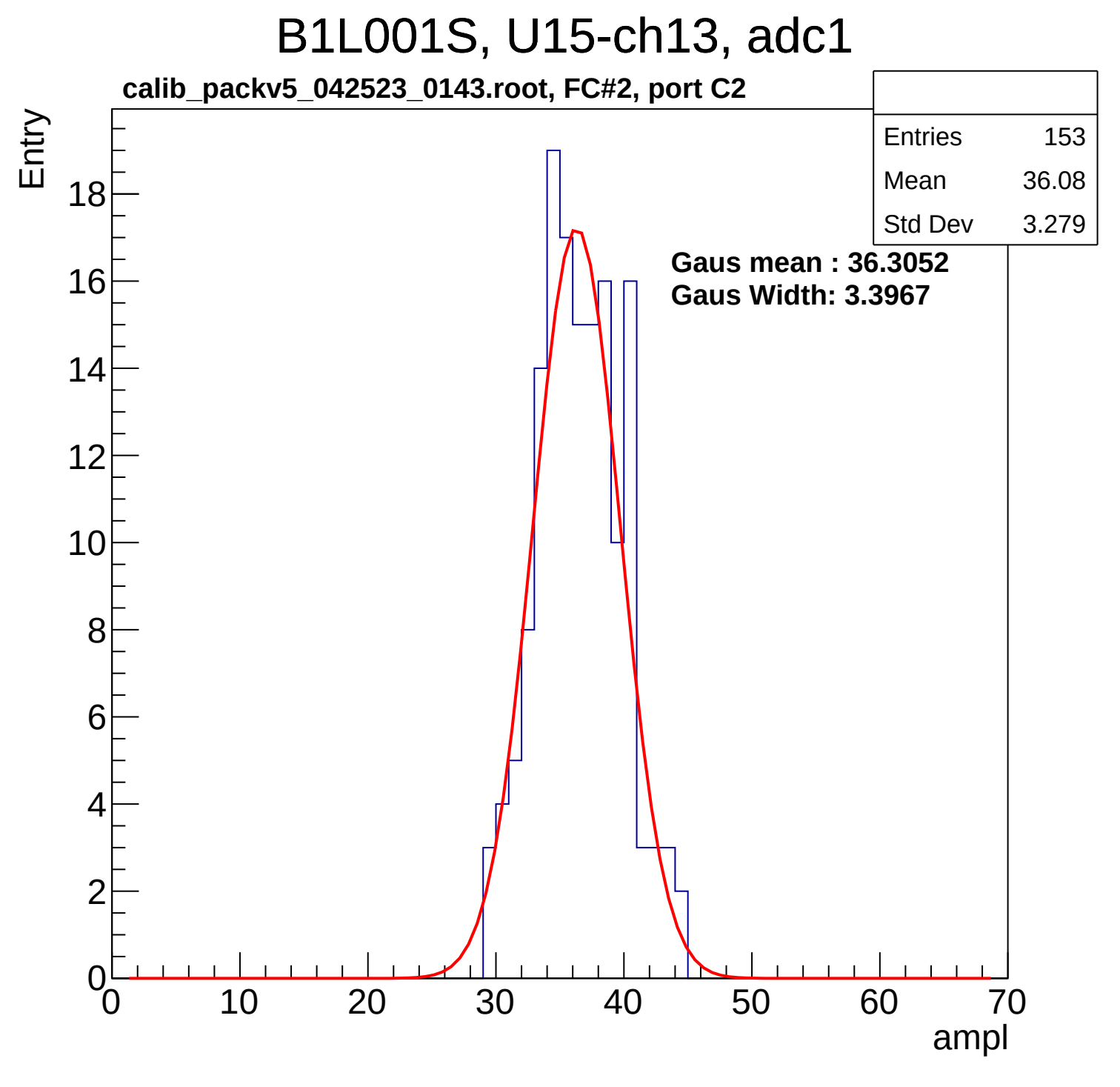
B1L001S, U15-ch13, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 36.08
Std Dev 3.279
Gaus mean : 36.3052
Gaus Width: 3.3967
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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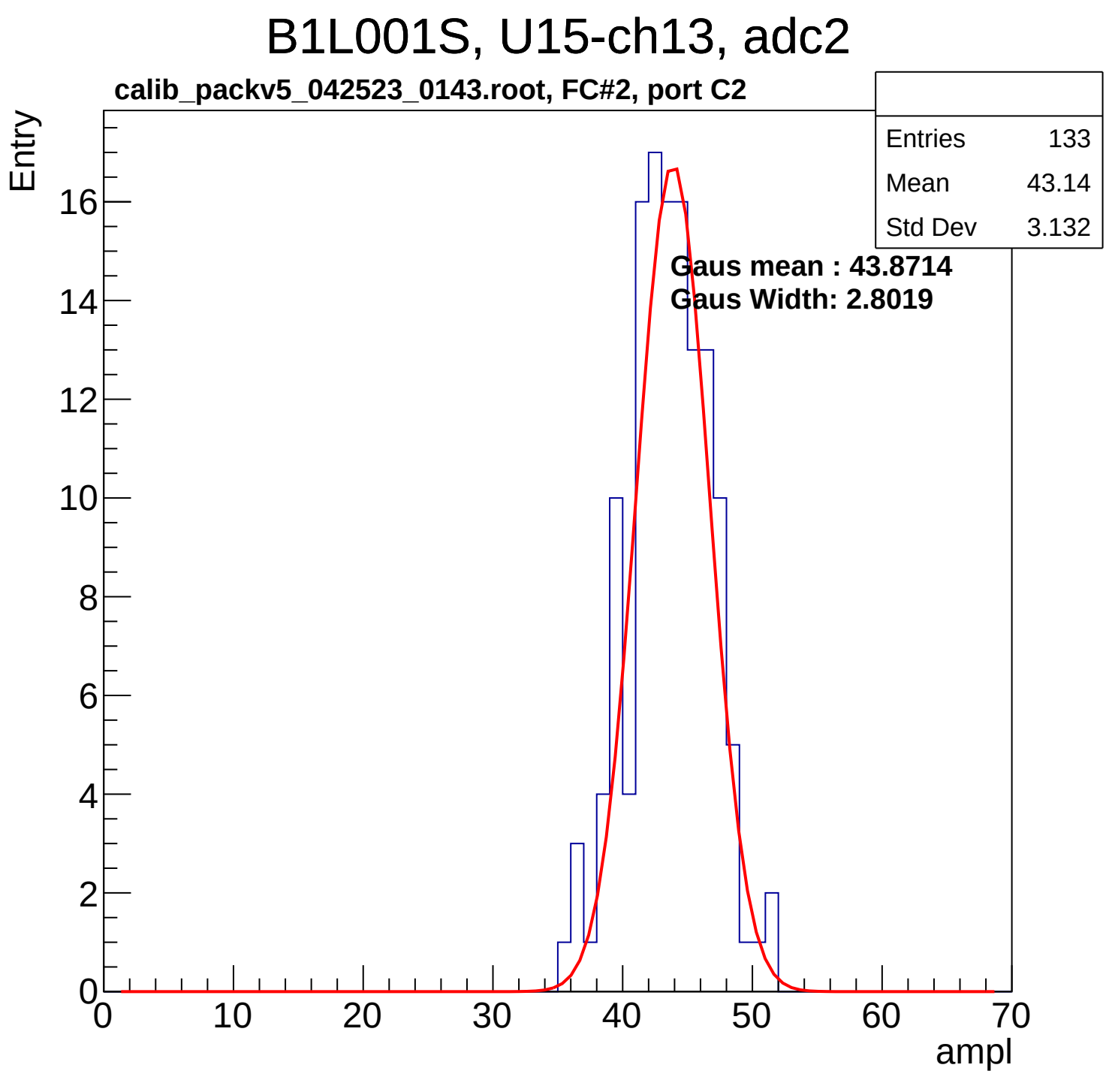
B1L001S, U15-ch13, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 43.14
Std Dev 3.132
Gaus mean : 43.8714
Gaus Width: 2.8019
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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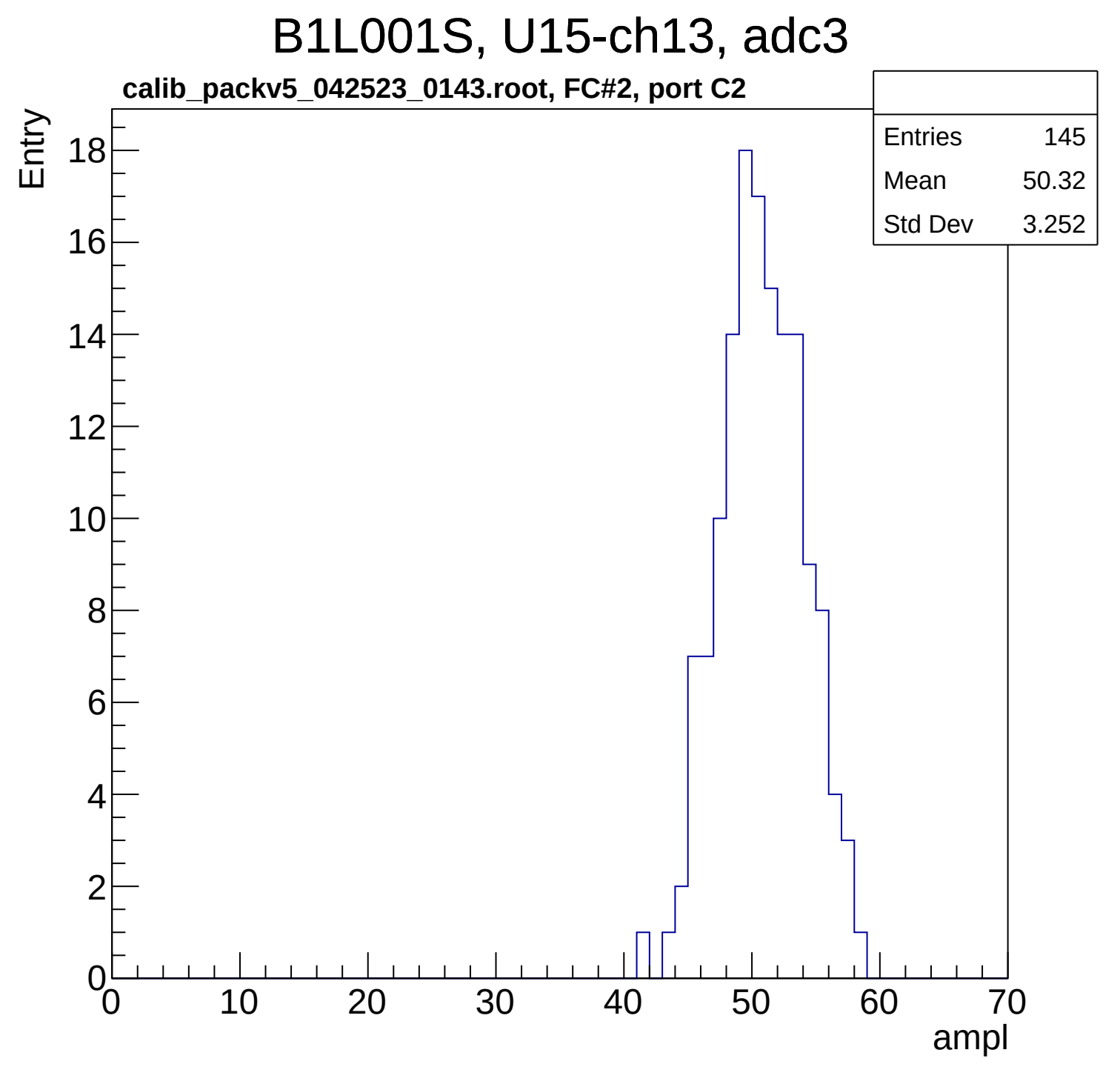
B1L001S, U15-ch13, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 50.32
Std Dev 3.252
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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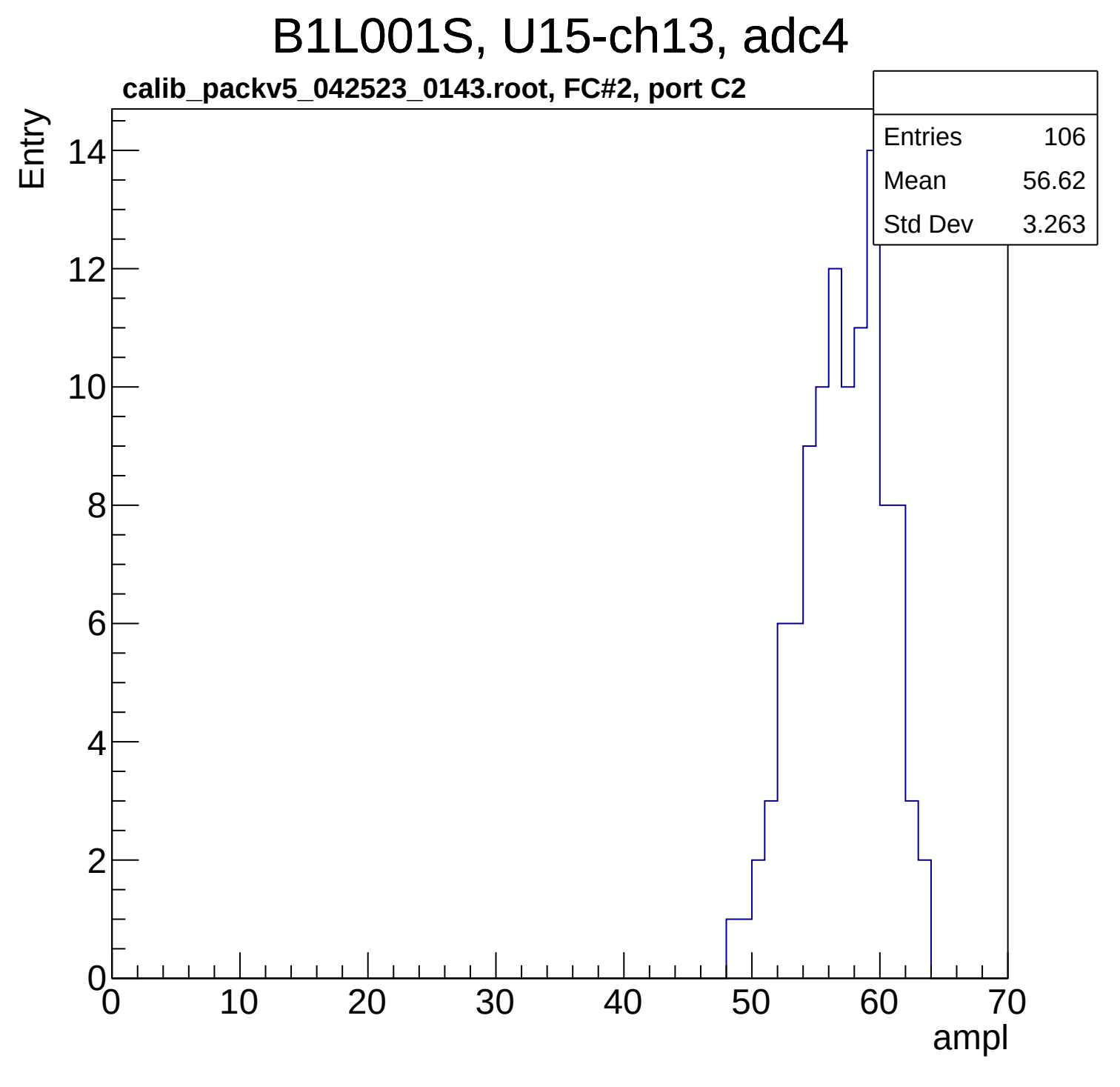
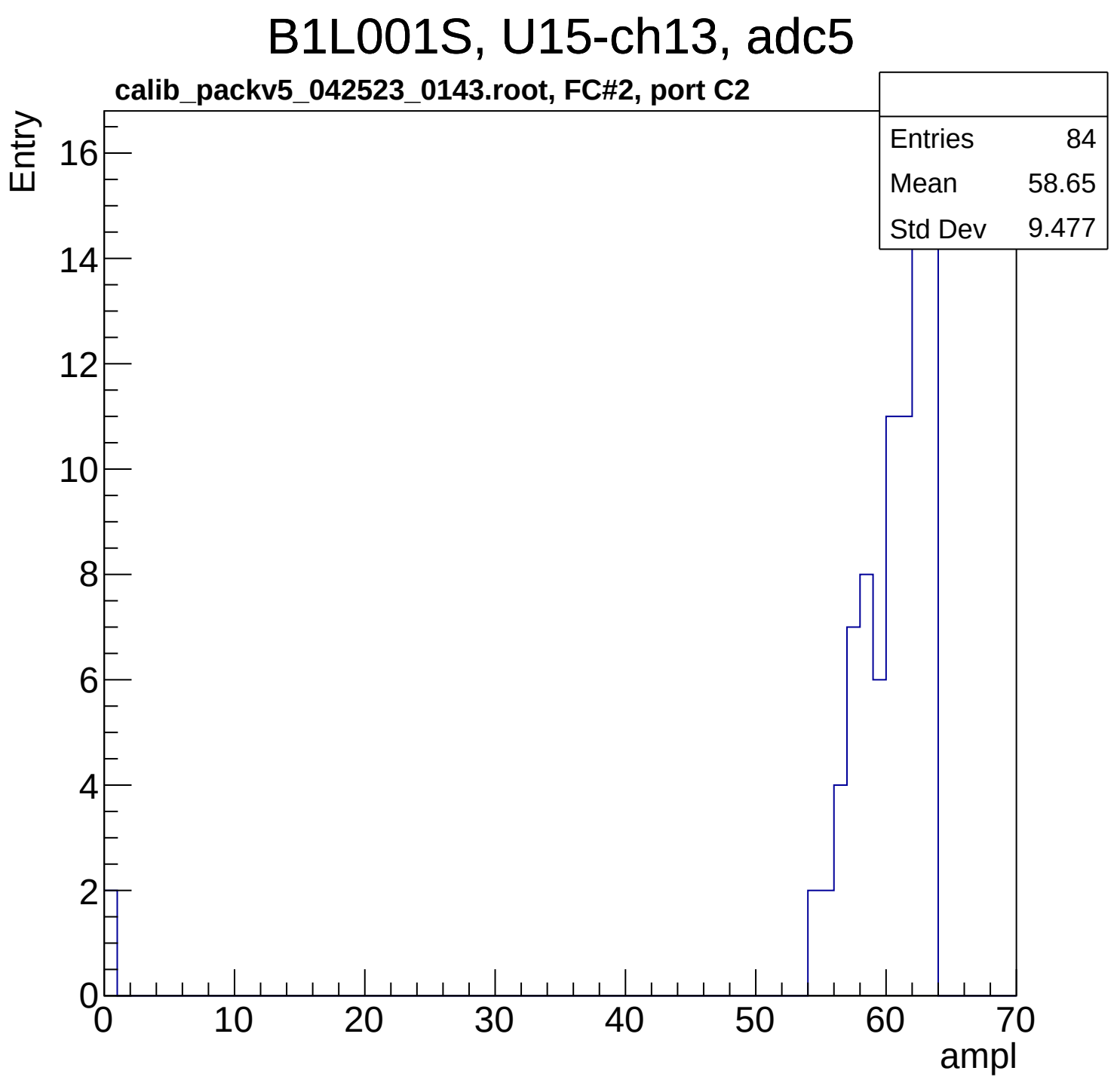

B1L001S, U15-ch13, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 56.62
Std Dev 3.263
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch13, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 58.65
Std Dev 9.477
Entry
ampl
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6
8
10
12
14
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0
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30
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60
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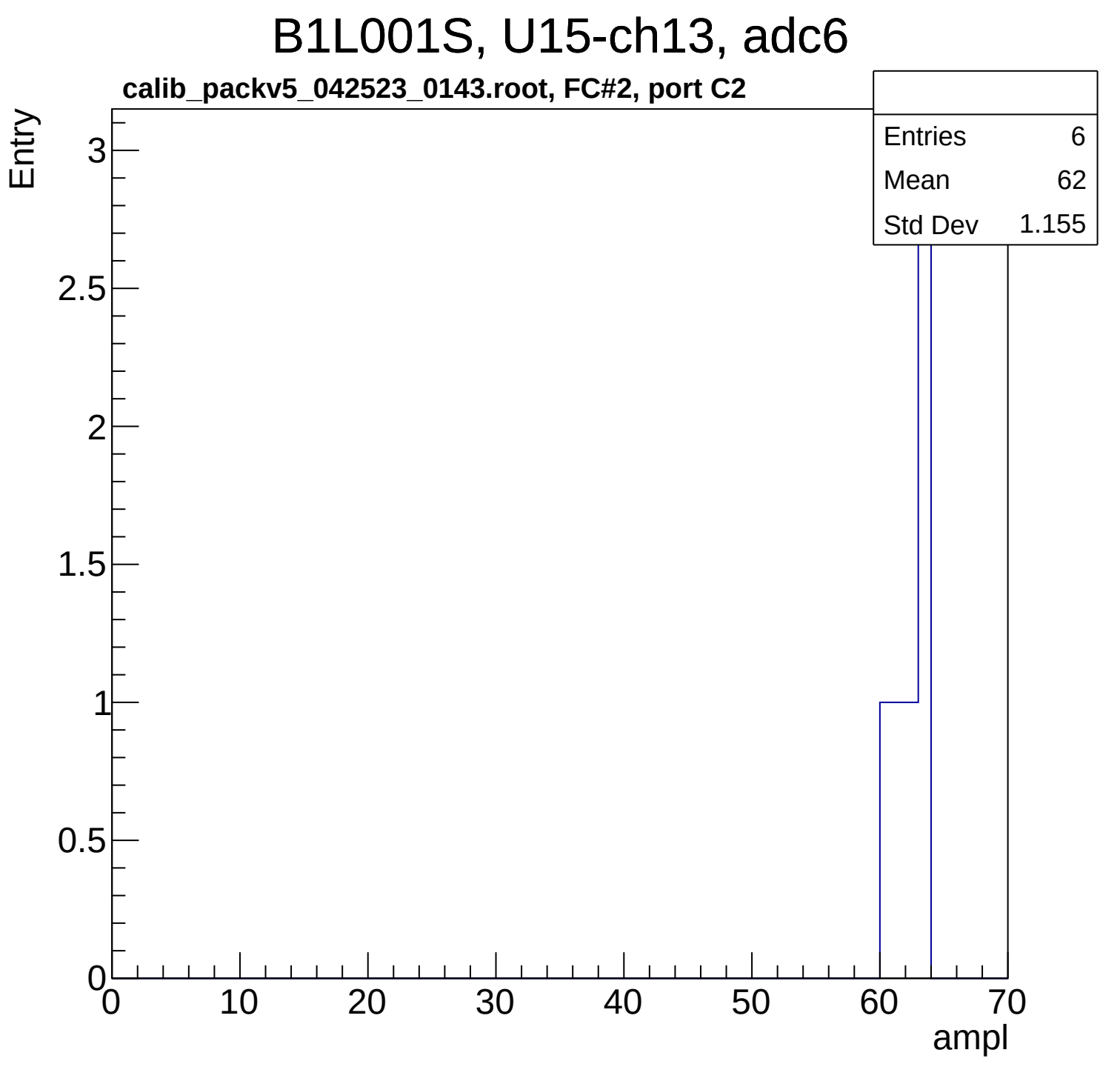
B1L001S, U15-ch13, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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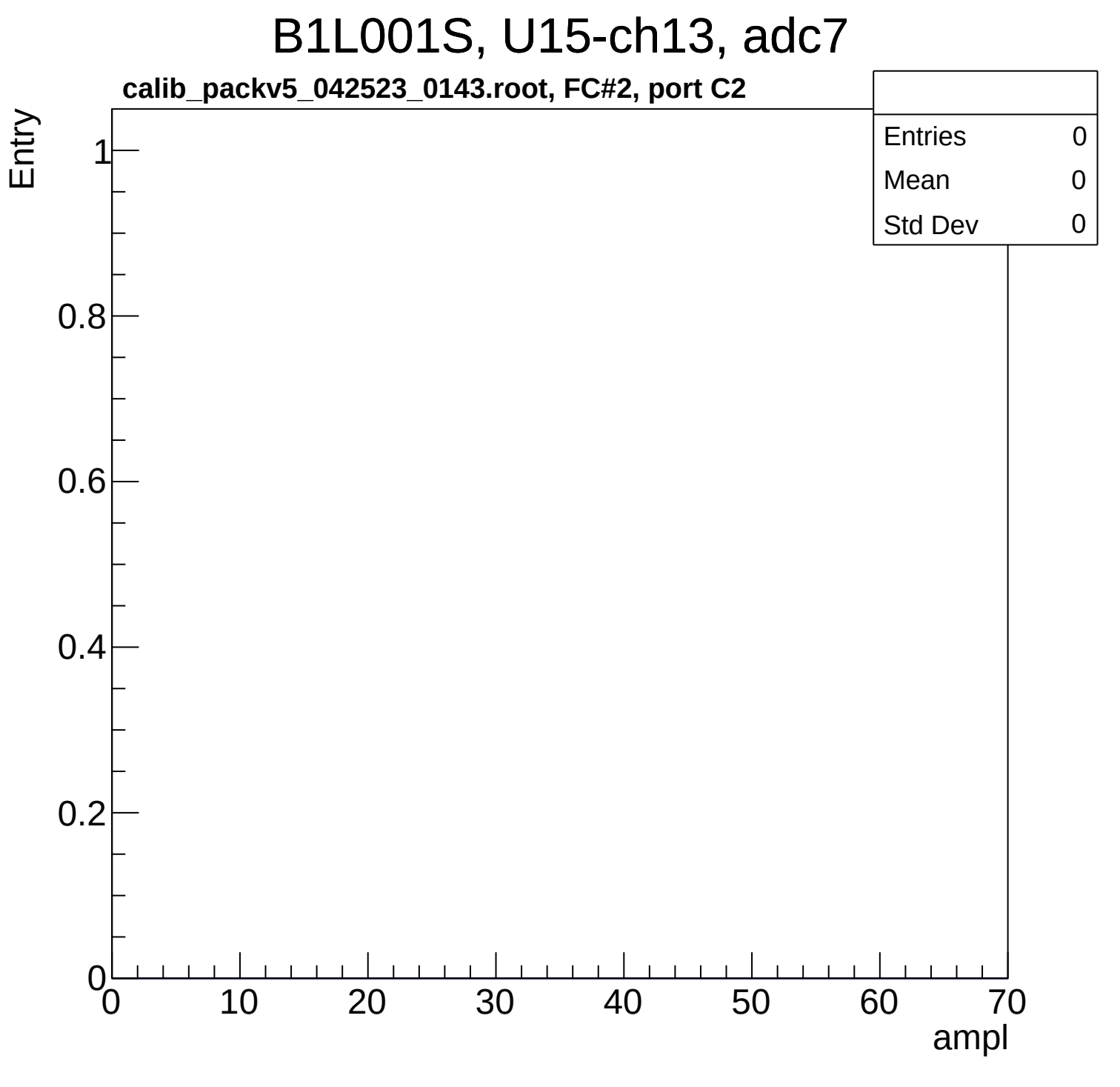
B1L001S, U15-ch13, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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30
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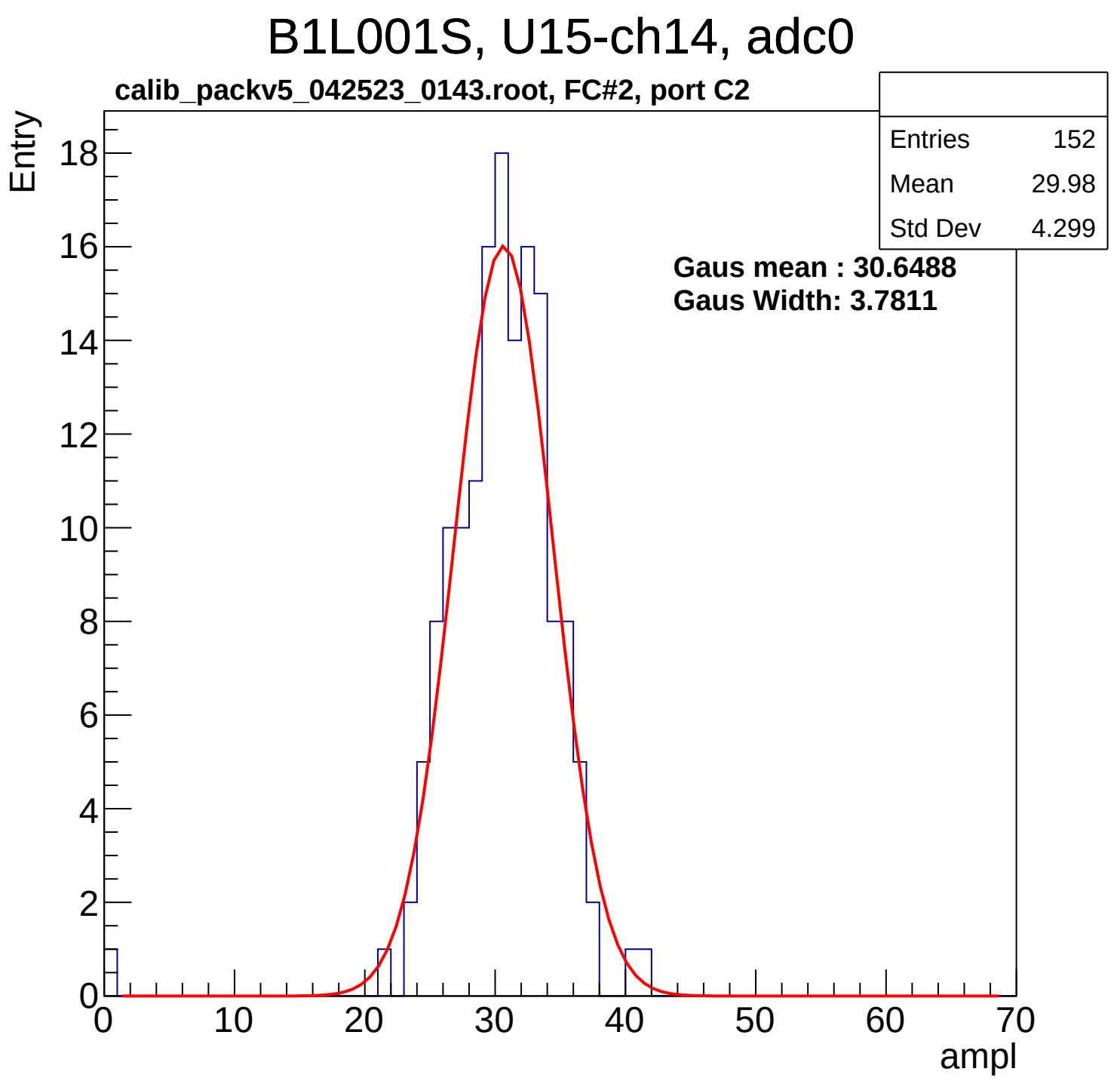
60
70
ampl


B1L001S, U15-ch14, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 29.98
Std Dev 4.299
Gaus mean : 30.6488
Gaus Width: 3.7811
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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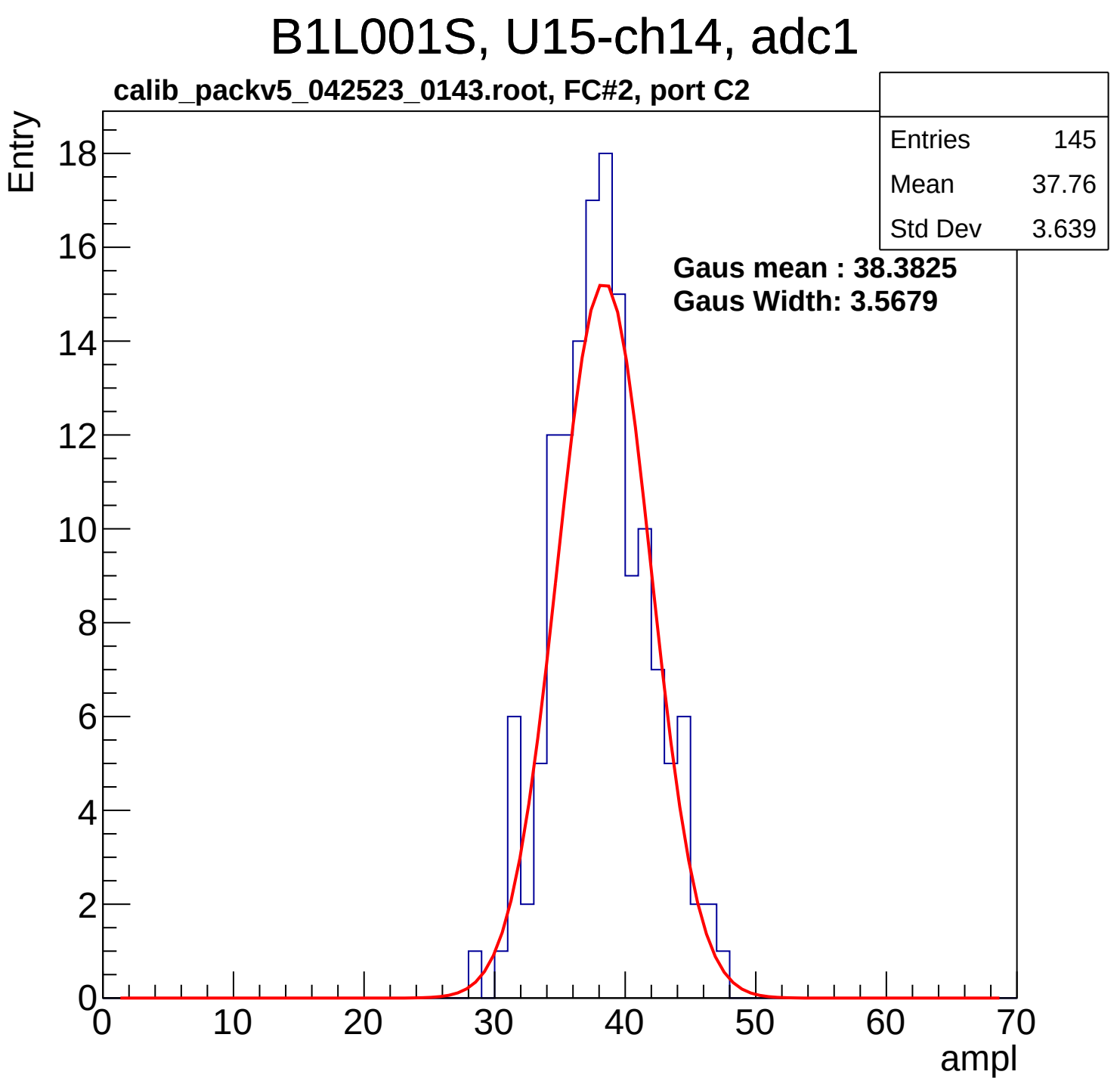
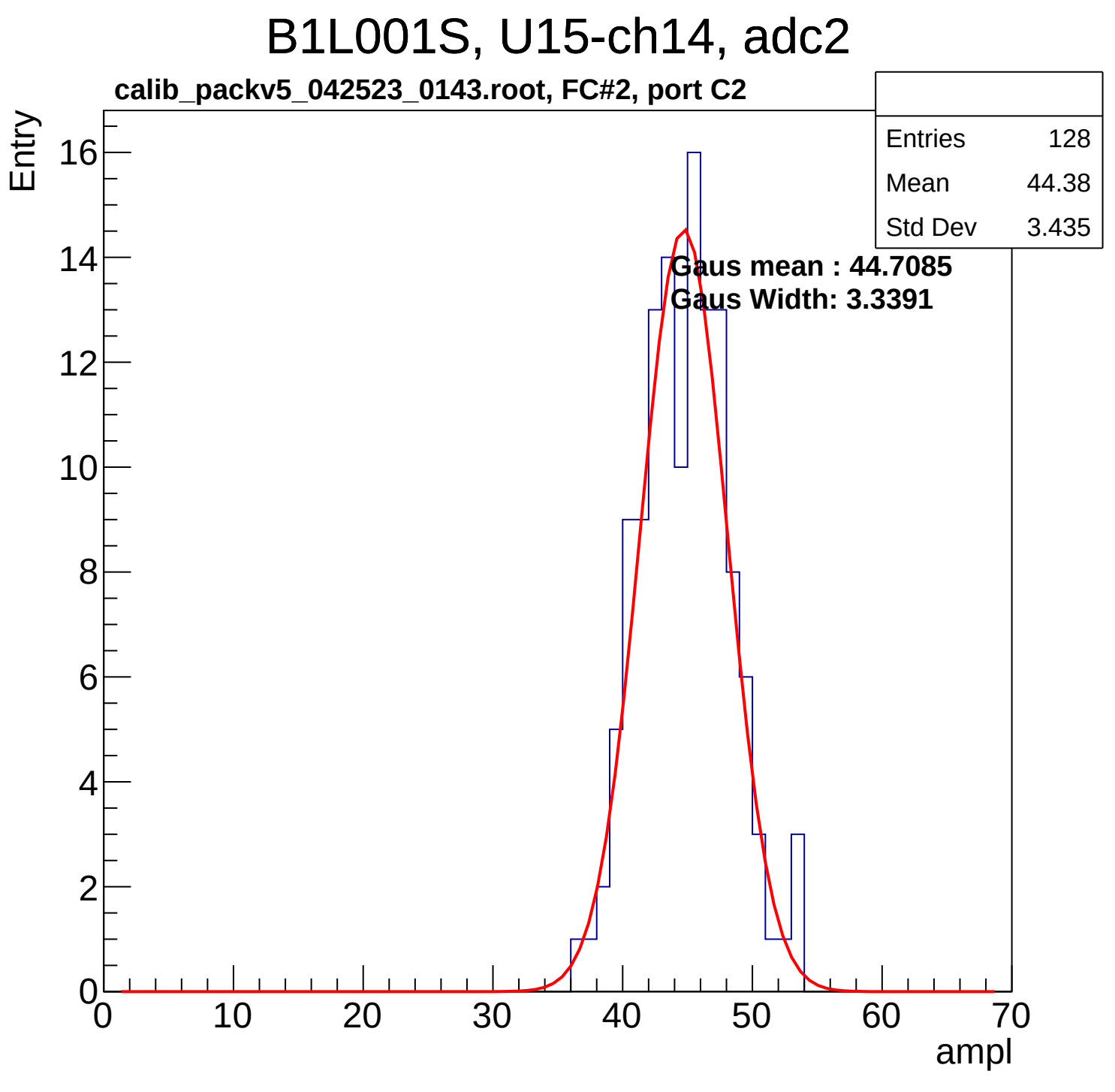
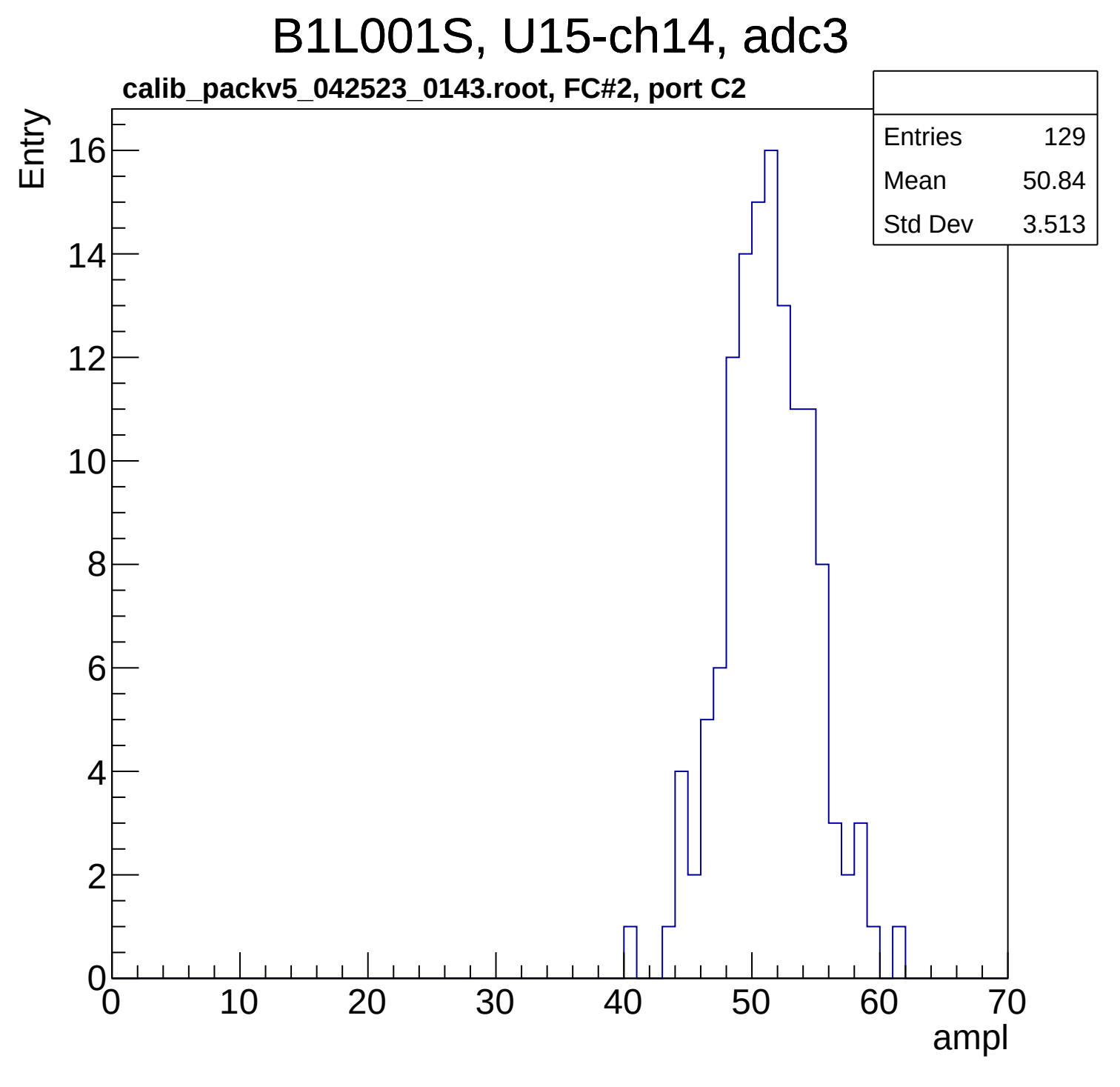

B1L001S, U15-ch14, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 37.76
Std Dev 3.639
Gaus mean : 38.3825
Gaus Width: 3.5679
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch14, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 44.38
Std Dev 3.435
Gaus mean : 44.7085
Gaus Width: 3.3391
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch14, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 50.84
Std Dev 3.513
Entry
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6
8
10
12
14
16
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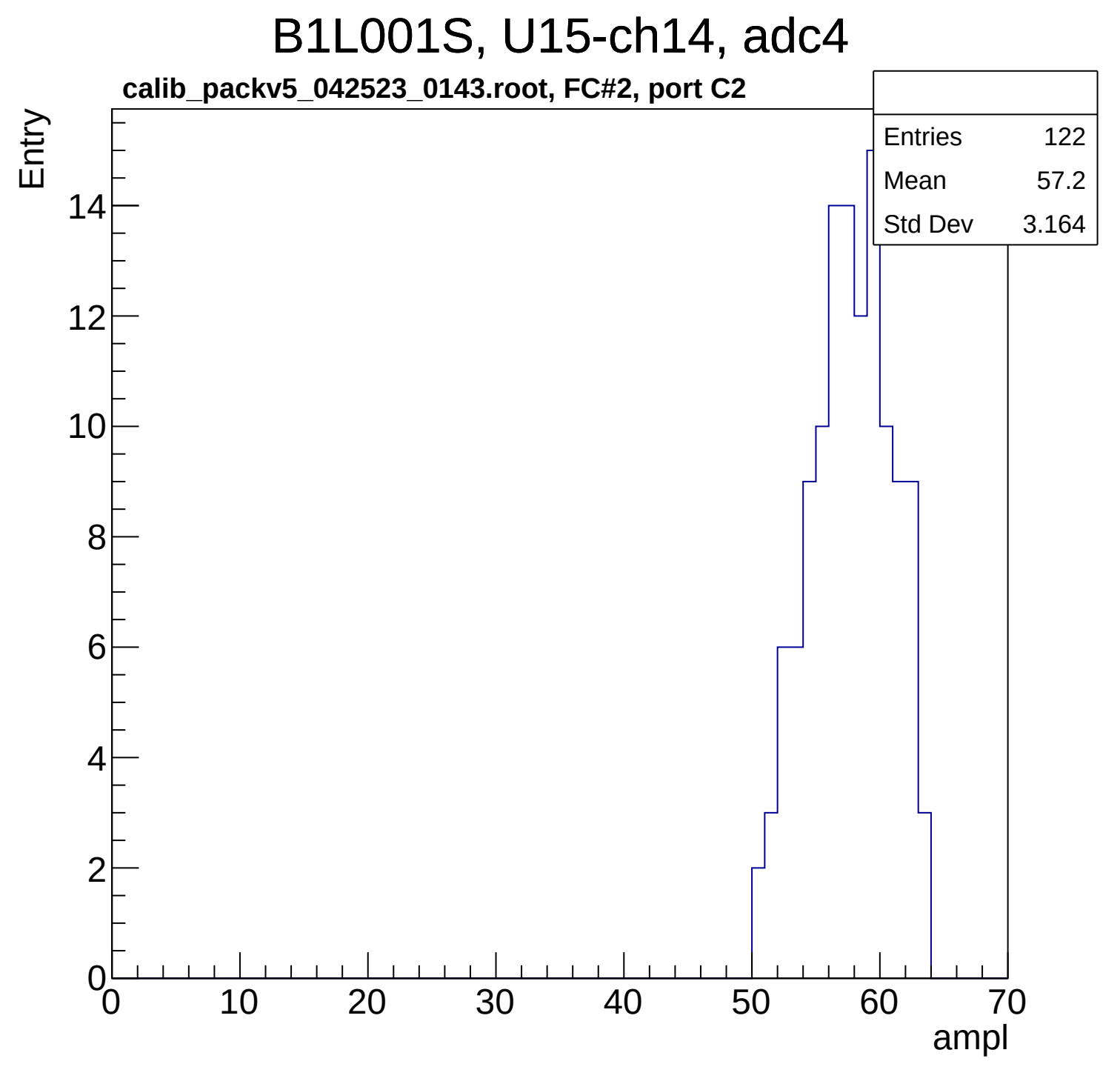
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch14, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 57.2
Std Dev 3.164
Entry
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2
4
6
8
10
12
14
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10
20
30
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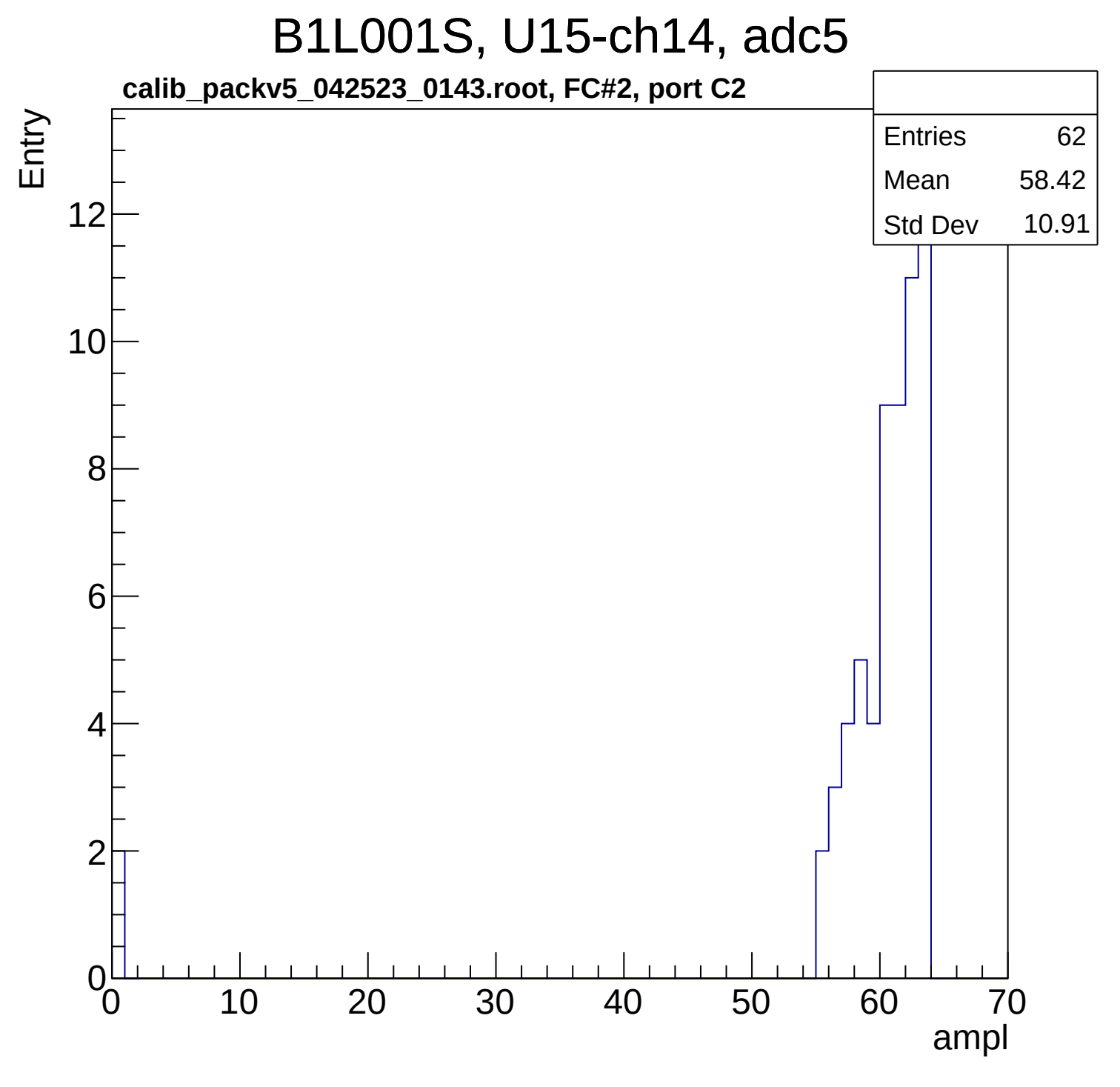
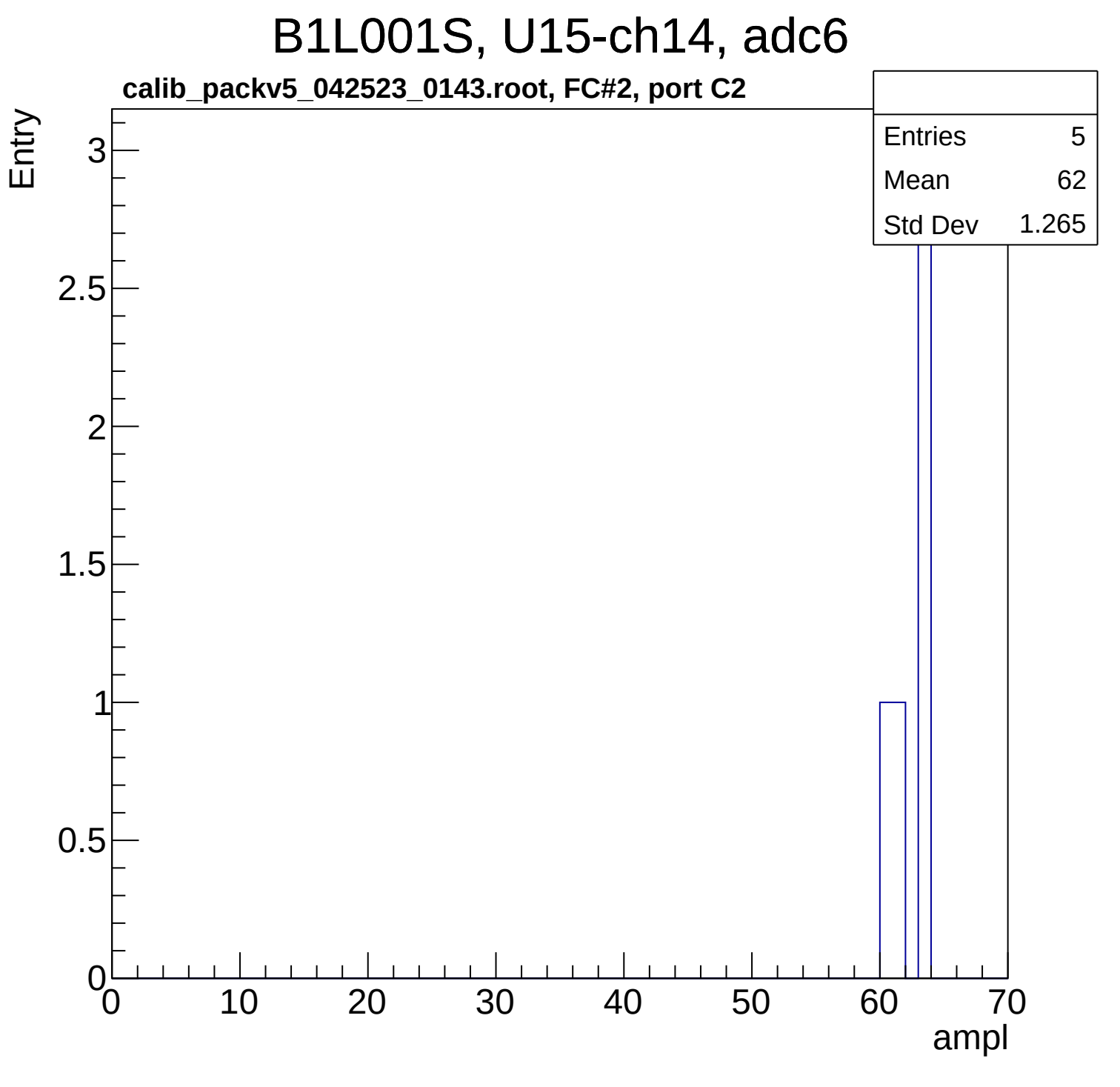
50
60
70
ampl


B1L001S, U15-ch14, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 62
Mean 58.42
Std Dev 10.91
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch14, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
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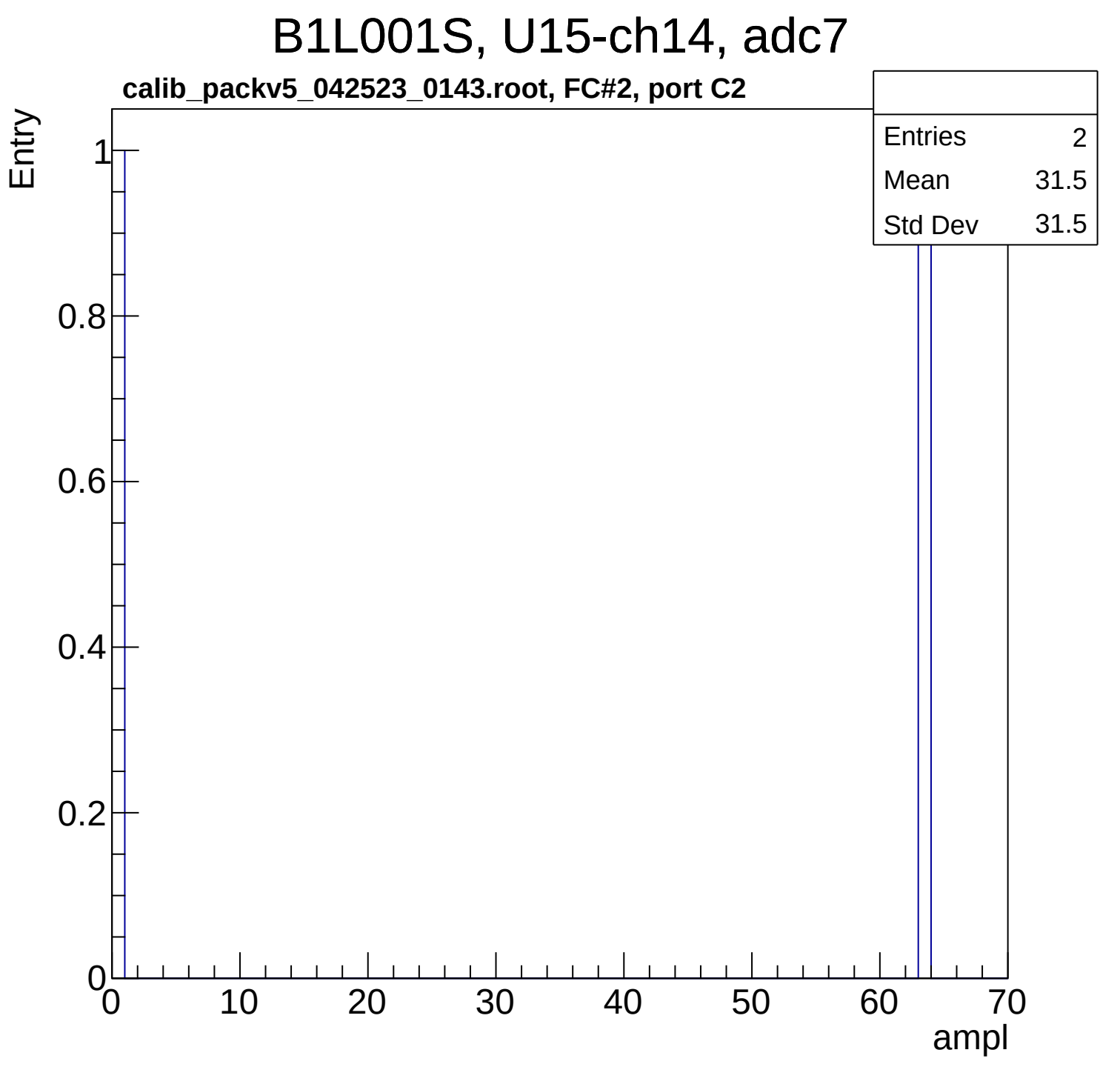
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch14, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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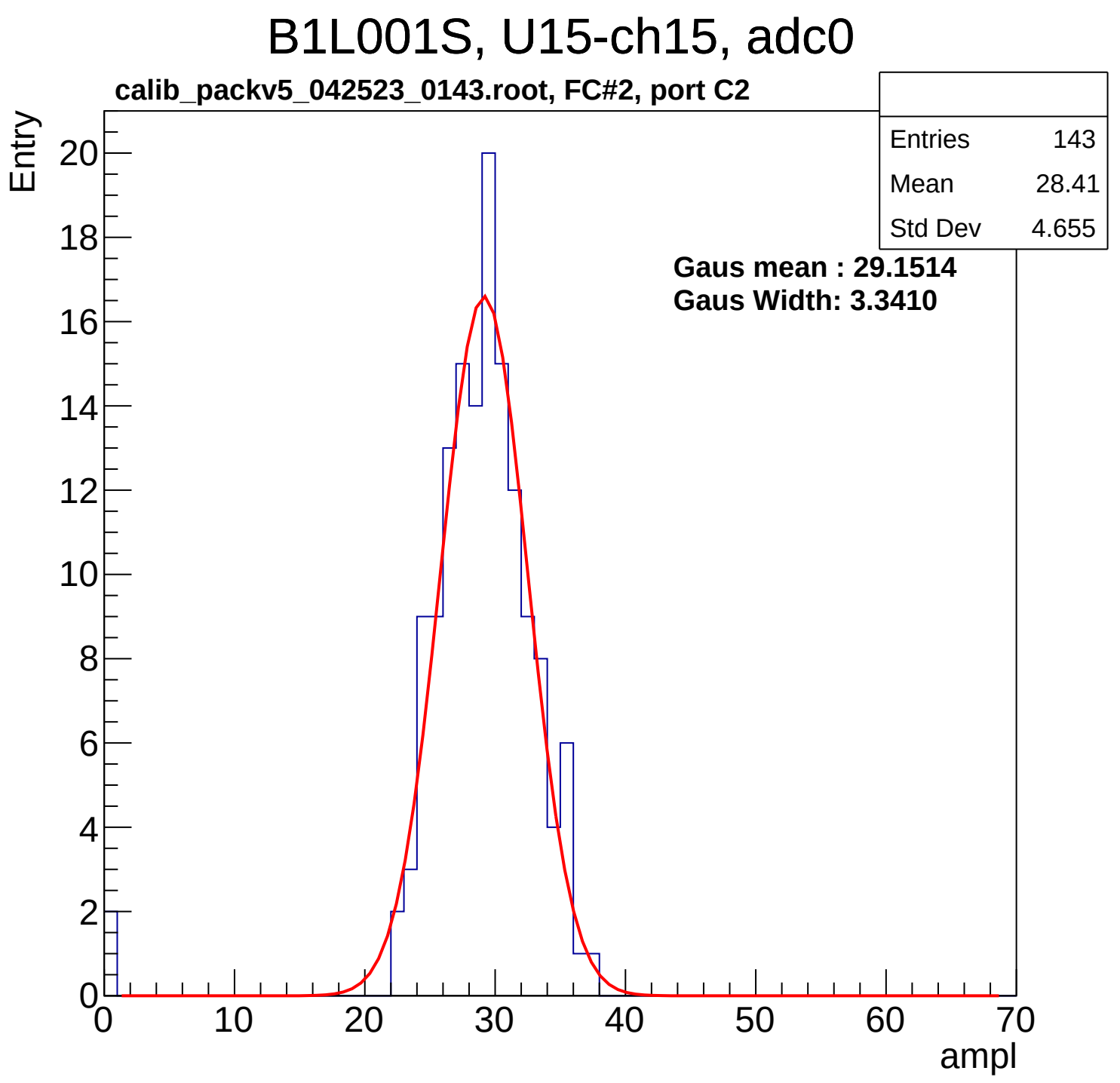
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch15, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 28.41
Std Dev 4.655
Gaus mean : 29.1514
Gaus Width: 3.3410
Entry
ampl
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6
8
10
12
14
16
18
20
0
10
20
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40
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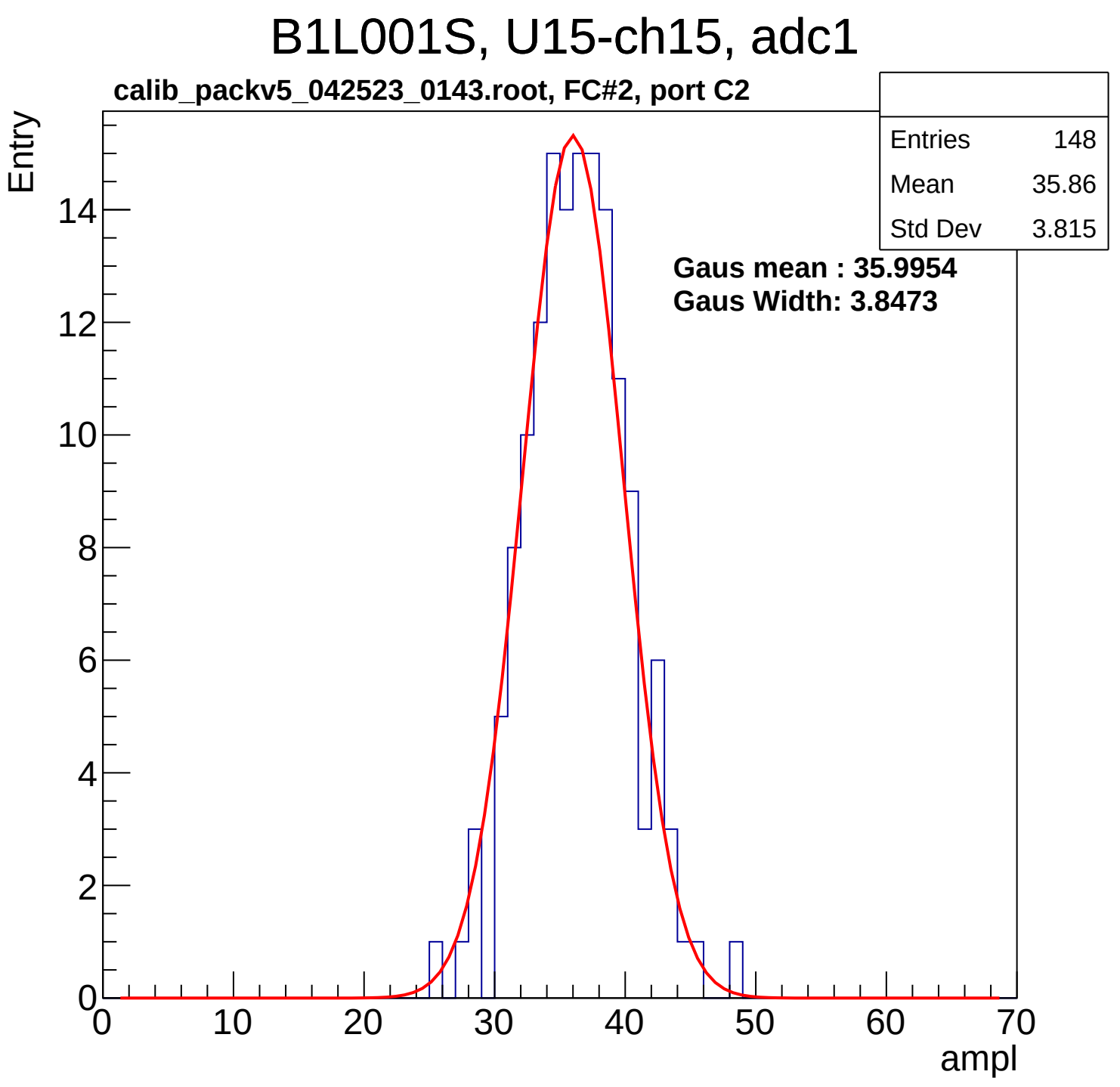
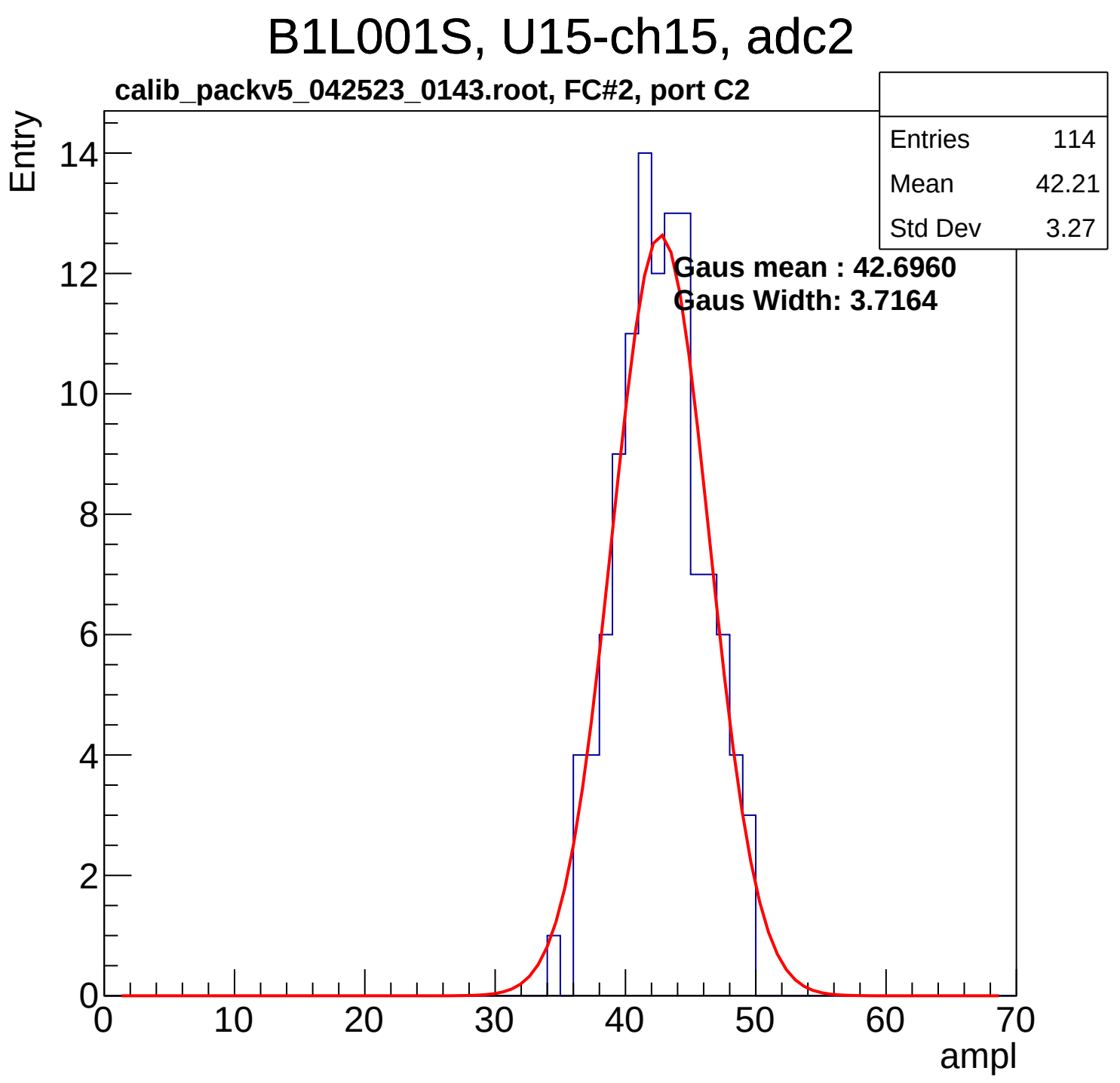

B1L001S, U15-ch15, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 35.86
Std Dev 3.815
Gaus mean : 35.9954
Gaus Width: 3.8473
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch15, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 42.21
Std Dev 3.27
Gaus mean : 42.6960
Gaus Width: 3.7164
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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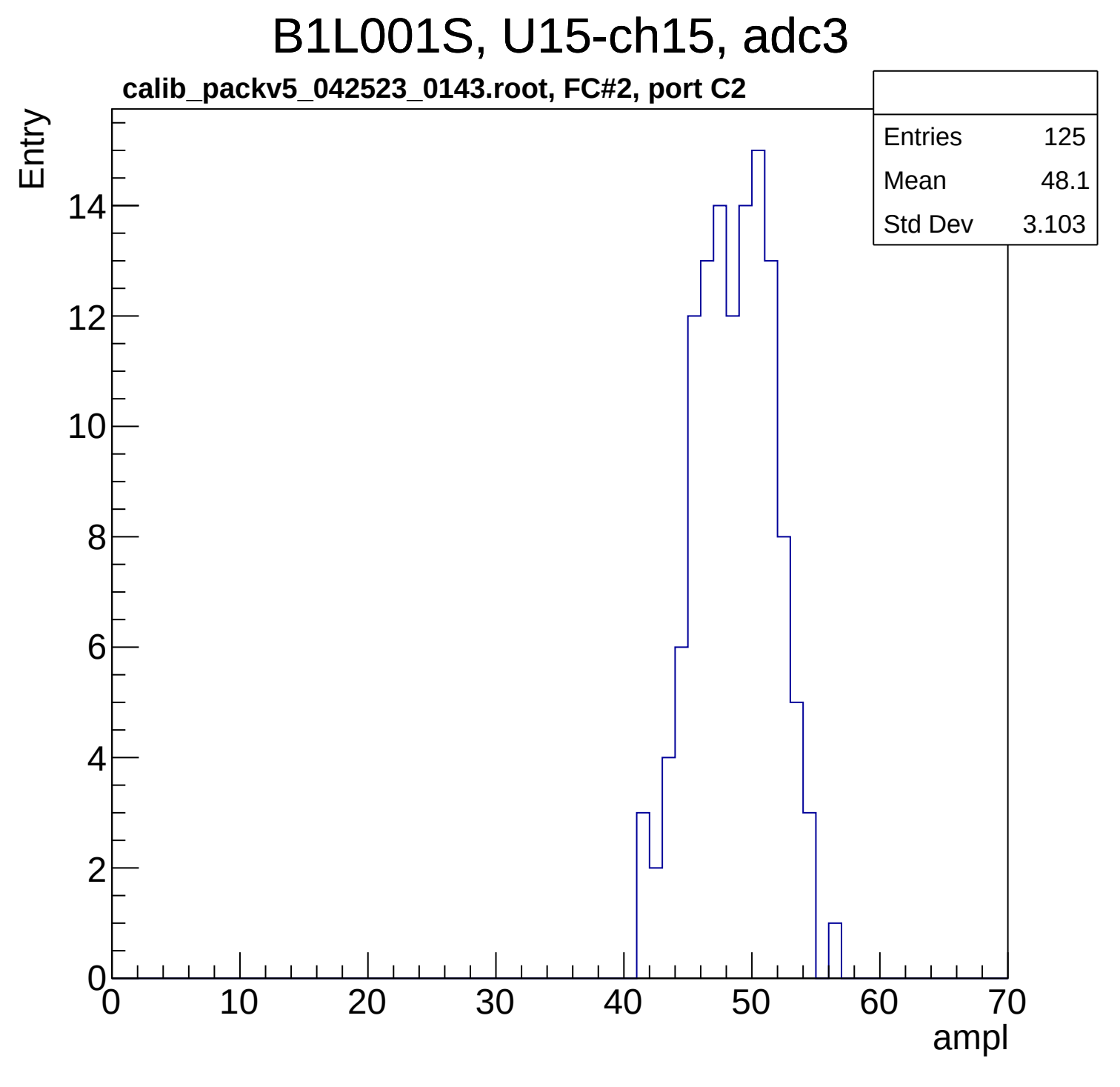
B1L001S, U15-ch15, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 48.1
Std Dev 3.103
Entry
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2
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10
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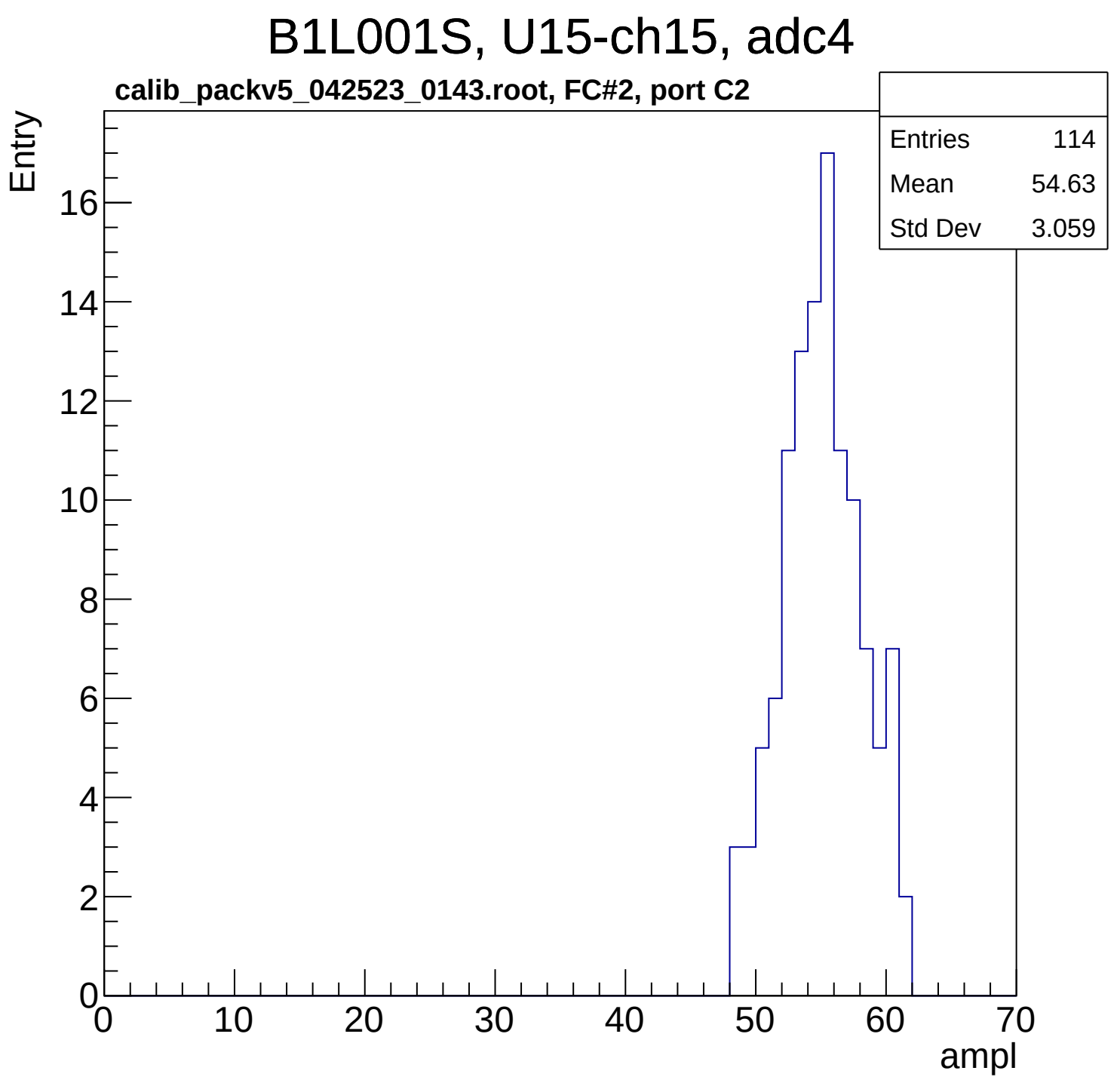
ampl


B1L001S, U15-ch15, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 54.63
Std Dev 3.059
Entry
ampl
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4
6
8
10
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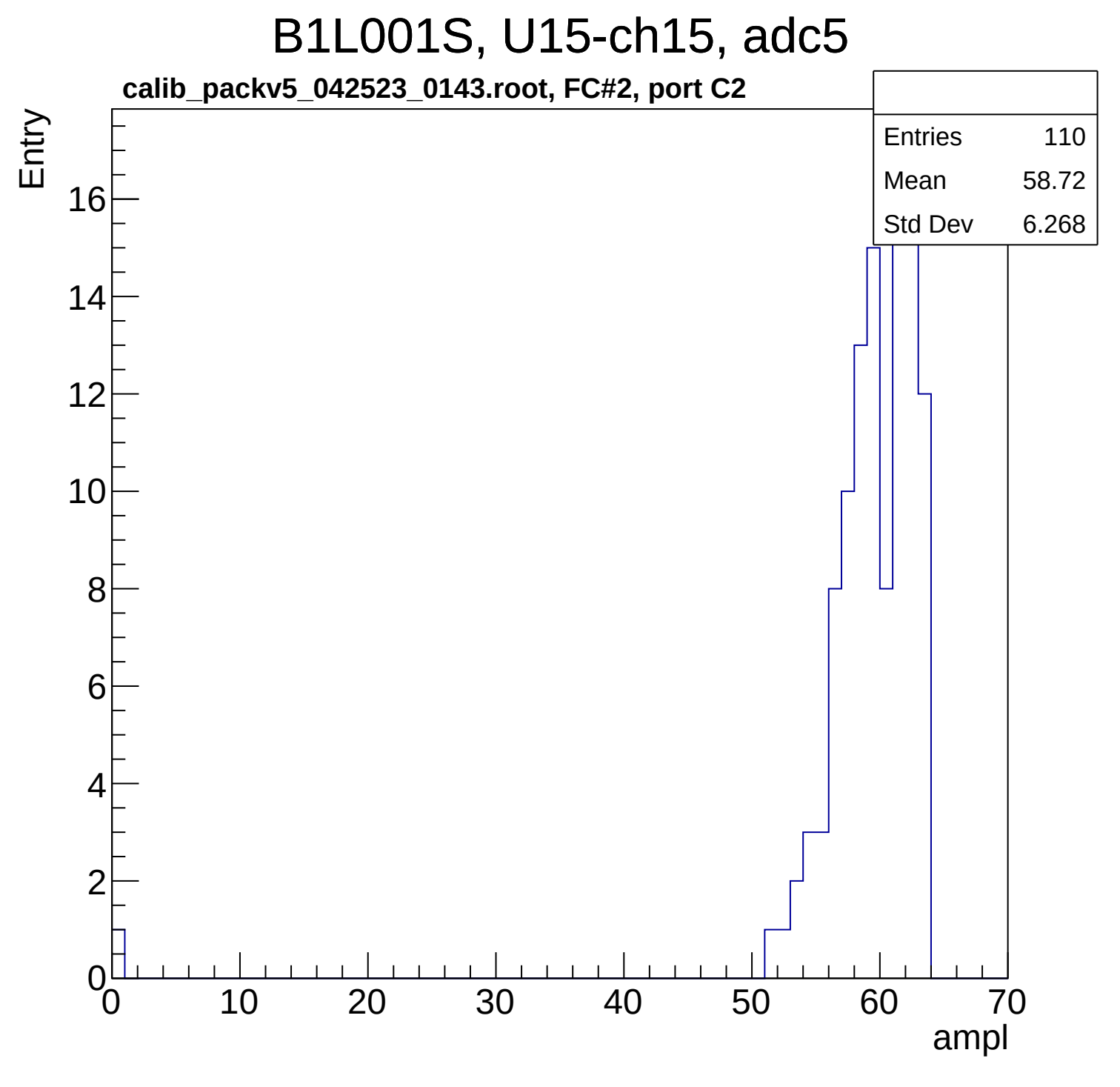
B1L001S, U15-ch15, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 58.72
Std Dev 6.268
Entry
ampl
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2
4
6
8
10
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14
16
0
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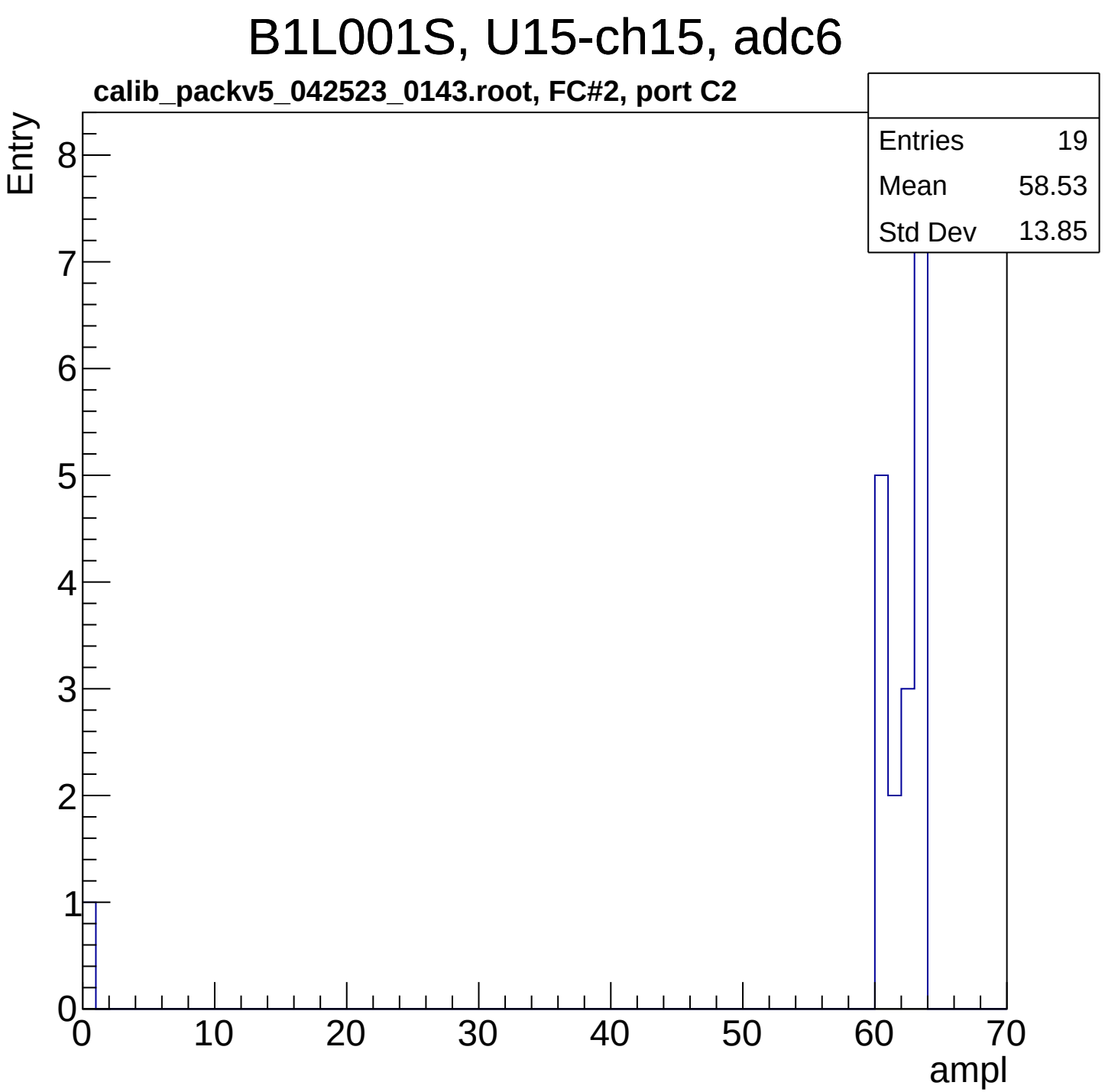
B1L001S, U15-ch15, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 19
Mean 58.53
Std Dev 13.85
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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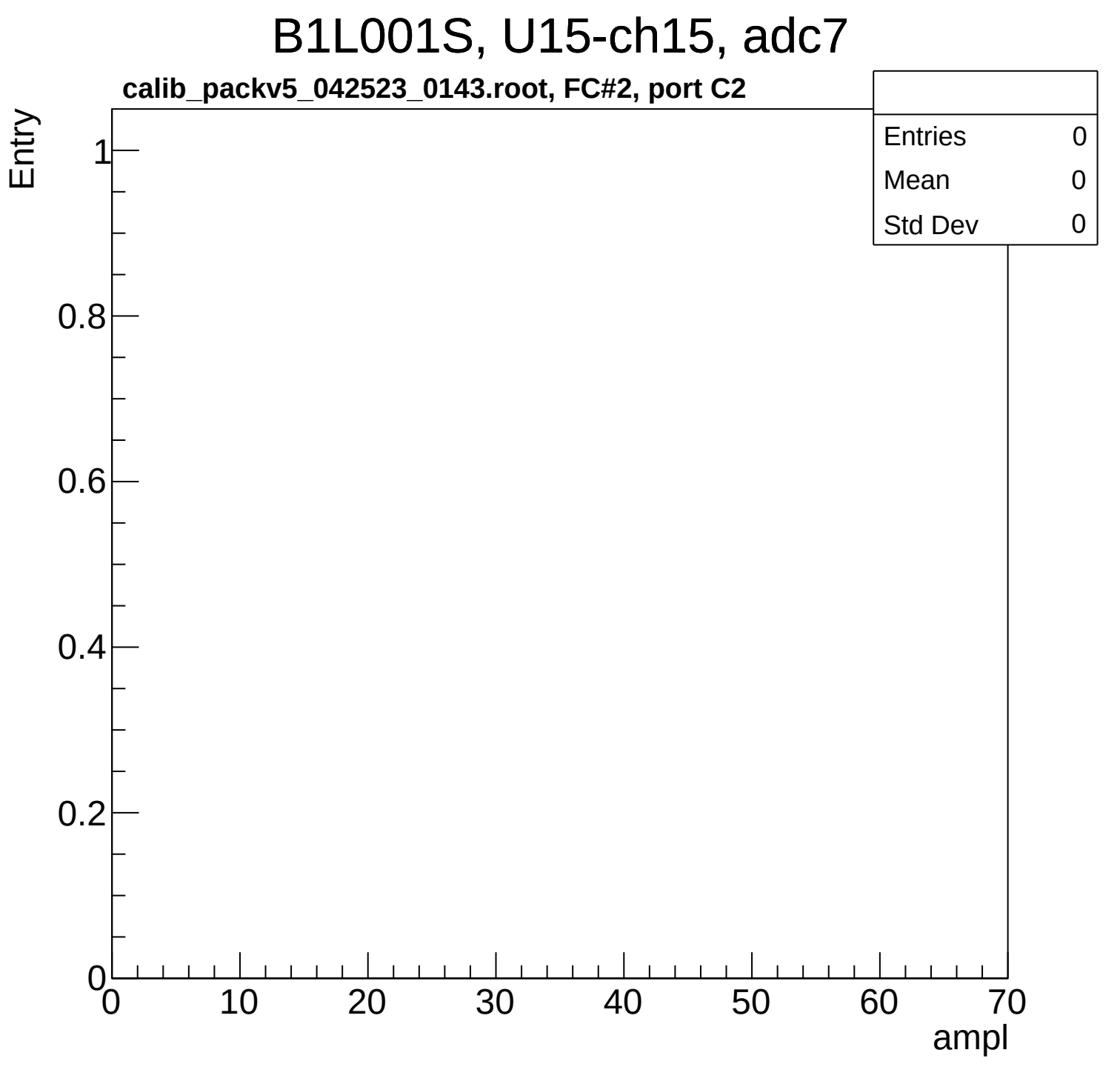
B1L001S, U15-ch15, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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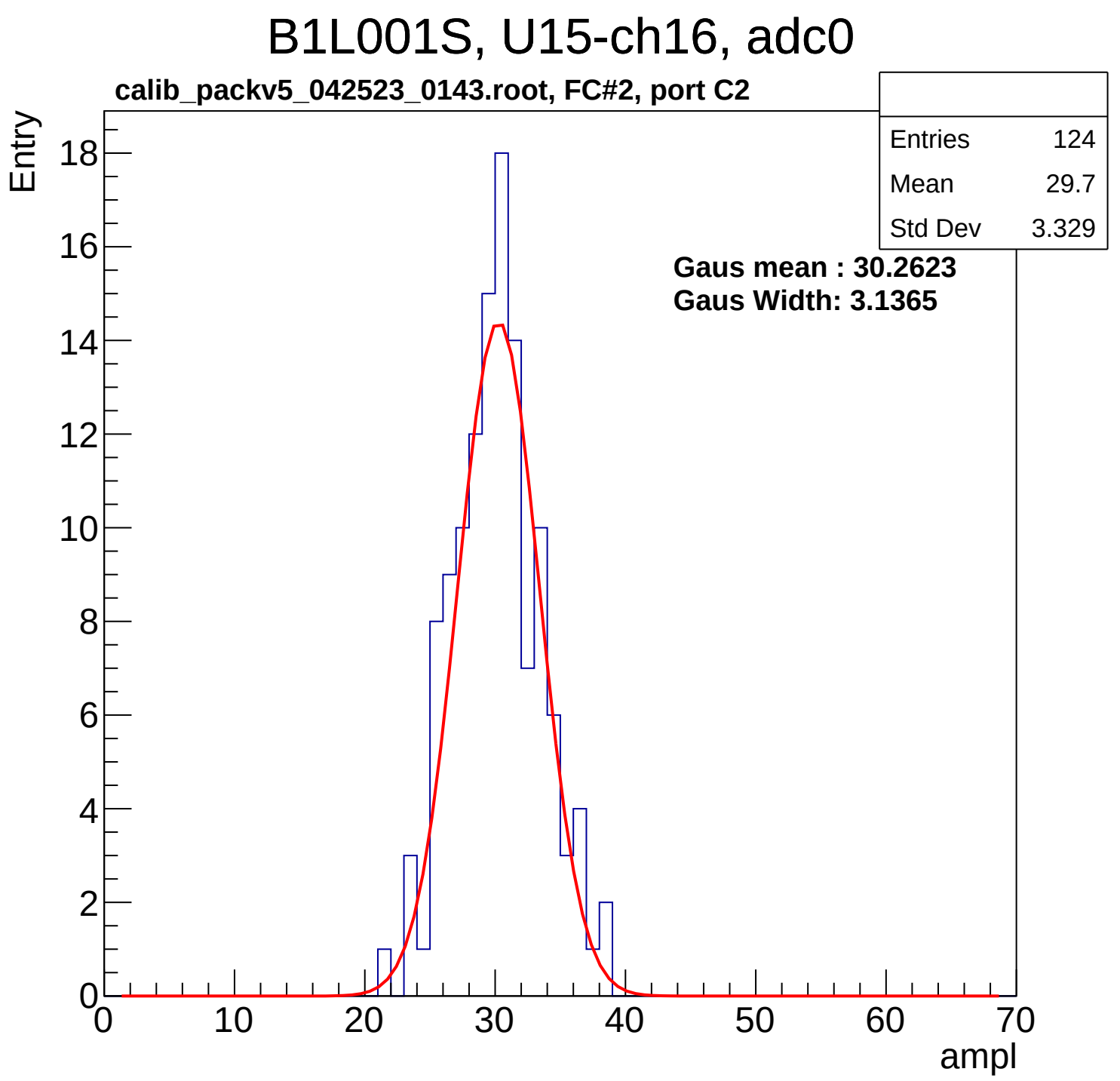
40
50
60
70
ampl


B1L001S, U15-ch16, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 29.7
Std Dev 3.329
Gaus mean : 30.2623
Gaus Width: 3.1365
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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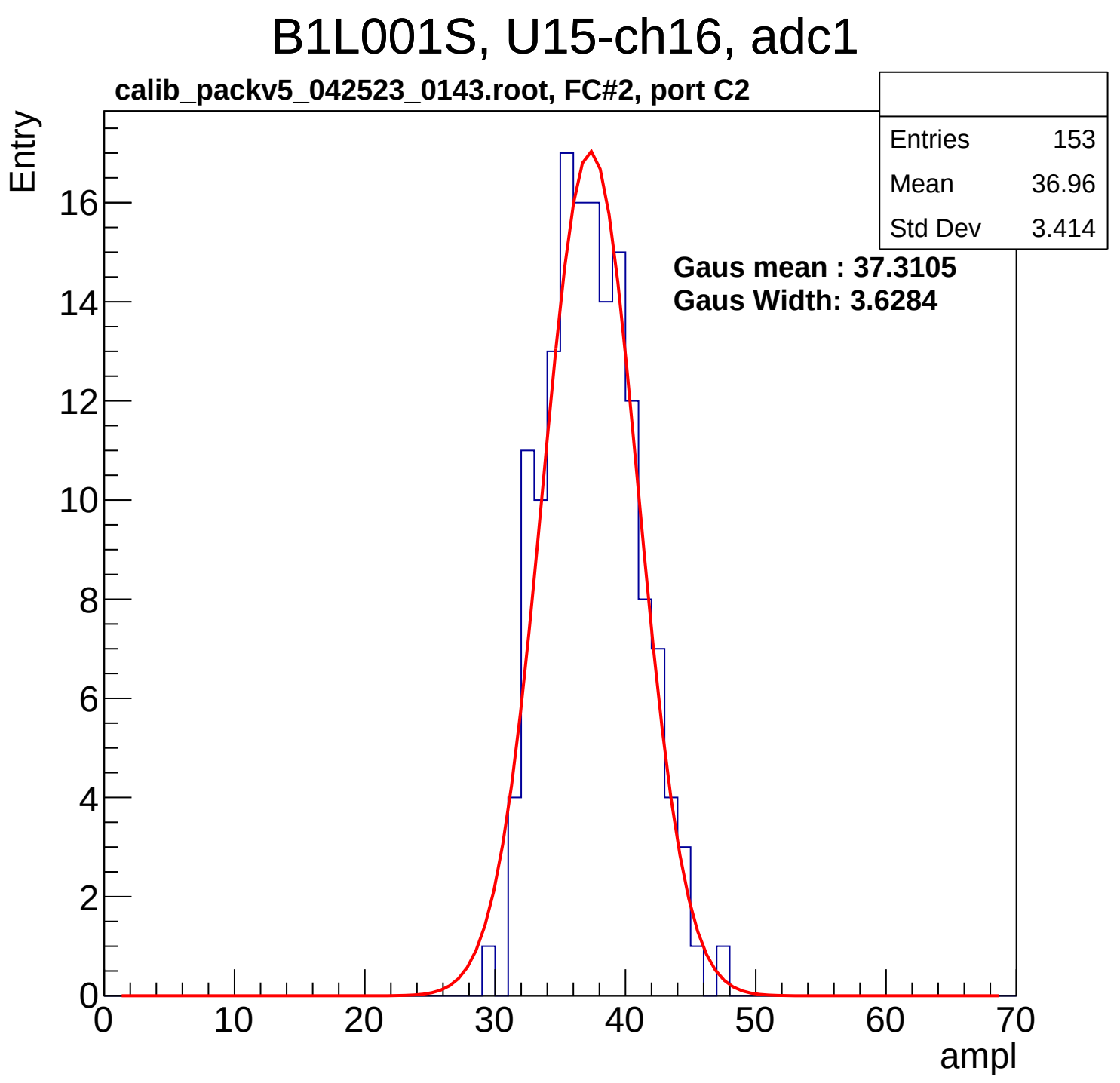
B1L001S, U15-ch16, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 36.96
Std Dev 3.414
Gaus mean : 37.3105
Gaus Width: 3.6284
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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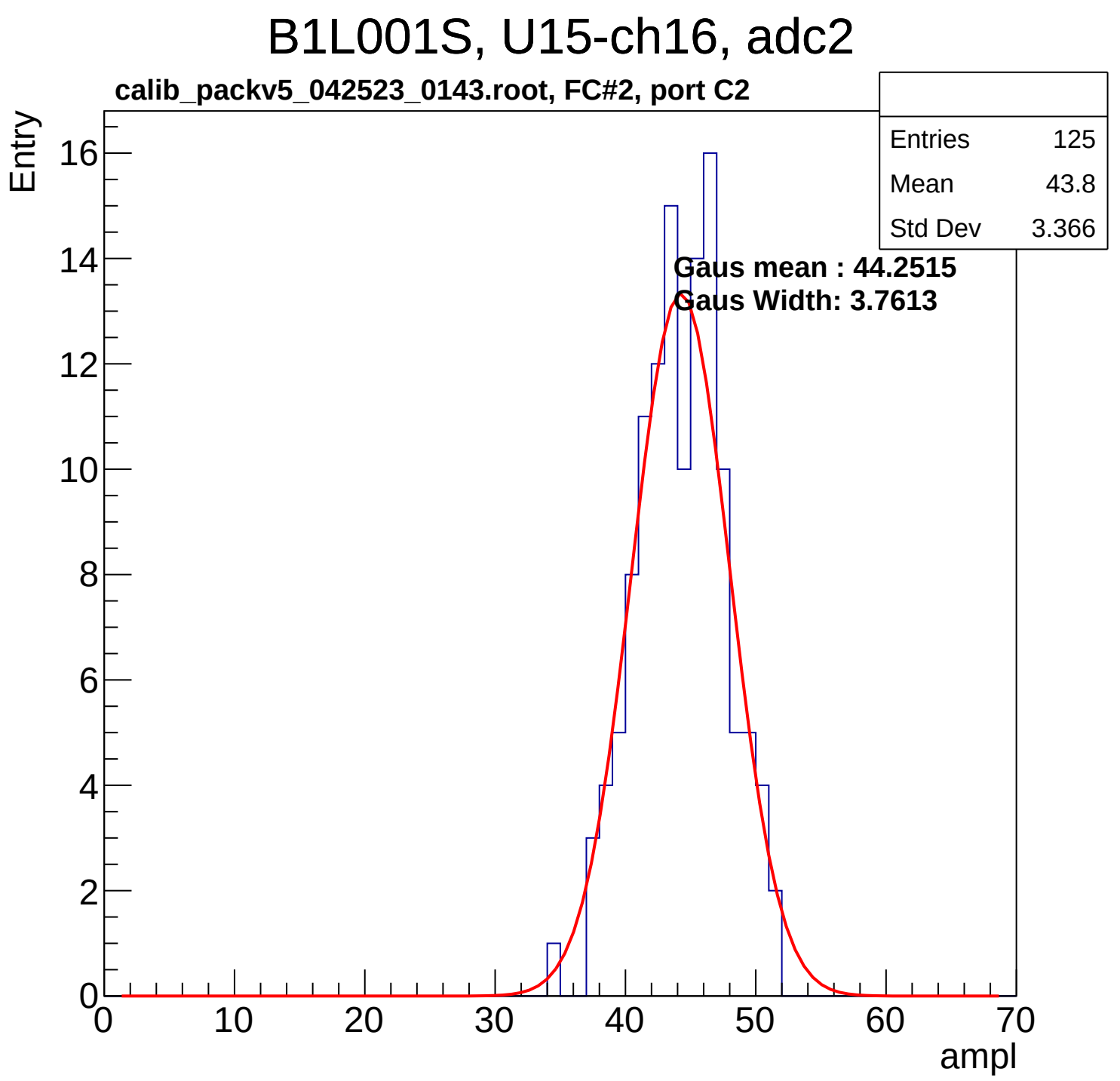
B1L001S, U15-ch16, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 43.8
Std Dev 3.366
Gaus mean : 44.2515
Gaus Width: 3.7613
Entry
ampl
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4
6
8
10
12
14
16
0
10
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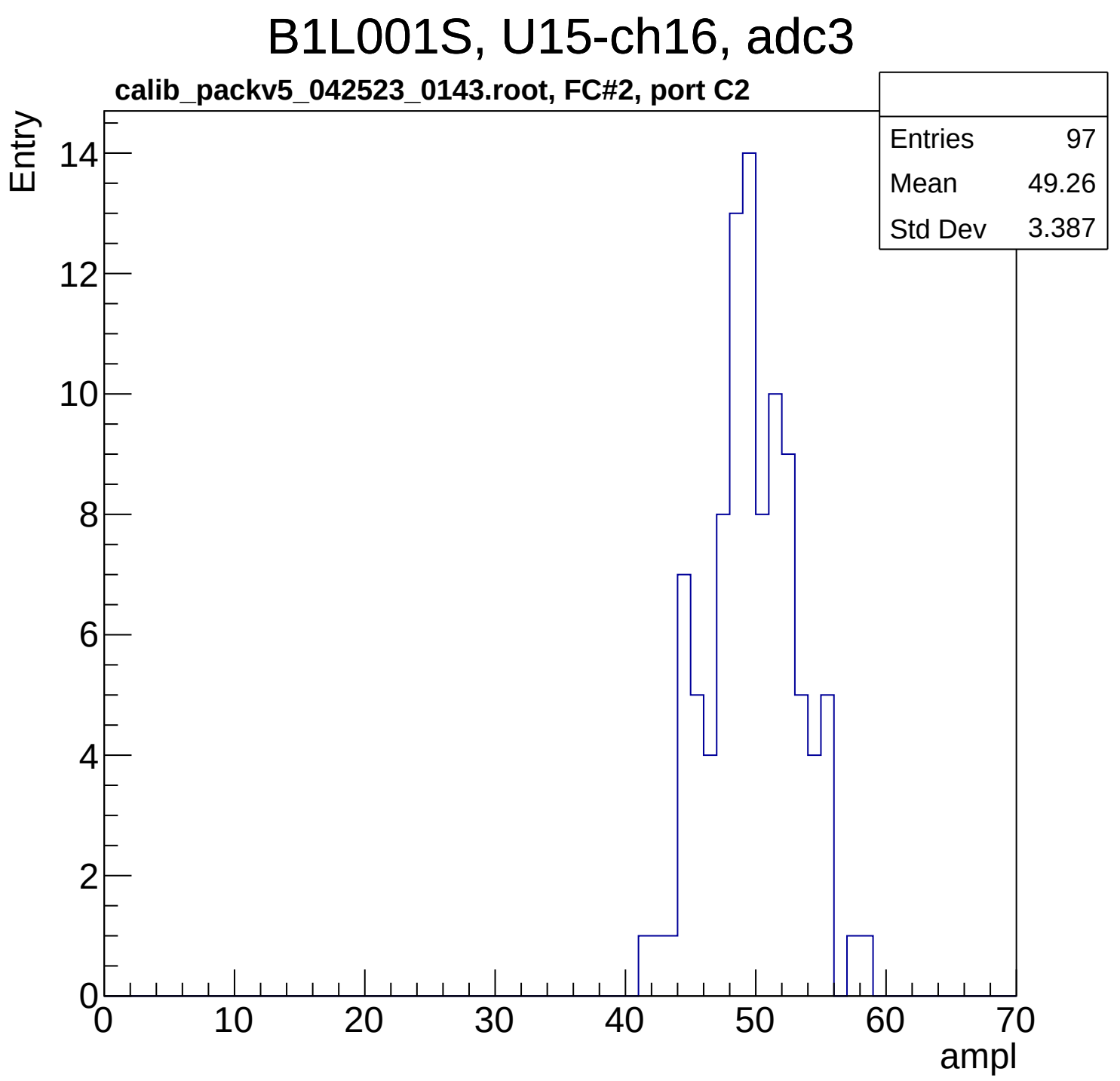
B1L001S, U15-ch16, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 49.26
Std Dev 3.387
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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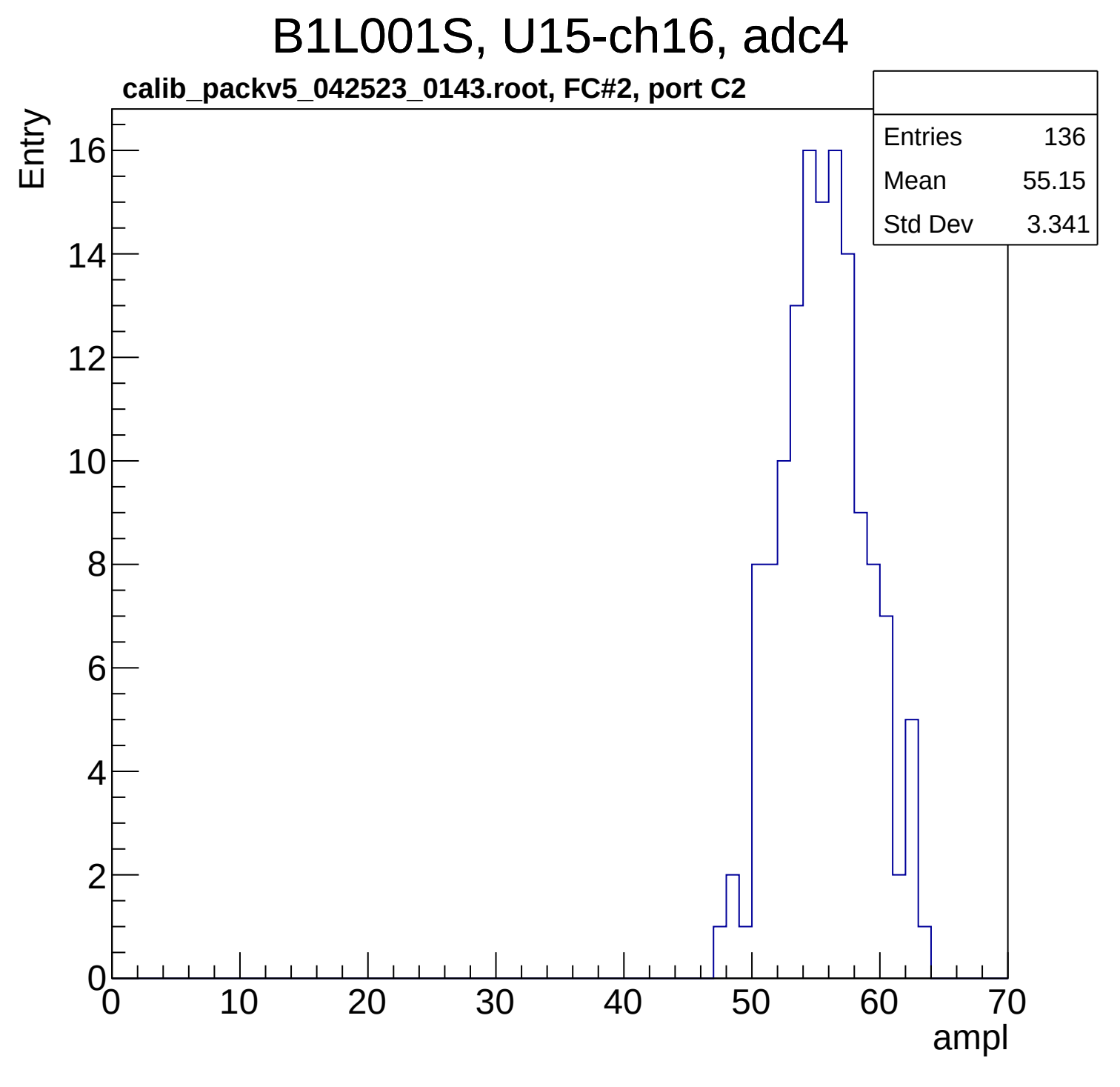
B1L001S, U15-ch16, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 55.15
Std Dev 3.341
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
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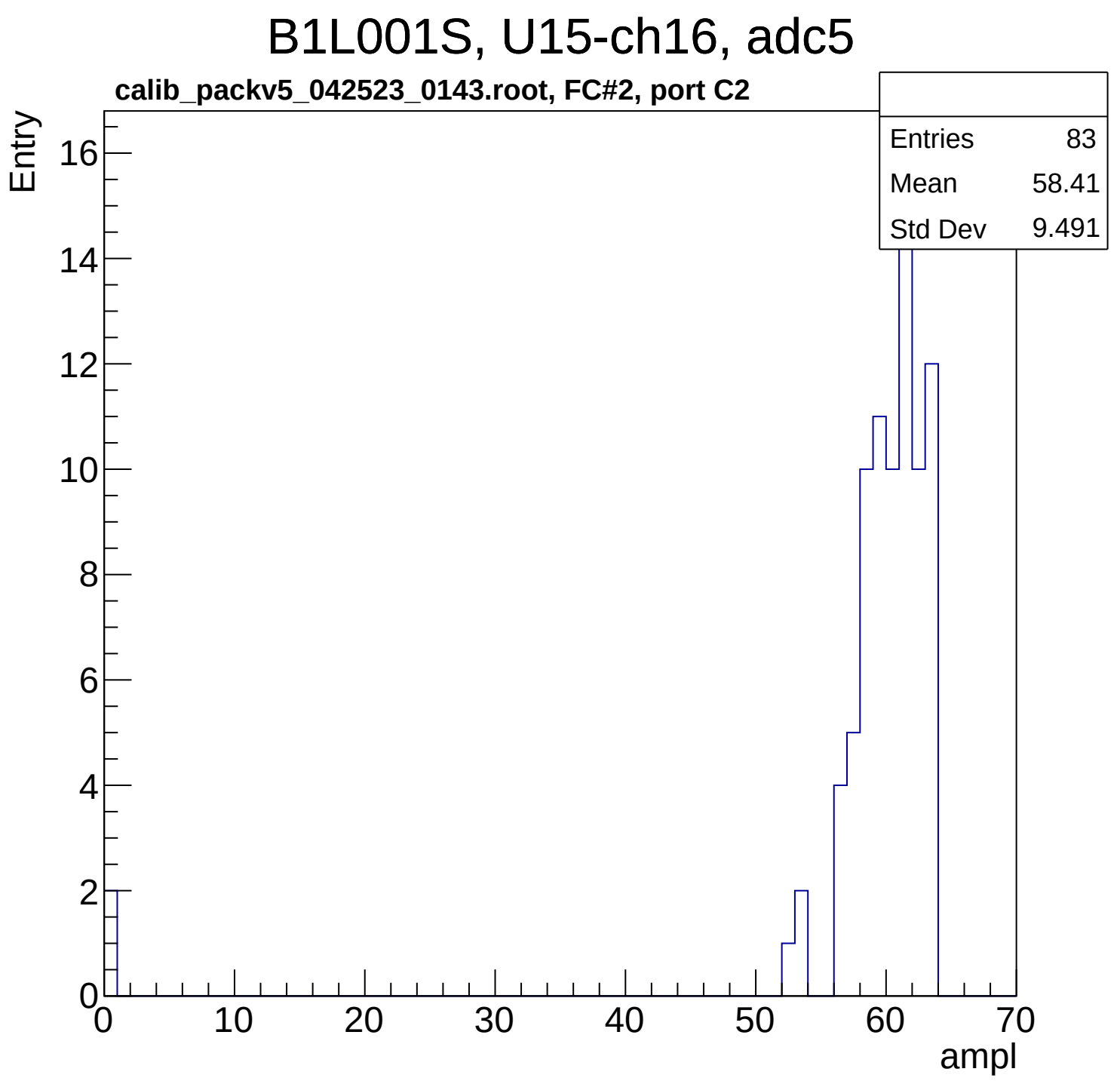
B1L001S, U15-ch16, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 58.41
Std Dev 9.491
Entry
ampl
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4
6
8
10
12
14
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0
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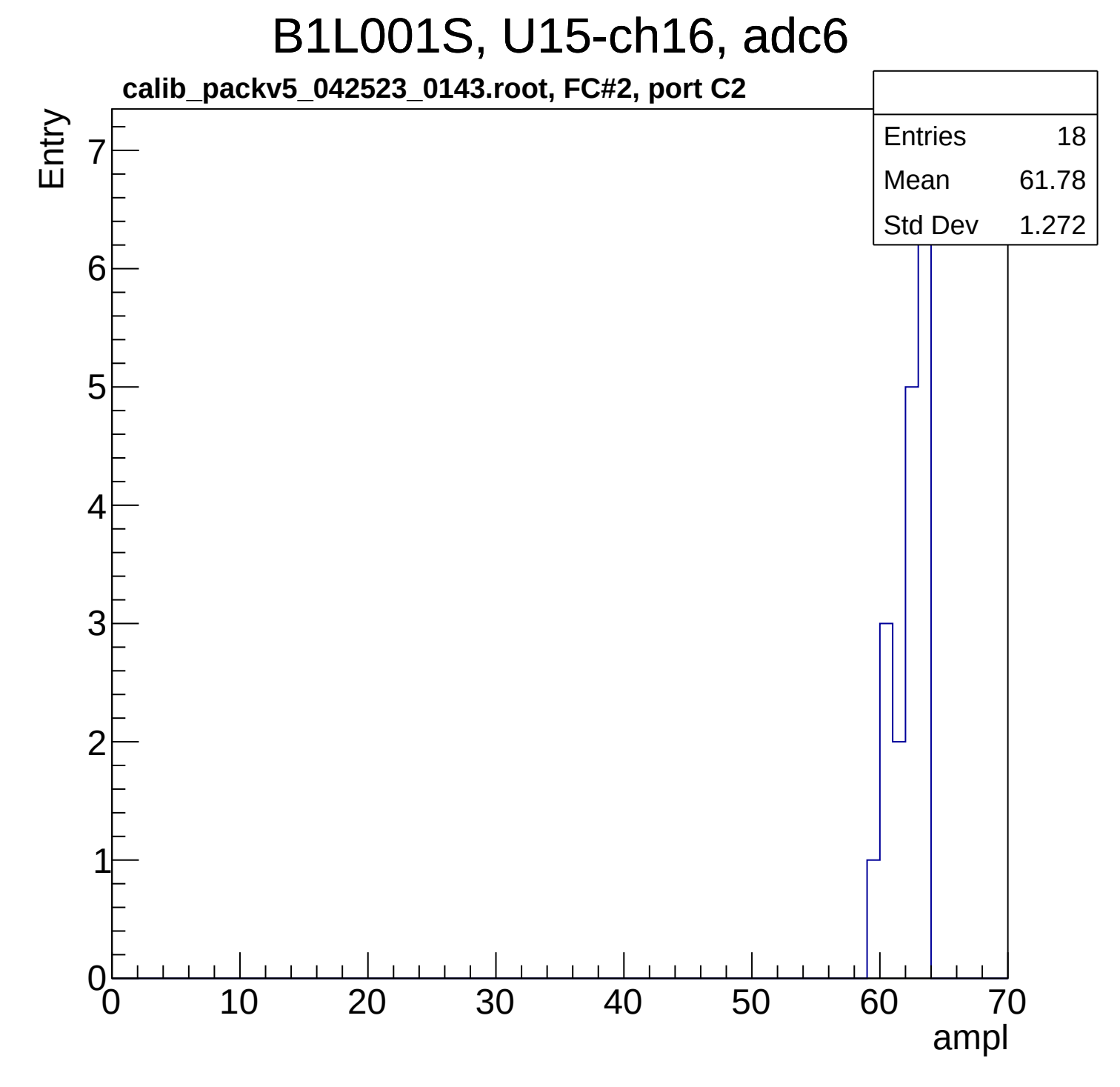
B1L001S, U15-ch16, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 61.78
Std Dev 1.272
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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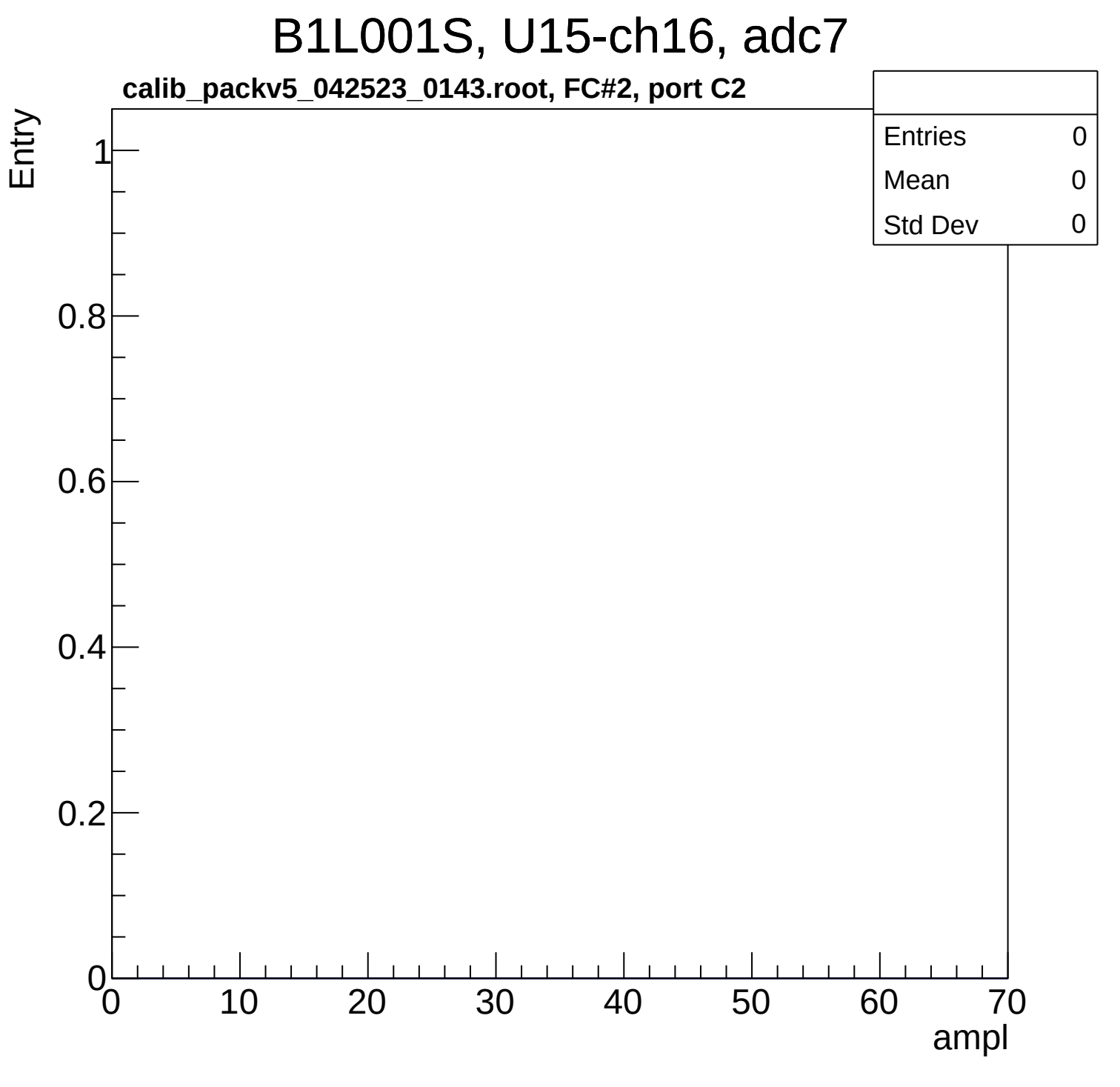
B1L001S, U15-ch16, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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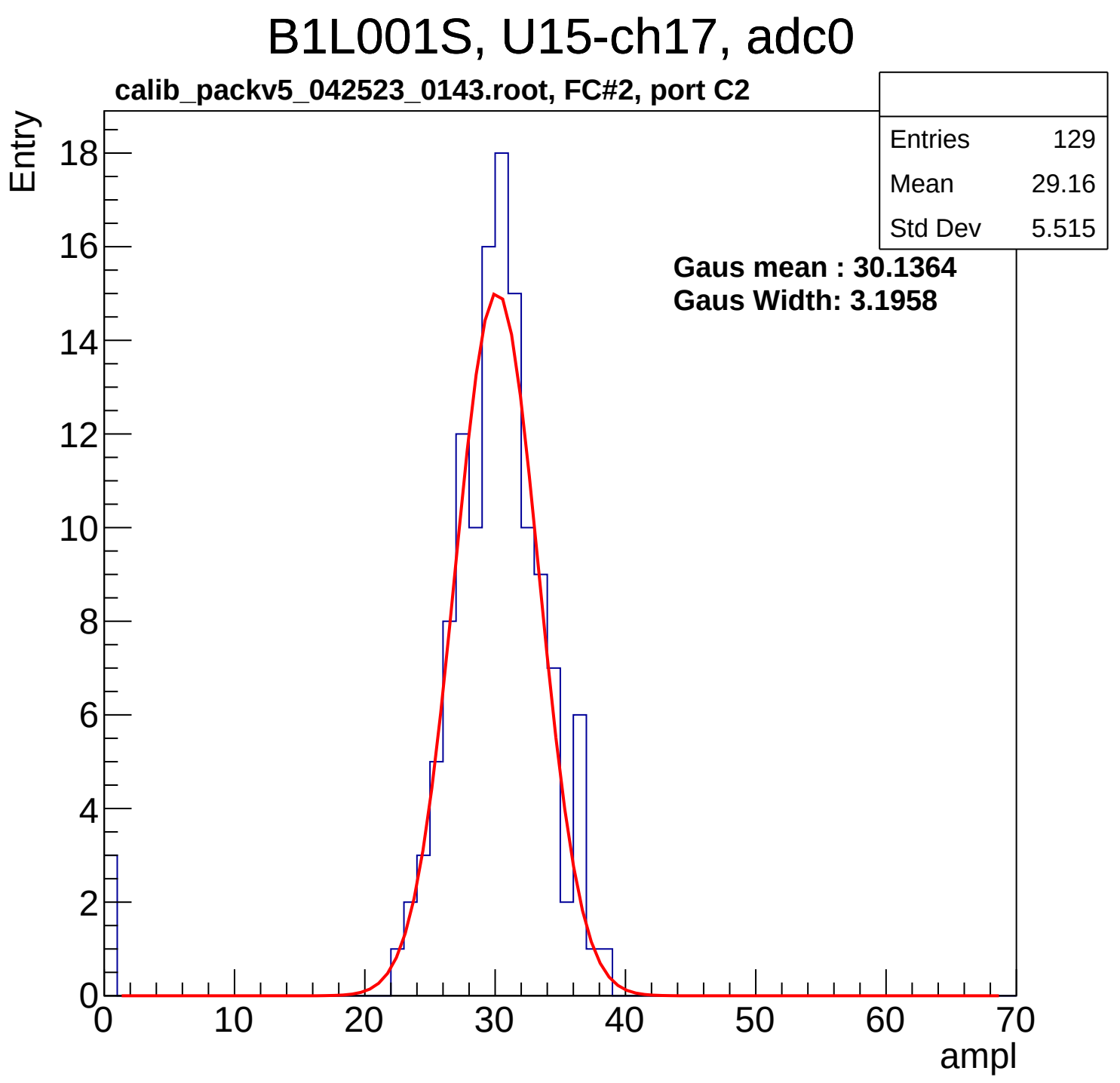
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch17, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 29.16
Std Dev 5.515
Gaus mean : 30.1364
Gaus Width: 3.1958
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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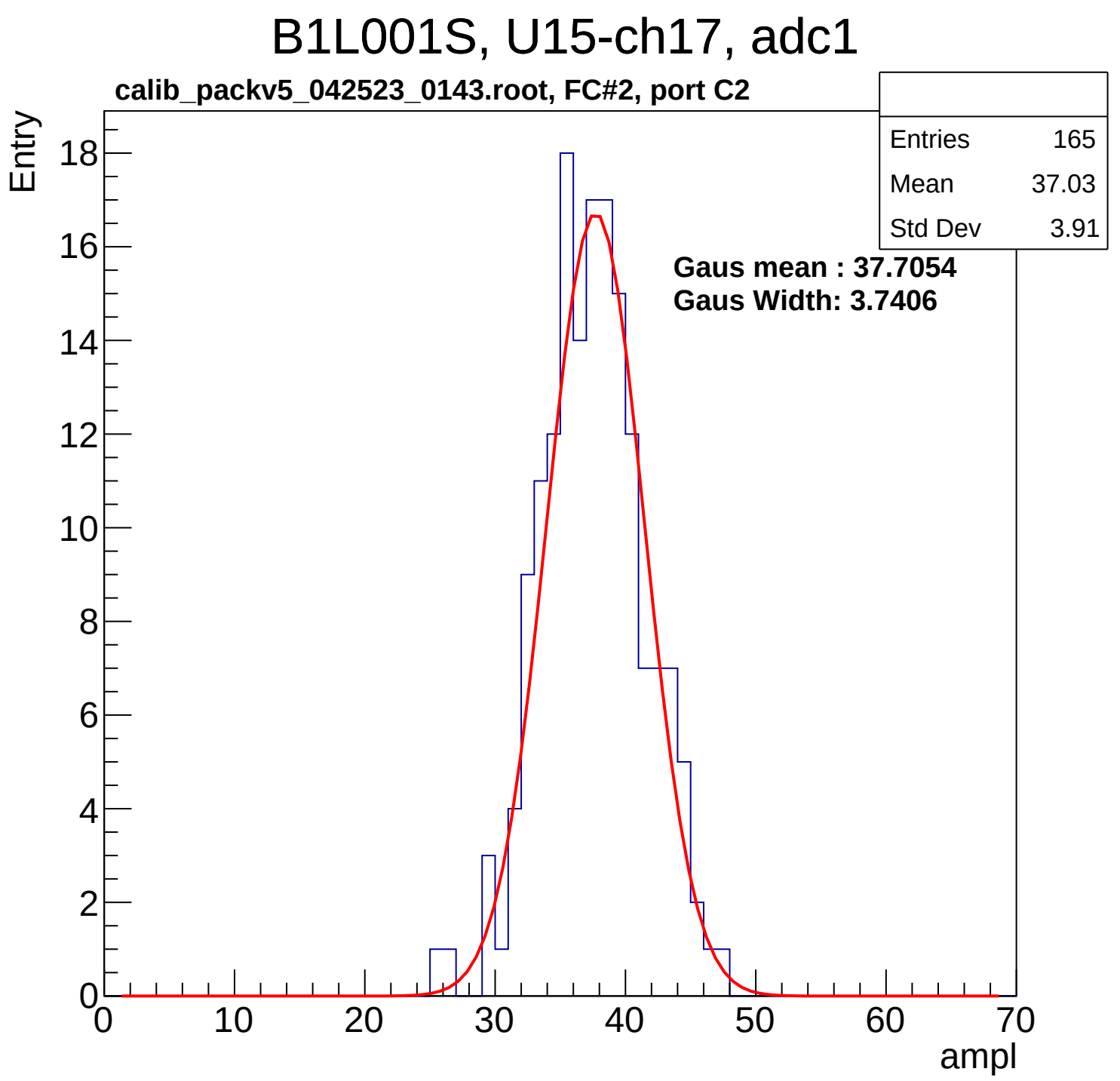
B1L001S, U15-ch17, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 165
Mean 37.03
Std Dev 3.91
Gaus mean : 37.7054
Gaus Width: 3.7406
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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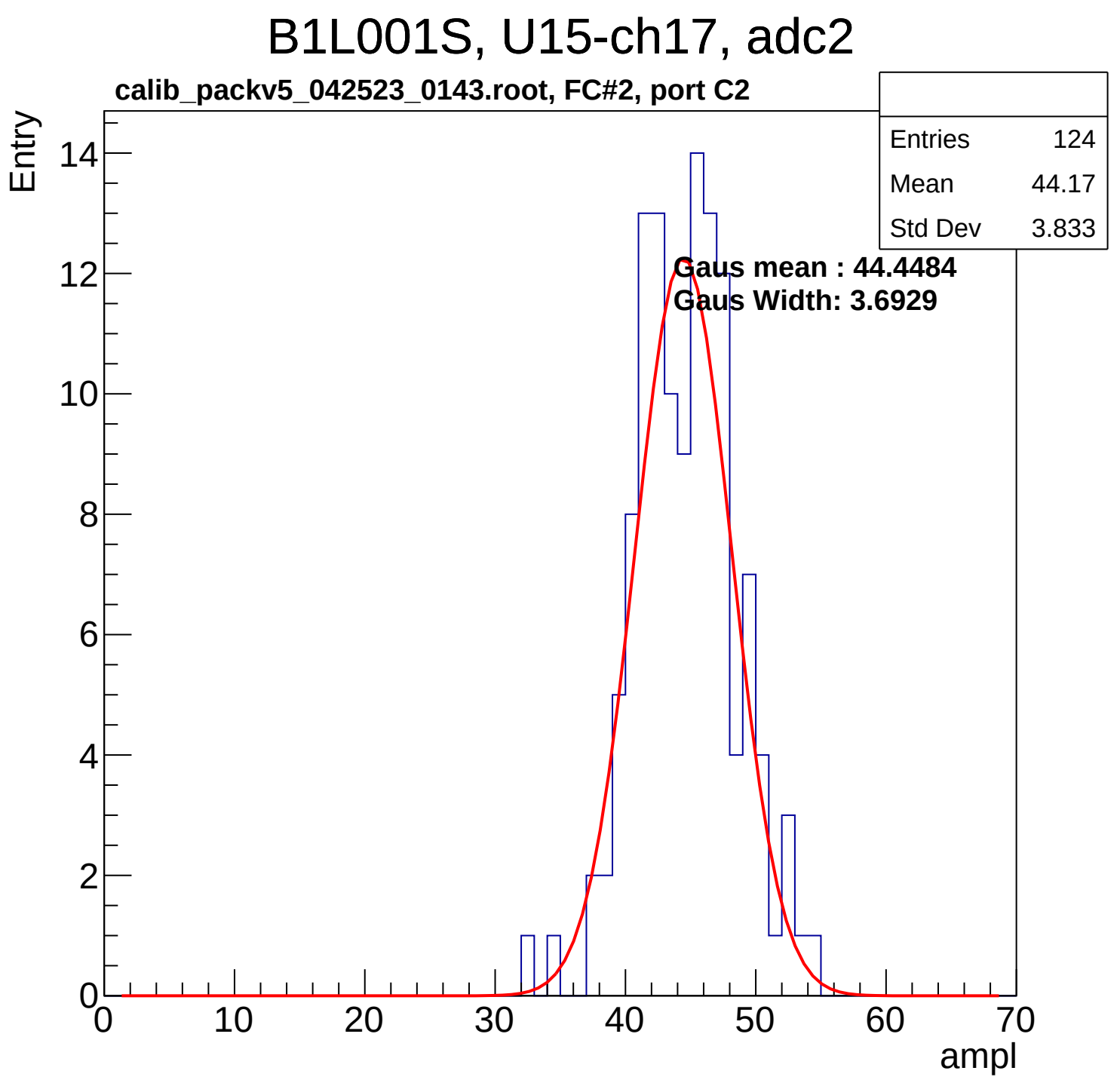
B1L001S, U15-ch17, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 44.17
Std Dev 3.833
Gaus mean : 44.4484
Gaus Width: 3.6929
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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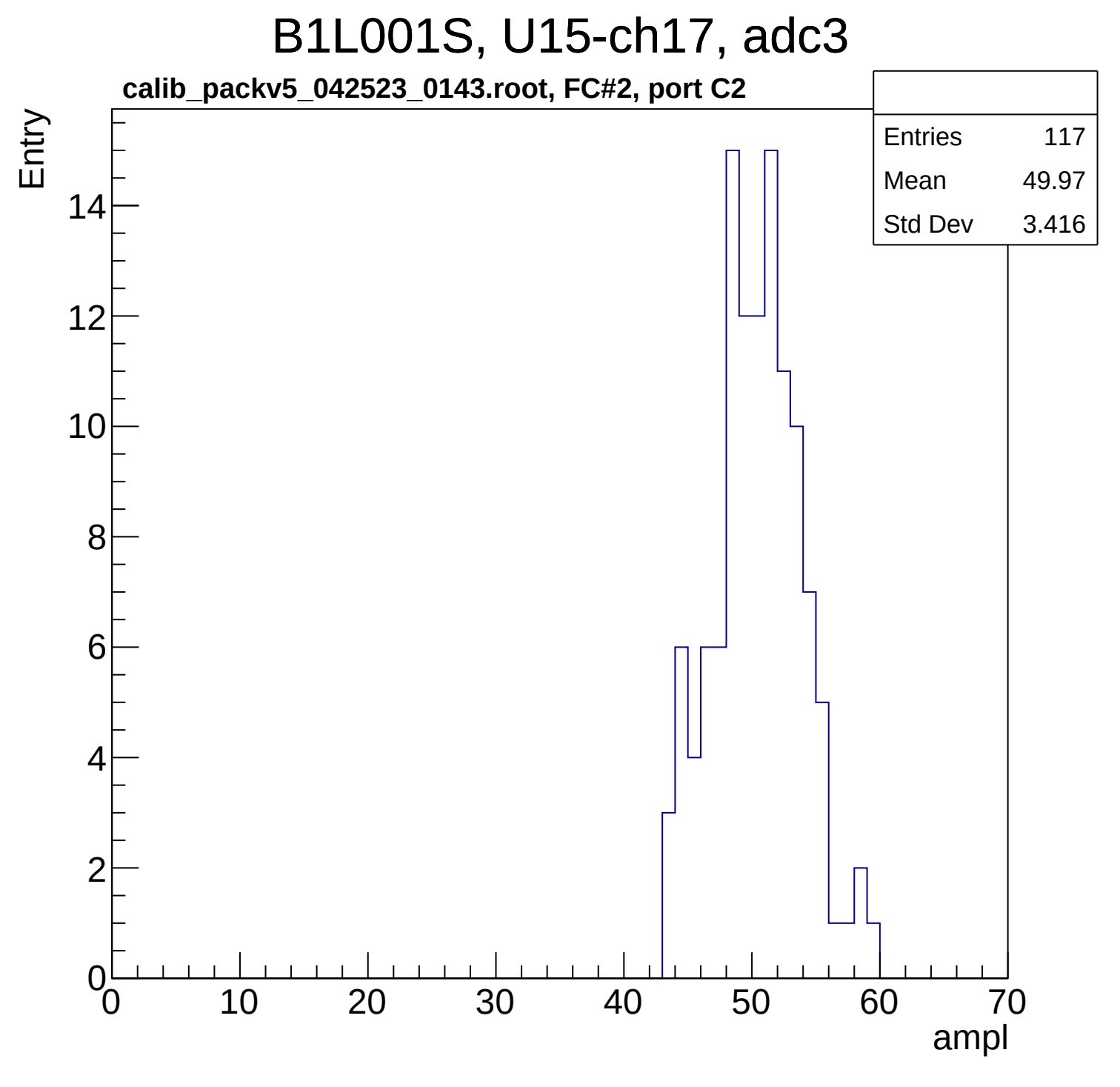
B1L001S, U15-ch17, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 49.97
Std Dev 3.416
Entry
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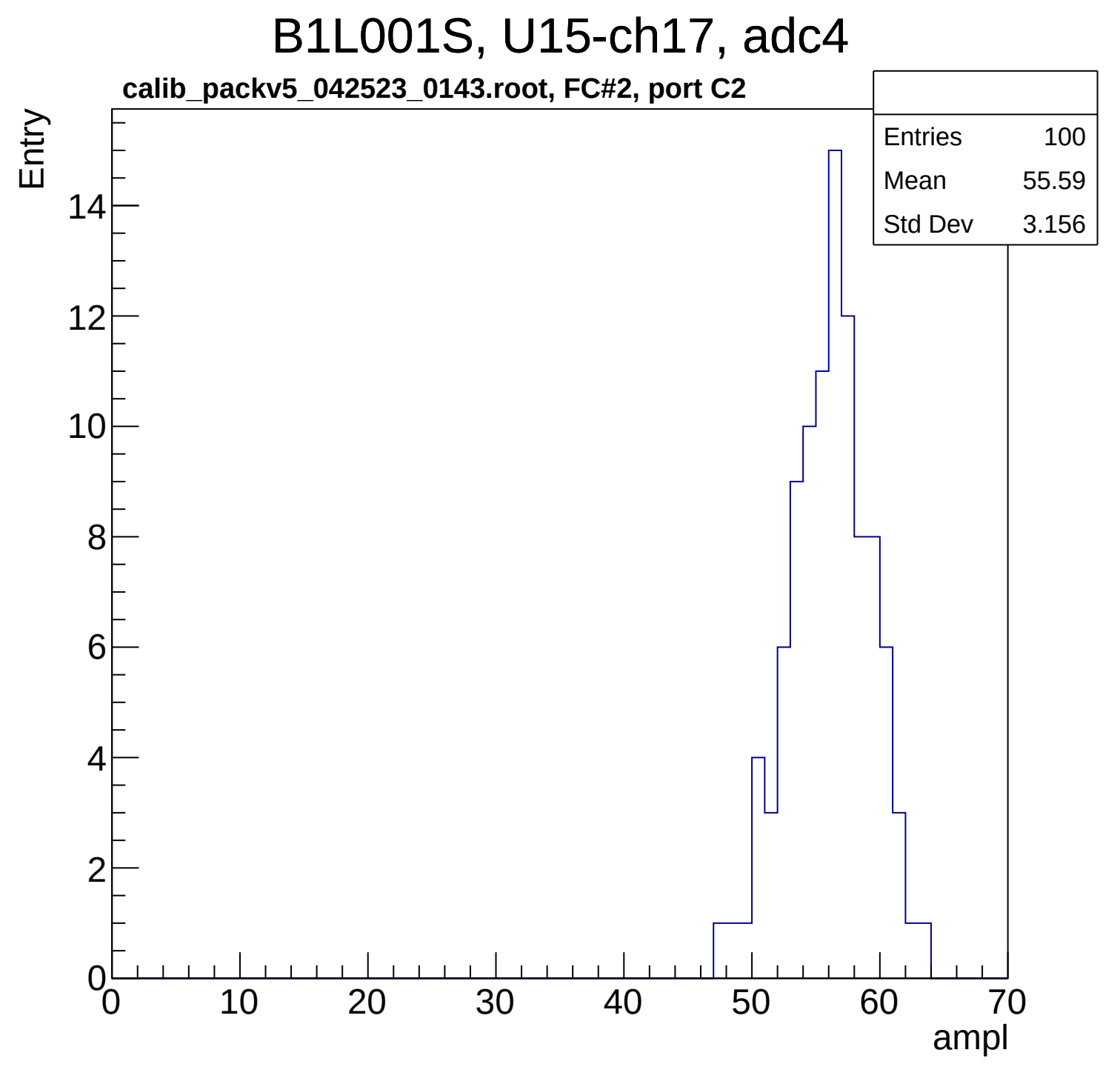
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch17, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 55.59
Std Dev 3.156
Entry
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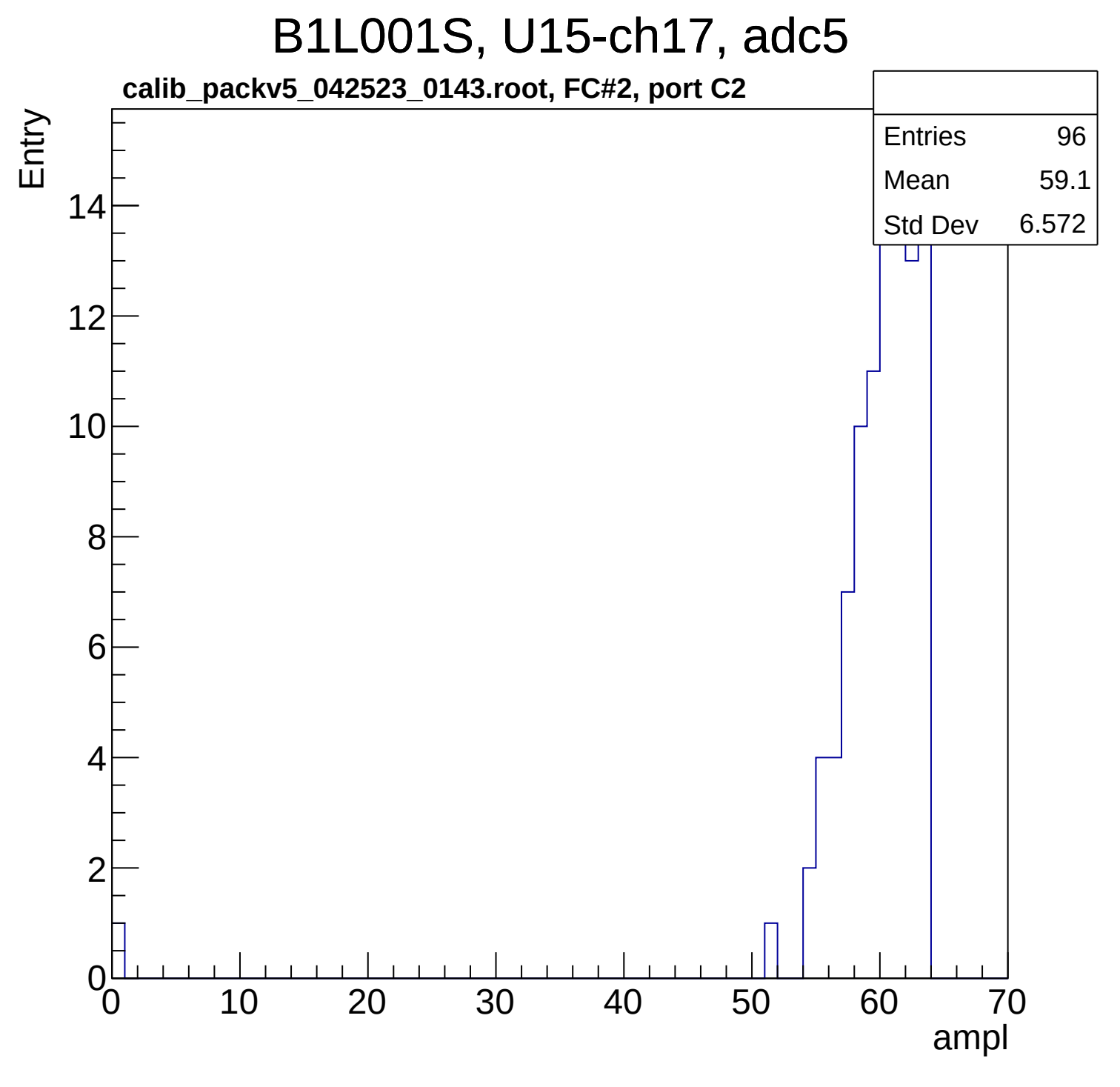
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch17, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 59.1
Std Dev 6.572
Entry
ampl
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2
4
6
8
10
12
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0
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20
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40
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60
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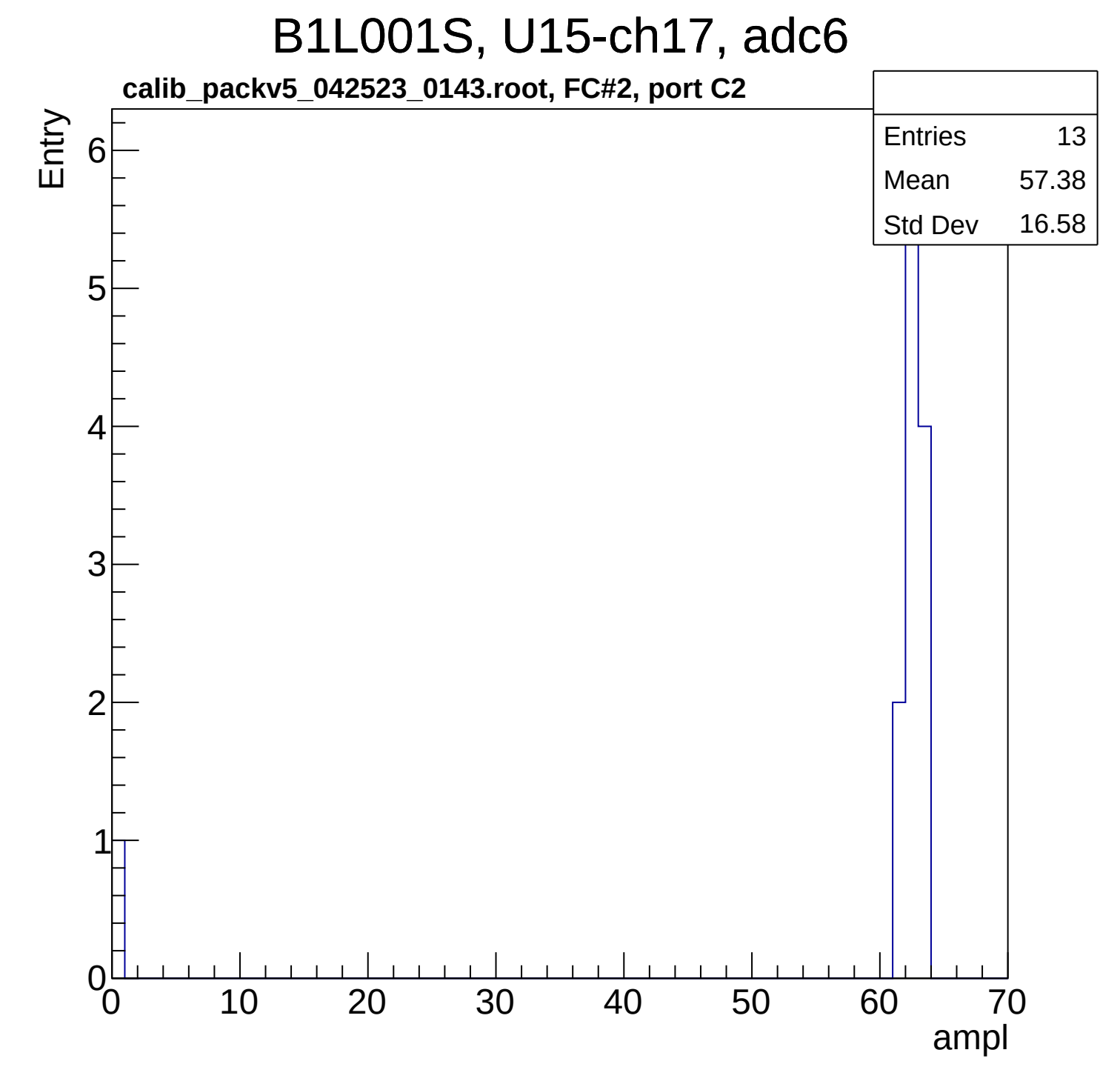
B1L001S, U15-ch17, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 57.38
Std Dev 16.58
Entry
ampl
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1
2
3
4
5
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0
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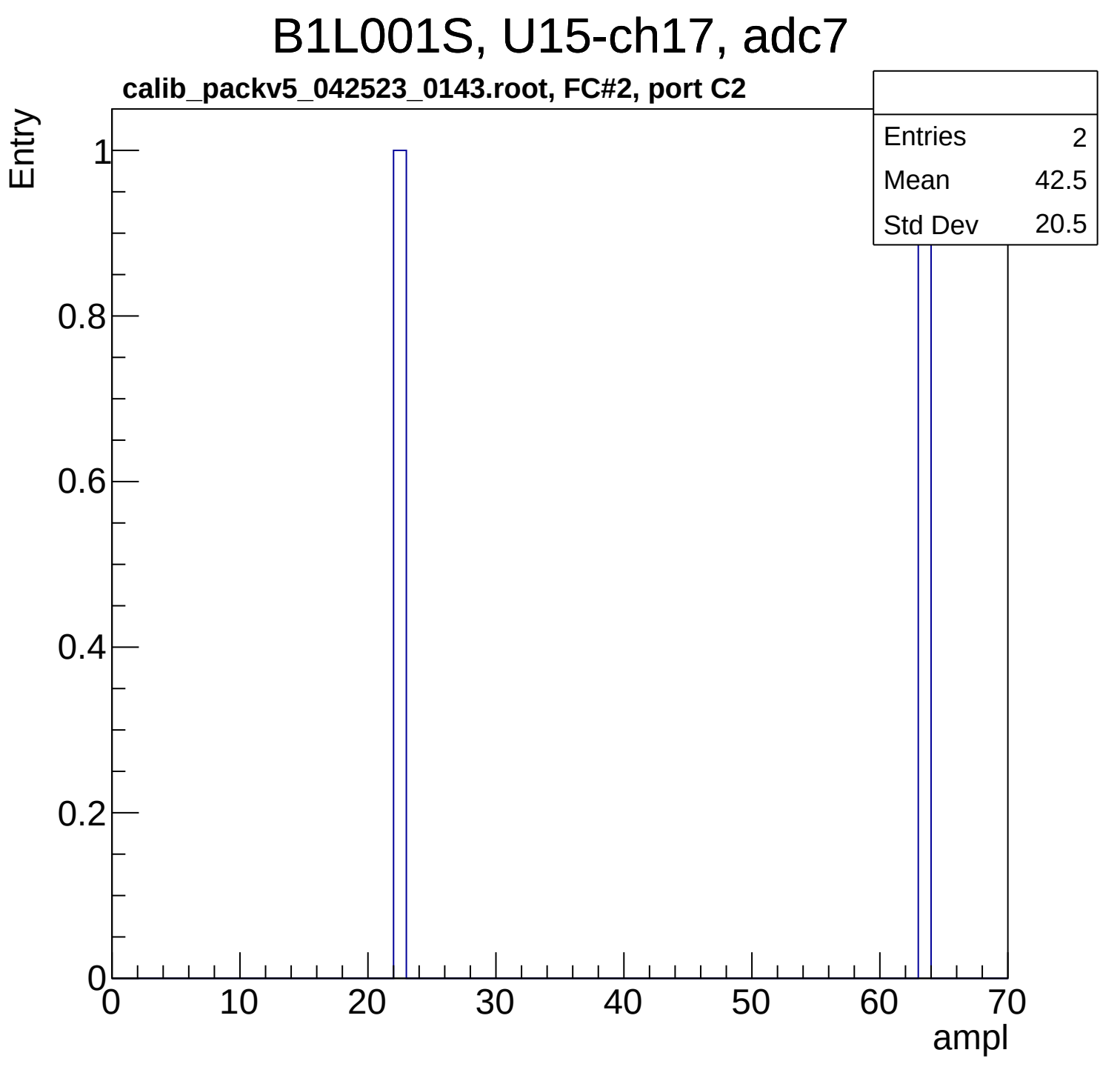
20
30
40
50
60
70


B1L001S, U15-ch17, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
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0.4
0.6
0.8
1
0
10
20
30
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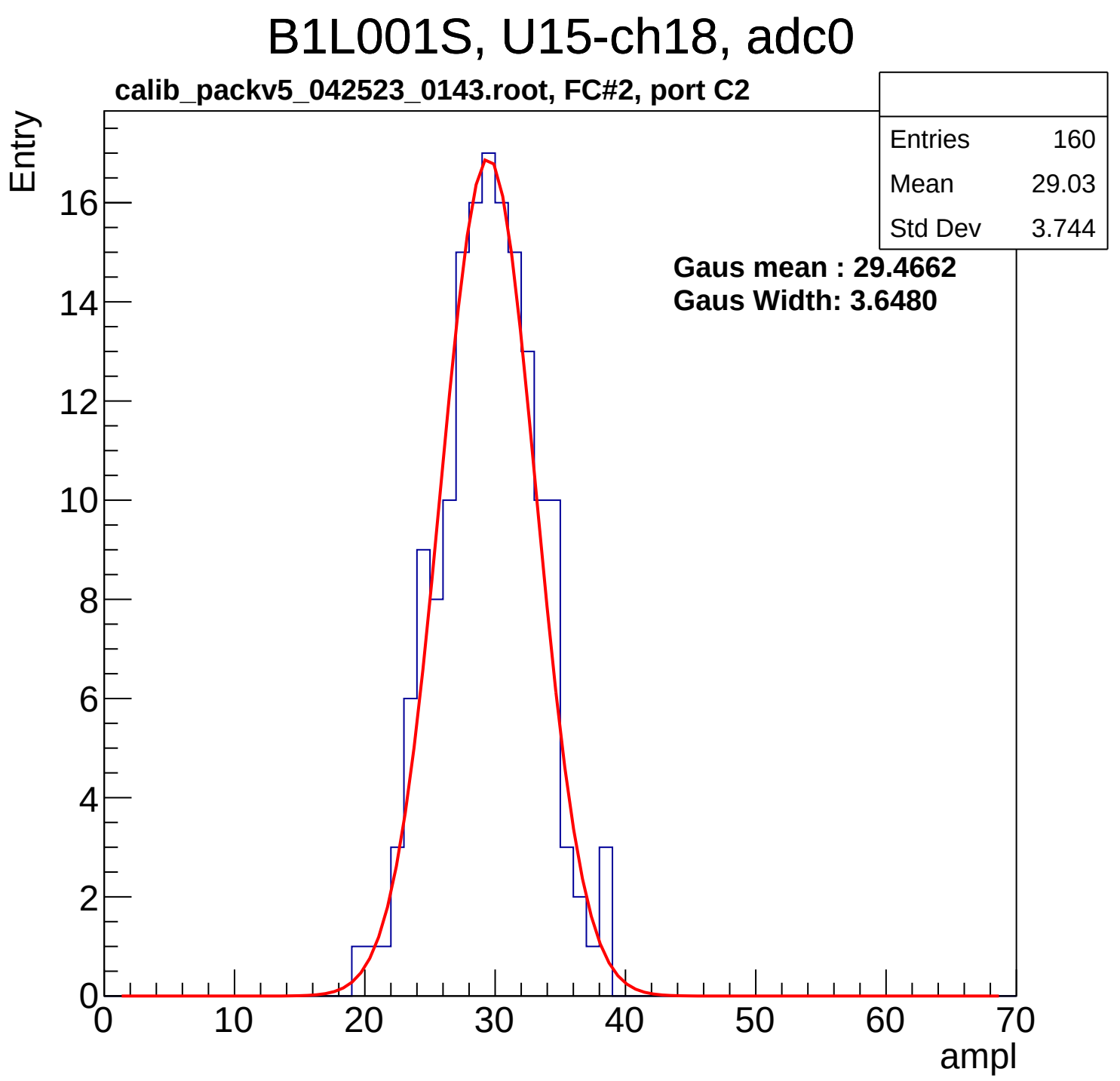
50
60
70
ampl


B1L001S, U15-ch18, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 29.03
Std Dev 3.744
Gaus mean : 29.4662
Gaus Width: 3.6480
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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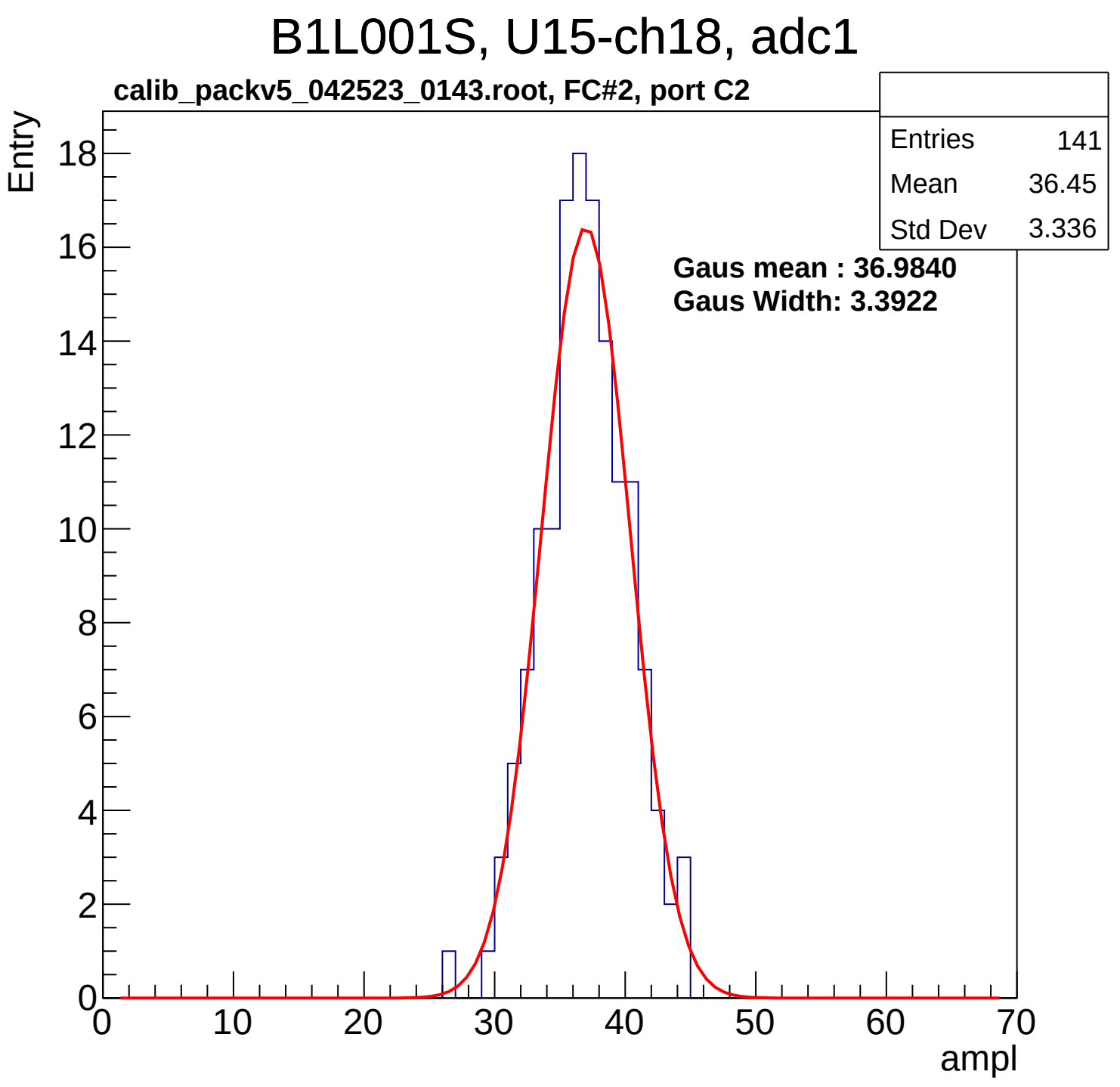
40
50
60
70


B1L001S, U15-ch18, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 36.45
Std Dev 3.336
Gaus mean : 36.9840
Gaus Width: 3.3922
Entry
ampl
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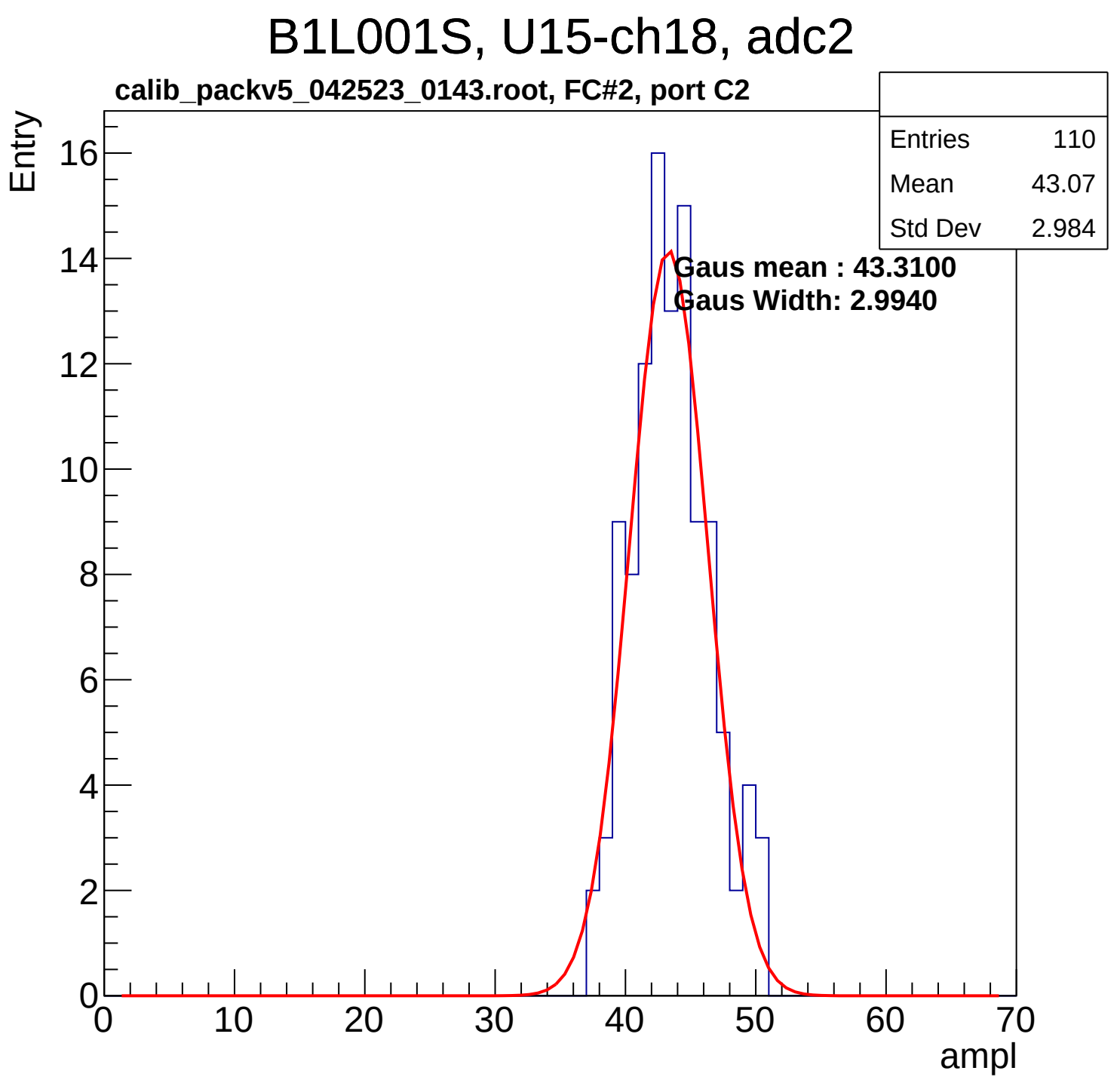
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch18, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 43.07
Std Dev 2.984
Gaus mean : 43.3100
Gaus Width: 2.9940
Entry
ampl
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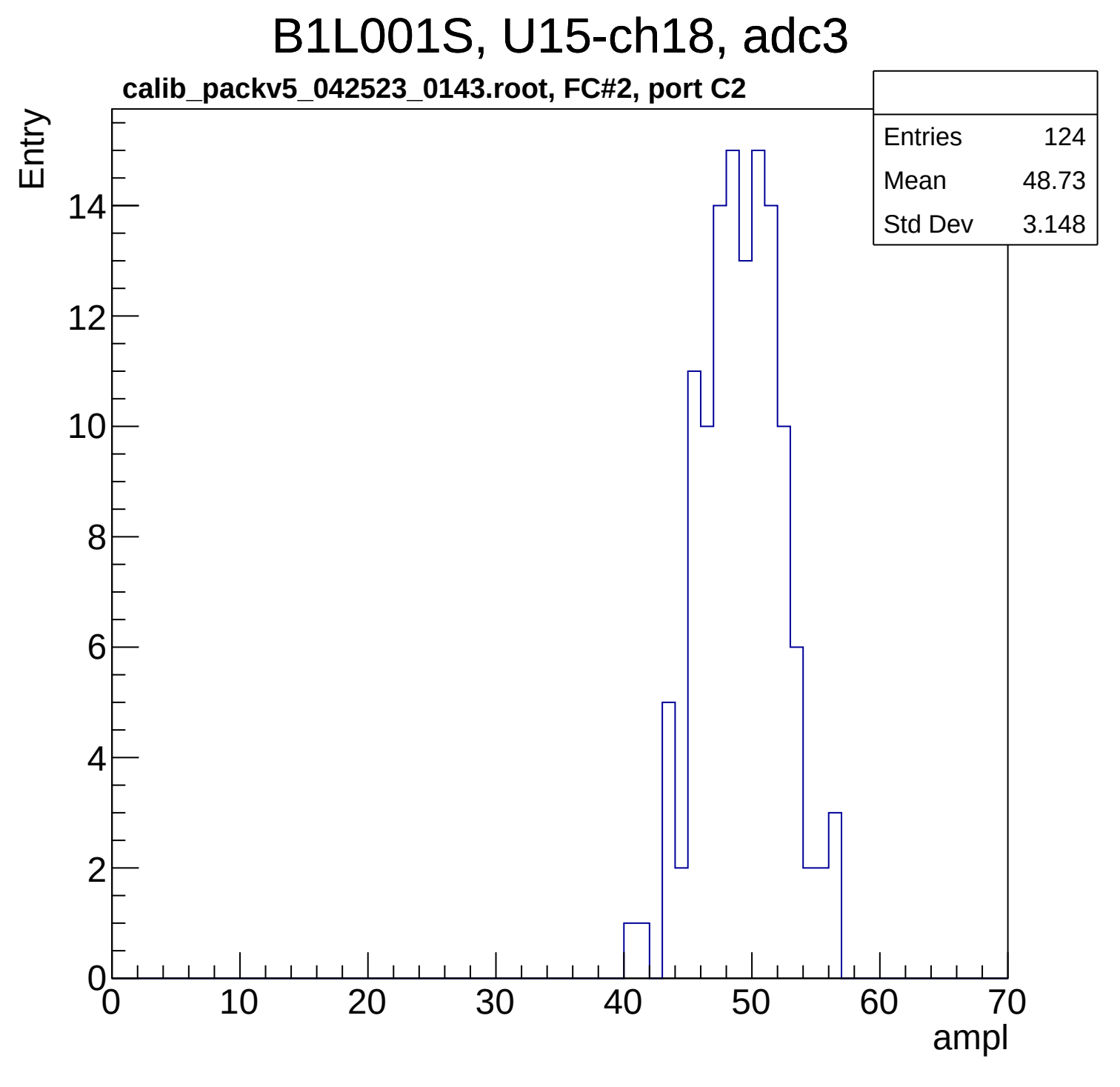
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch18, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 48.73
Std Dev 3.148
Entry
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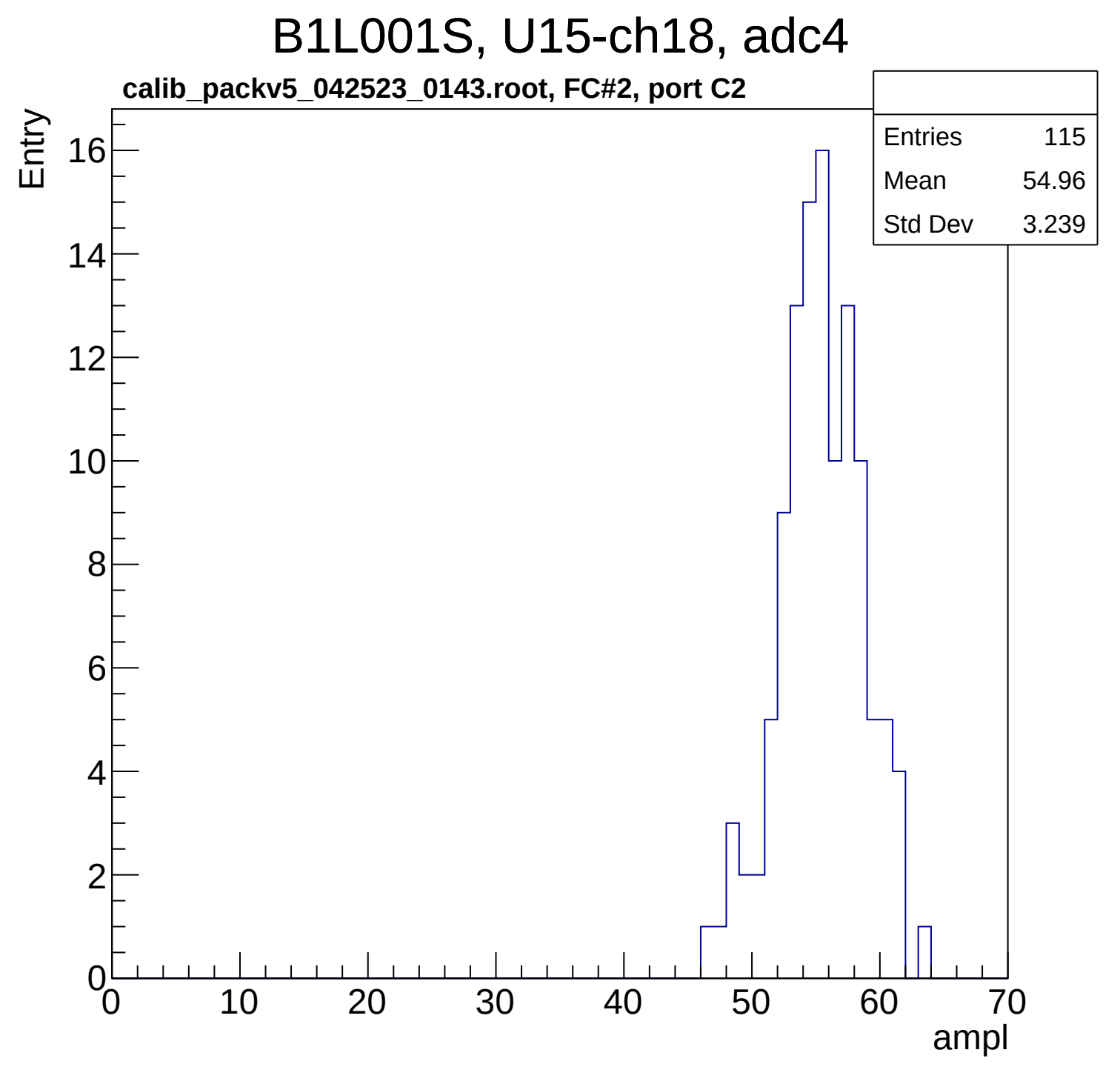
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch18, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 54.96
Std Dev 3.239
Entry
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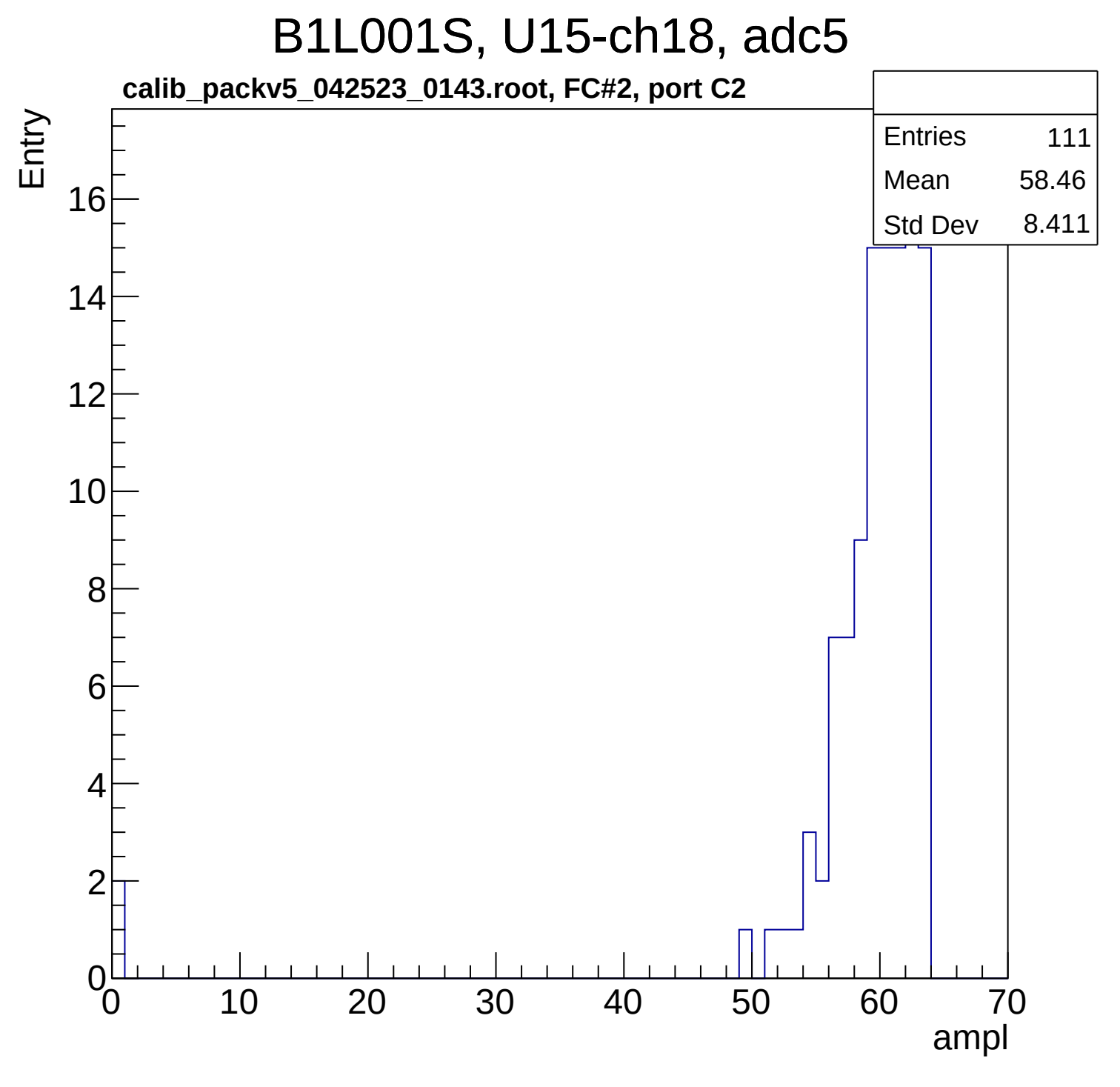
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch18, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 58.46
Std Dev 8.411
Entry
ampl
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4
6
8
10
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0
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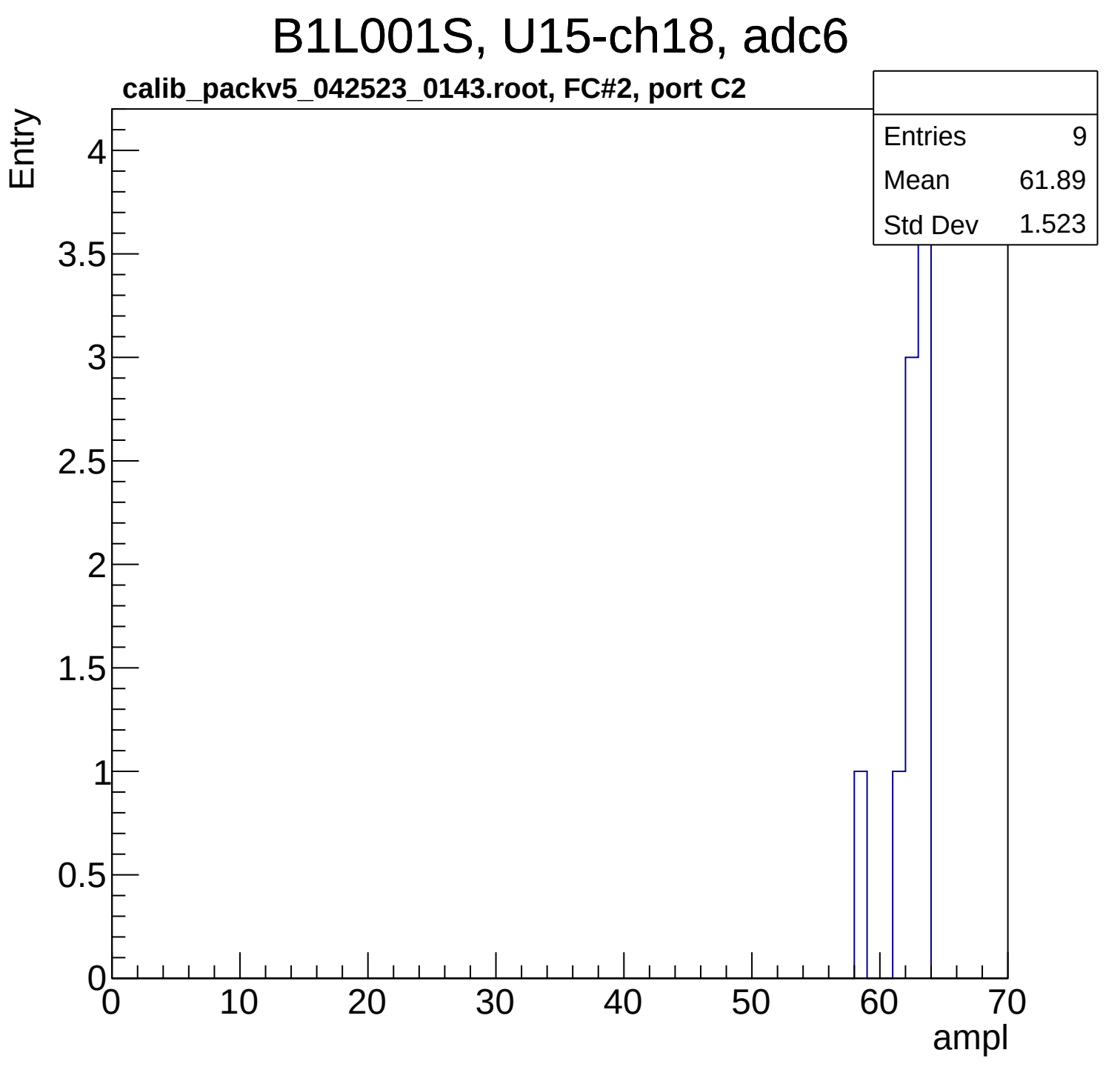
B1L001S, U15-ch18, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 61.89
Std Dev 1.523
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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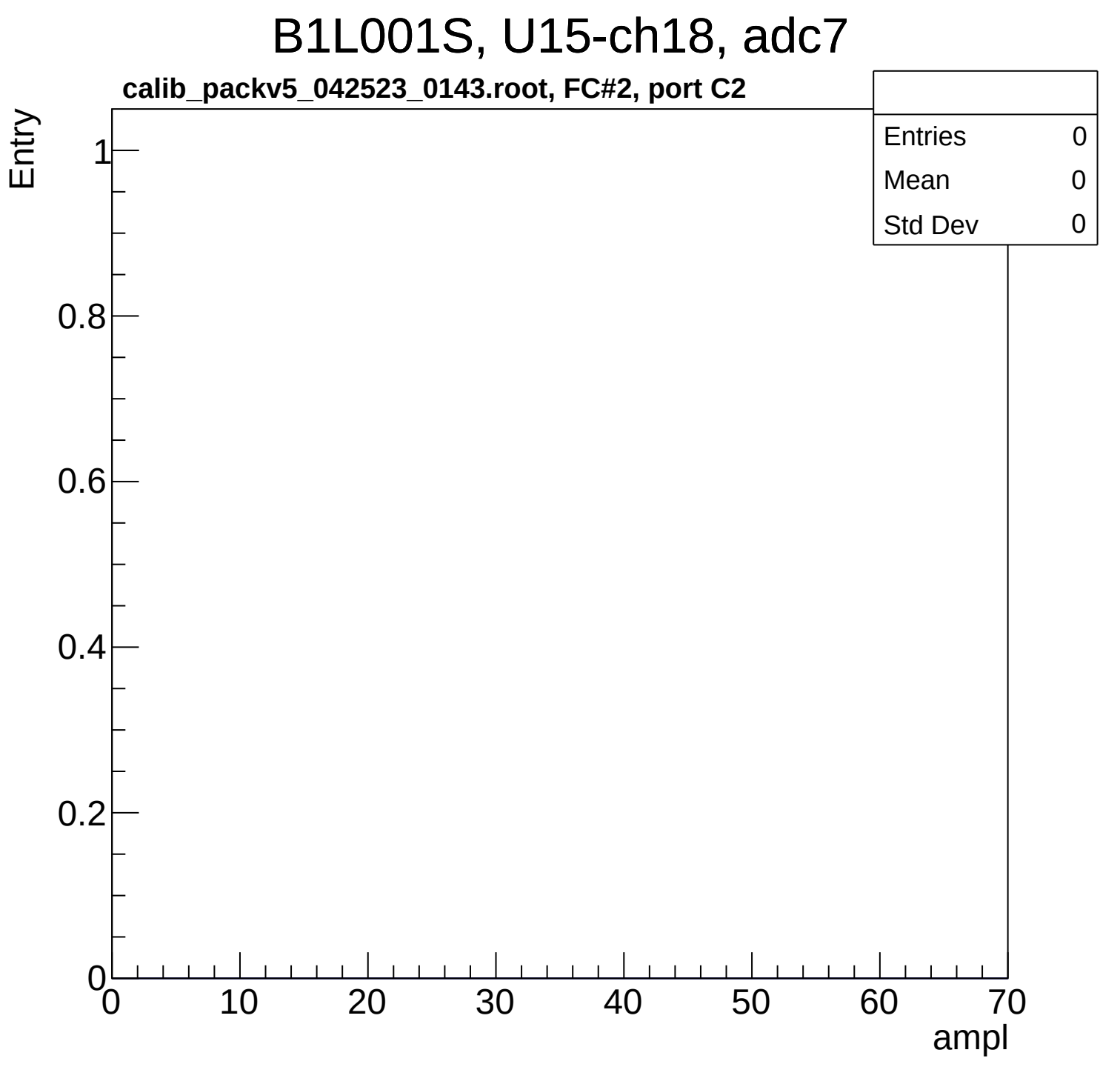
B1L001S, U15-ch18, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
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20
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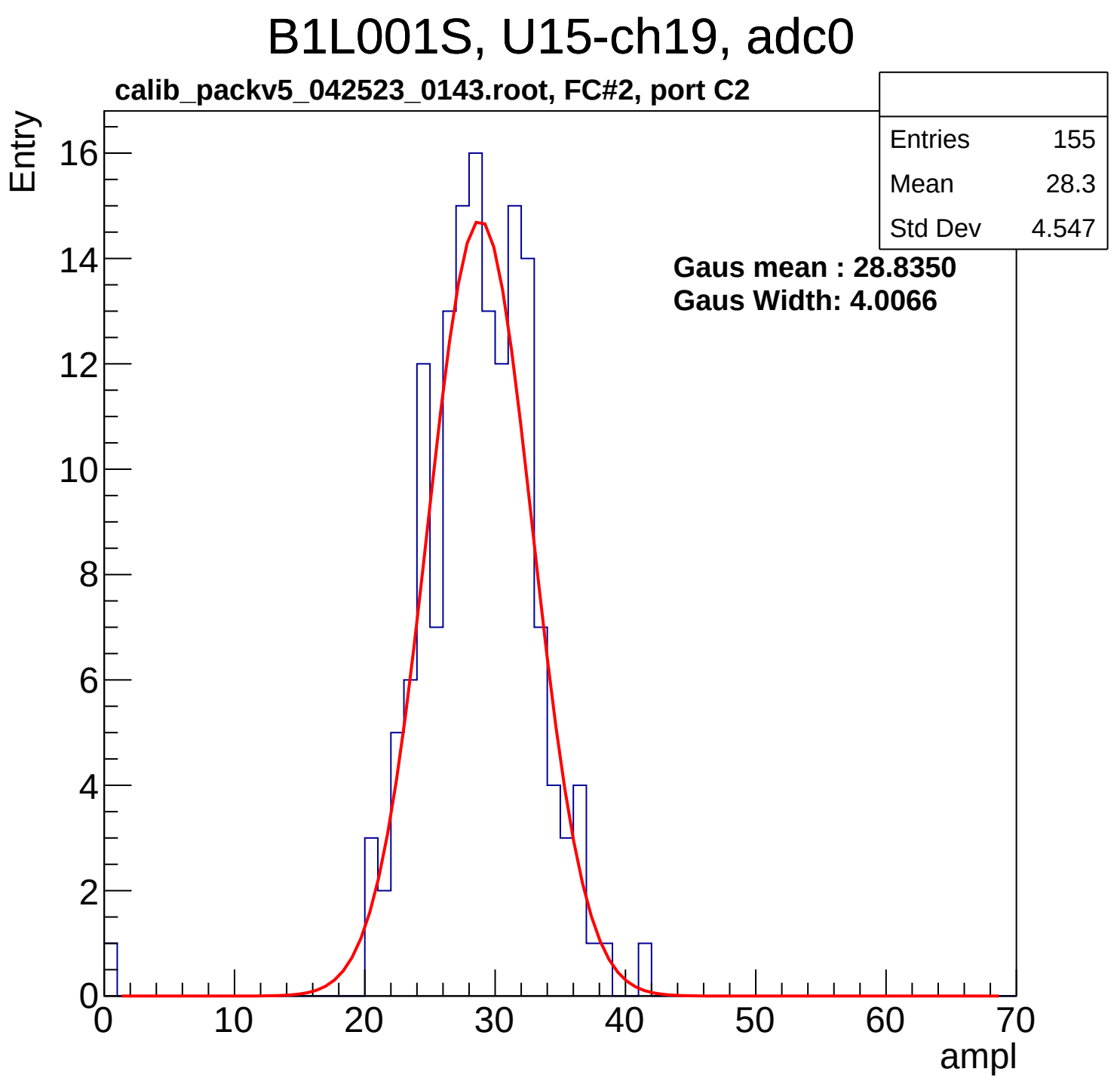
40
50
60
70
ampl


B1L001S, U15-ch19, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 28.3
Std Dev 4.547
Gaus mean : 28.8350
Gaus Width: 4.0066
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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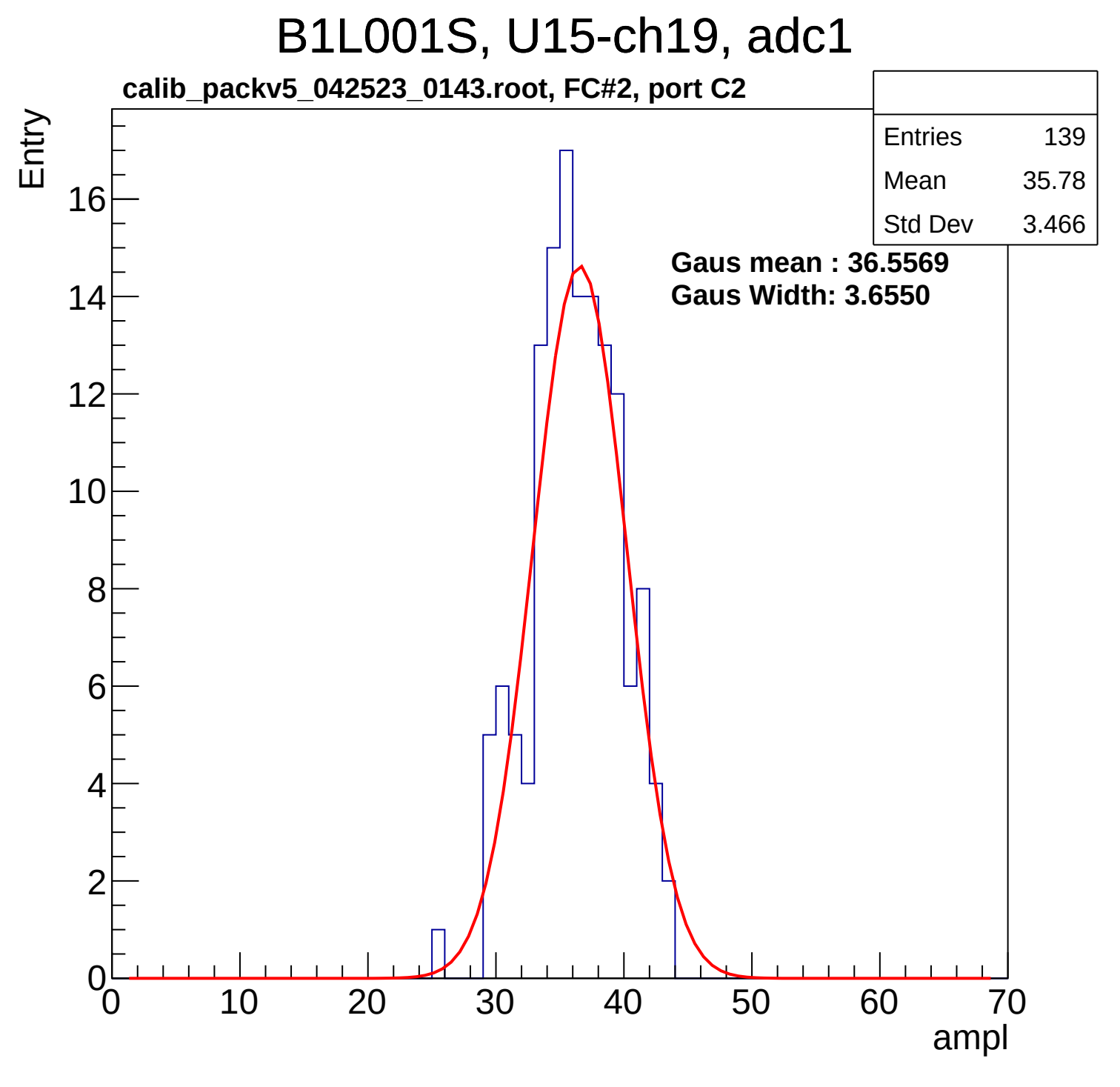
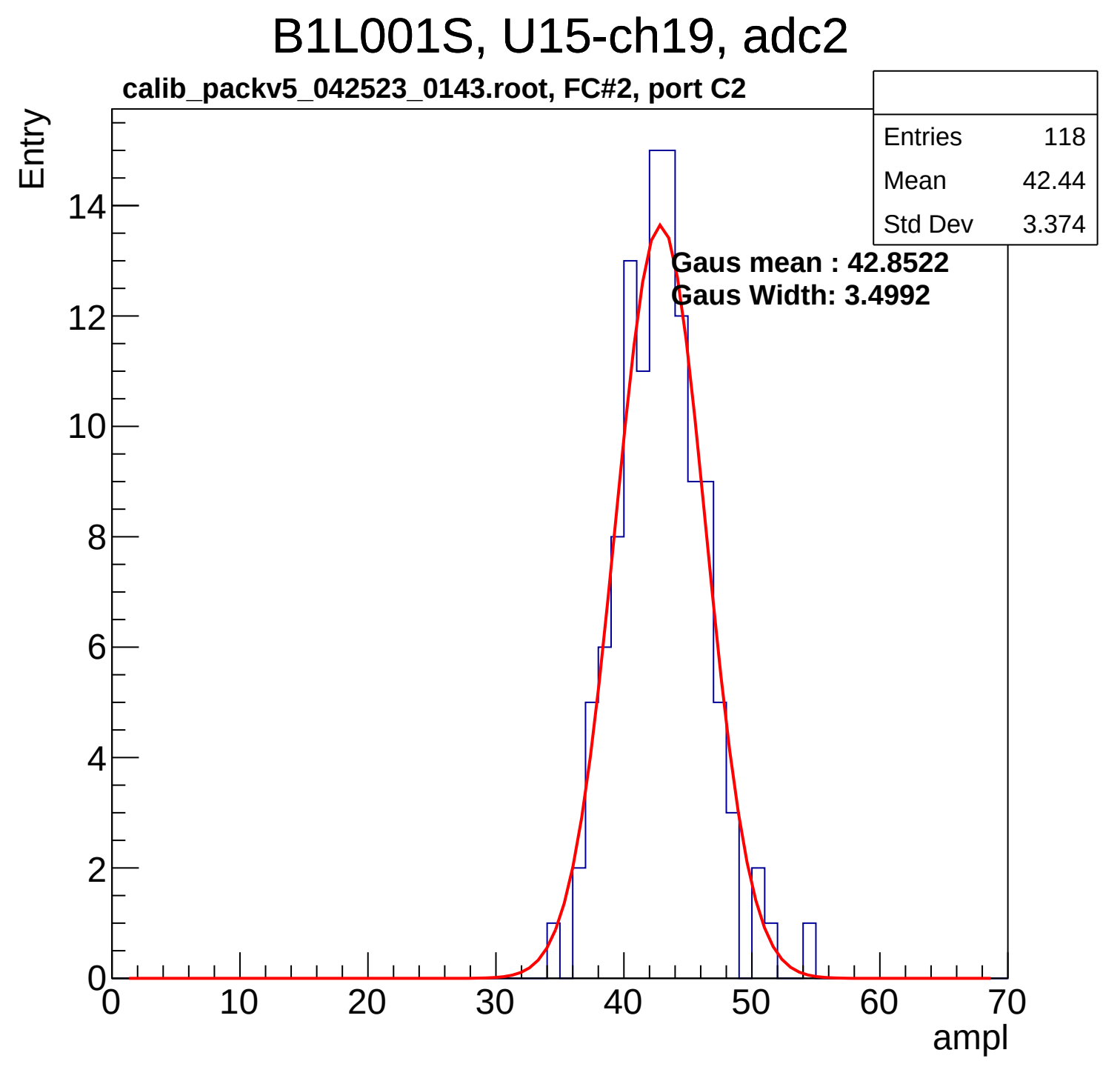

B1L001S, U15-ch19, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 35.78
Std Dev 3.466
Gaus mean : 36.5569
Gaus Width: 3.6550
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch19, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 42.44
Std Dev 3.374
Gaus mean : 42.8522
Gaus Width: 3.4992
Entry
ampl
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2
4
6
8
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60
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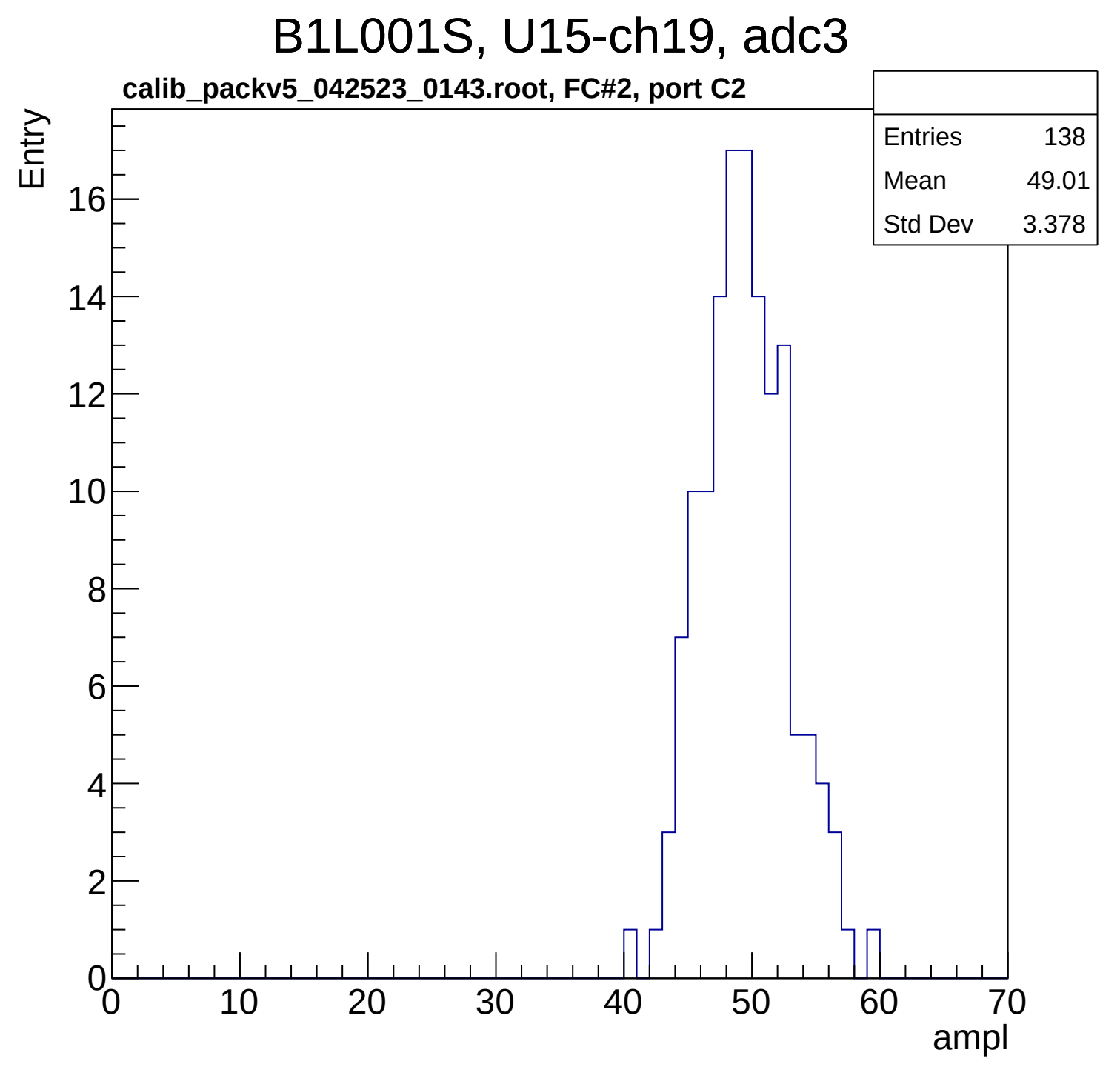
B1L001S, U15-ch19, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 49.01
Std Dev 3.378
Entry
ampl
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2
4
6
8
10
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16
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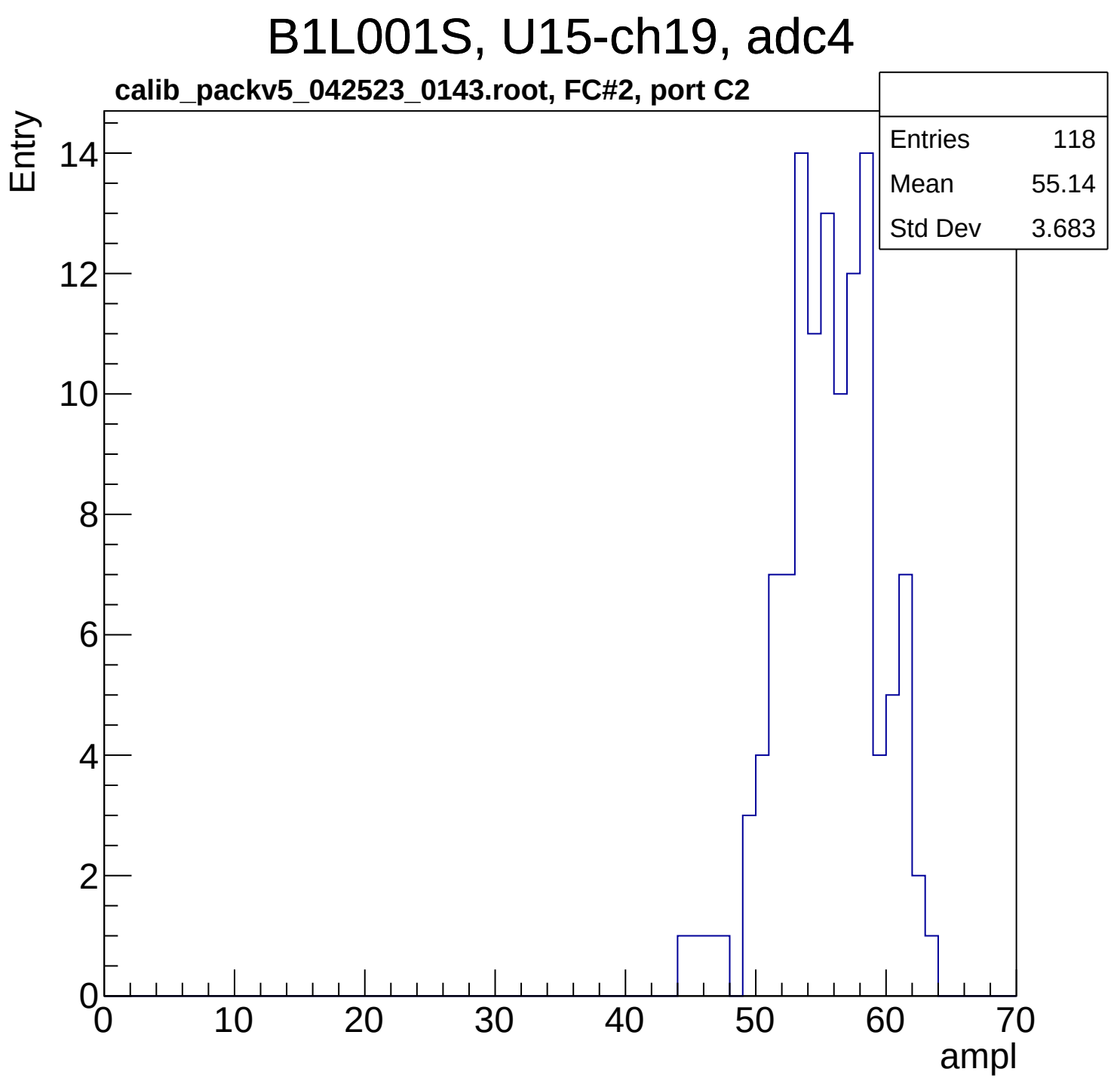
B1L001S, U15-ch19, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 55.14
Std Dev 3.683
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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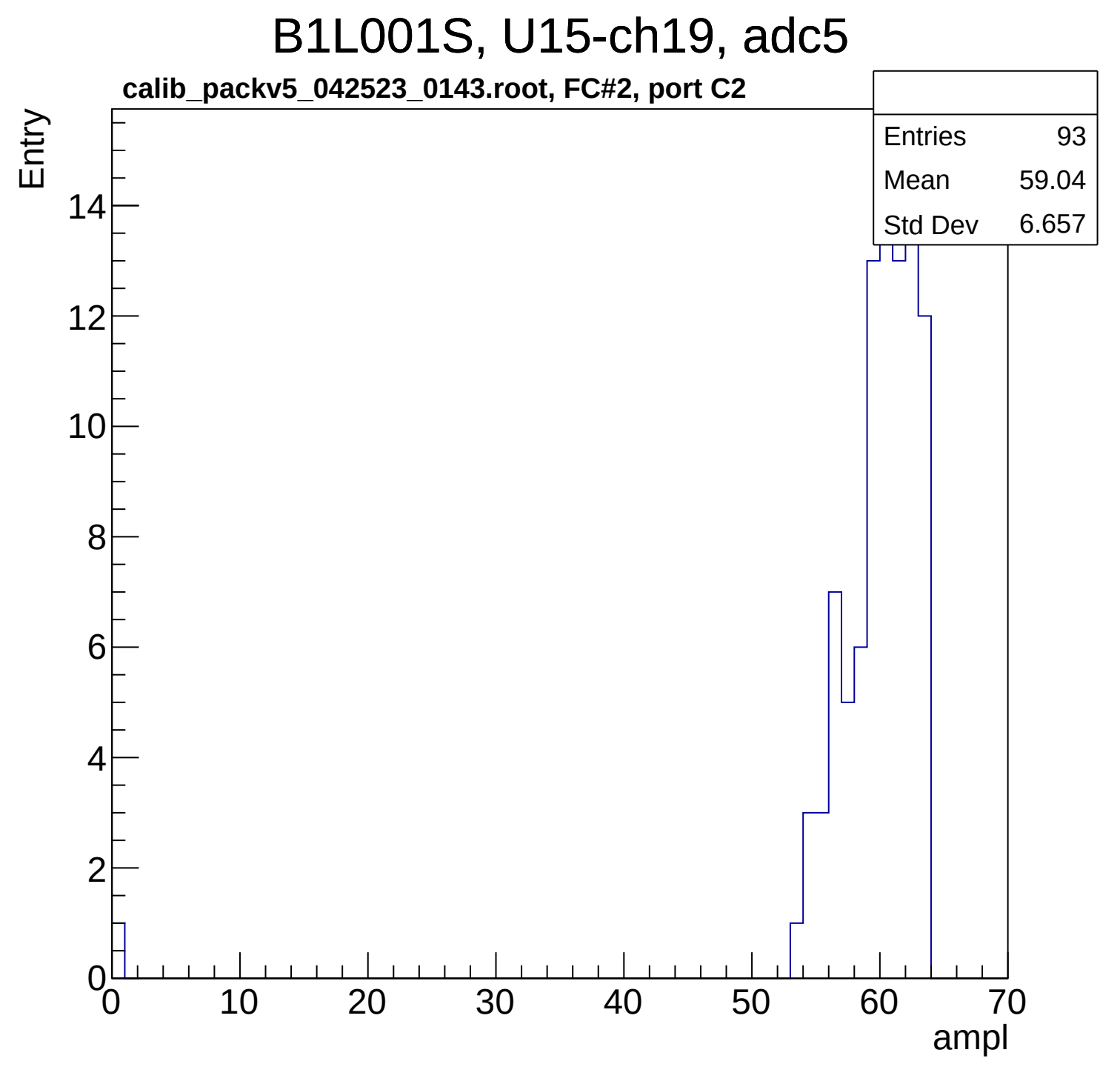
B1L001S, U15-ch19, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 59.04
Std Dev 6.657
Entry
ampl
0
2
4
6
8
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12
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0
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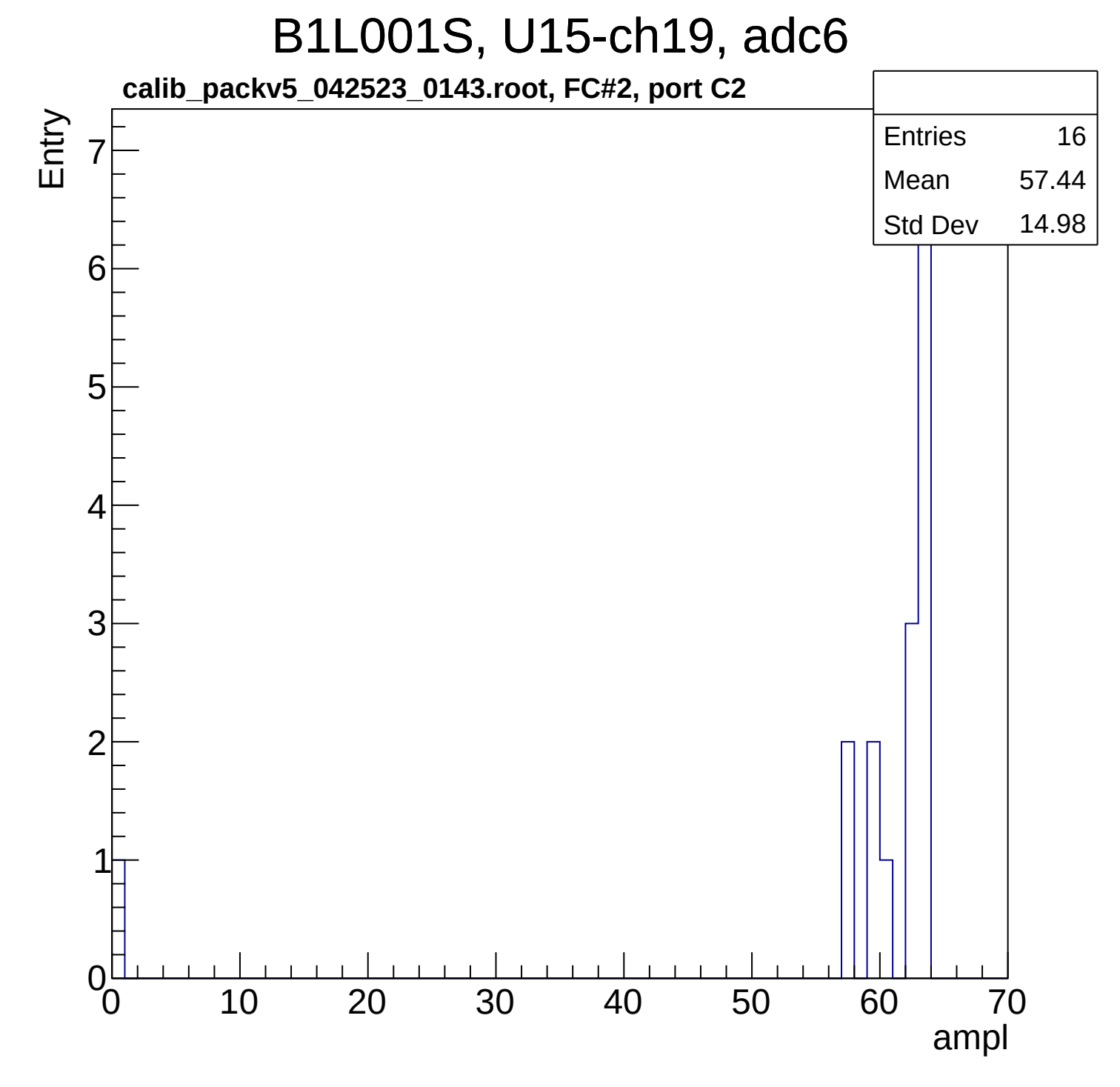
B1L001S, U15-ch19, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 57.44
Std Dev 14.98
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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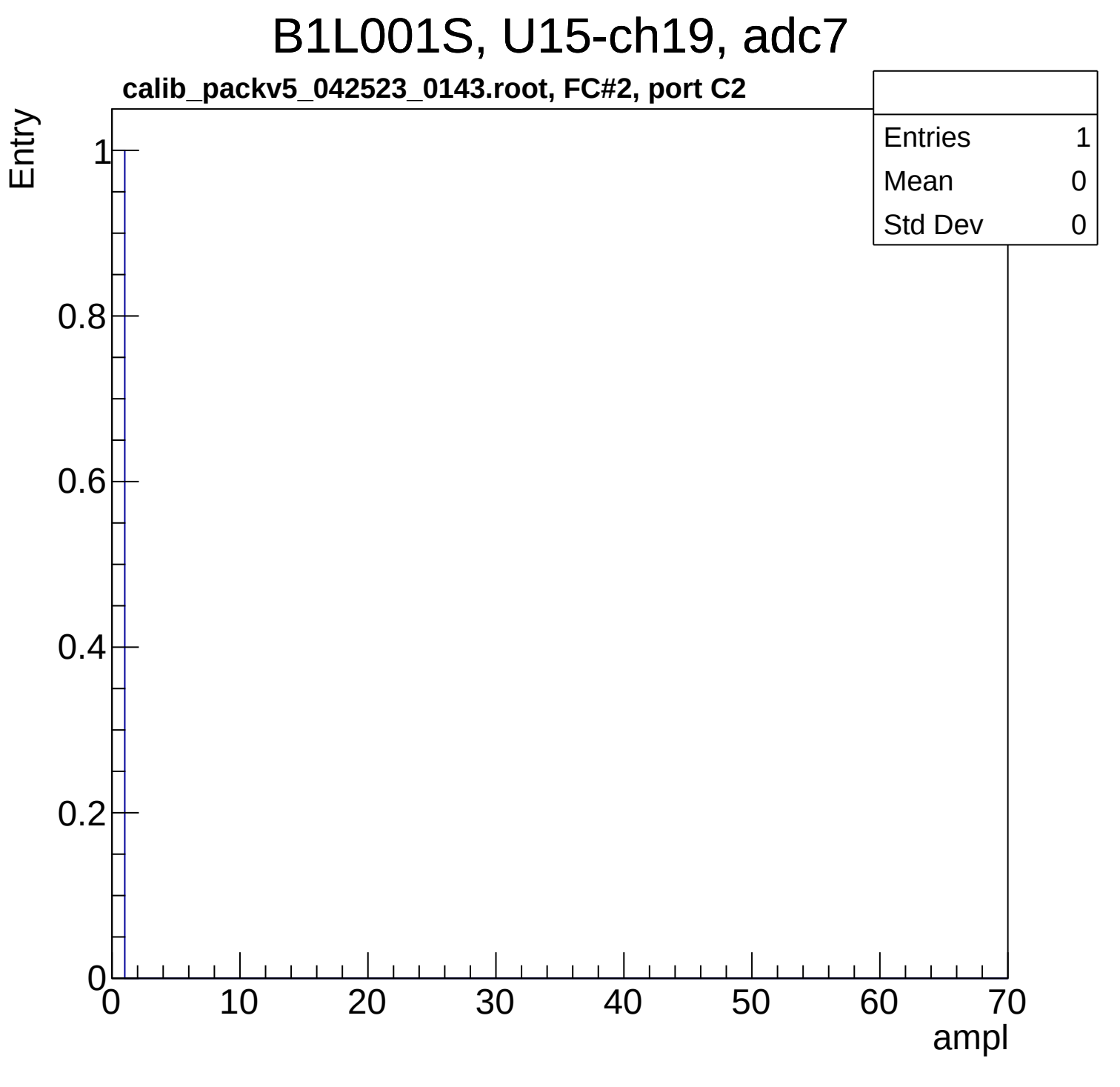
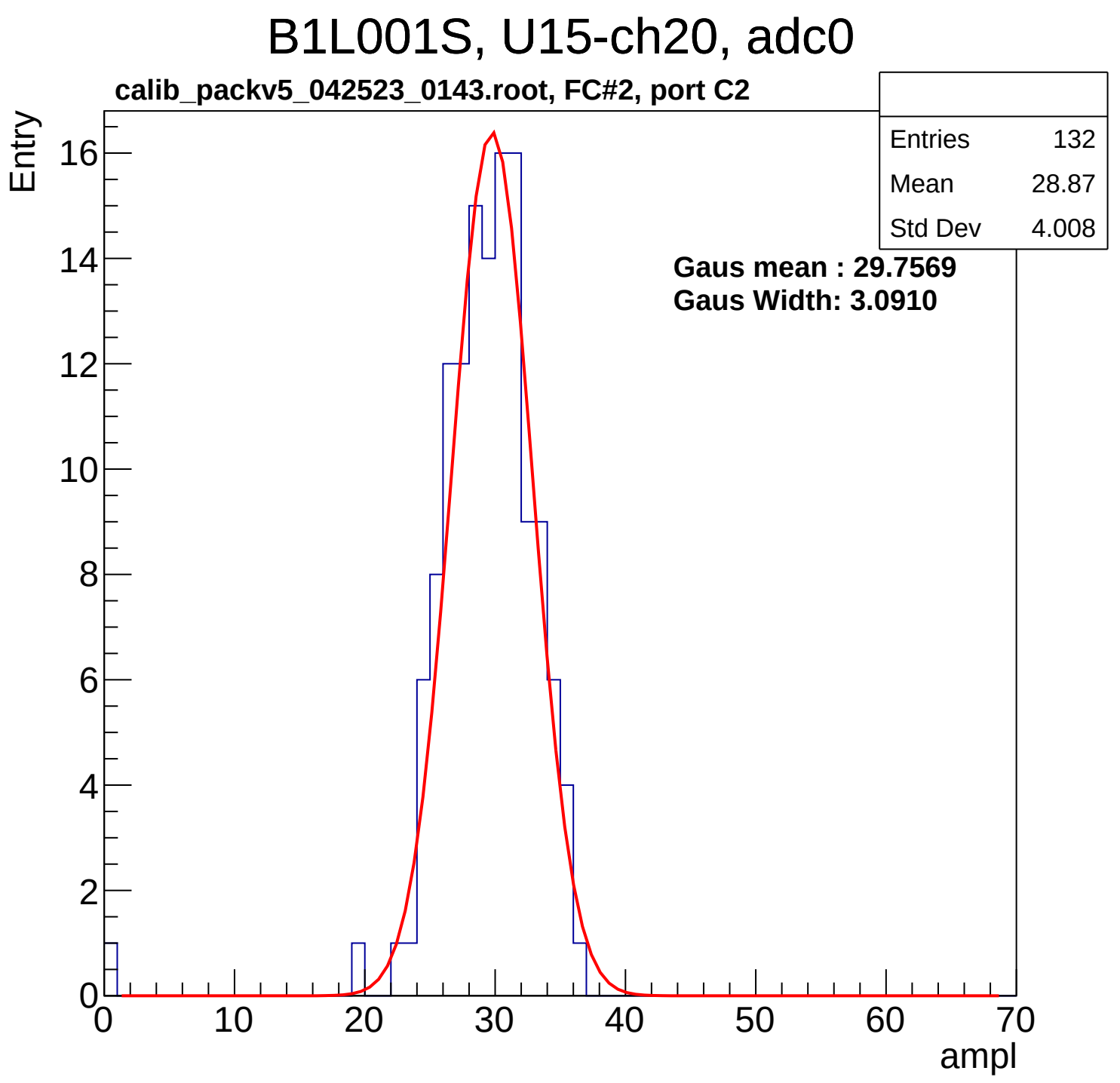

B1L001S, U15-ch19, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch20, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 28.87
Std Dev 4.008
Gaus mean : 29.7569
Gaus Width: 3.0910
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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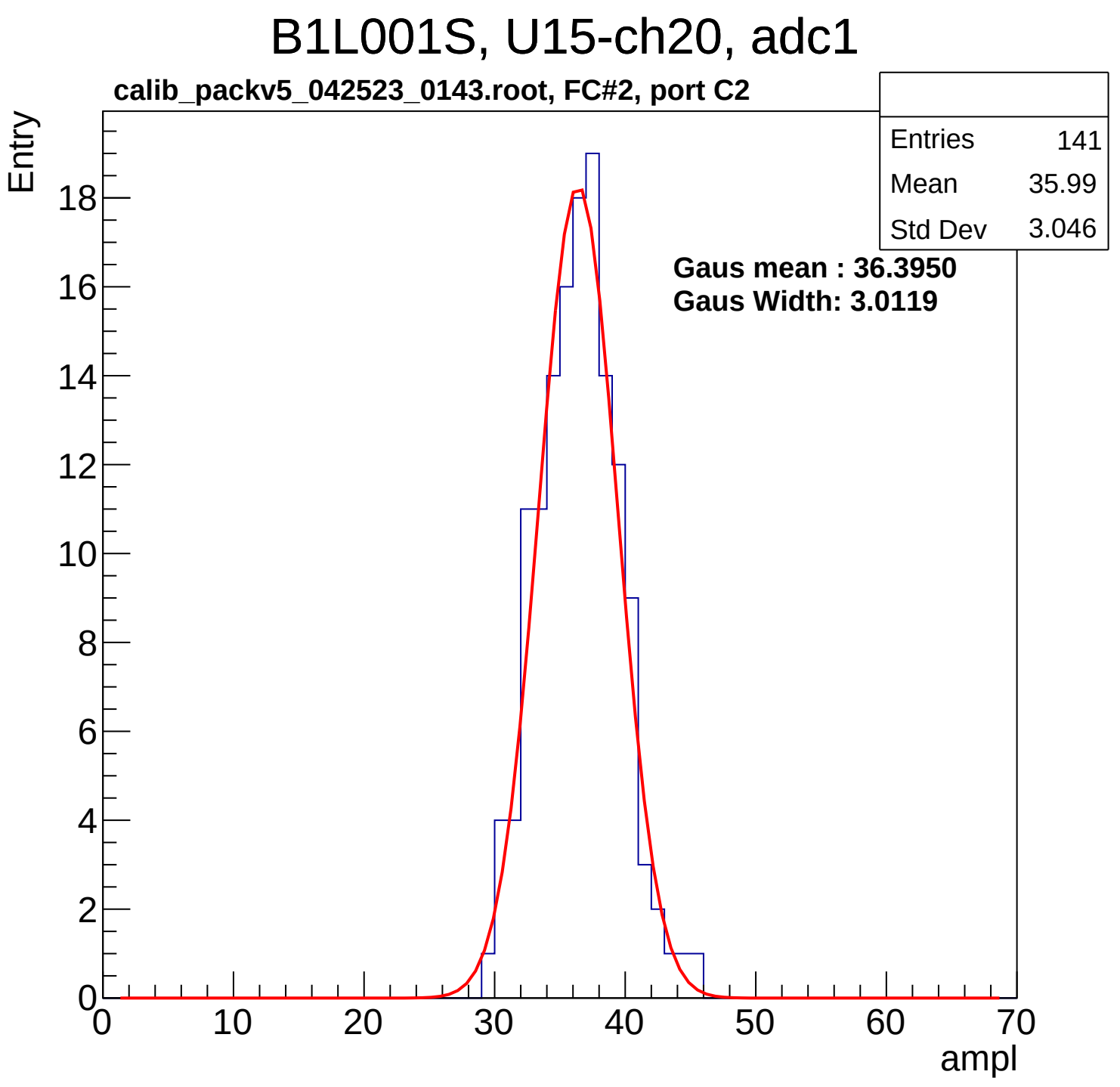
B1L001S, U15-ch20, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 35.99
Std Dev 3.046
Gaus mean : 36.3950
Gaus Width: 3.0119
Entry
ampl
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4
6
8
10
12
14
16
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60
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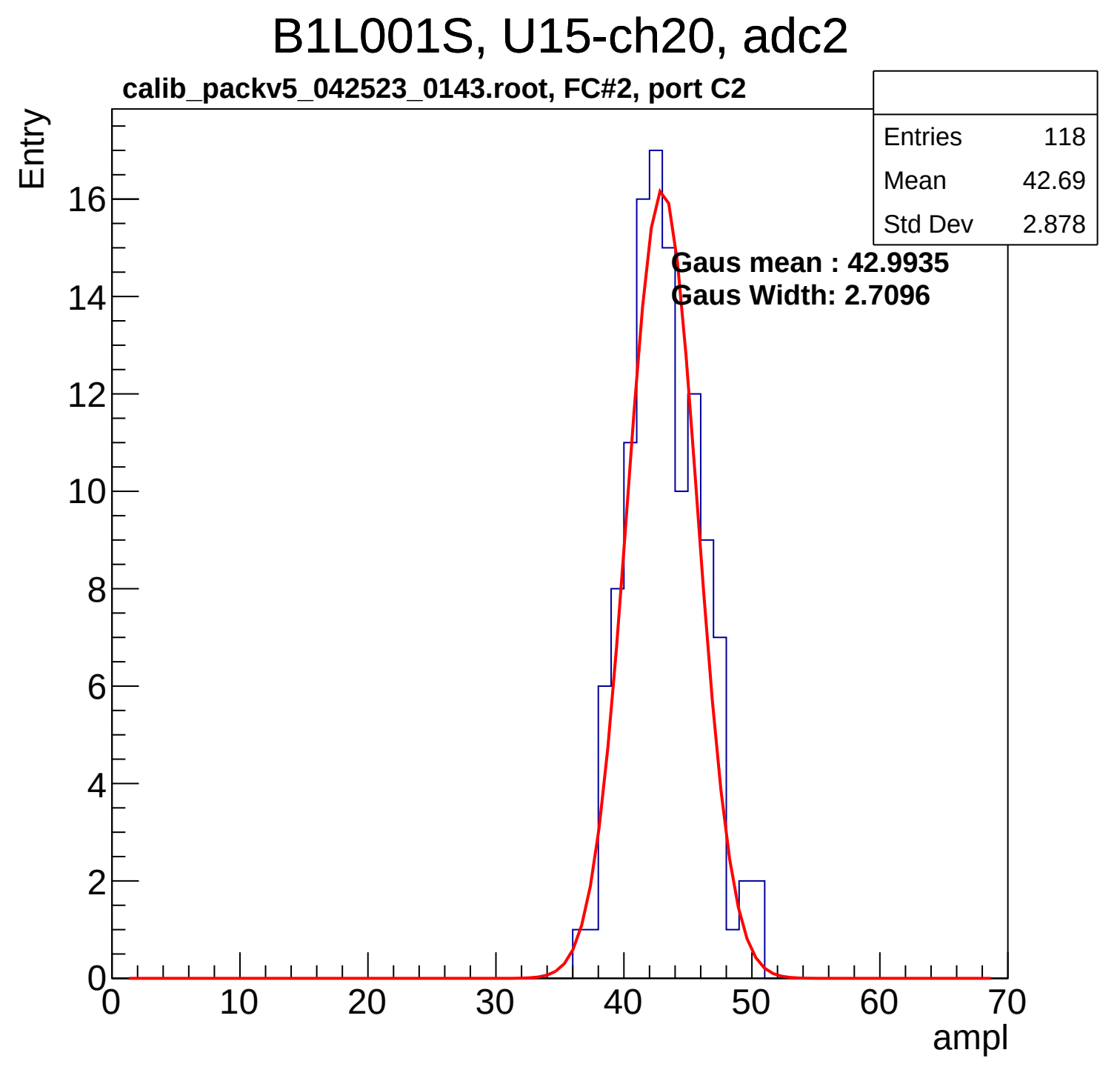
B1L001S, U15-ch20, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 42.69
Std Dev 2.878
Gaus mean : 42.9935
Gaus Width: 2.7096
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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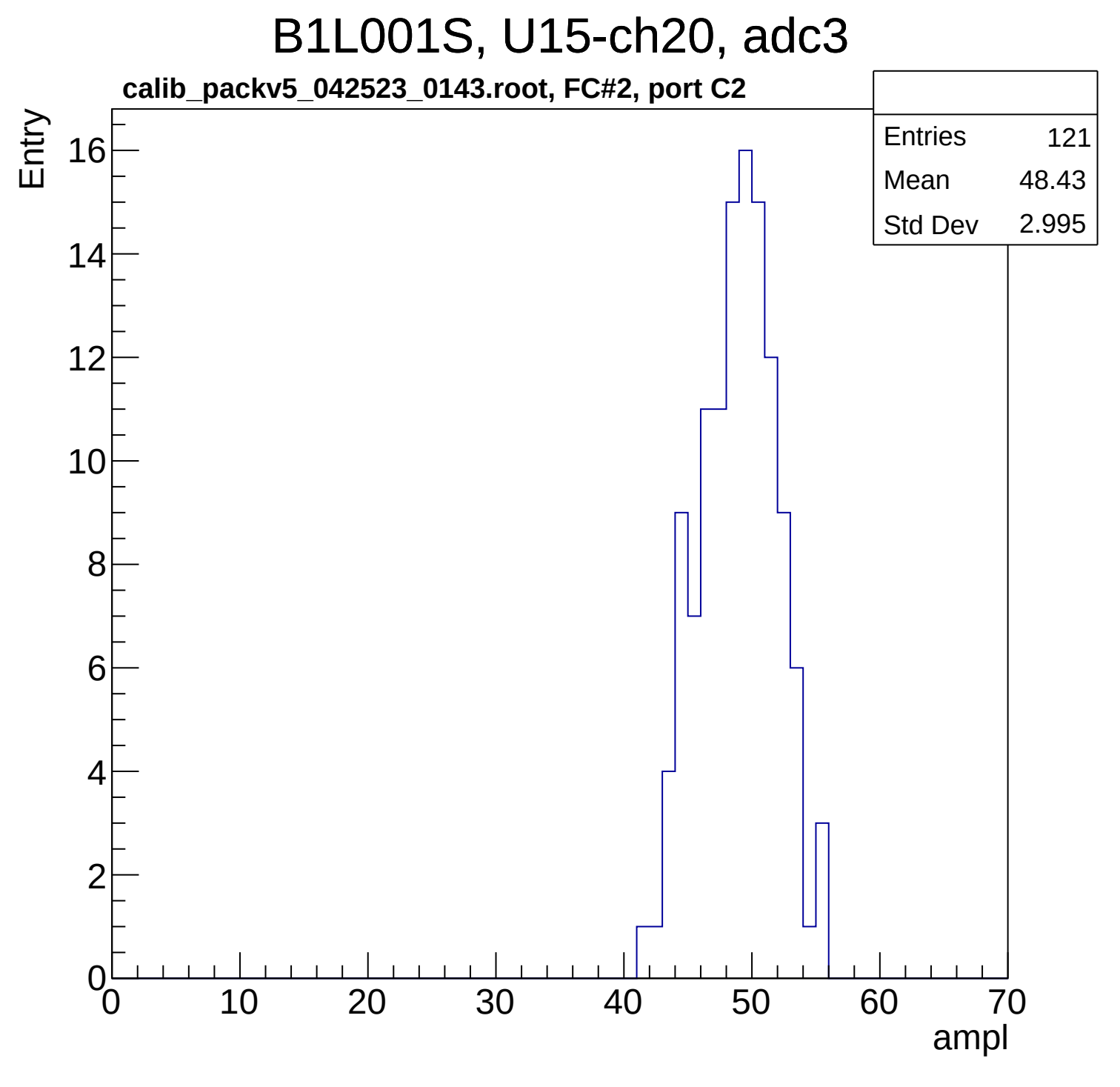
B1L001S, U15-ch20, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 48.43
Std Dev 2.995
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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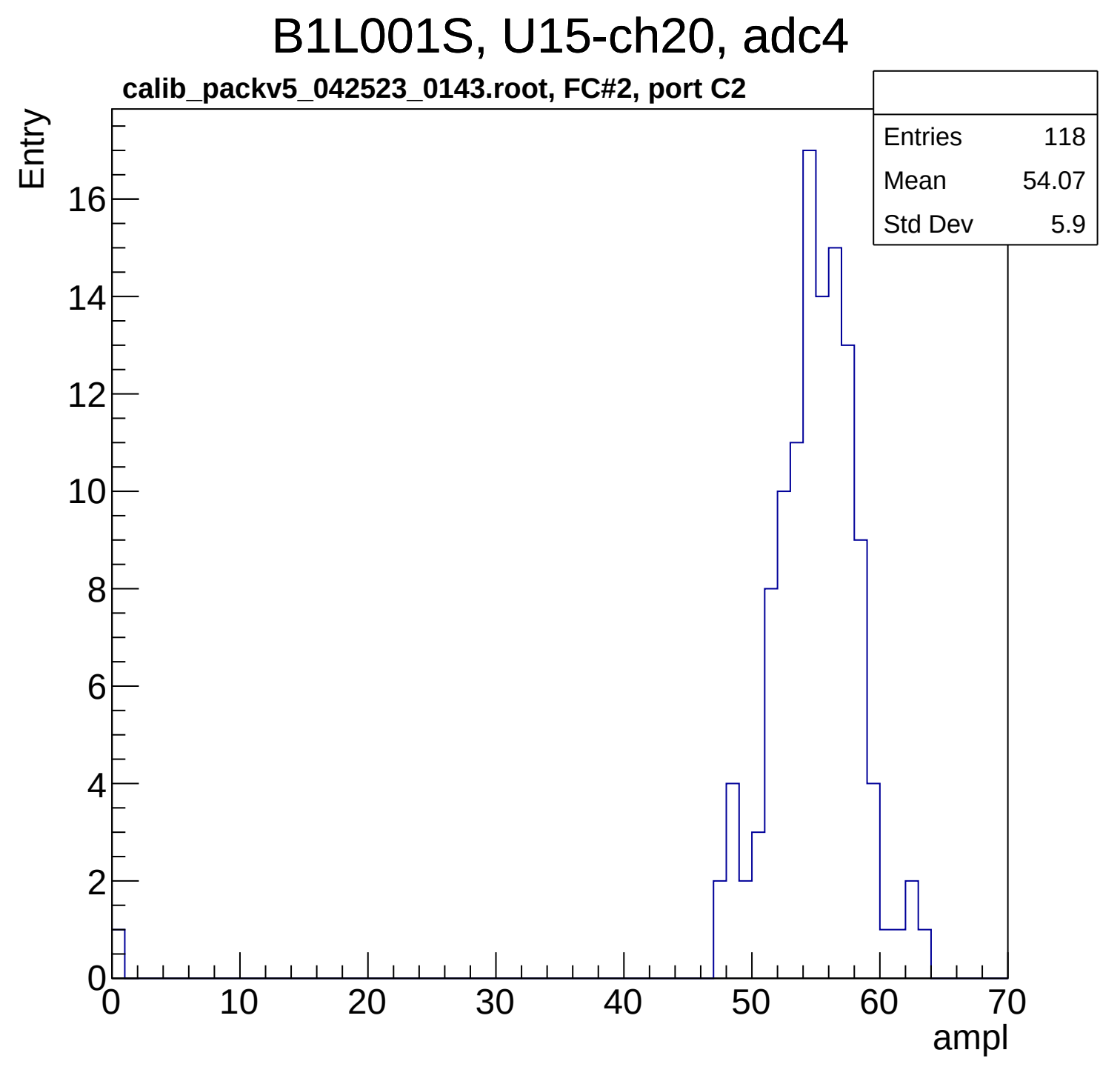
B1L001S, U15-ch20, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 54.07
Std Dev 5.9
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
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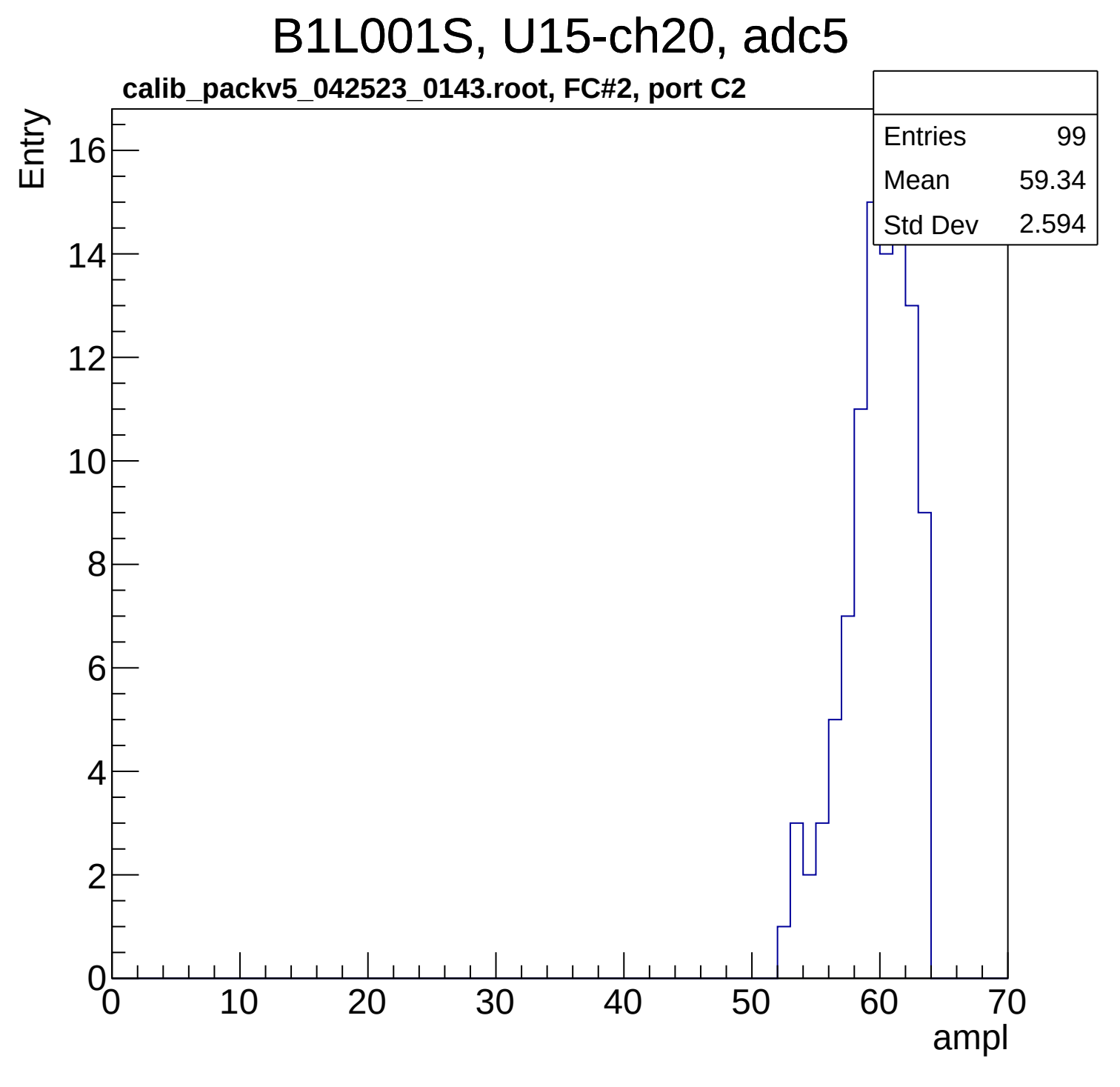
50
60
70
ampl


B1L001S, U15-ch20, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 59.34
Std Dev 2.594
Entry
ampl
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2
4
6
8
10
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0
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20
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40
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60
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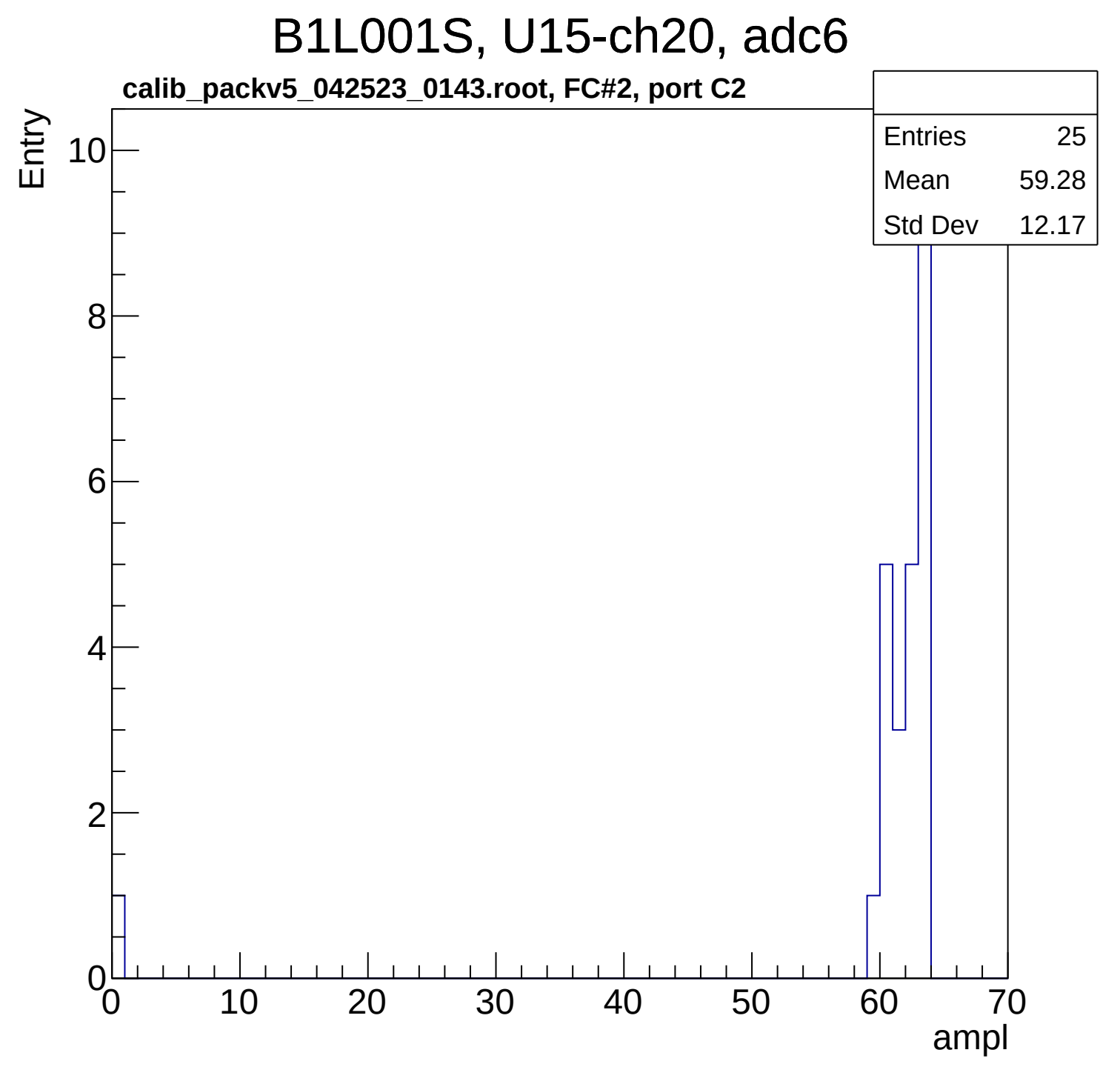
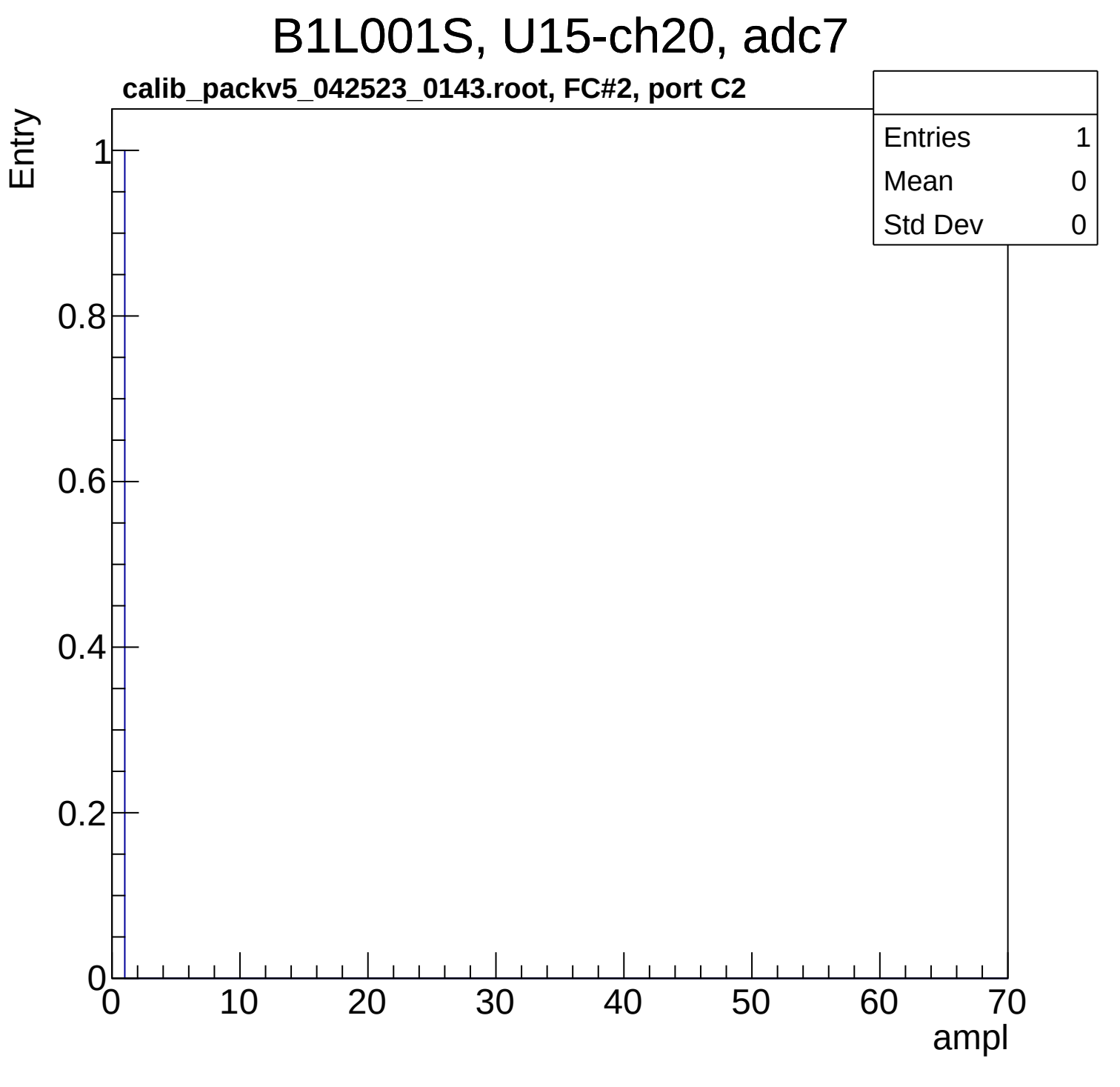
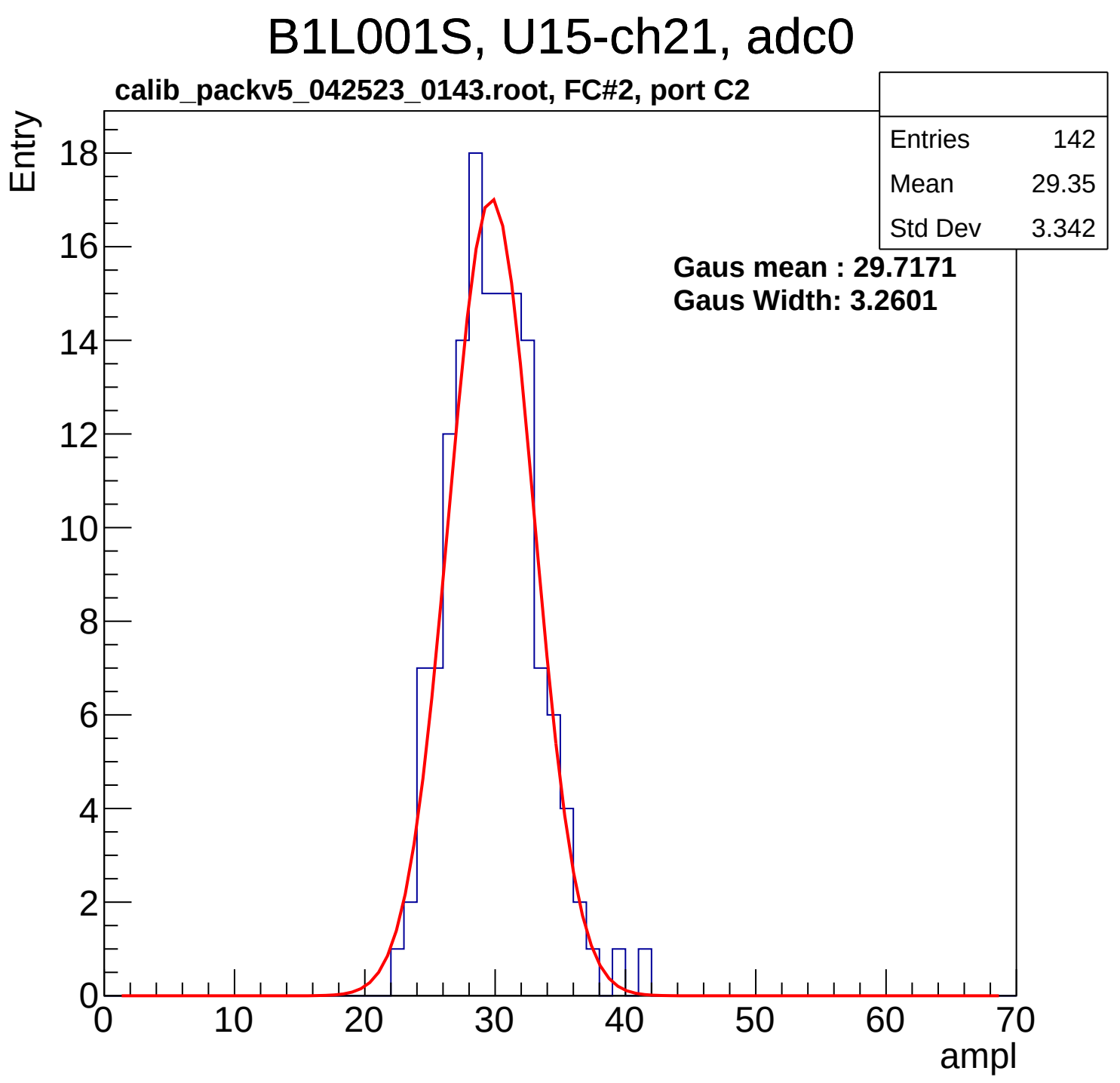

B1L001S, U15-ch20, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 25
Mean 59.28
Std Dev 12.17
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch20, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch21, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 29.35
Std Dev 3.342
Gaus mean : 29.7171
Gaus Width: 3.2601
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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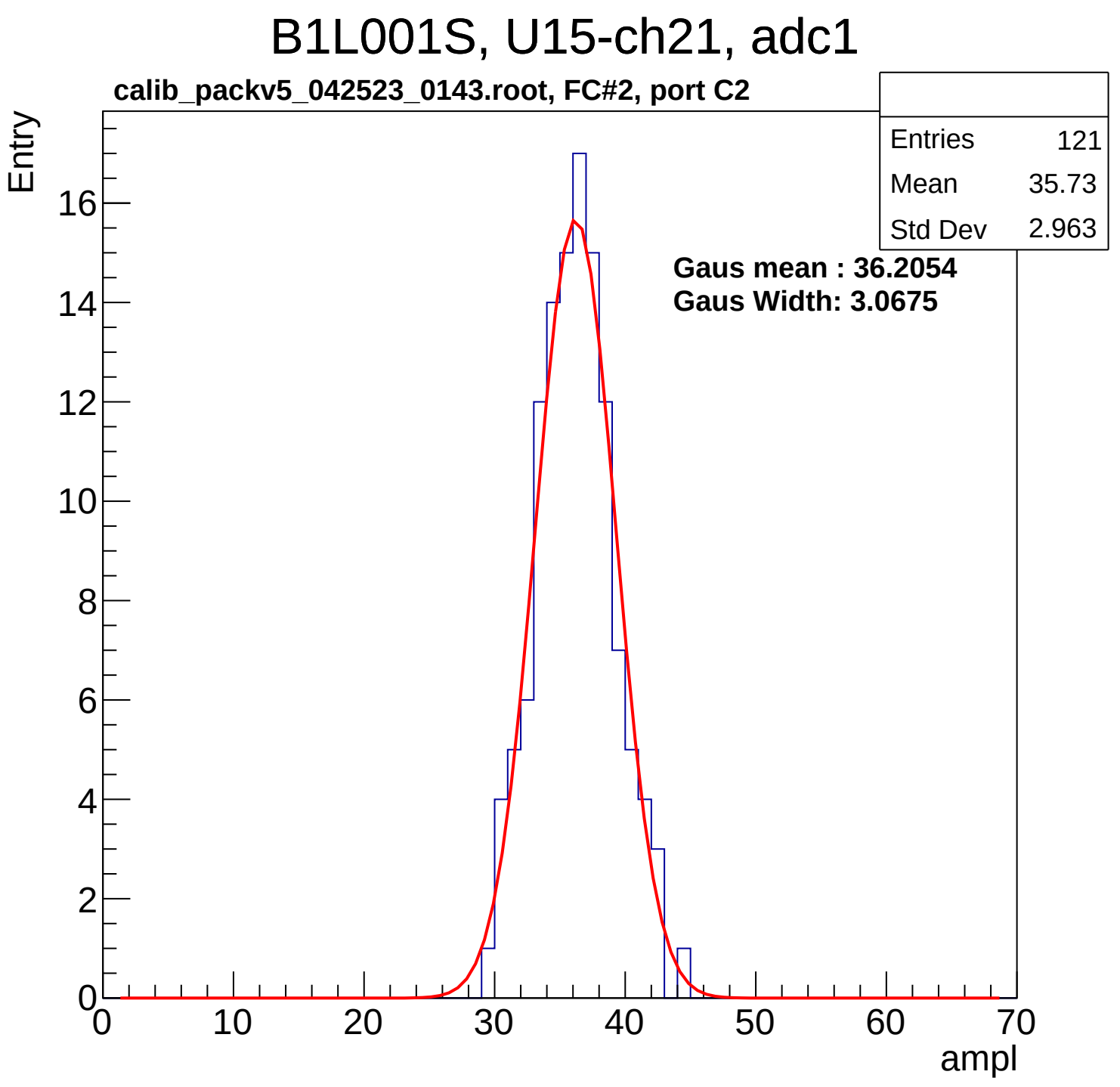
B1L001S, U15-ch21, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 35.73
Std Dev 2.963
Gaus mean : 36.2054
Gaus Width: 3.0675
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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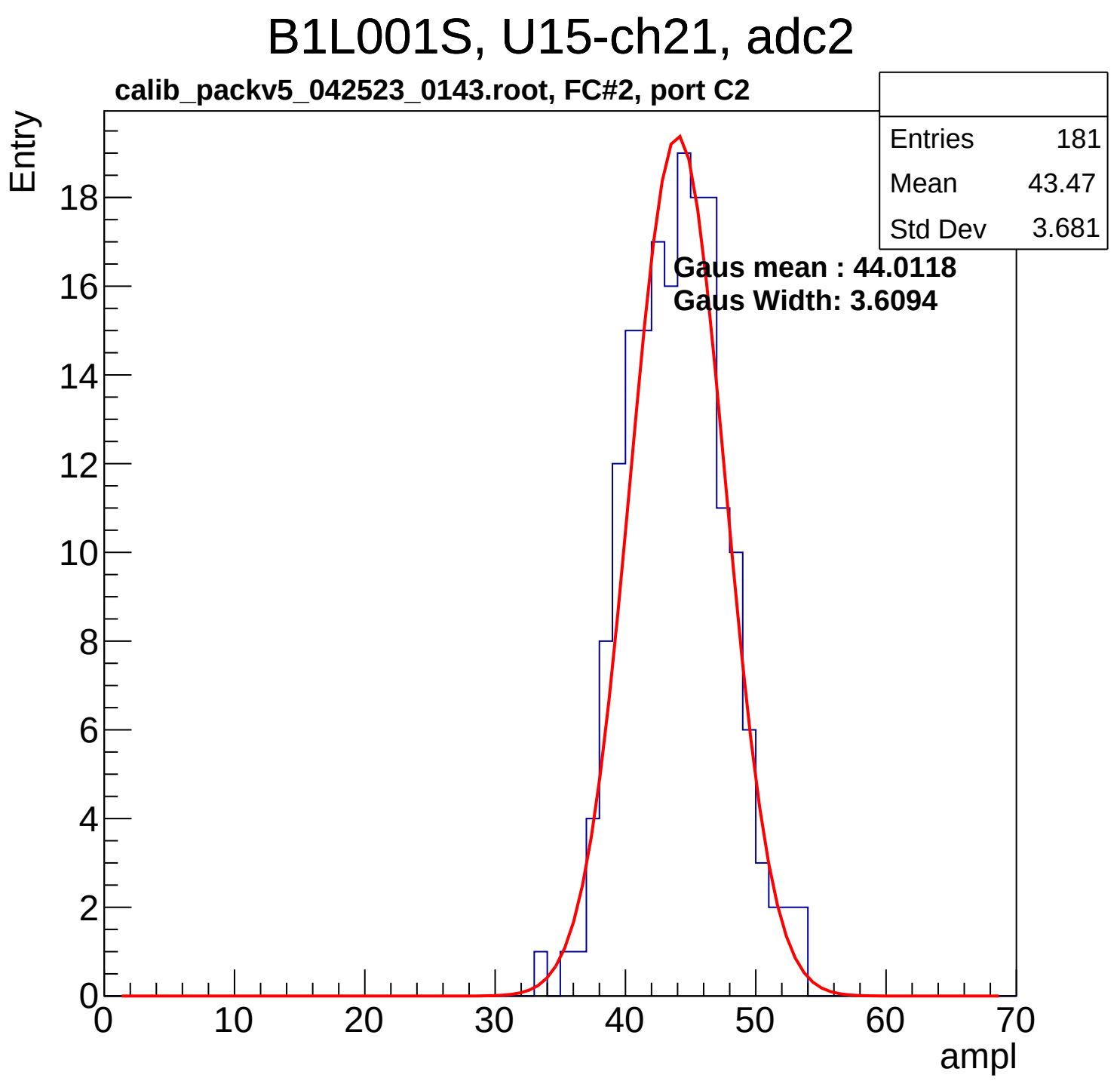
B1L001S, U15-ch21, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 181
Mean 43.47
Std Dev 3.681
Gaus mean : 44.0118
Gaus Width: 3.6094
Entry
ampl
0
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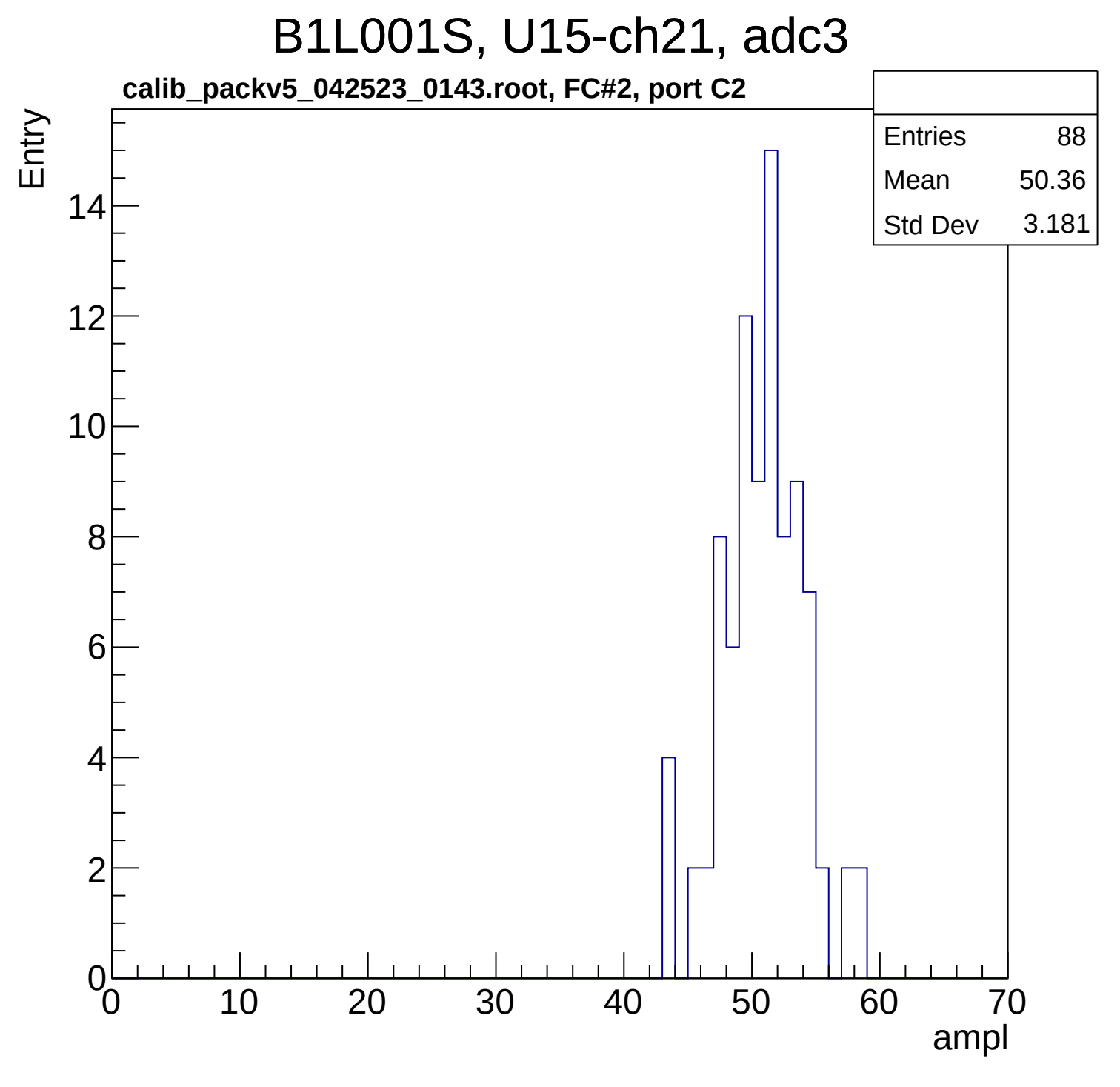
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch21, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 50.36
Std Dev 3.181
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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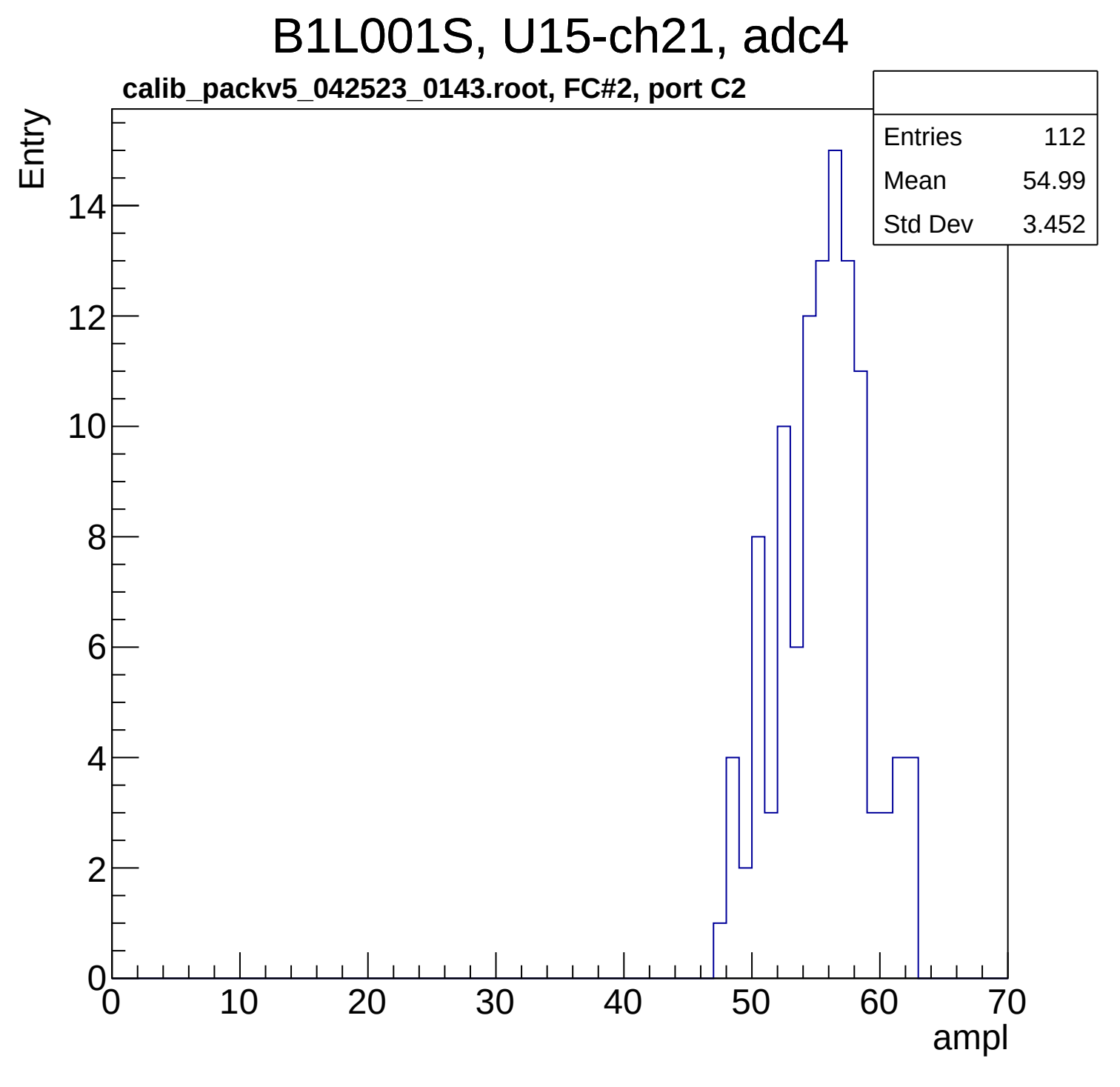
B1L001S, U15-ch21, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 54.99
Std Dev 3.452
Entry
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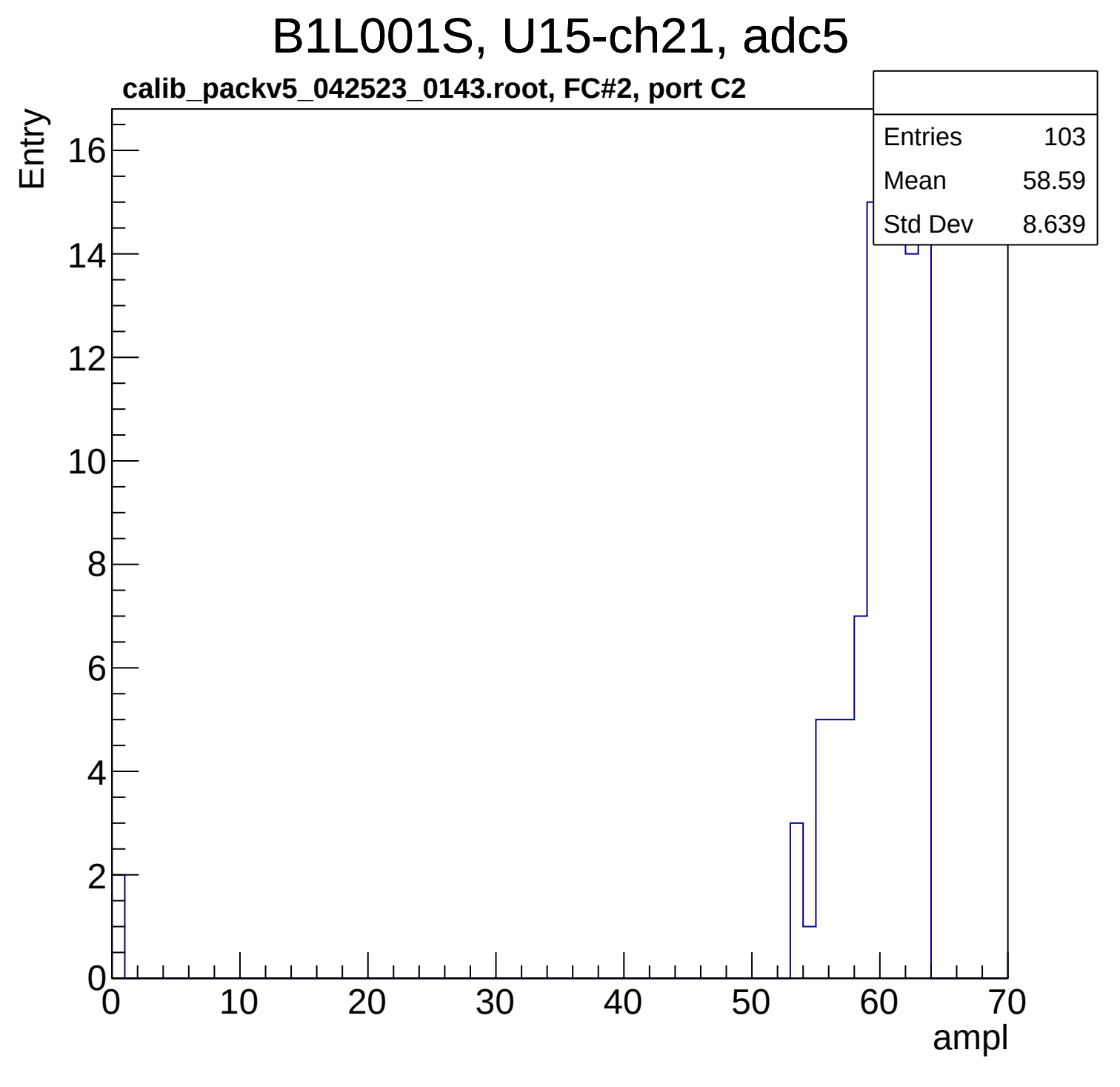
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch21, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.59
Std Dev 8.639
Entry
ampl
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6
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10
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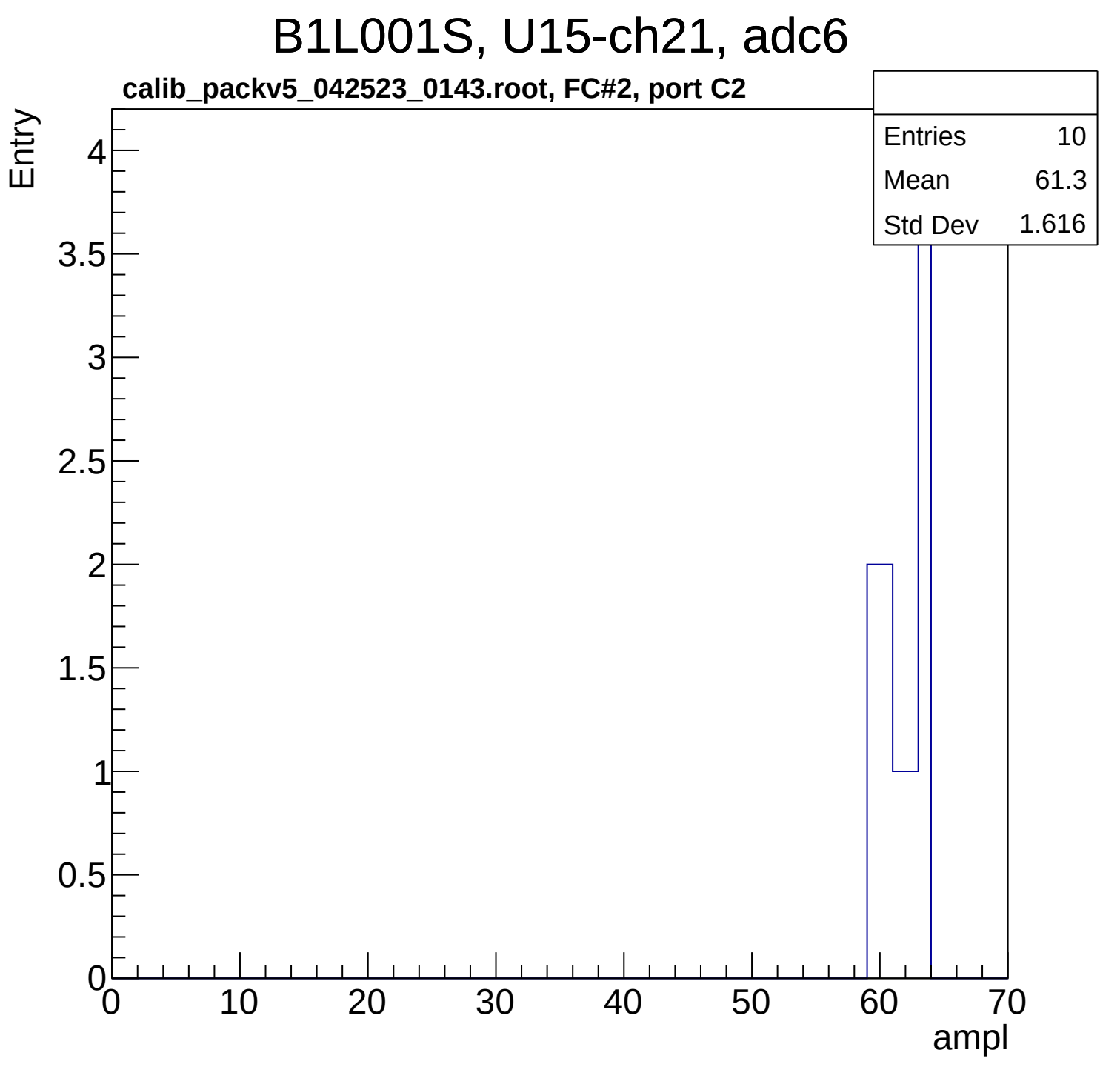
B1L001S, U15-ch21, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 61.3
Std Dev 1.616
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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60
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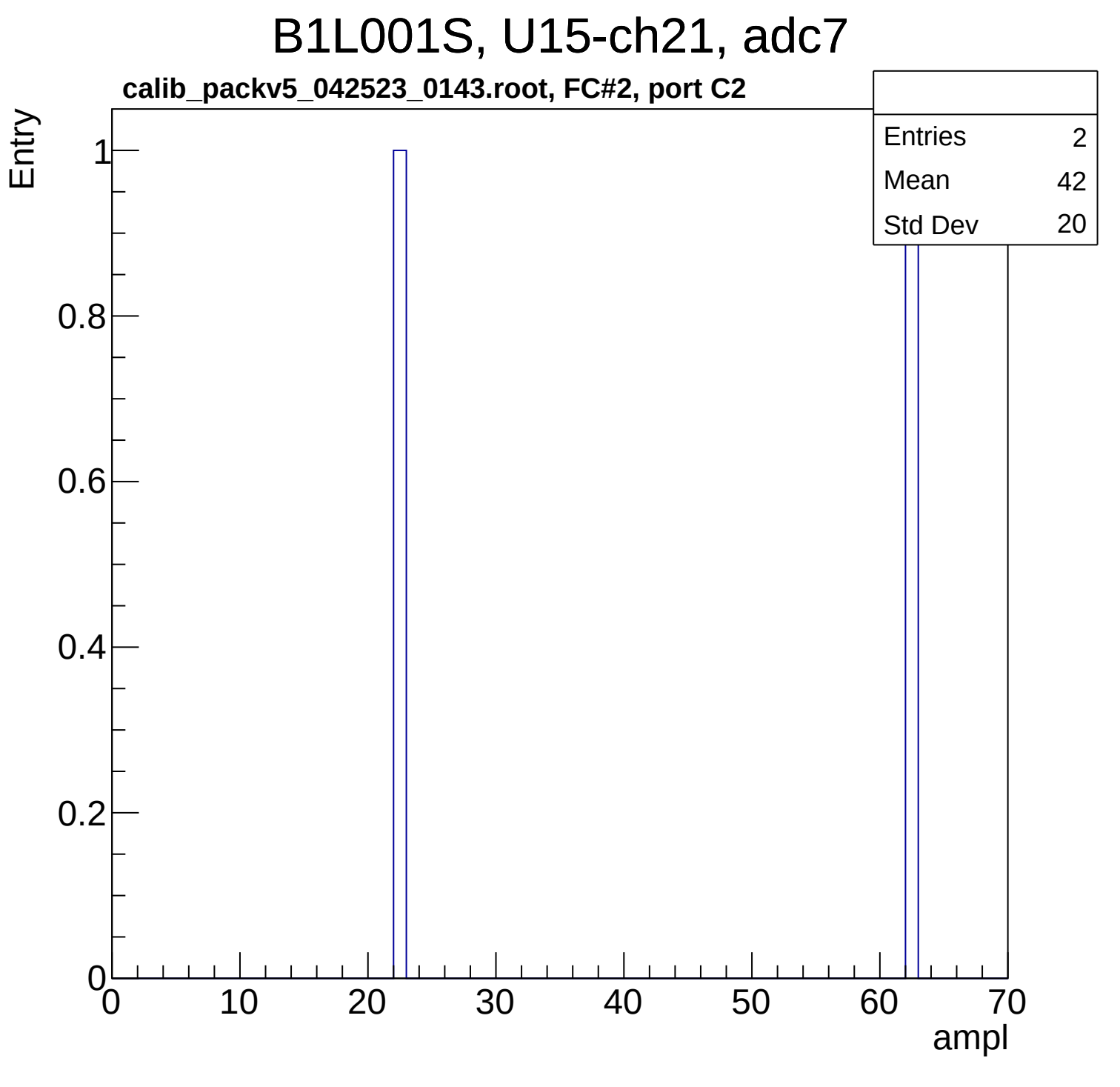
B1L001S, U15-ch21, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42
Std Dev 20
Entry
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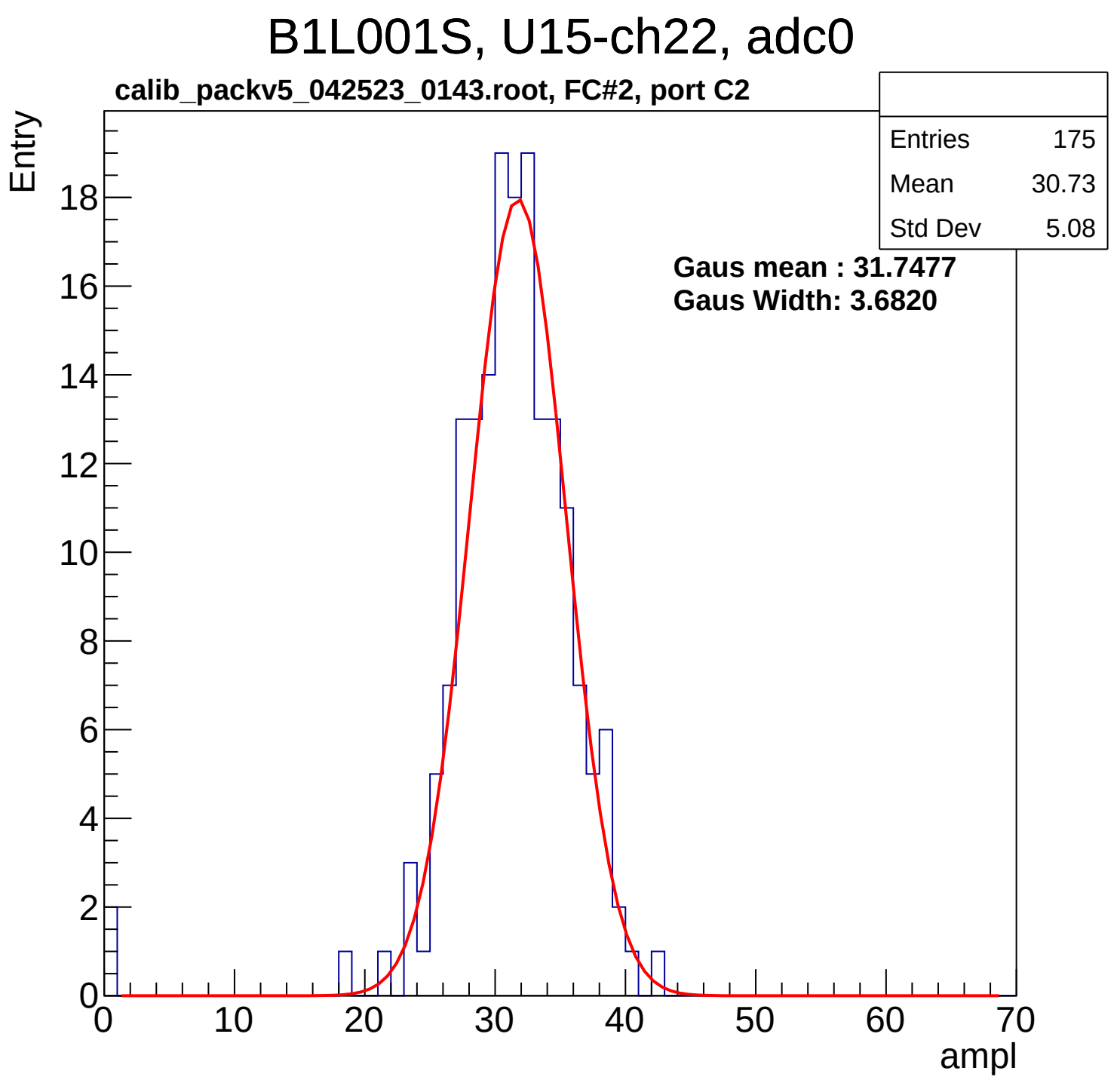
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch22, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 175
Mean 30.73
Std Dev 5.08
Gaus mean : 31.7477
Gaus Width: 3.6820
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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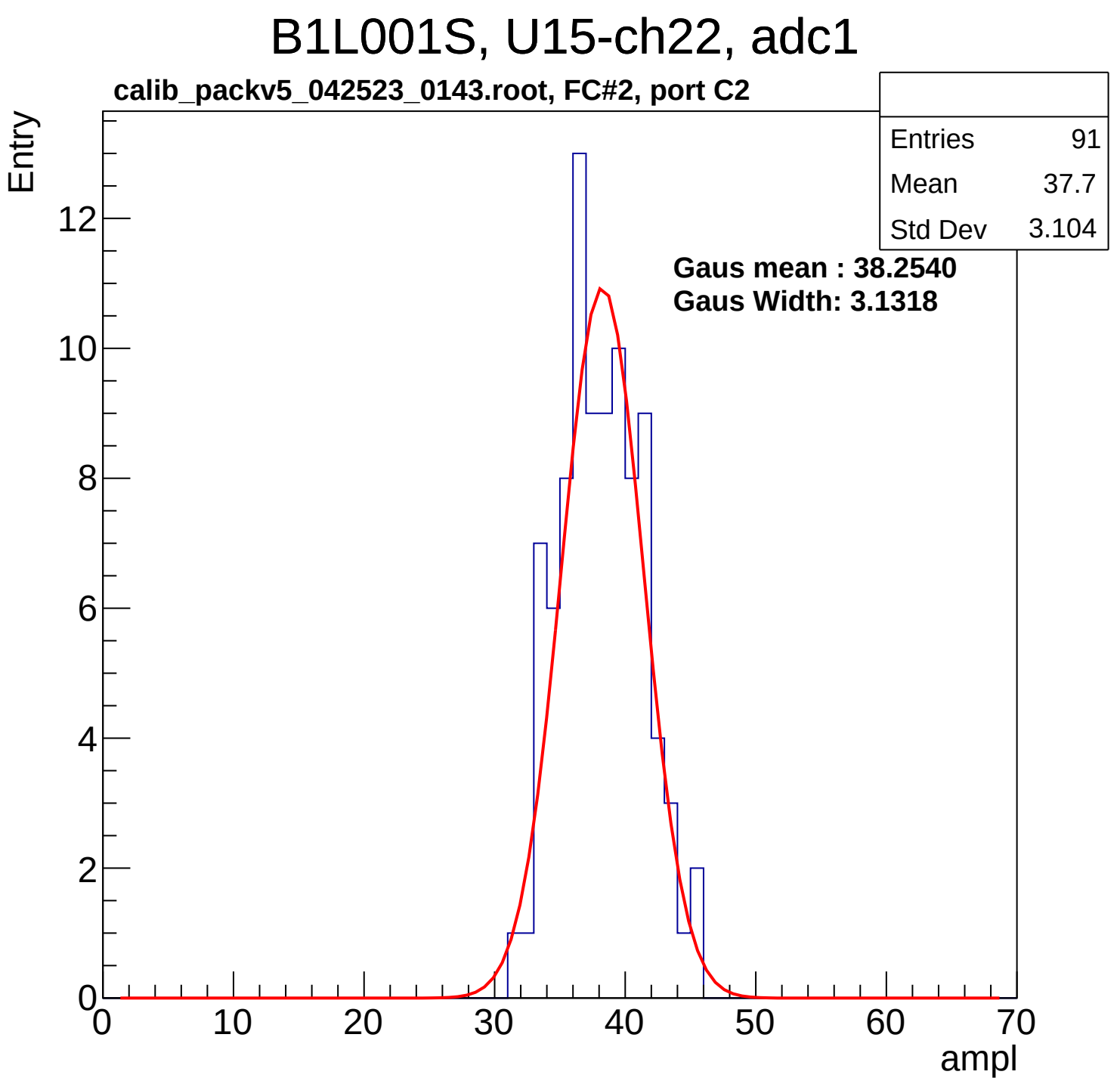
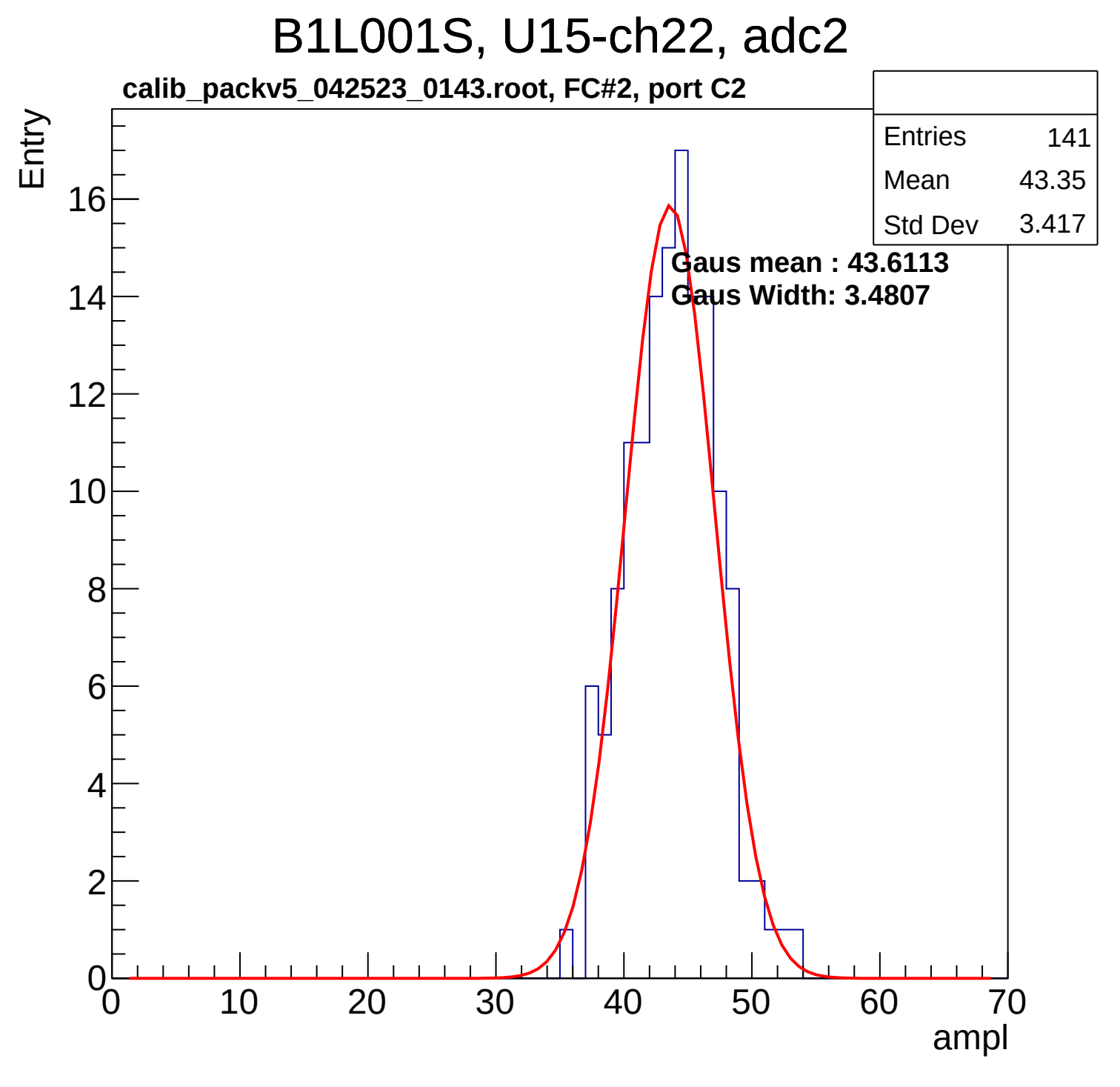
60
70


B1L001S, U15-ch22, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 37.7
Std Dev 3.104
Gaus mean : 38.2540
Gaus Width: 3.1318
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch22, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 43.35
Std Dev 3.417
Gaus mean : 43.6113
Gaus Width: 3.4807
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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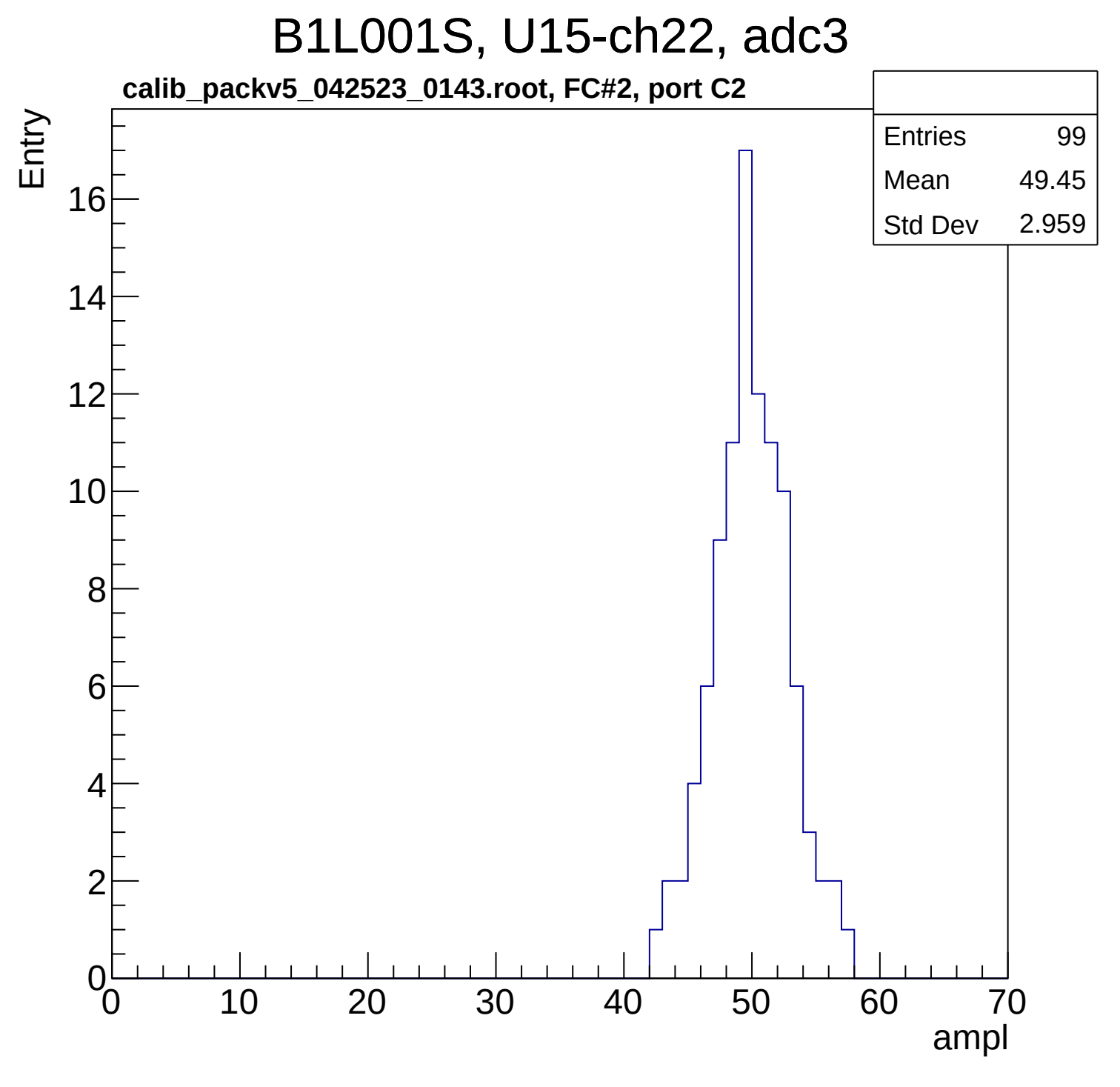
B1L001S, U15-ch22, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 49.45
Std Dev 2.959
Entry
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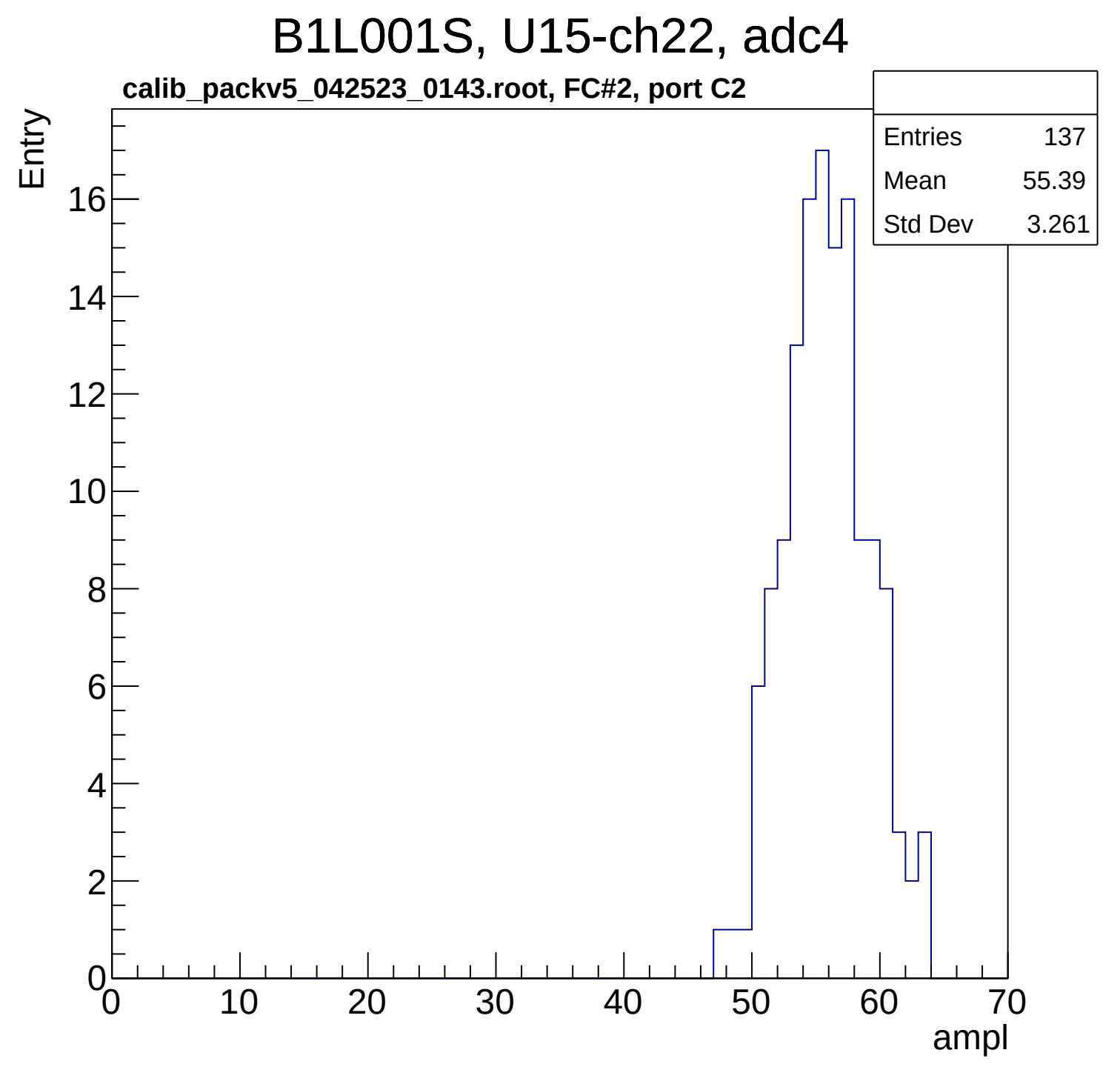
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch22, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 55.39
Std Dev 3.261
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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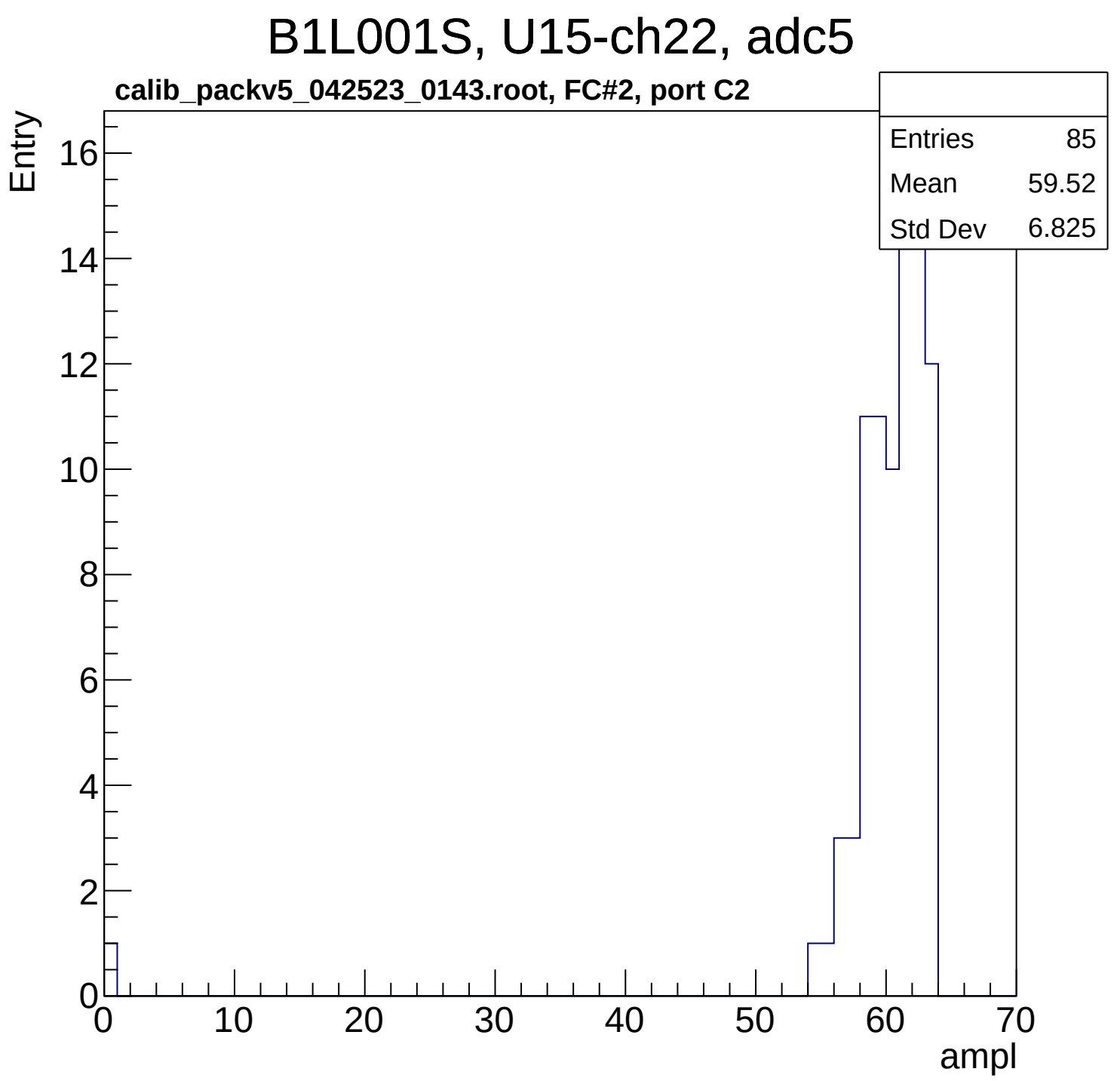
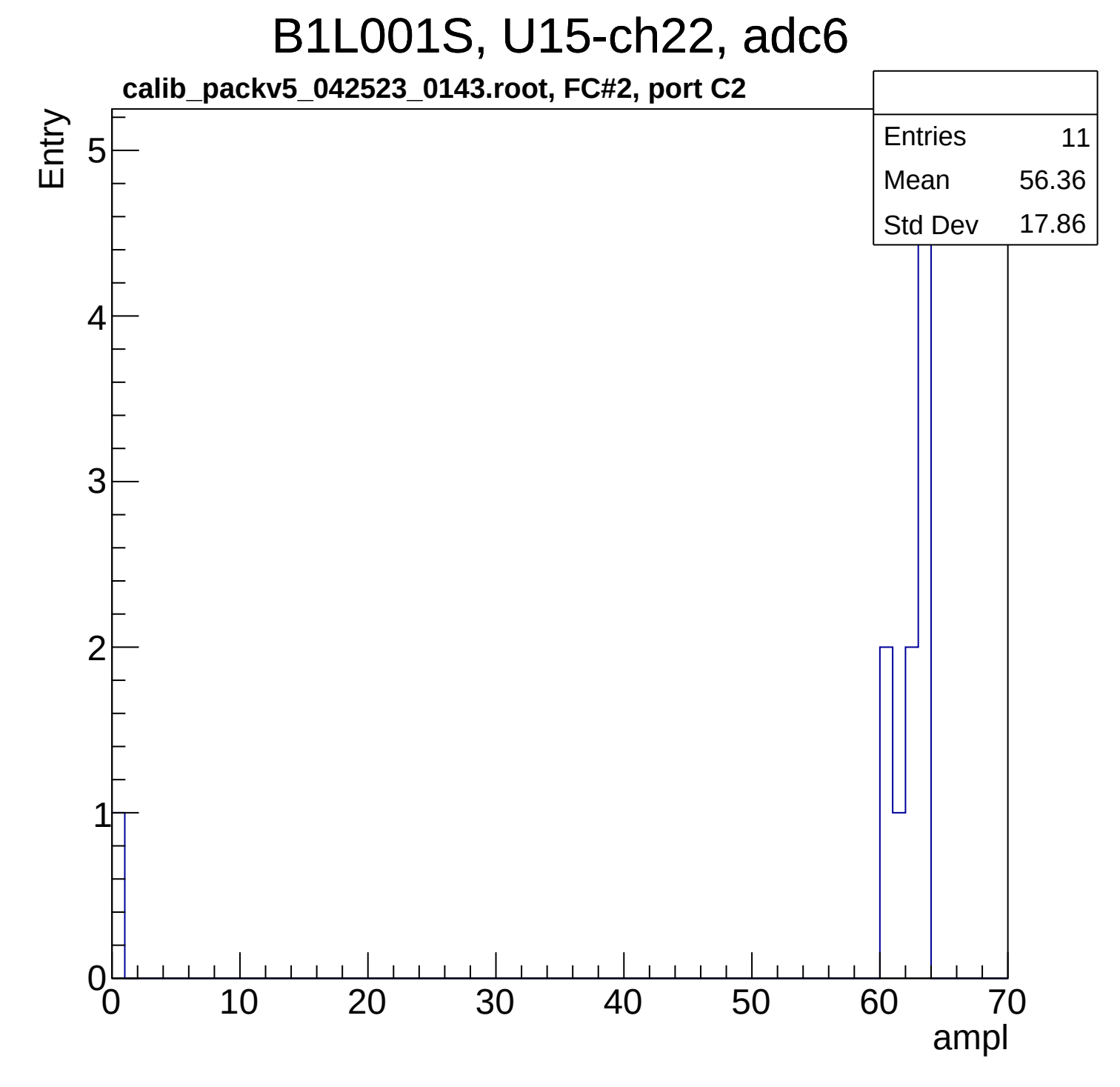

B1L001S, U15-ch22, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 59.52
Std Dev 6.825
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch22, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 56.36
Std Dev 17.86
Entry
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1
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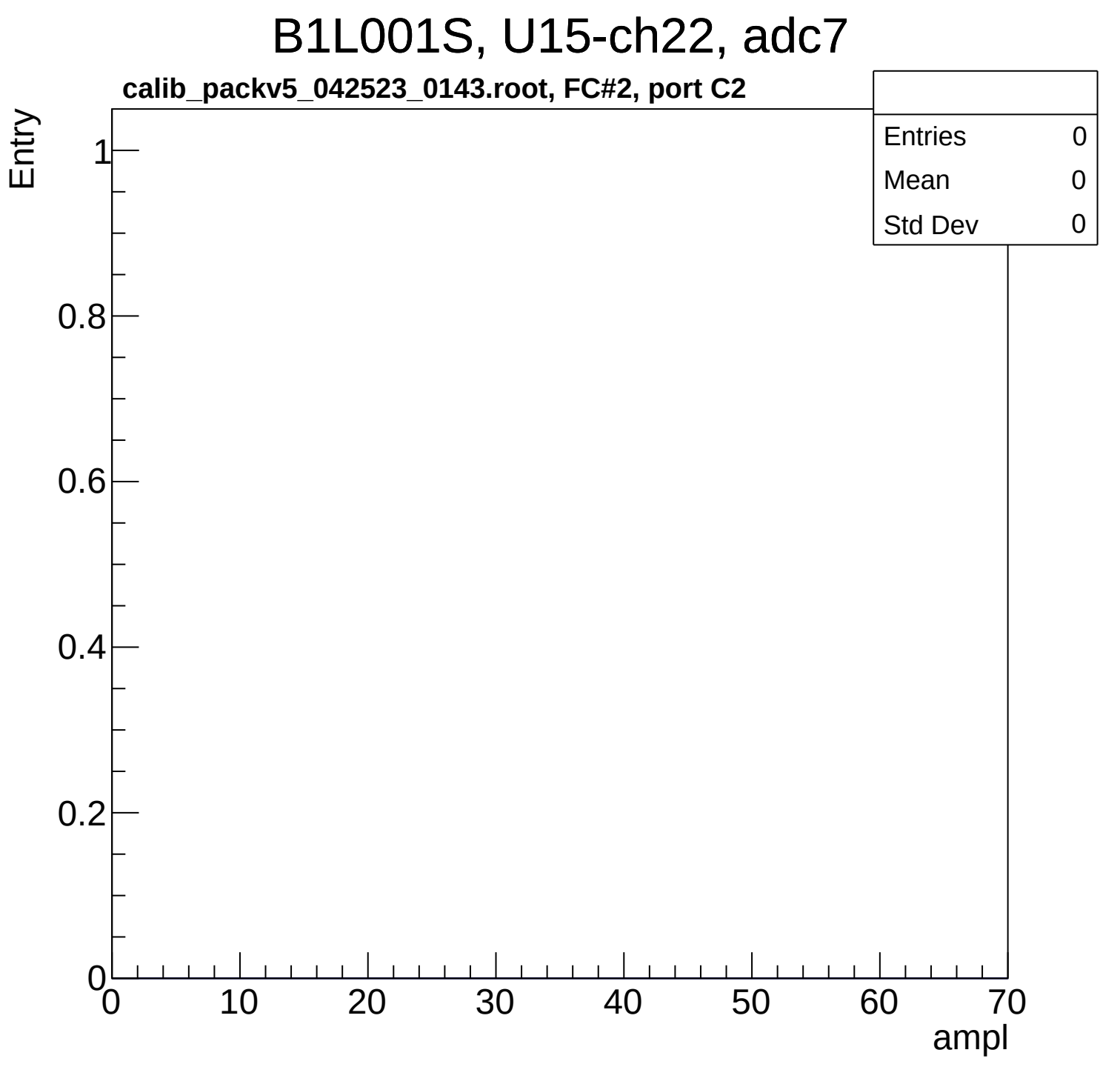
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch22, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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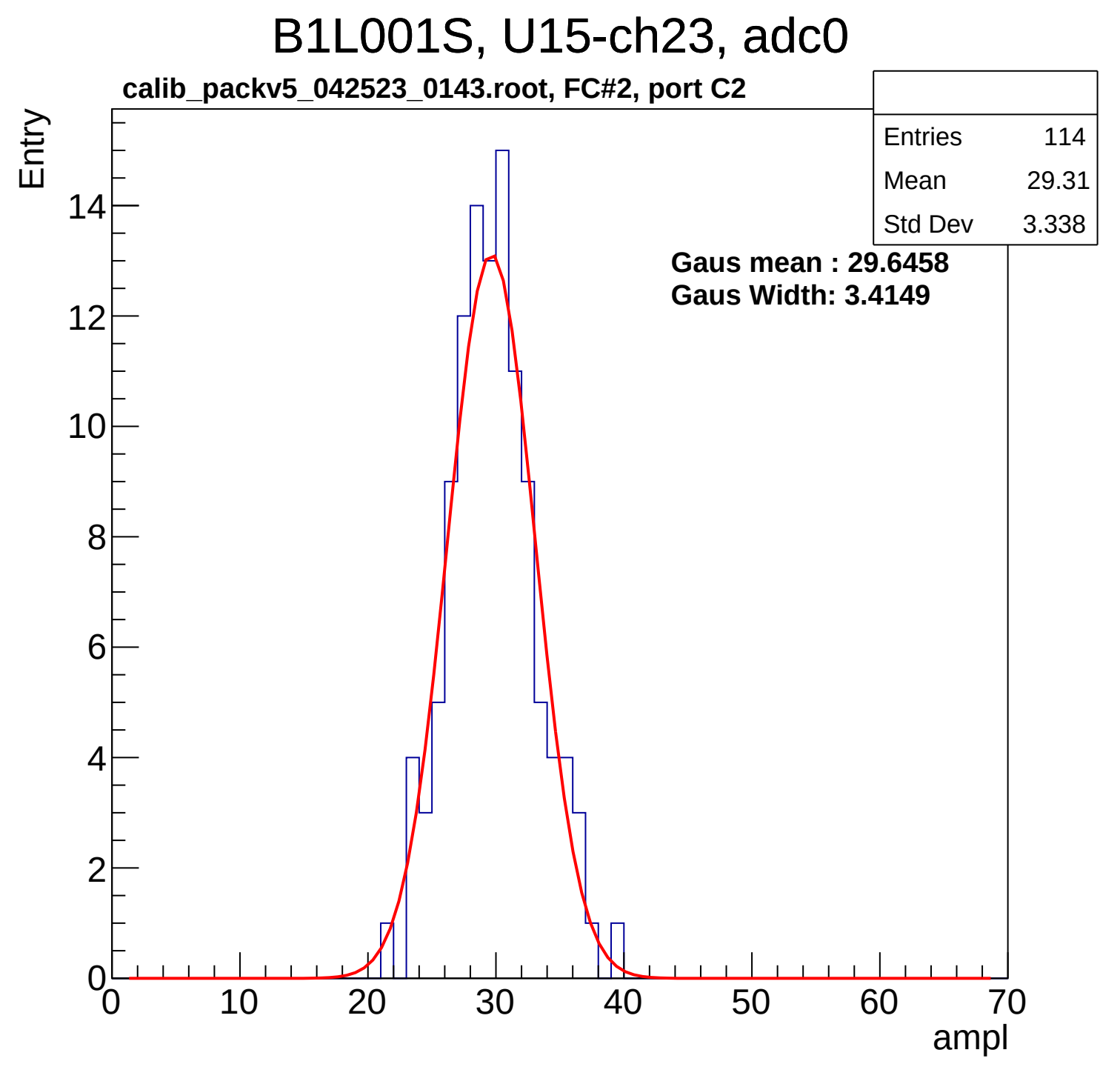
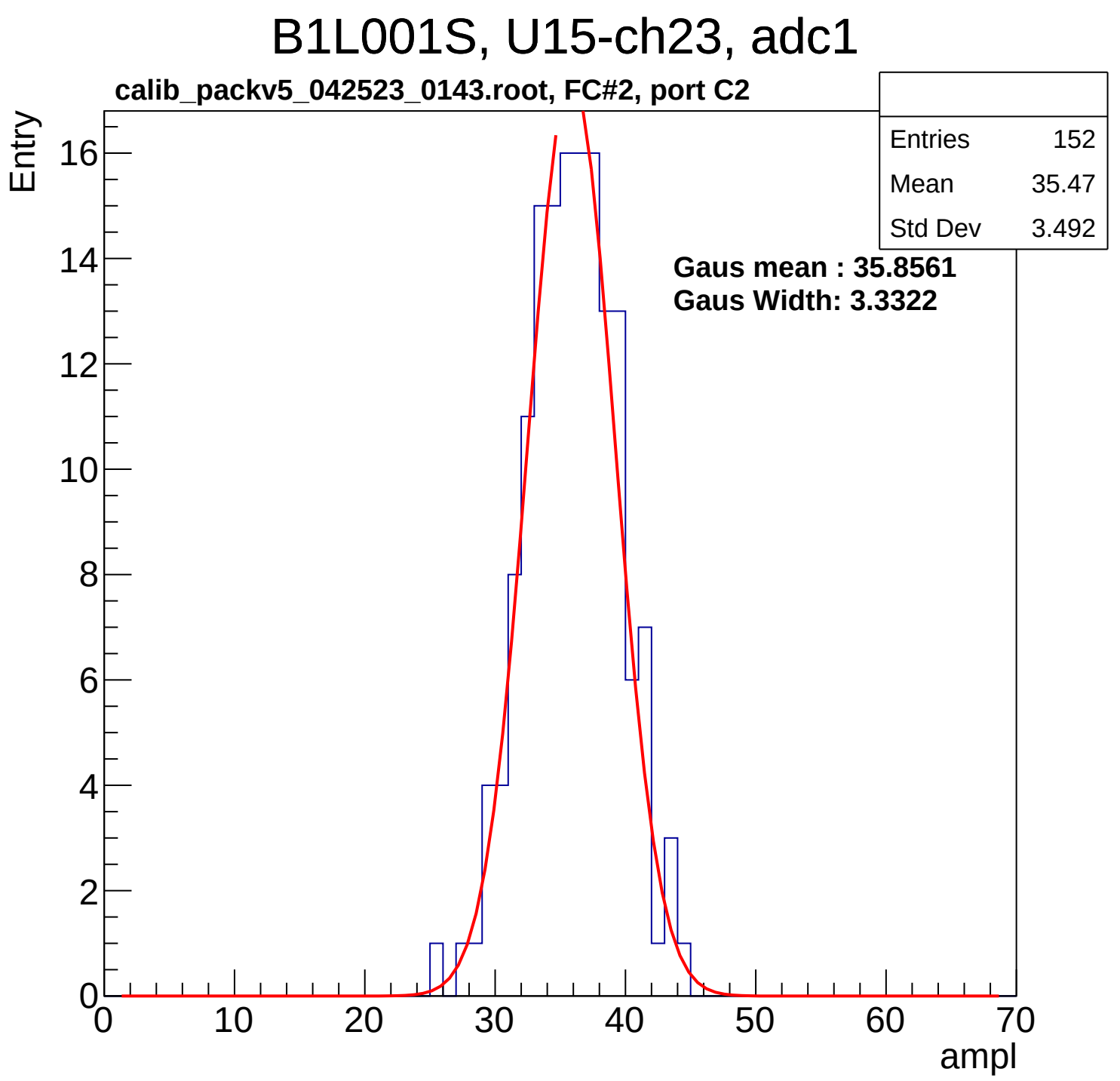
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch23, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 29.31
Std Dev 3.338
Gaus mean : 29.6458
Gaus Width: 3.4149
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch23, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 35.47
Std Dev 3.492
Gaus mean : 35.8561
Gaus Width: 3.3322
Entry
ampl
0
2
4
6
8
10
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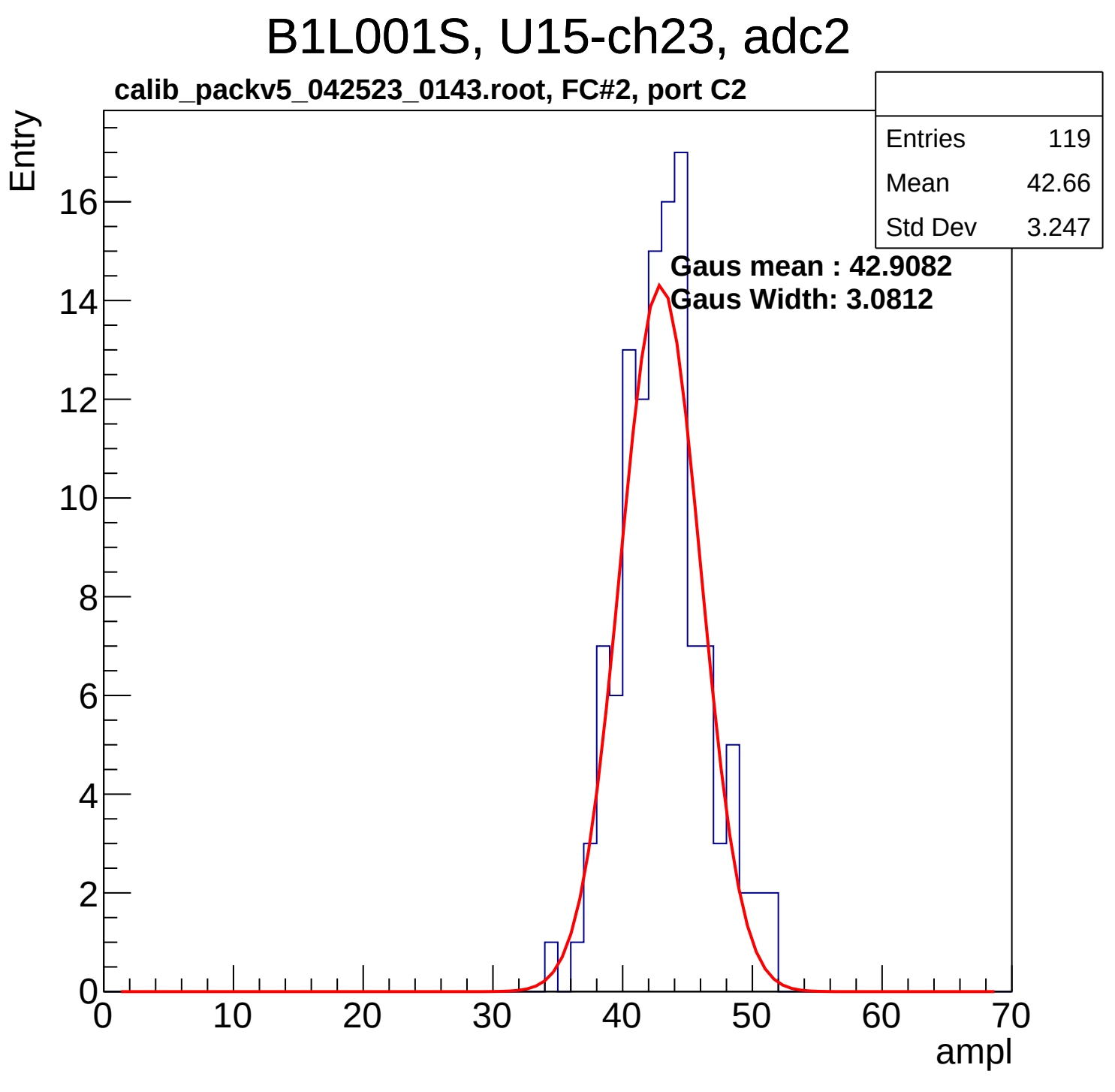
B1L001S, U15-ch23, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 42.66
Std Dev 3.247
Gaus mean : 42.9082
Gaus Width: 3.0812
Entry
ampl
0
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4
6
8
10
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14
16
0
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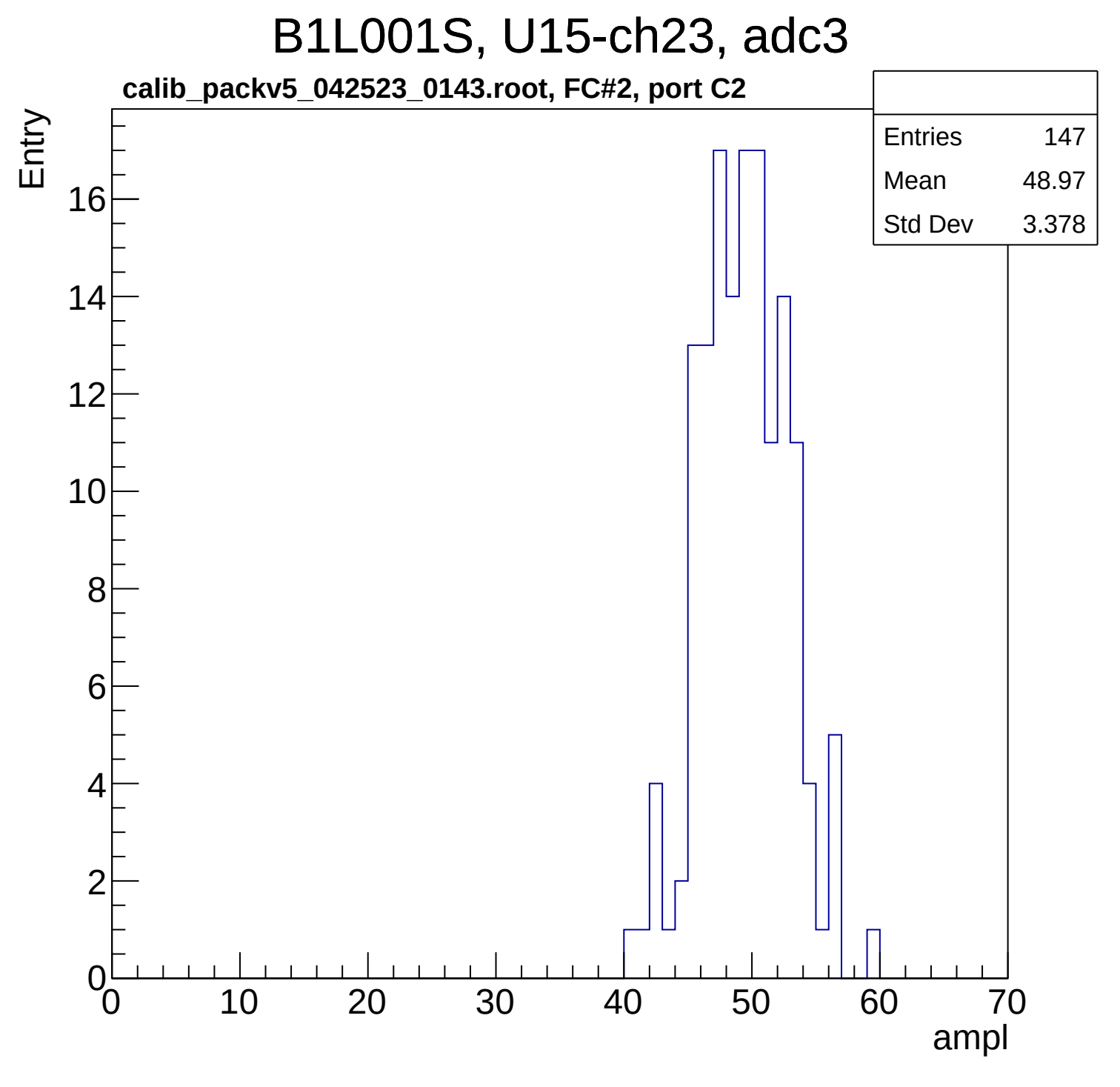
B1L001S, U15-ch23, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 48.97
Std Dev 3.378
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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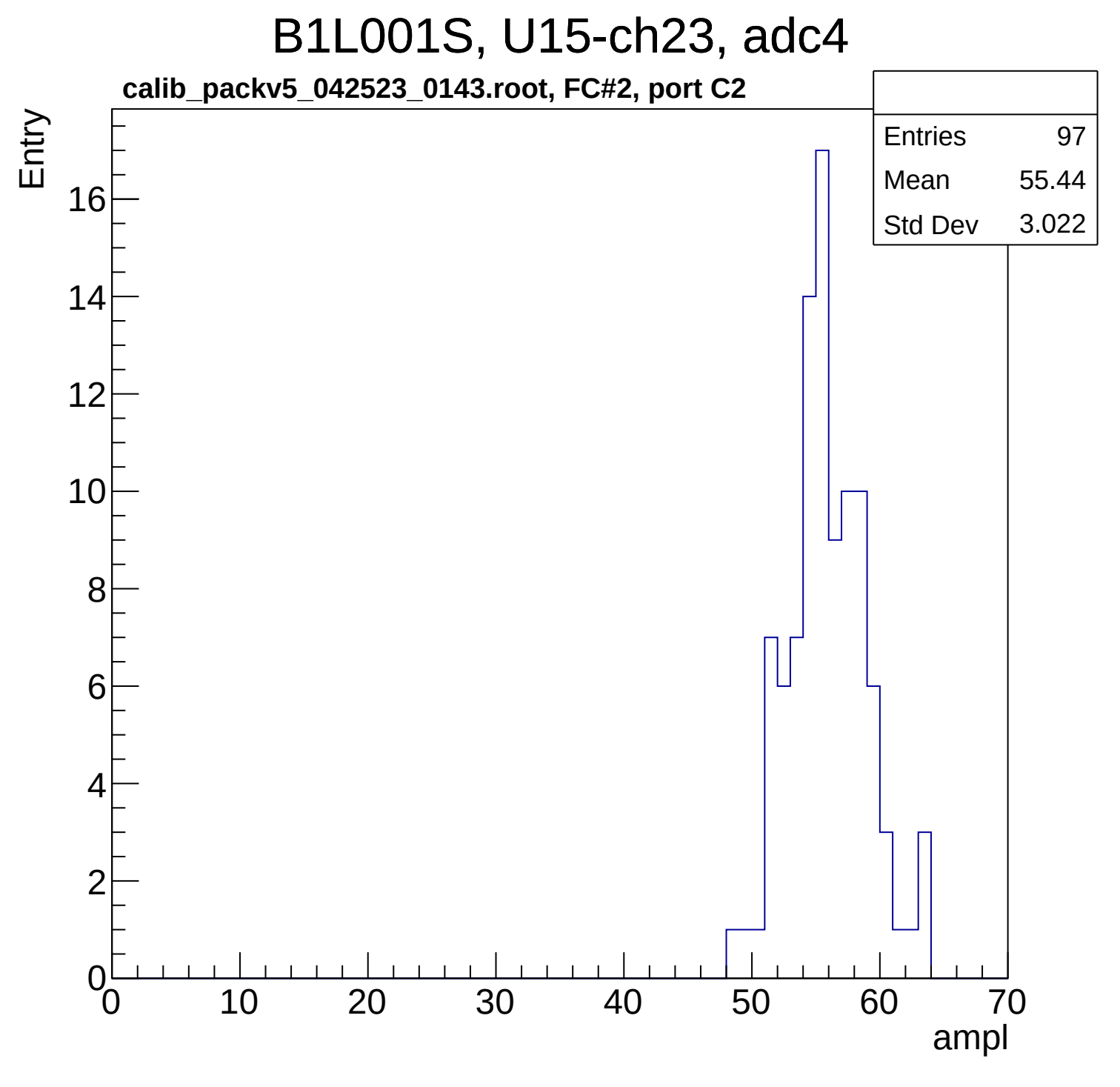
B1L001S, U15-ch23, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 55.44
Std Dev 3.022
Entry
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2
4
6
8
10
12
14
16
0
10
20
30
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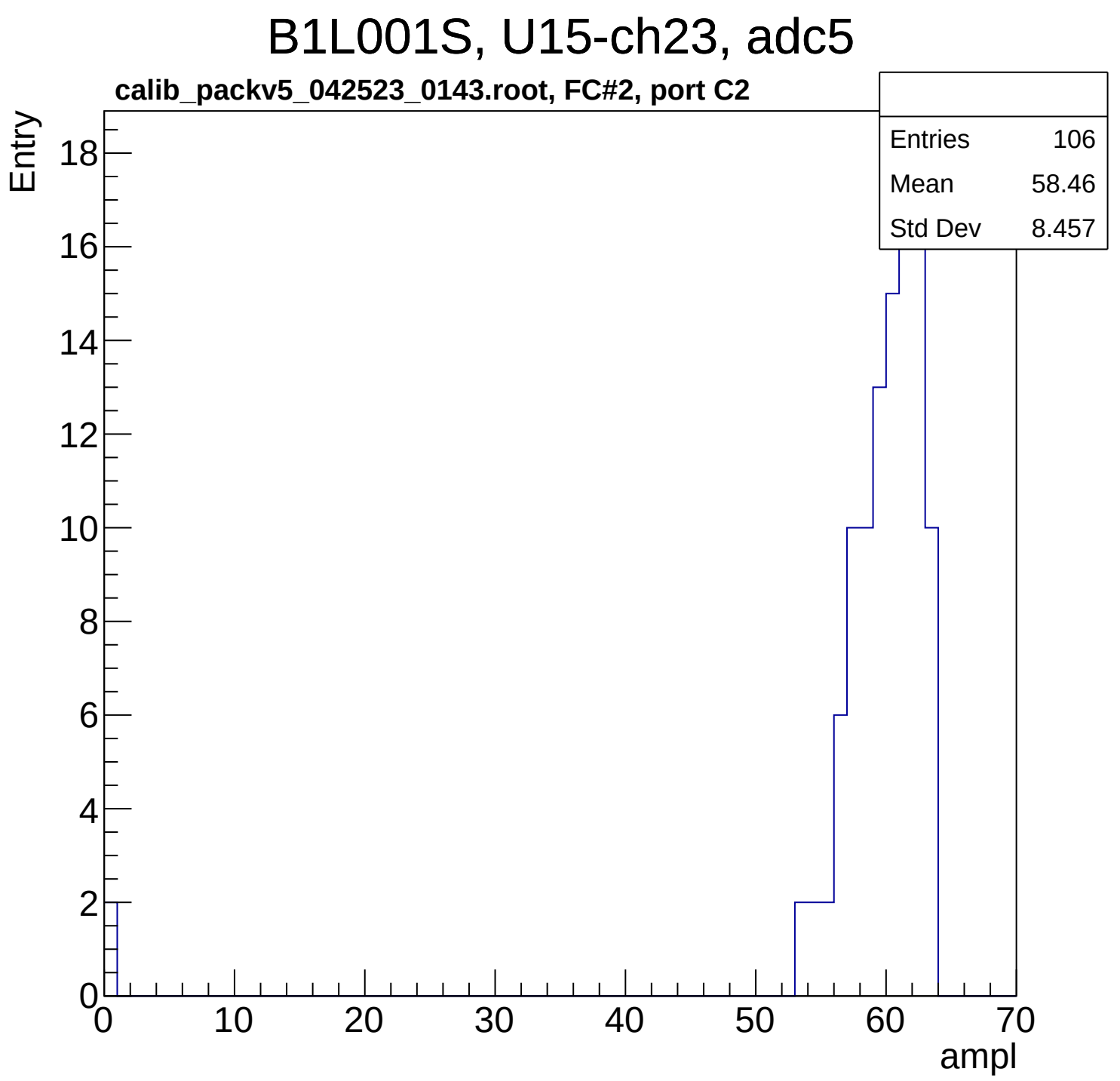
50
60
70
ampl


B1L001S, U15-ch23, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 58.46
Std Dev 8.457
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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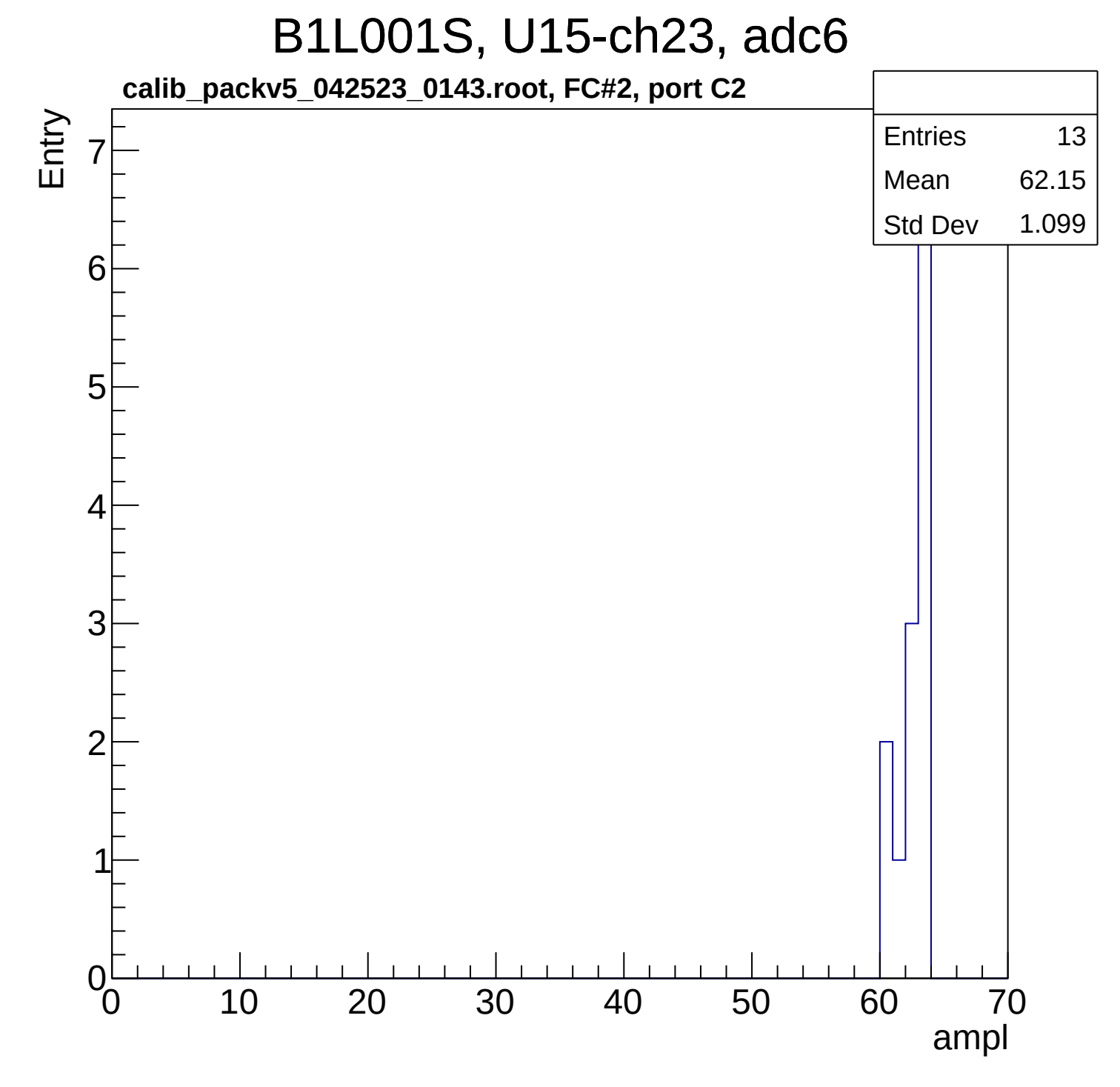
B1L001S, U15-ch23, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62.15
Std Dev 1.099
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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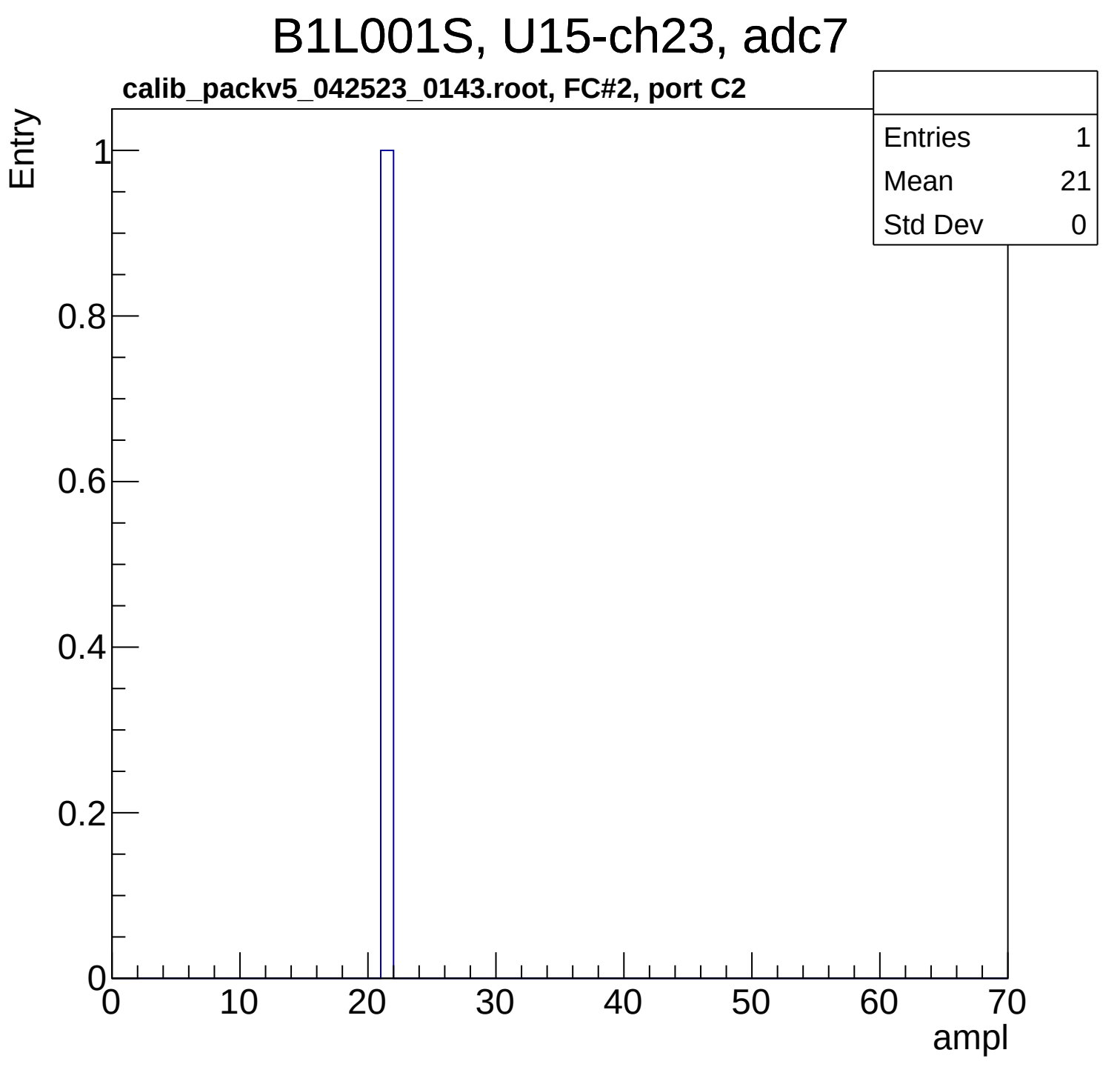
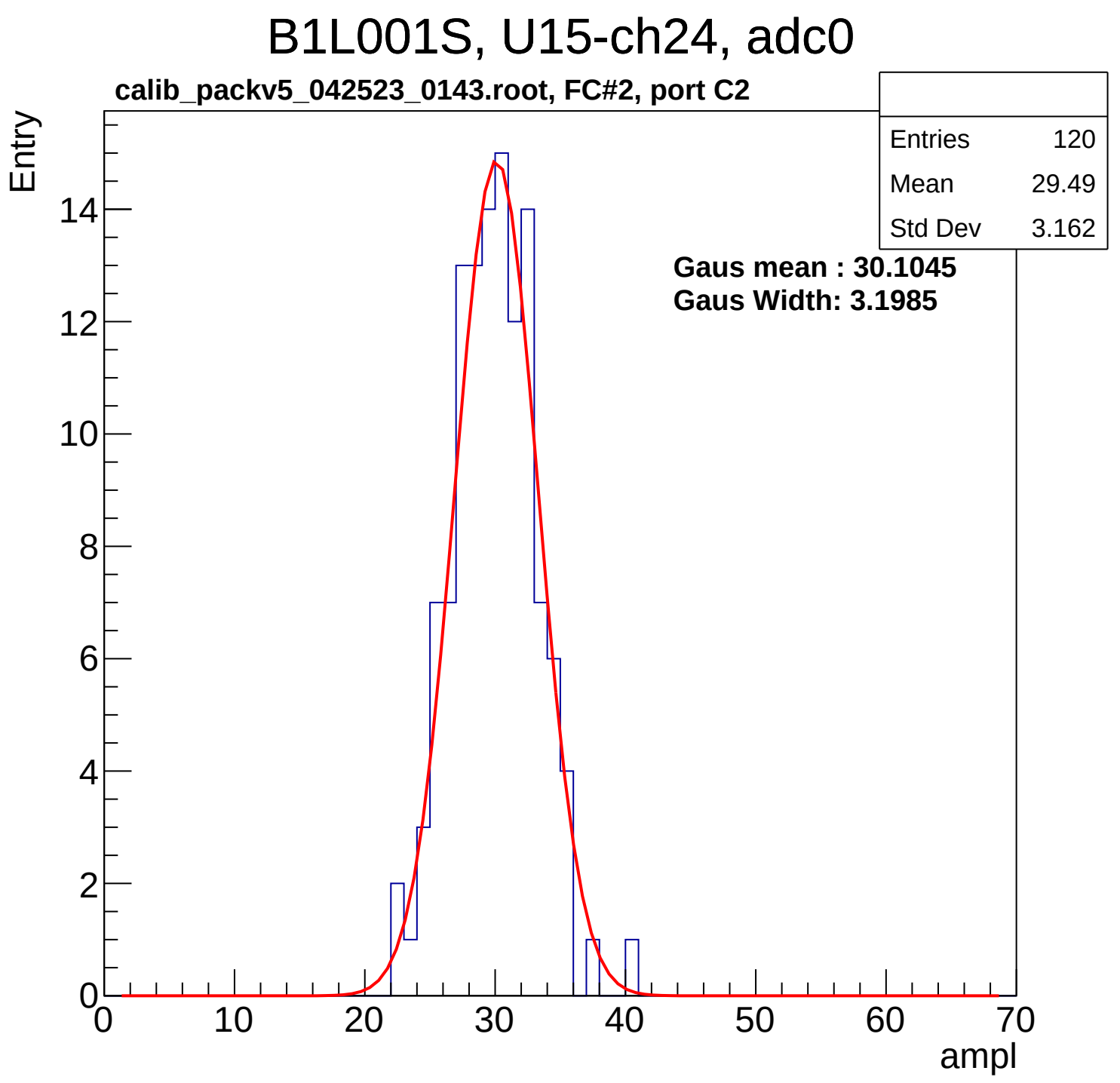

B1L001S, U15-ch23, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch24, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 29.49
Std Dev 3.162
Gaus mean : 30.1045
Gaus Width: 3.1985
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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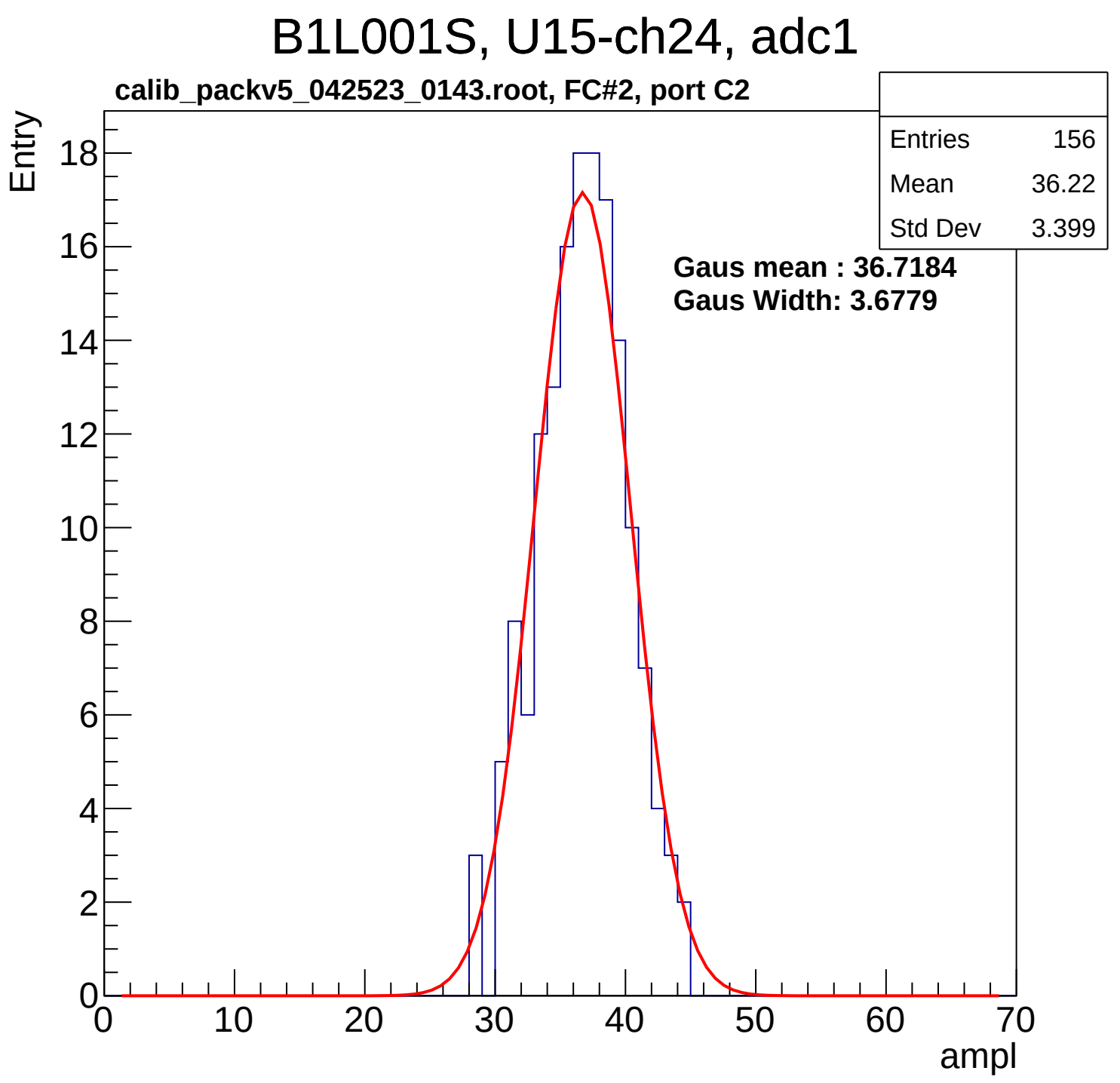
B1L001S, U15-ch24, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 36.22
Std Dev 3.399
Gaus mean : 36.7184
Gaus Width: 3.6779
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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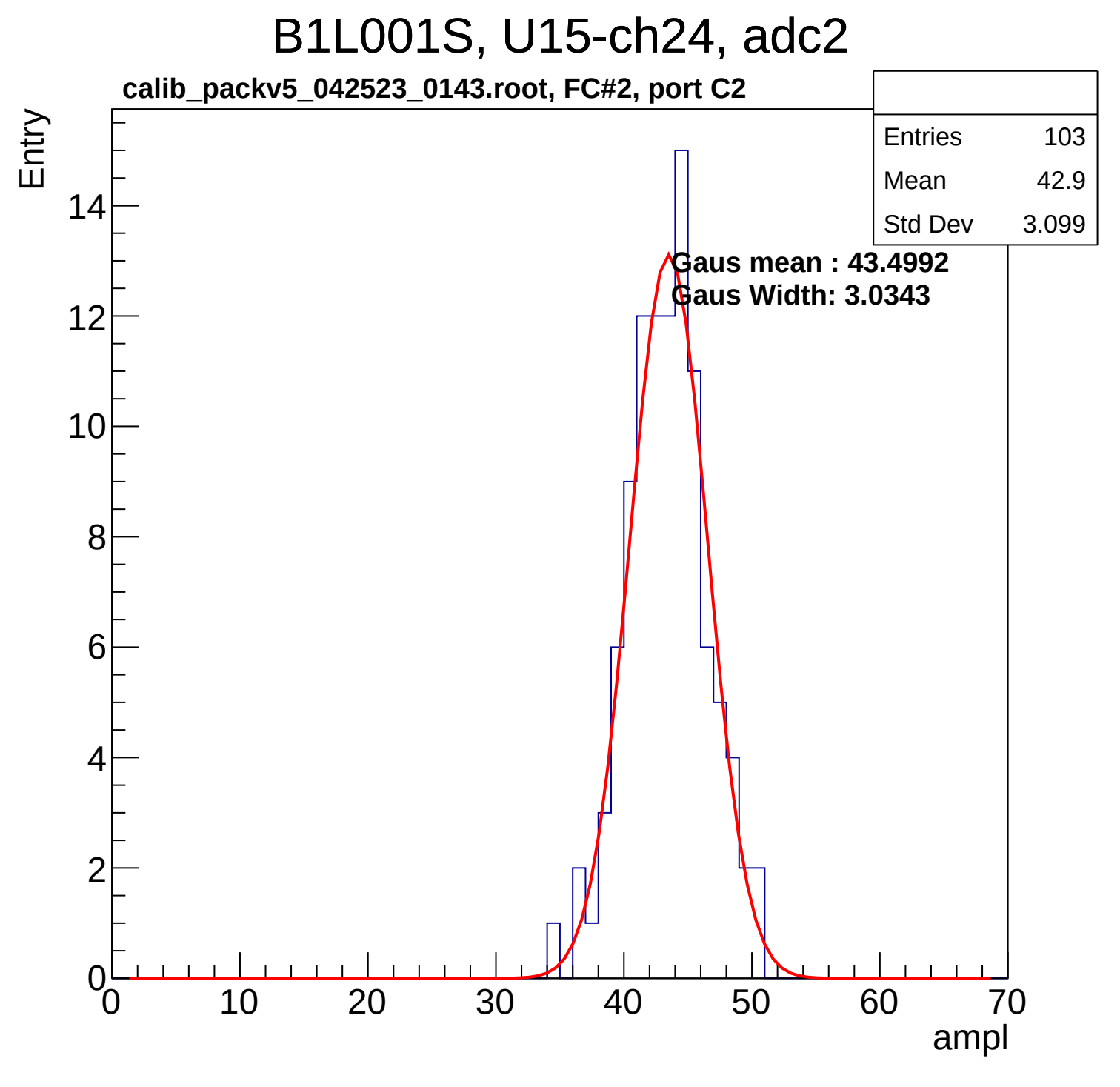
B1L001S, U15-ch24, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 42.9
Std Dev 3.099
Gaus mean : 43.4992
Gaus Width: 3.0343
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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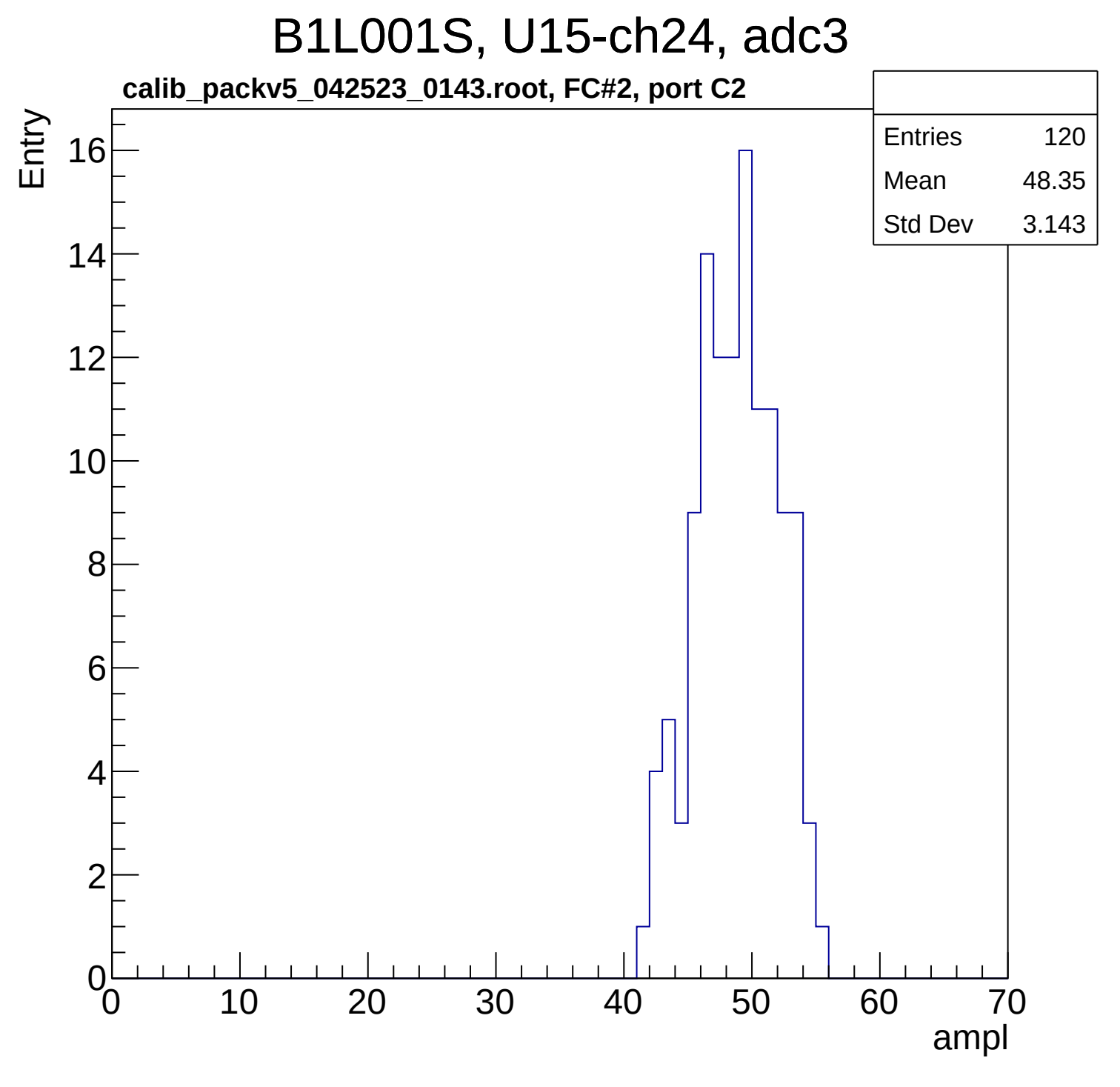
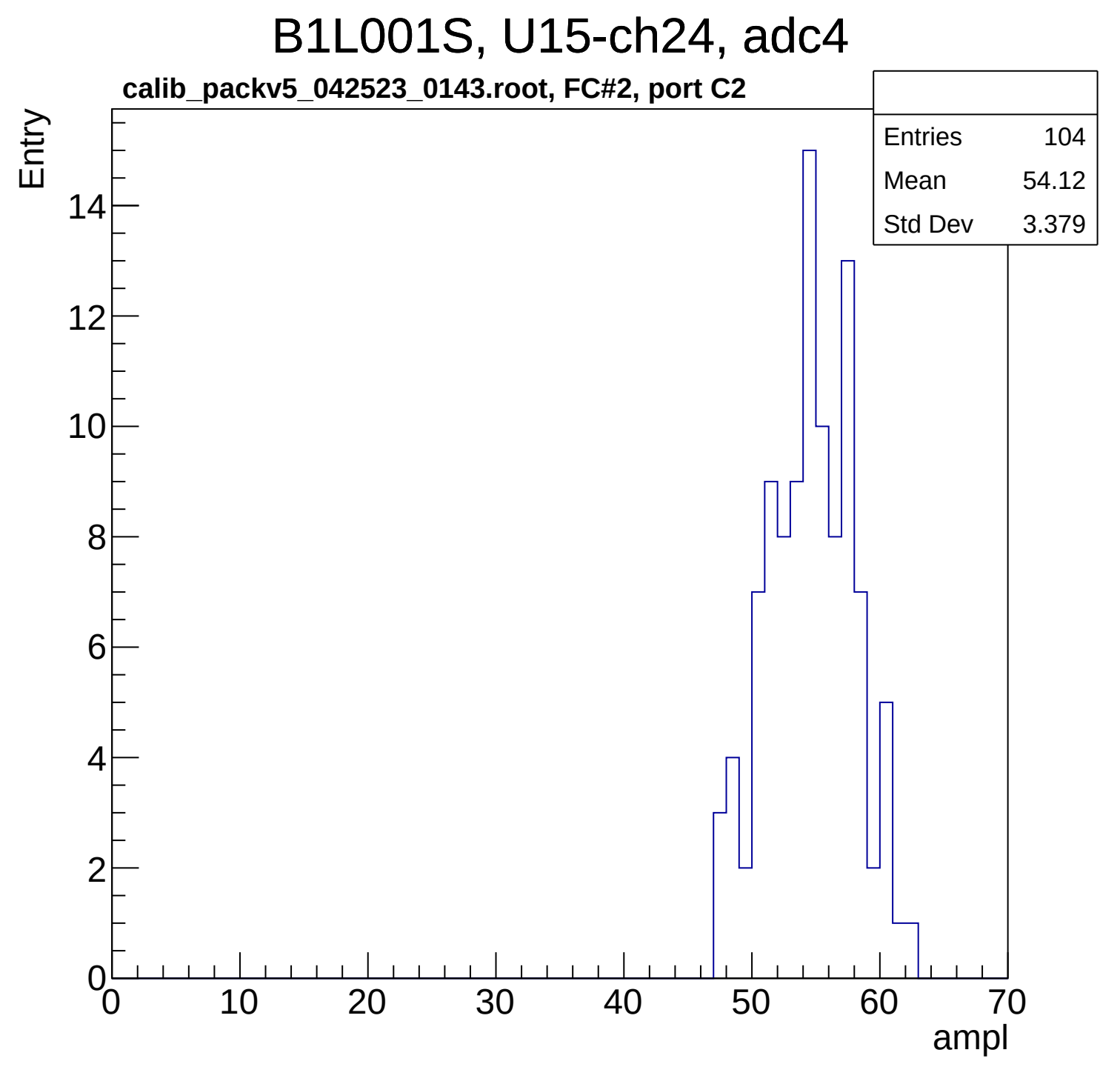

B1L001S, U15-ch24, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 48.35
Std Dev 3.143
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch24, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 54.12
Std Dev 3.379
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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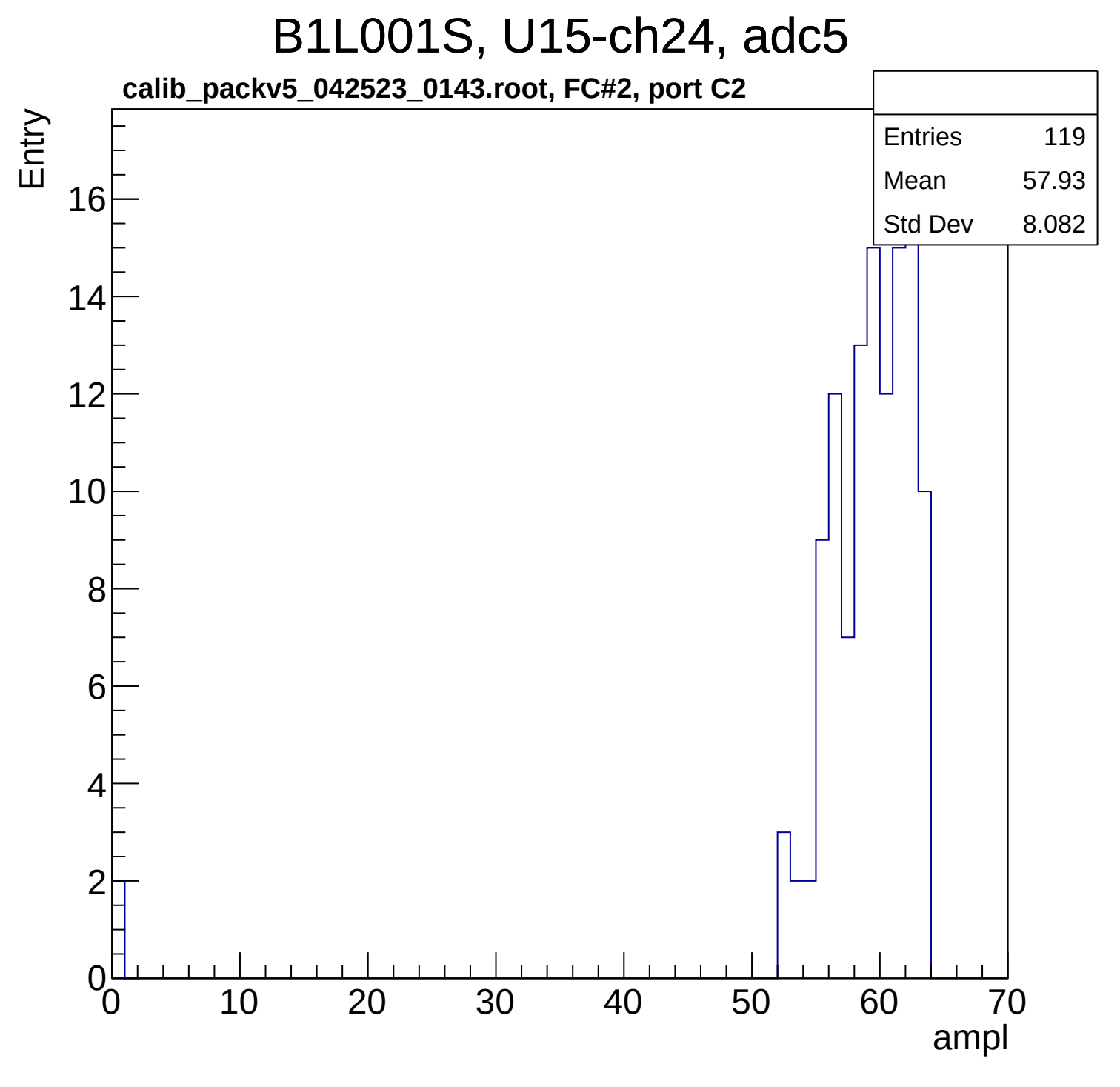
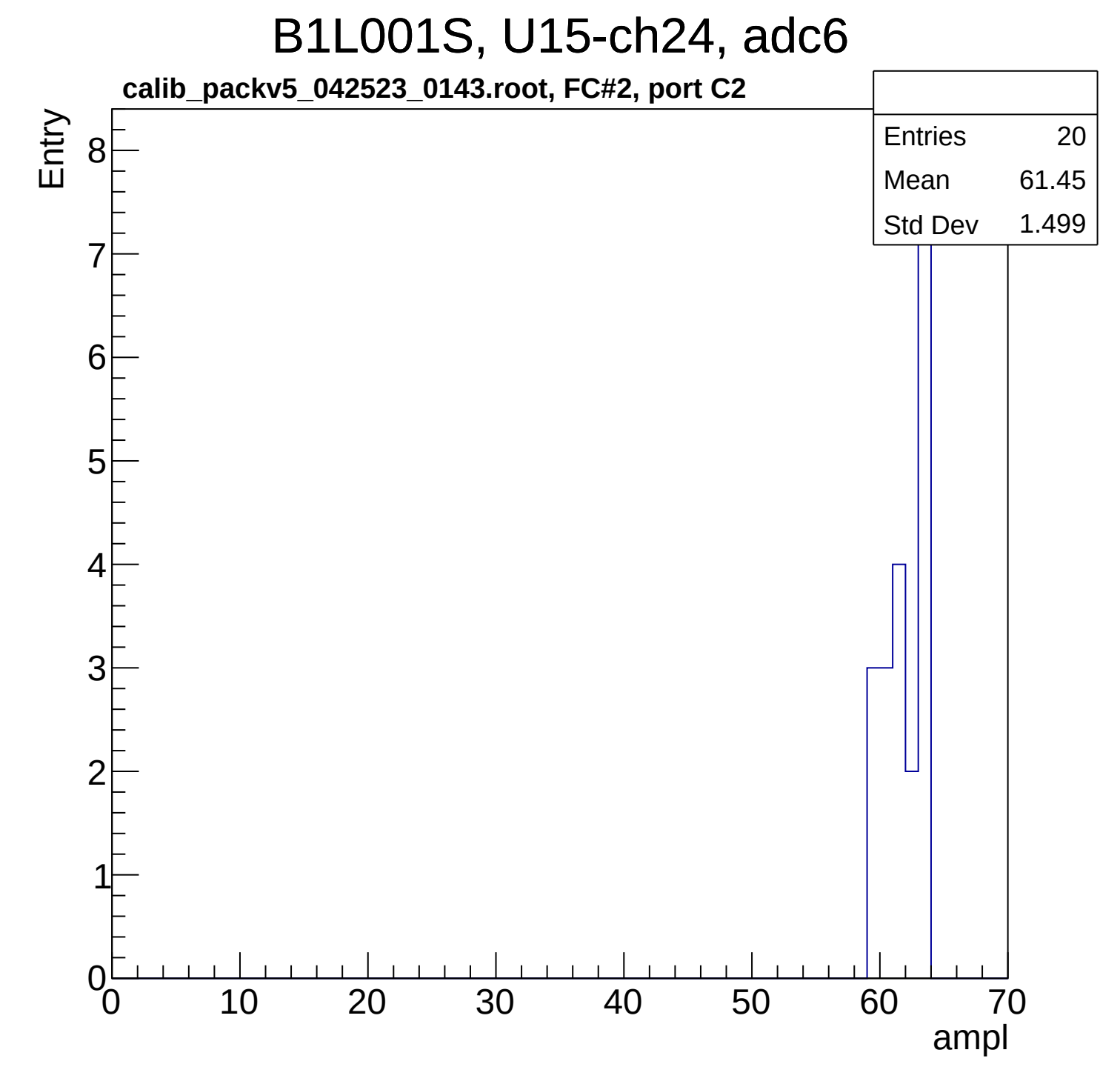

B1L001S, U15-ch24, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 57.93
Std Dev 8.082
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch24, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 20
Mean 61.45
Std Dev 1.499
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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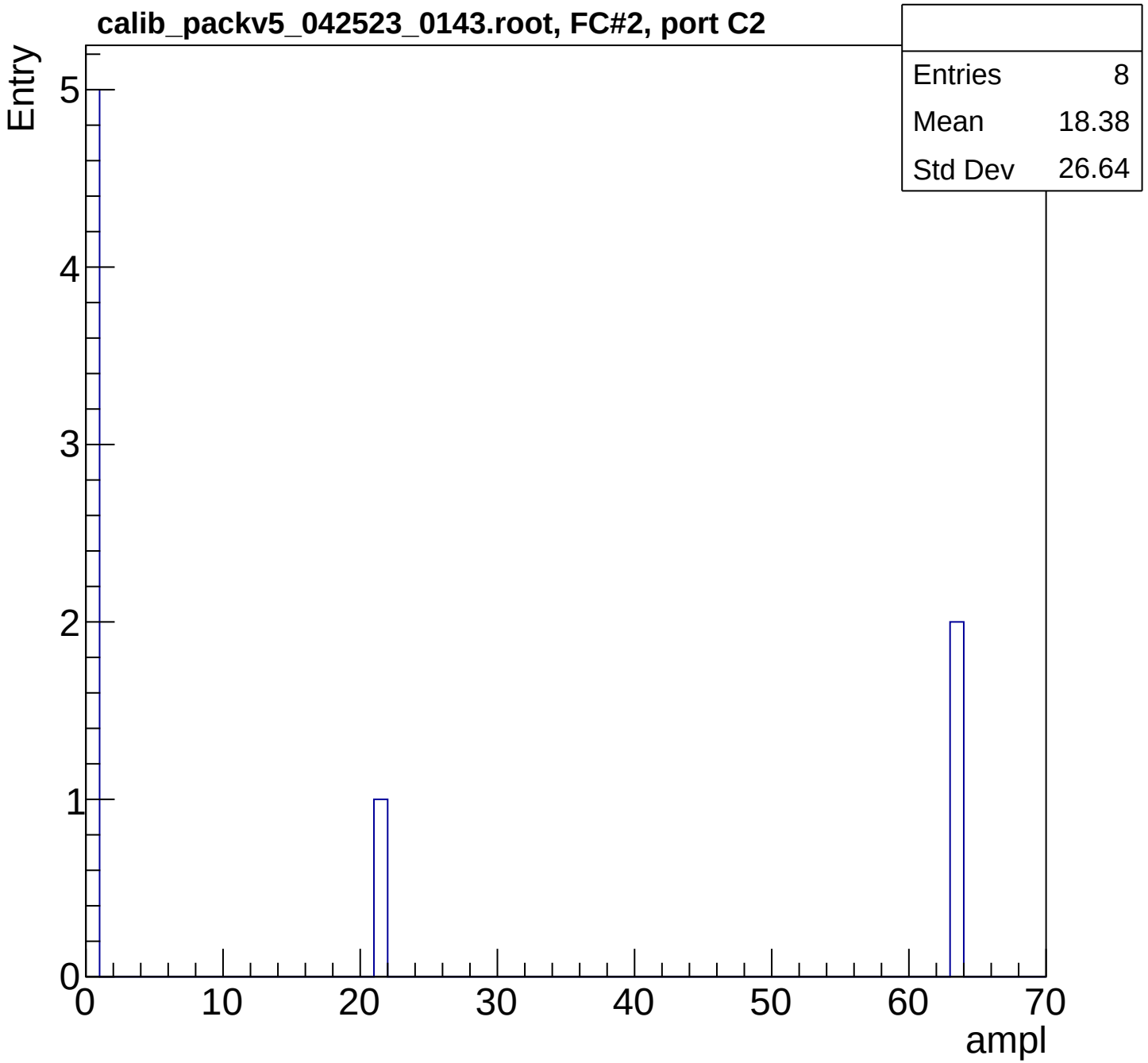
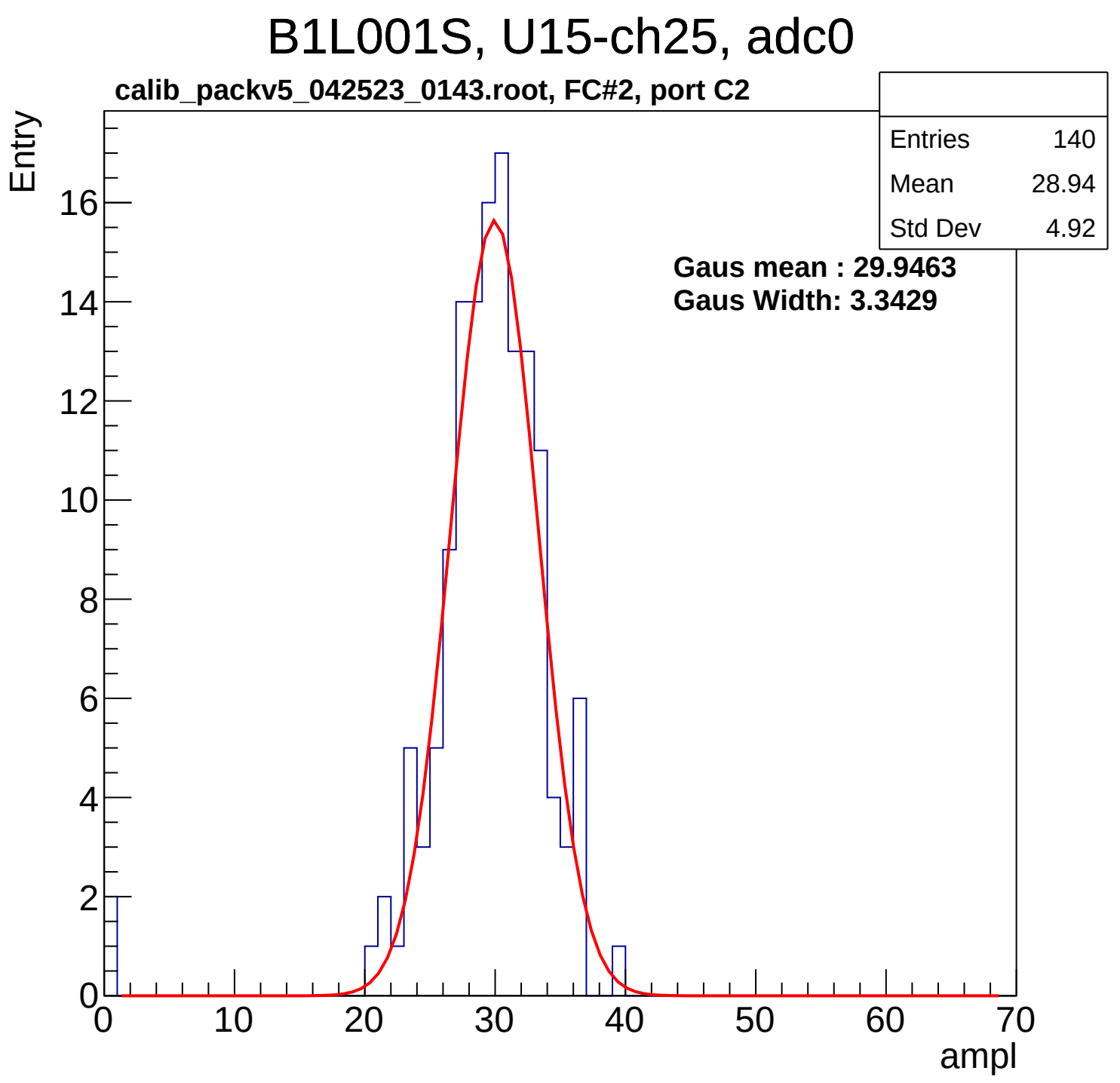

B1L001S, U15-ch24, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 18.38
Std Dev 26.64
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch25, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 28.94
Std Dev 4.92
Gaus mean : 29.9463
Gaus Width: 3.3429
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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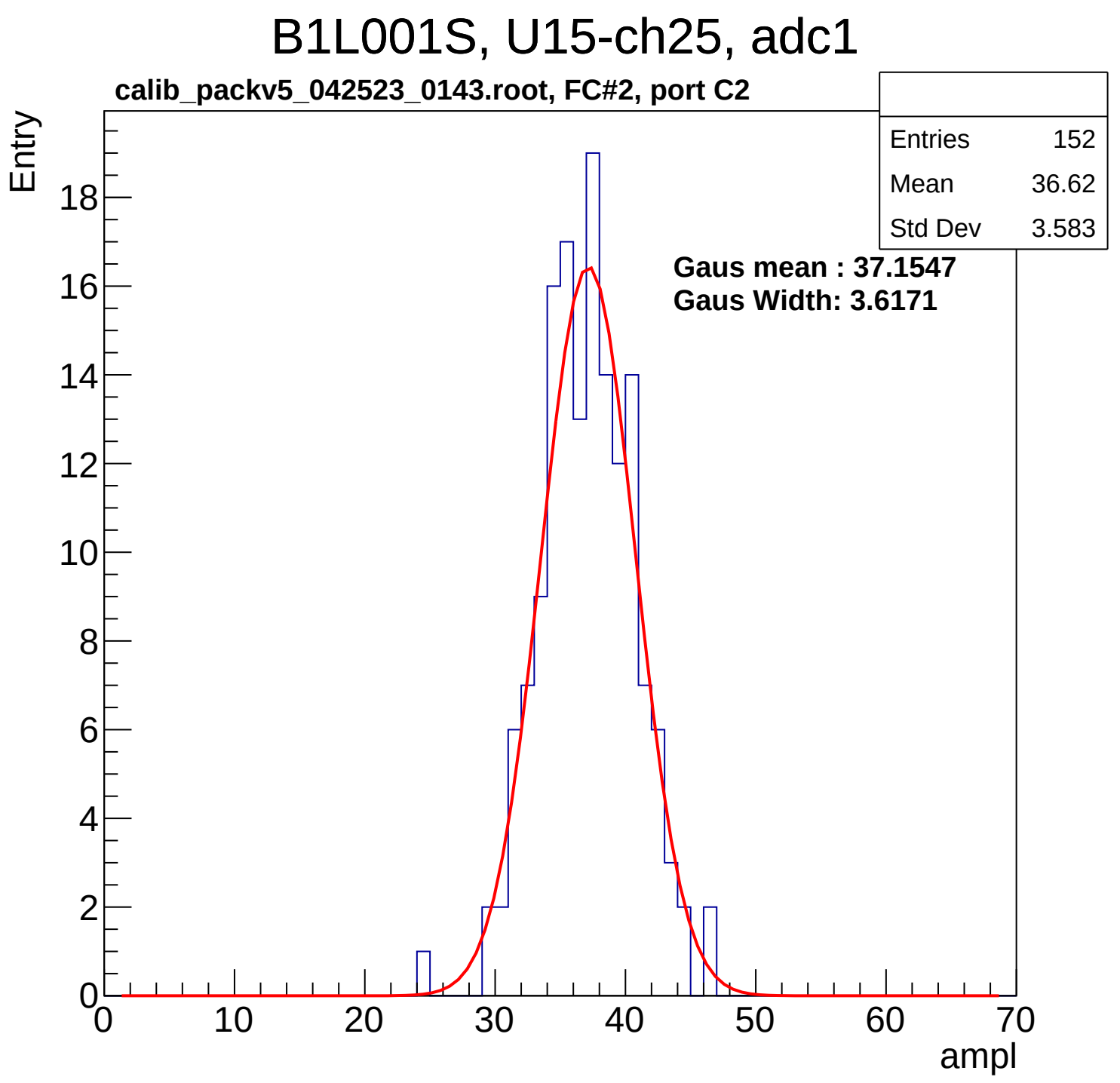
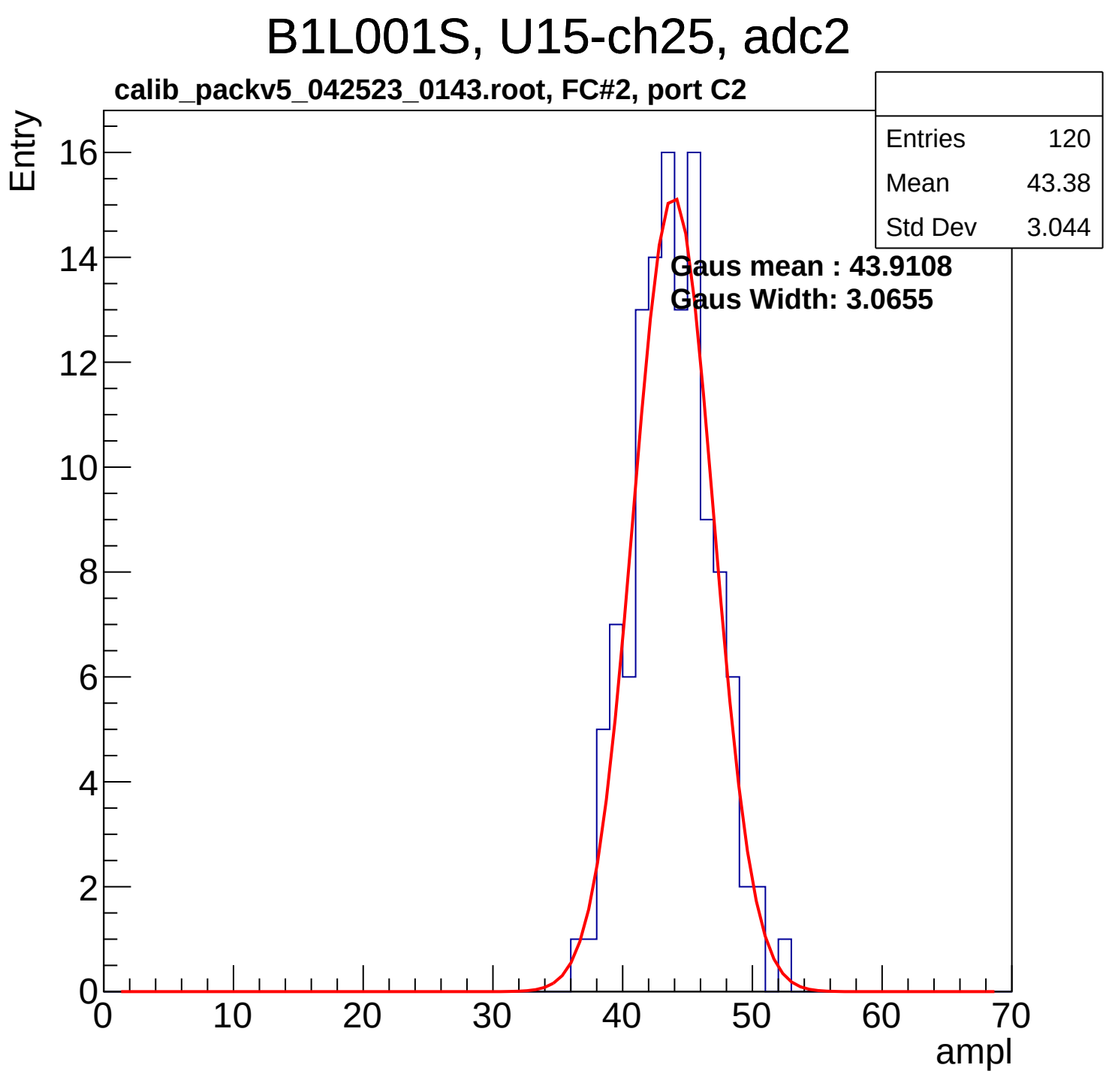

B1L001S, U15-ch25, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 36.62
Std Dev 3.583
Gaus mean : 37.1547
Gaus Width: 3.6171
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch25, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 43.38
Std Dev 3.044
Gaus mean : 43.9108
Gaus Width: 3.0655
Entry
ampl
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4
6
8
10
12
14
16
0
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60
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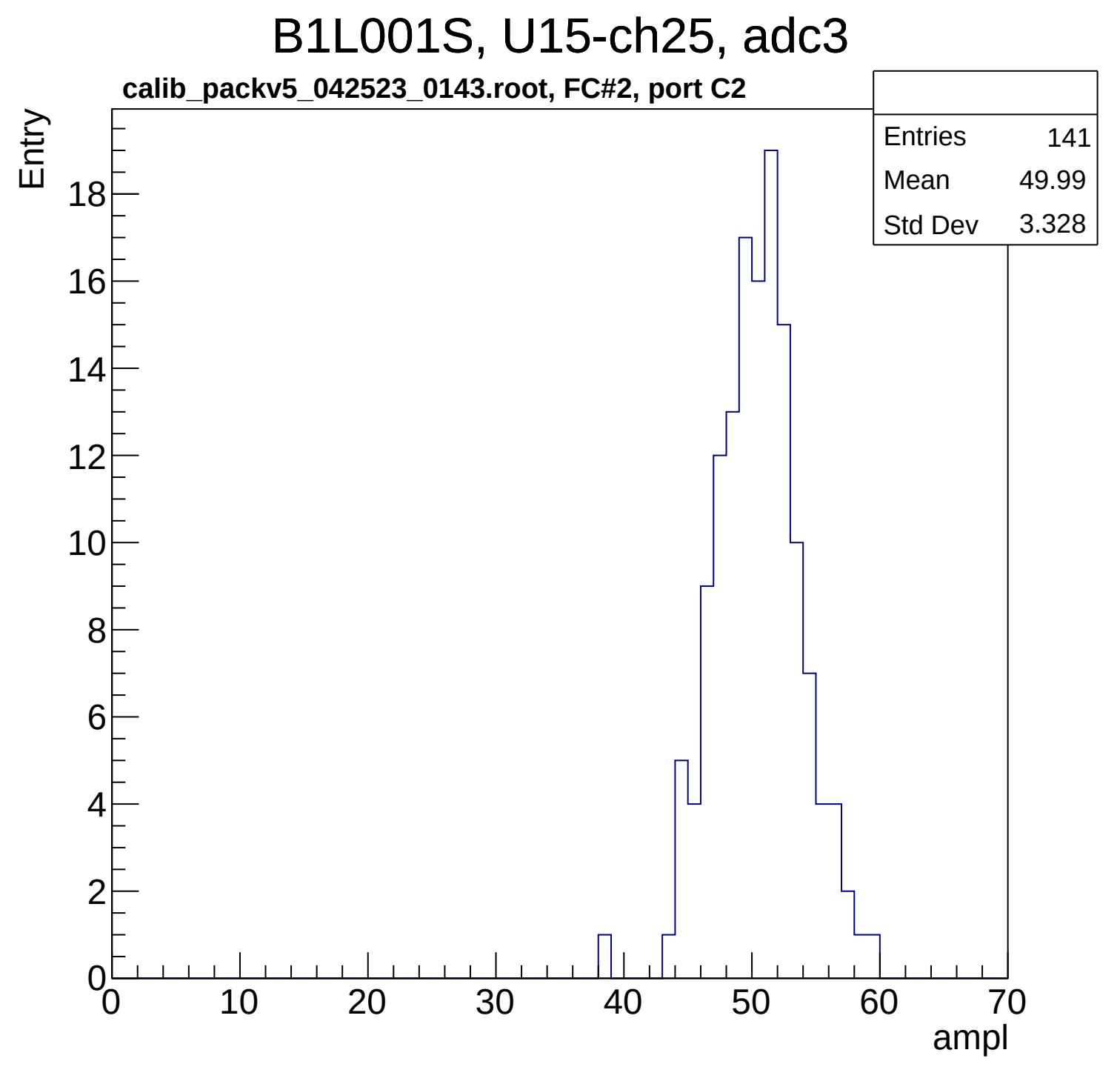
B1L001S, U15-ch25, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 49.99
Std Dev 3.328
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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20
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40
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60
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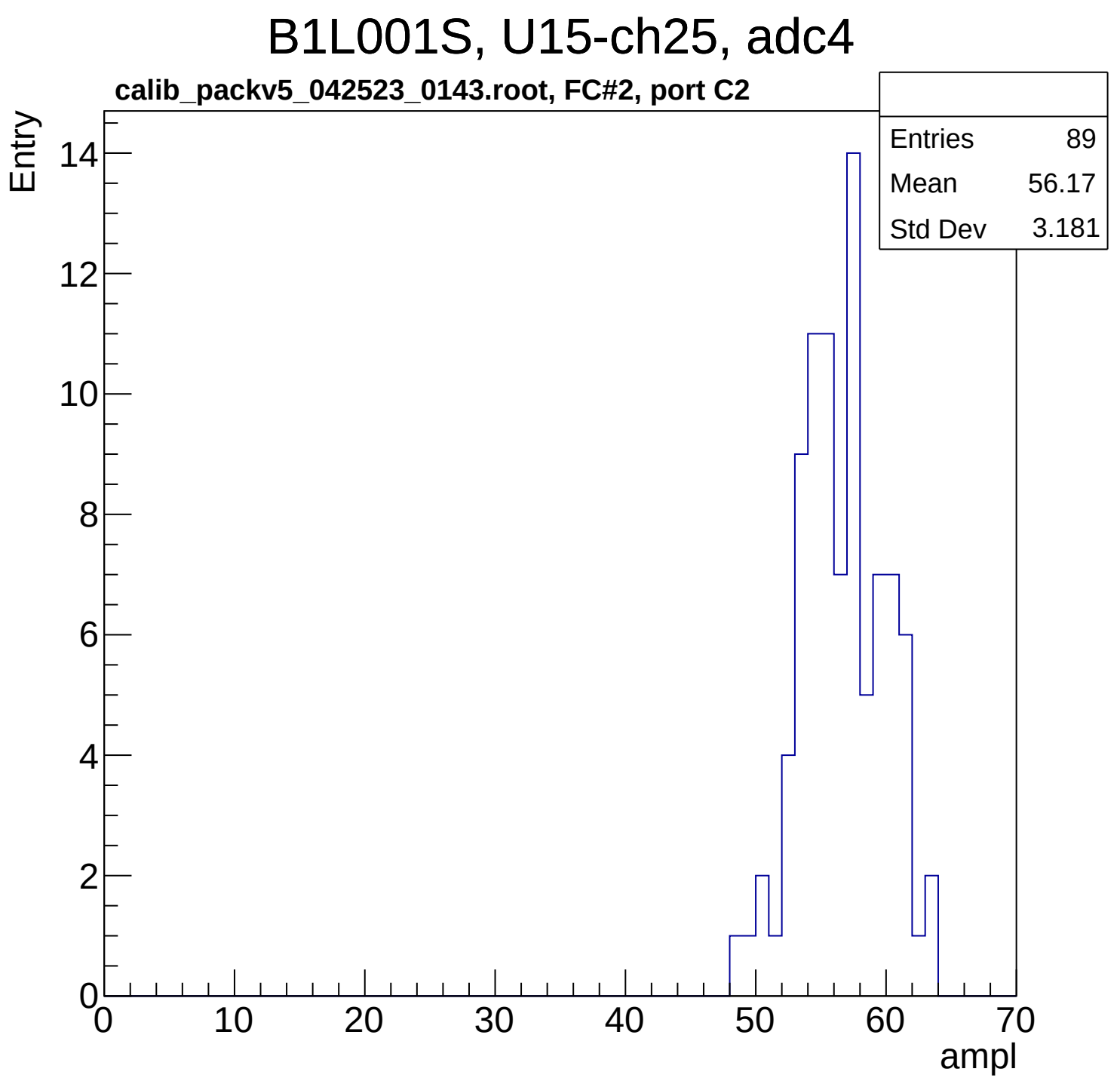
B1L001S, U15-ch25, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 56.17
Std Dev 3.181
Entry
ampl
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4
6
8
10
12
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0
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20
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40
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60
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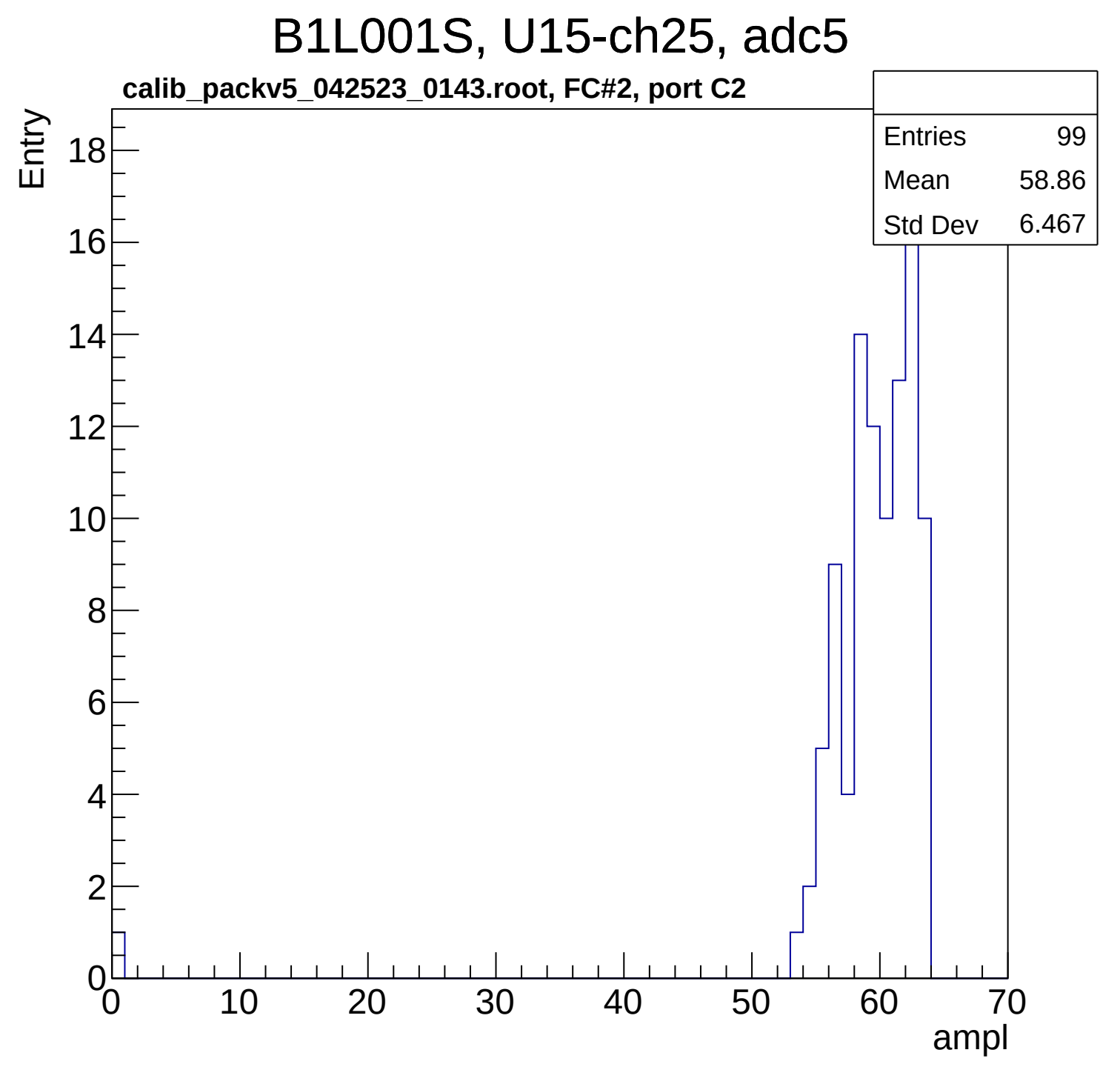
B1L001S, U15-ch25, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 58.86
Std Dev 6.467
Entry
ampl
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6
8
10
12
14
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0
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60
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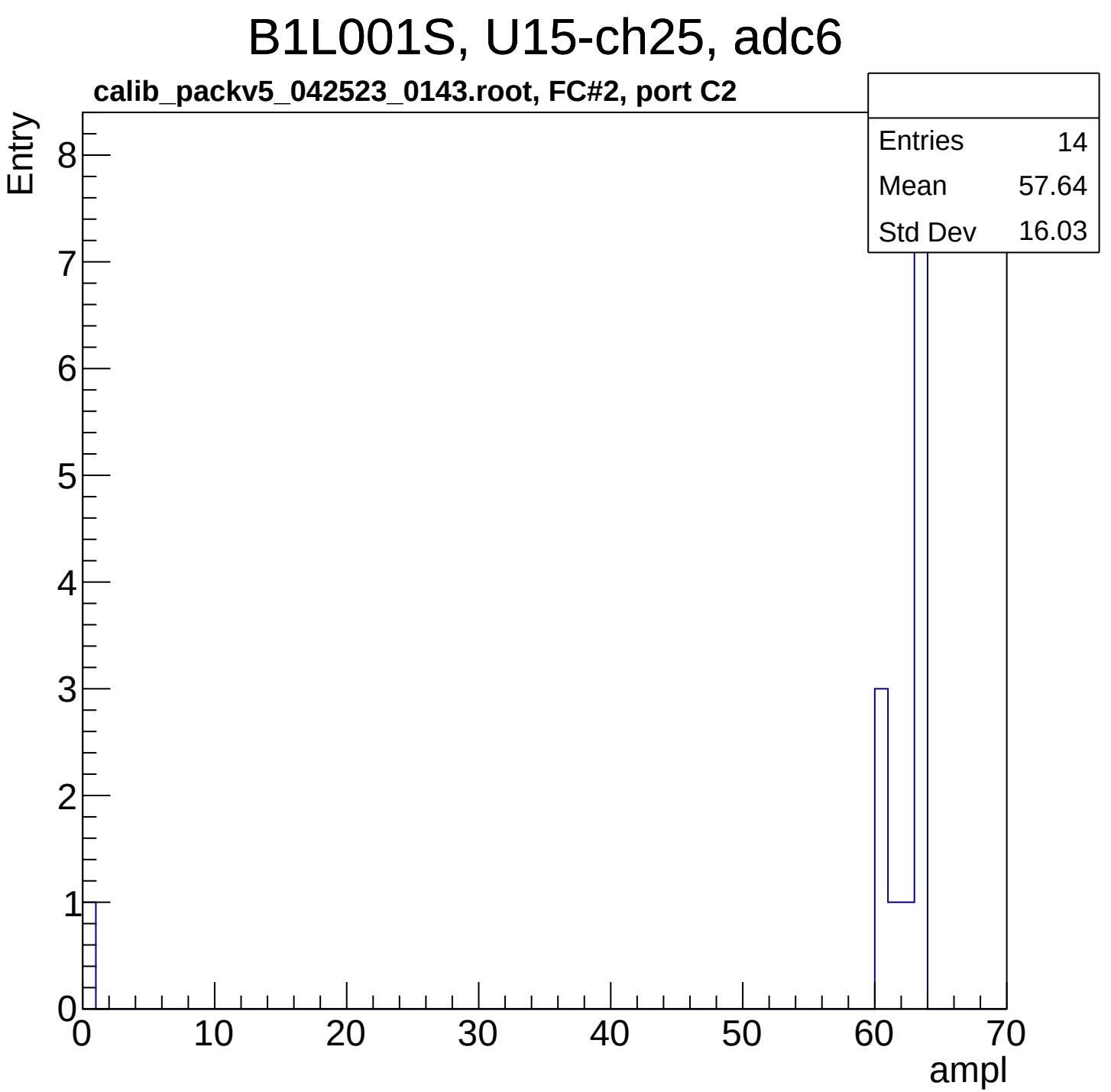
B1L001S, U15-ch25, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 57.64
Std Dev 16.03
Entry
ampl
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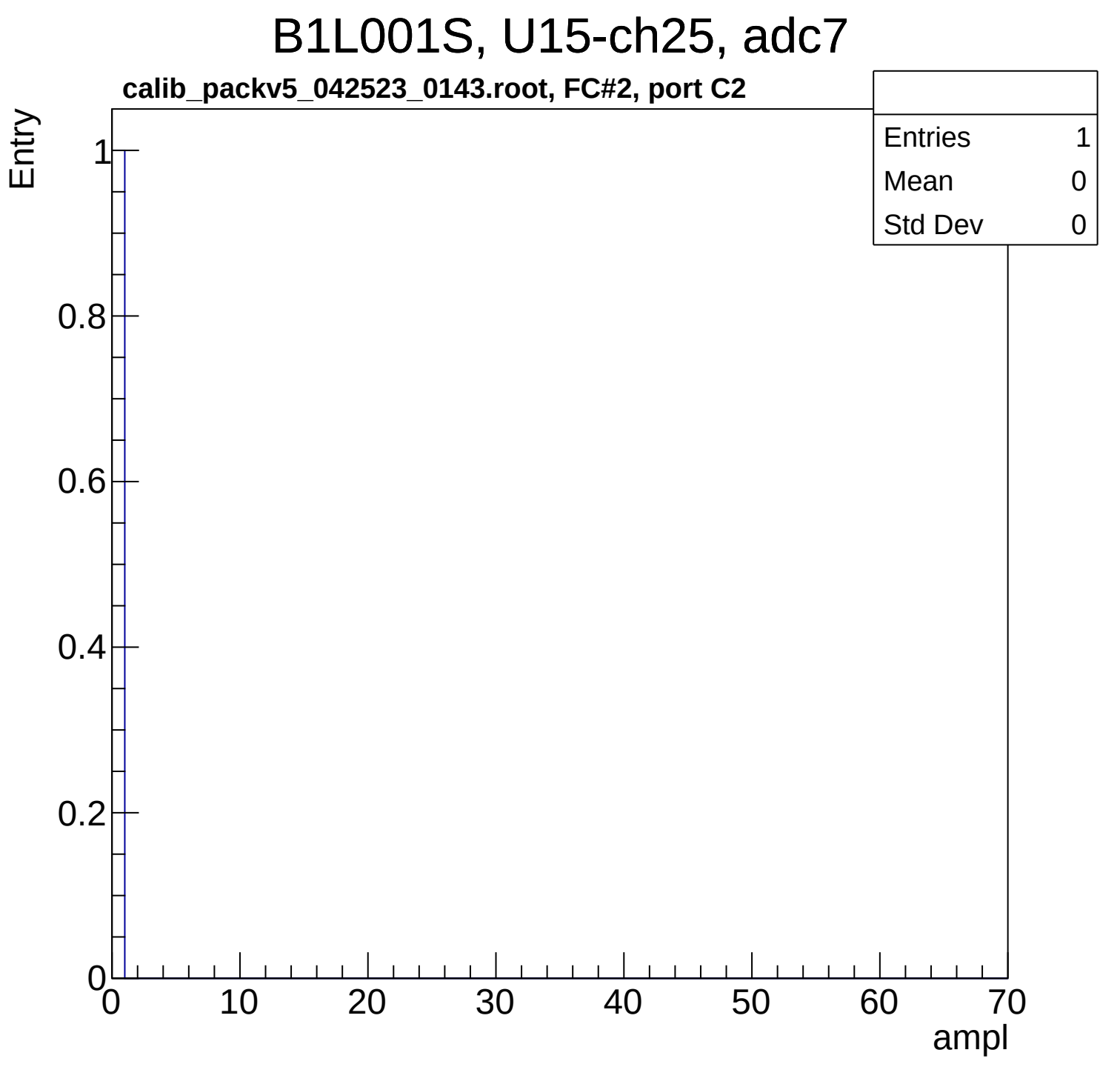
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch25, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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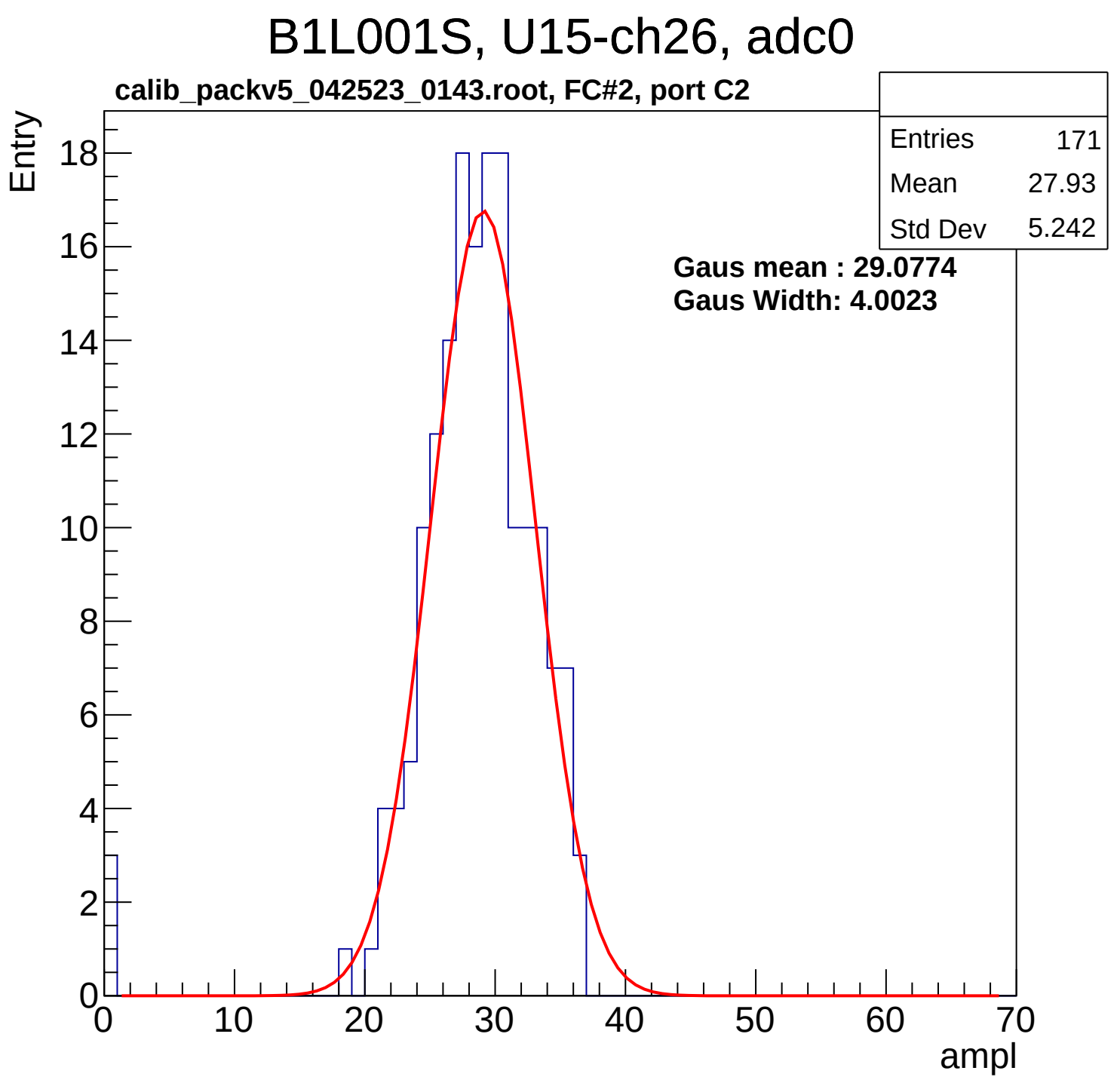
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch26, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 171
Mean 27.93
Std Dev 5.242
Gaus mean : 29.0774
Gaus Width: 4.0023
Entry
ampl
0
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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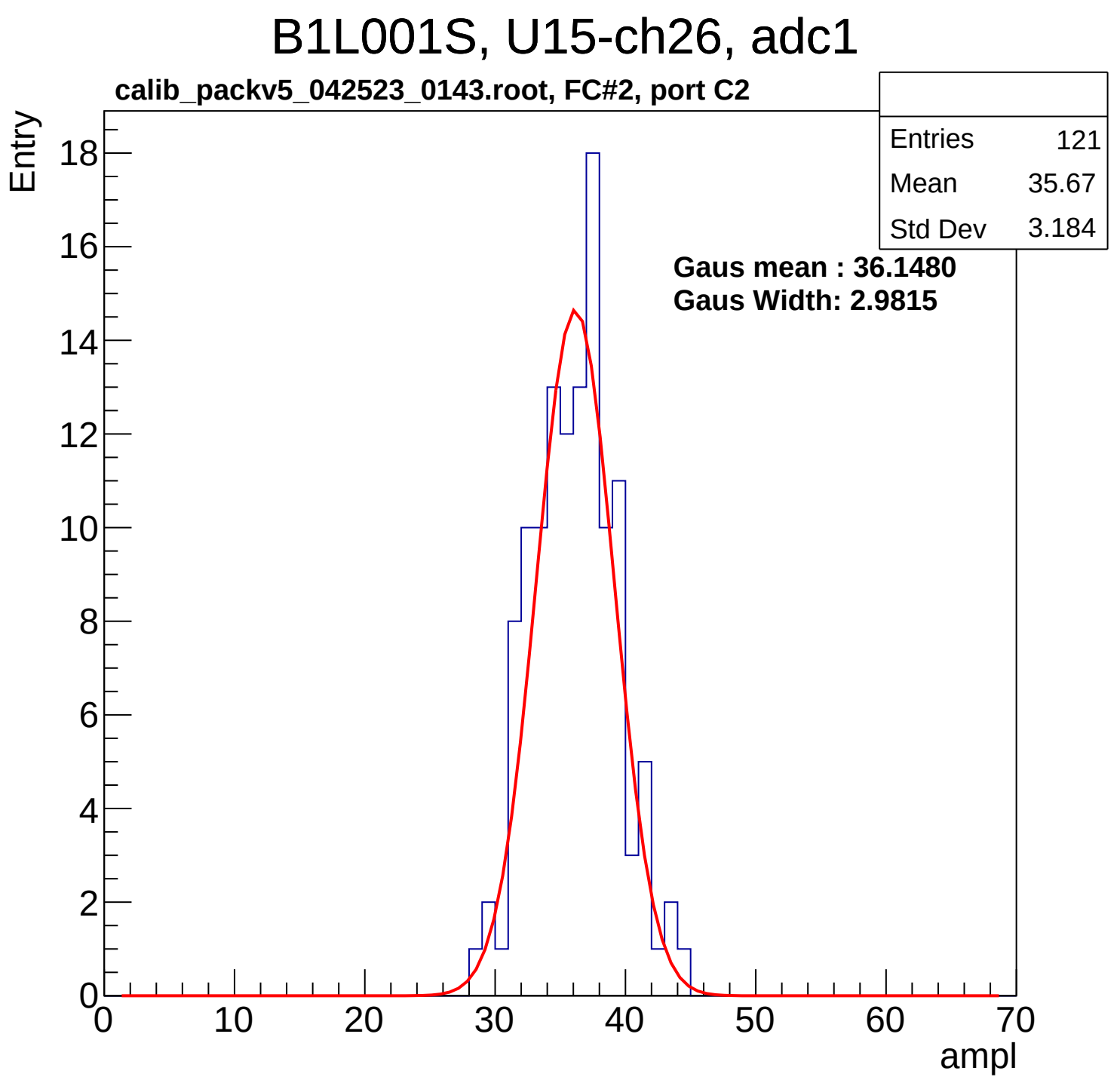
B1L001S, U15-ch26, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 35.67
Std Dev 3.184
Gaus mean : 36.1480
Gaus Width: 2.9815
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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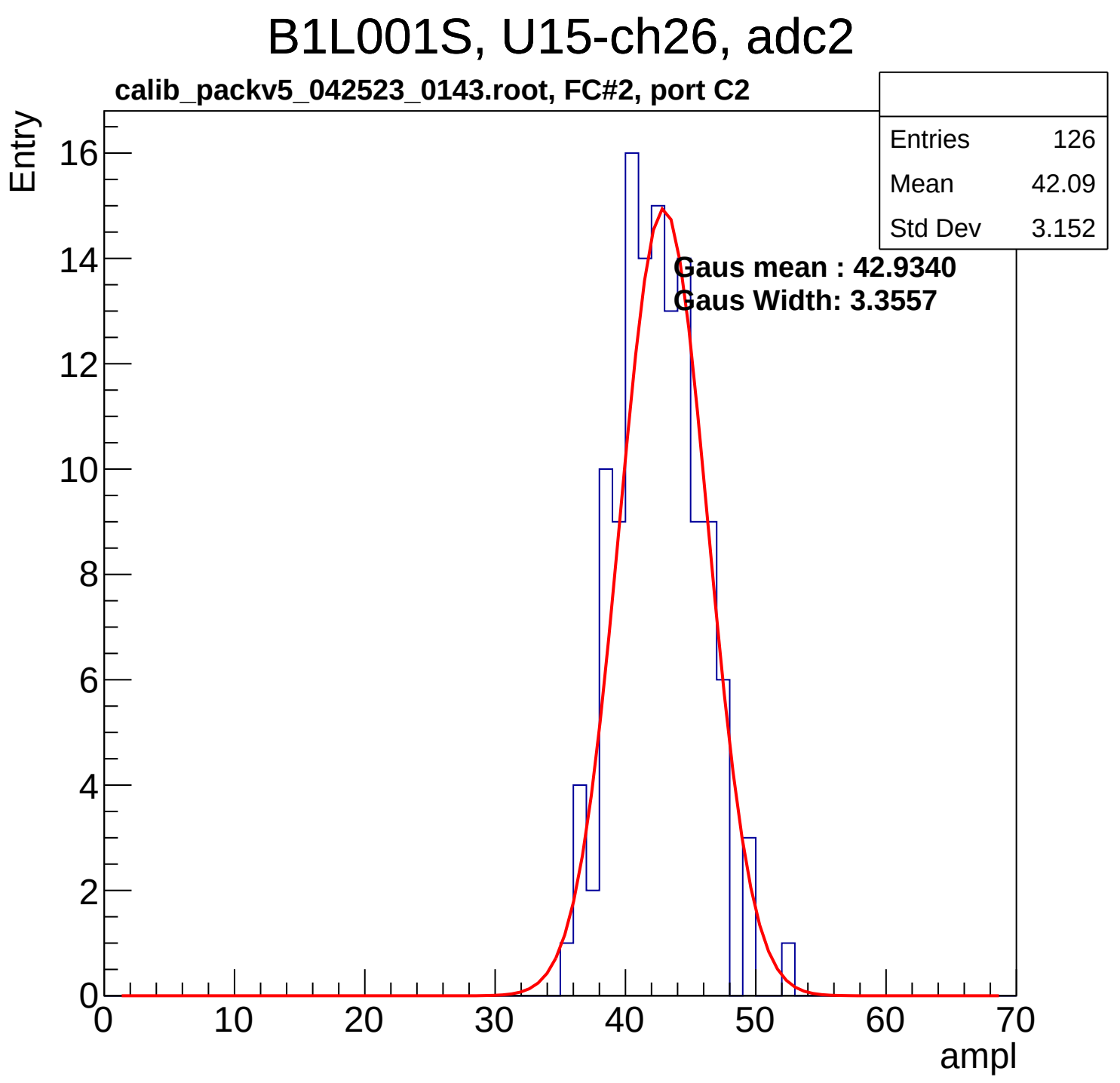
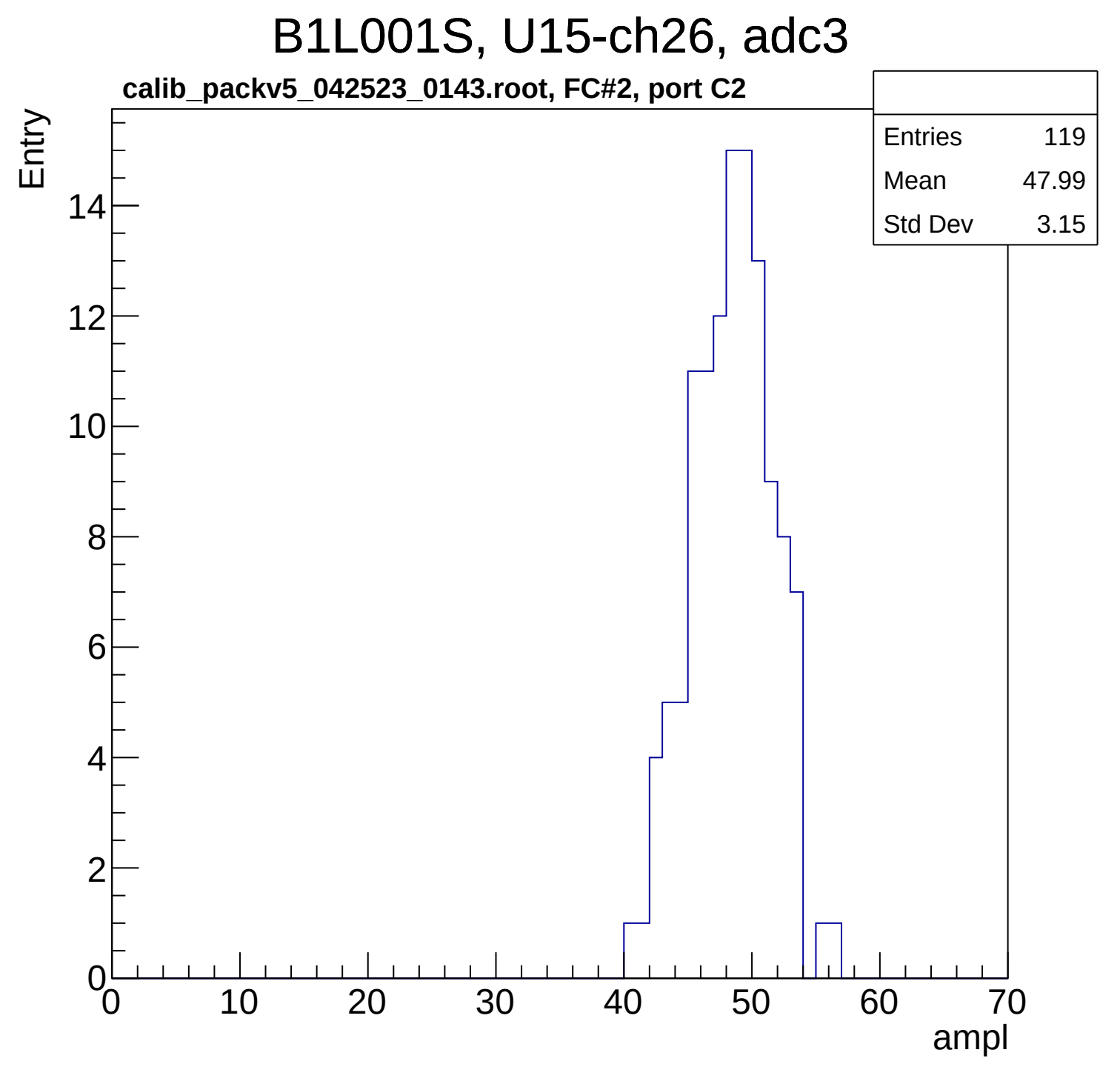

B1L001S, U15-ch26, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 42.09
Std Dev 3.152
Gaus mean : 42.9340
Gaus Width: 3.3557
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch26, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 47.99
Std Dev 3.15
Entry
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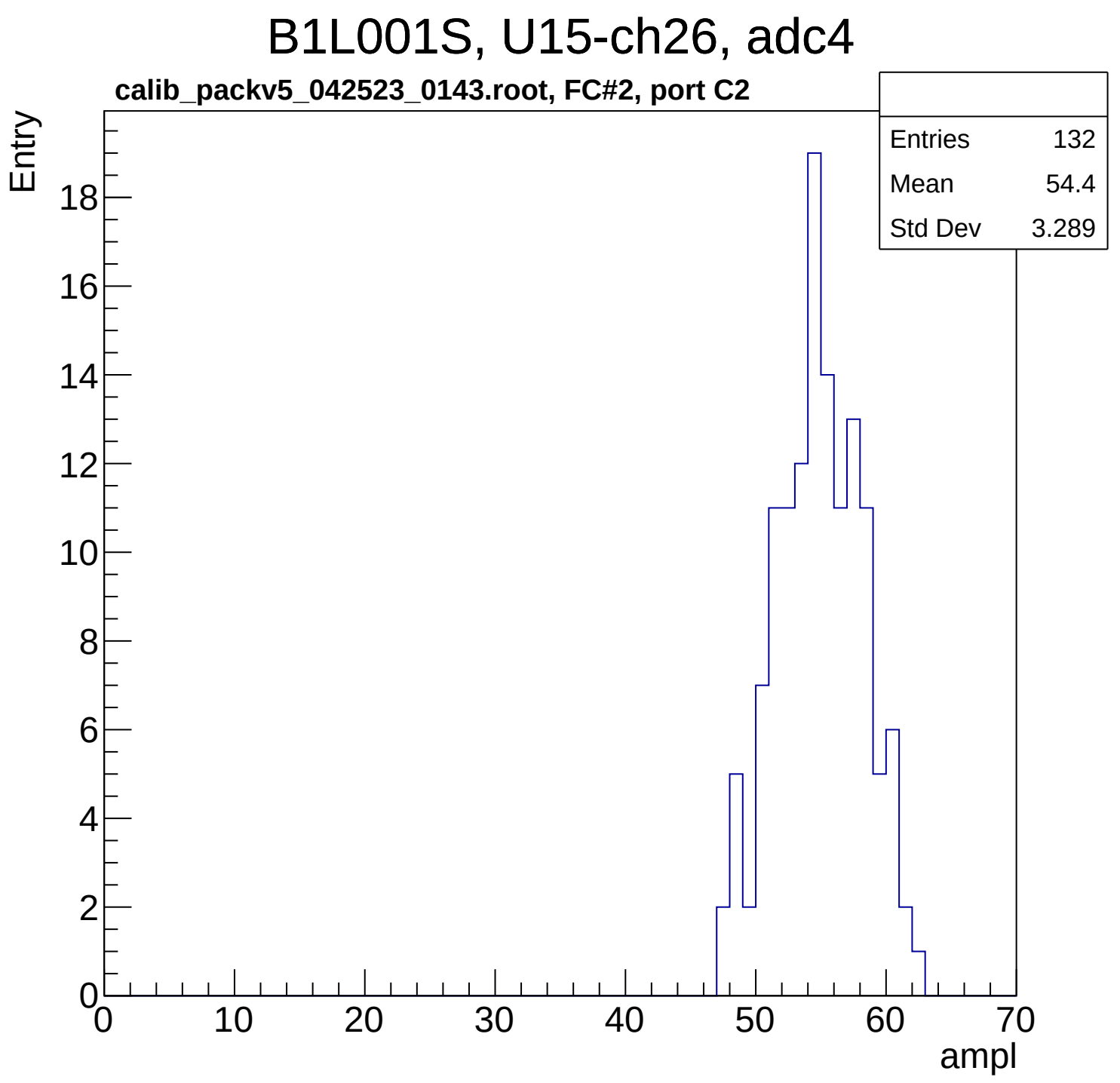
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch26, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 54.4
Std Dev 3.289
Entry
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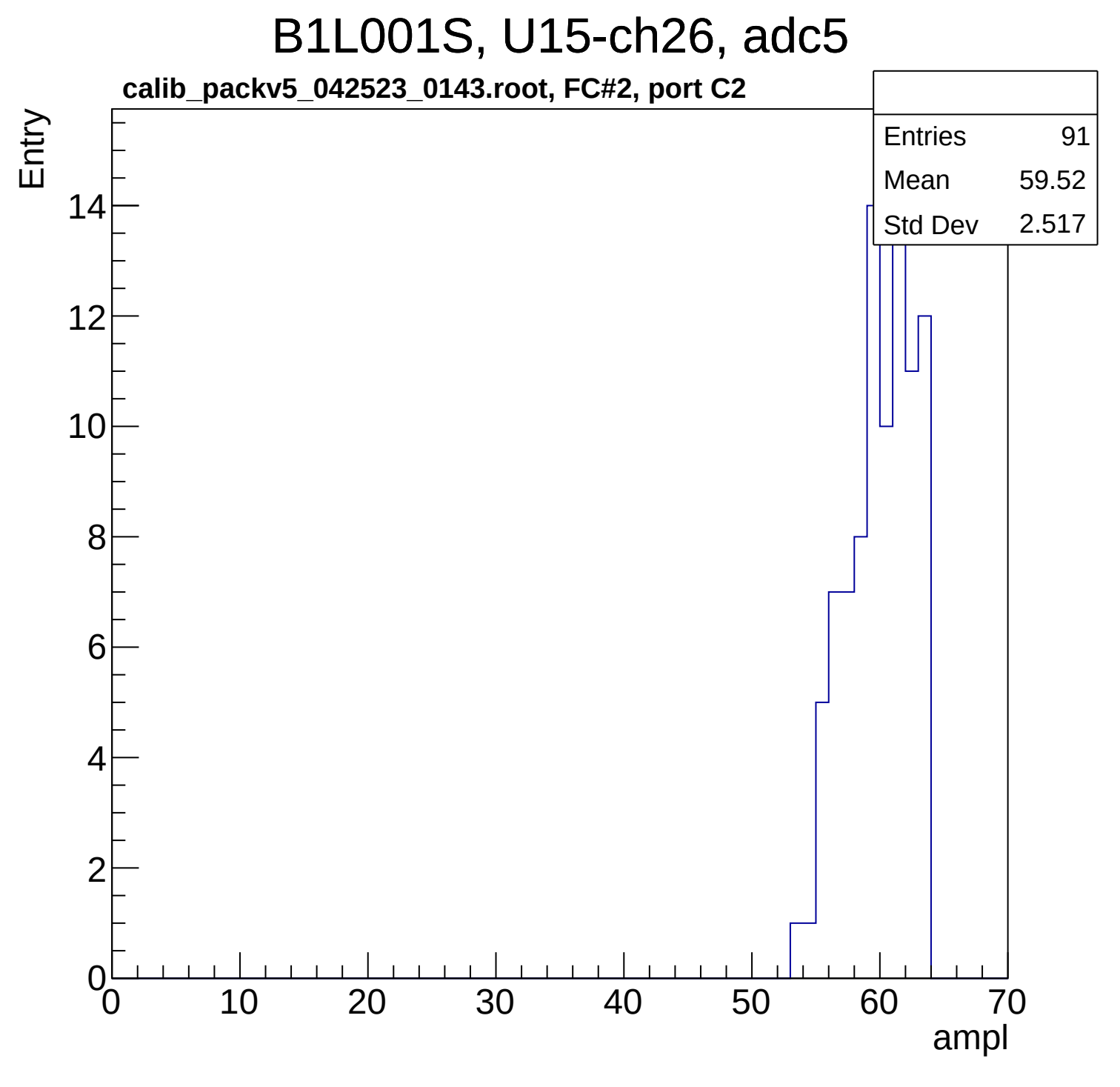
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch26, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.52
Std Dev 2.517
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
30
40
50
60
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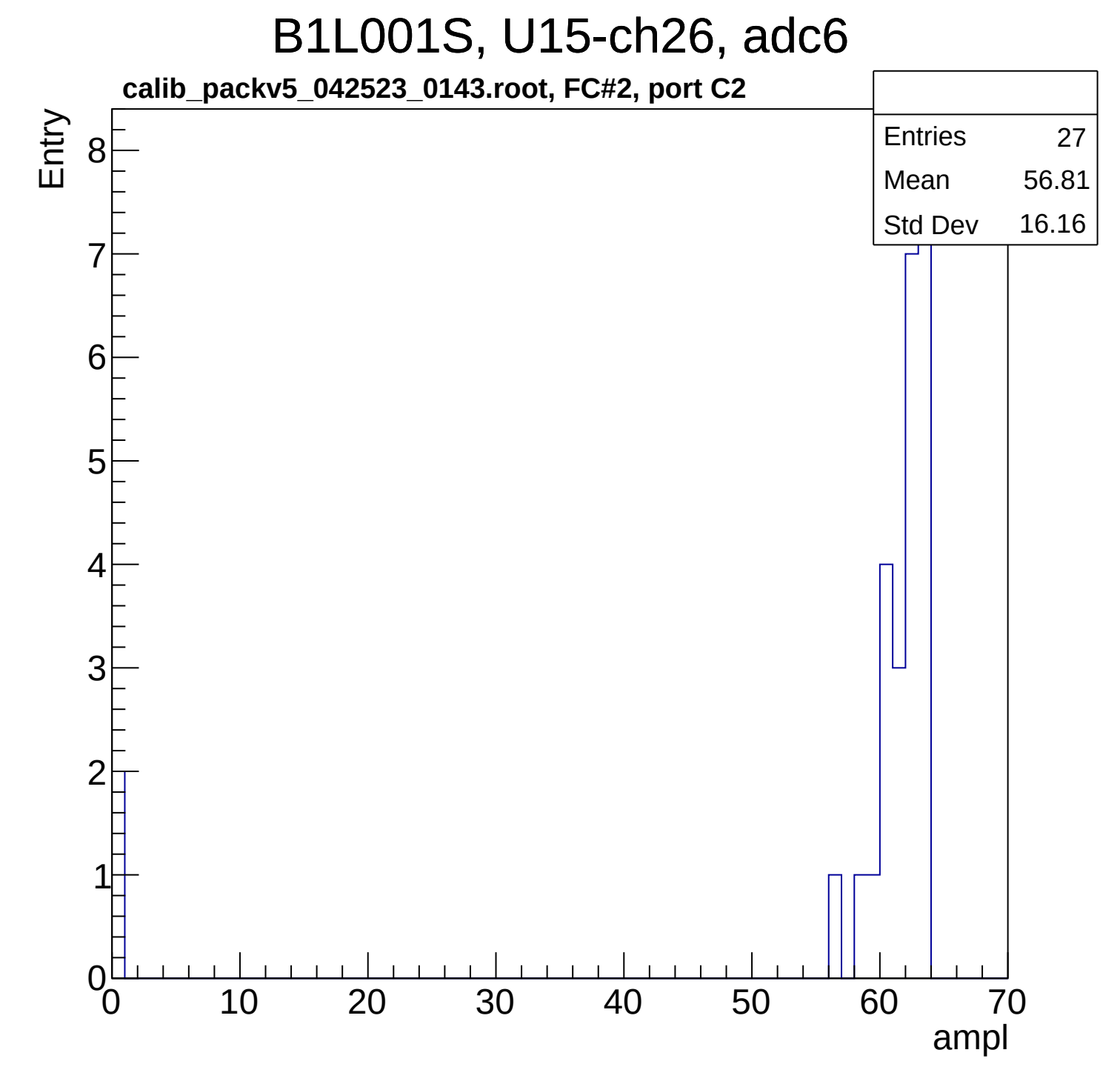
B1L001S, U15-ch26, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 27
Mean 56.81
Std Dev 16.16
Entry
ampl
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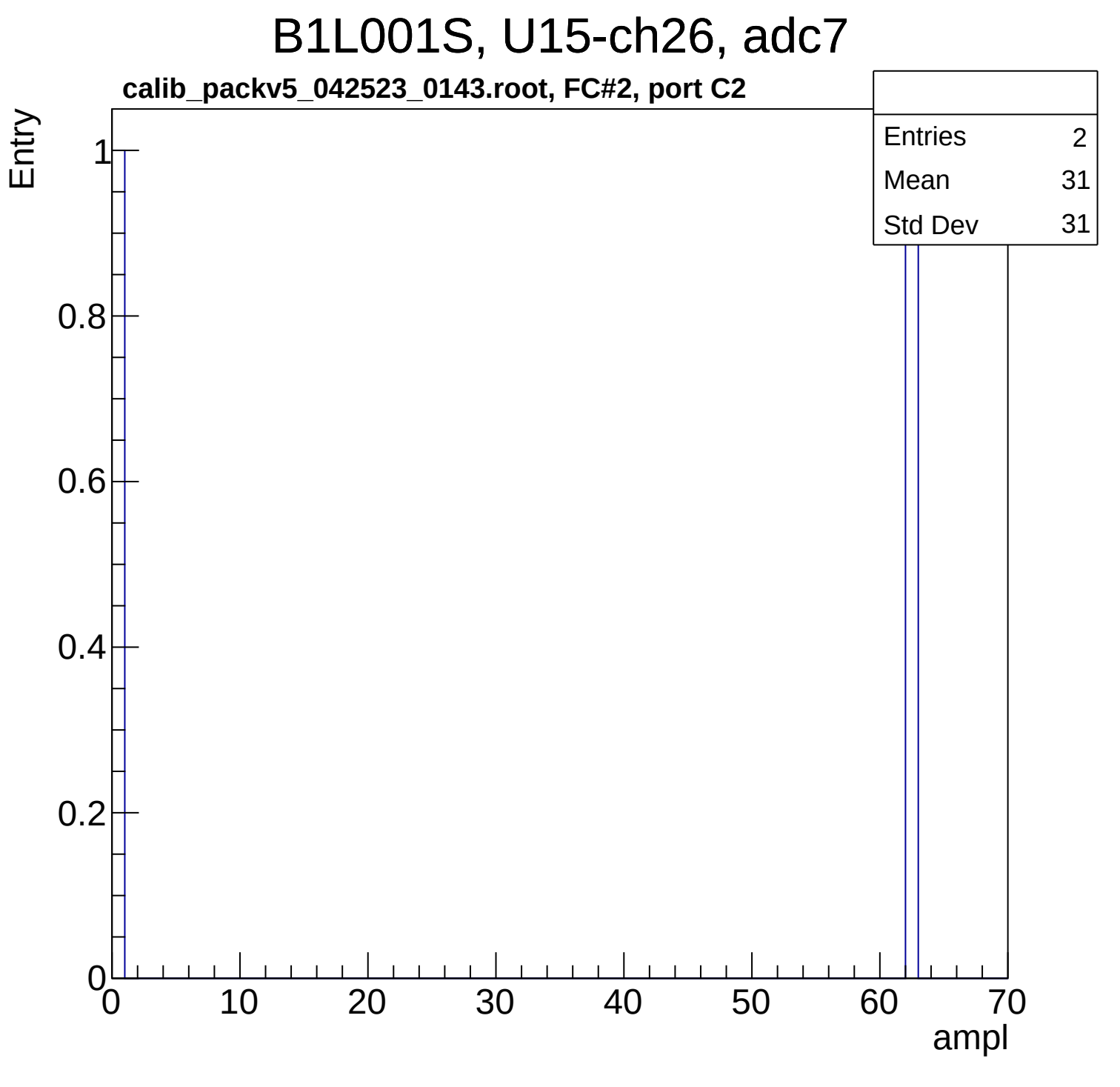
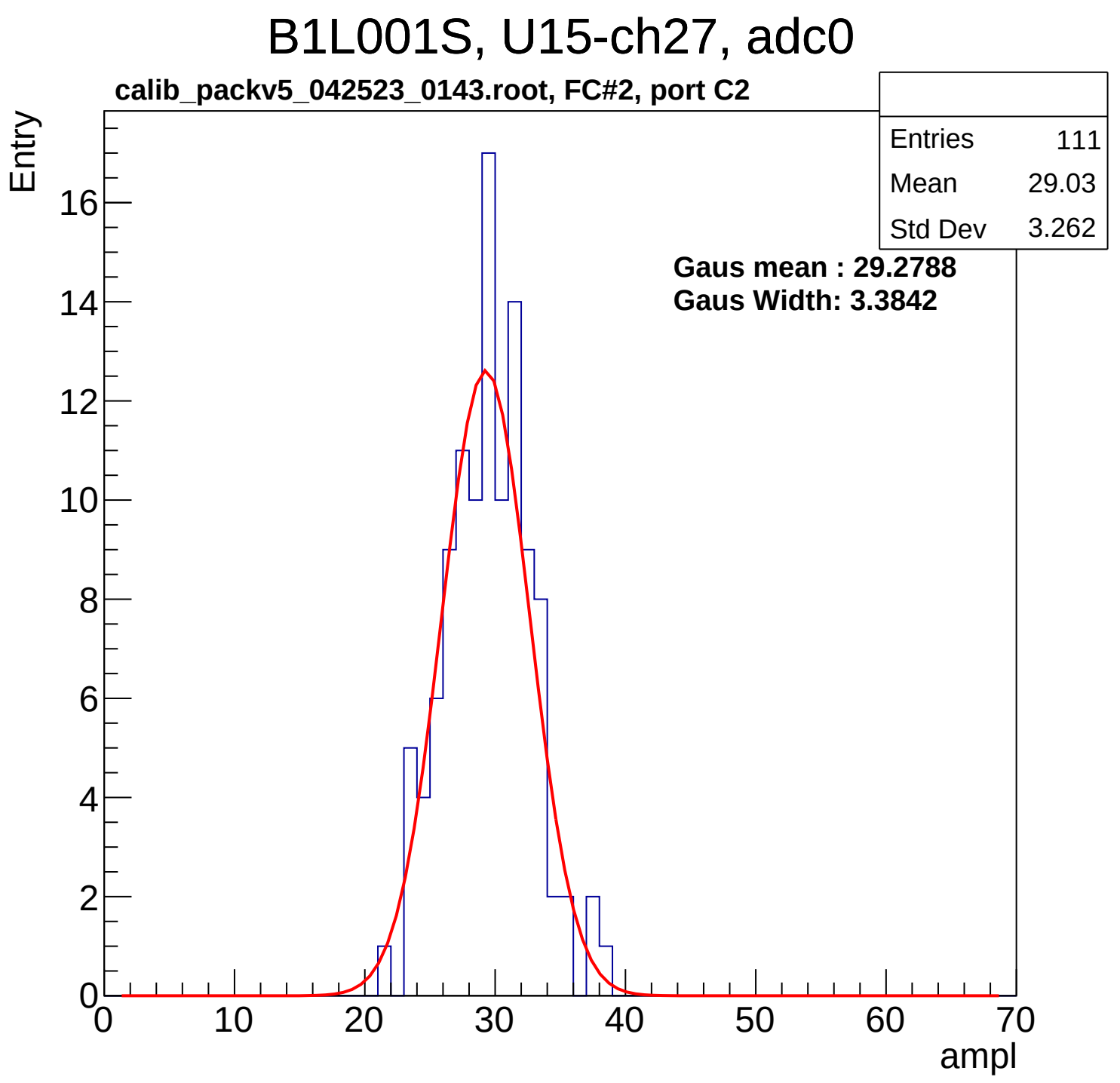
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch26, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch27, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 29.03
Std Dev 3.262
Gaus mean : 29.2788
Gaus Width: 3.3842
Entry
ampl
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2
4
6
8
10
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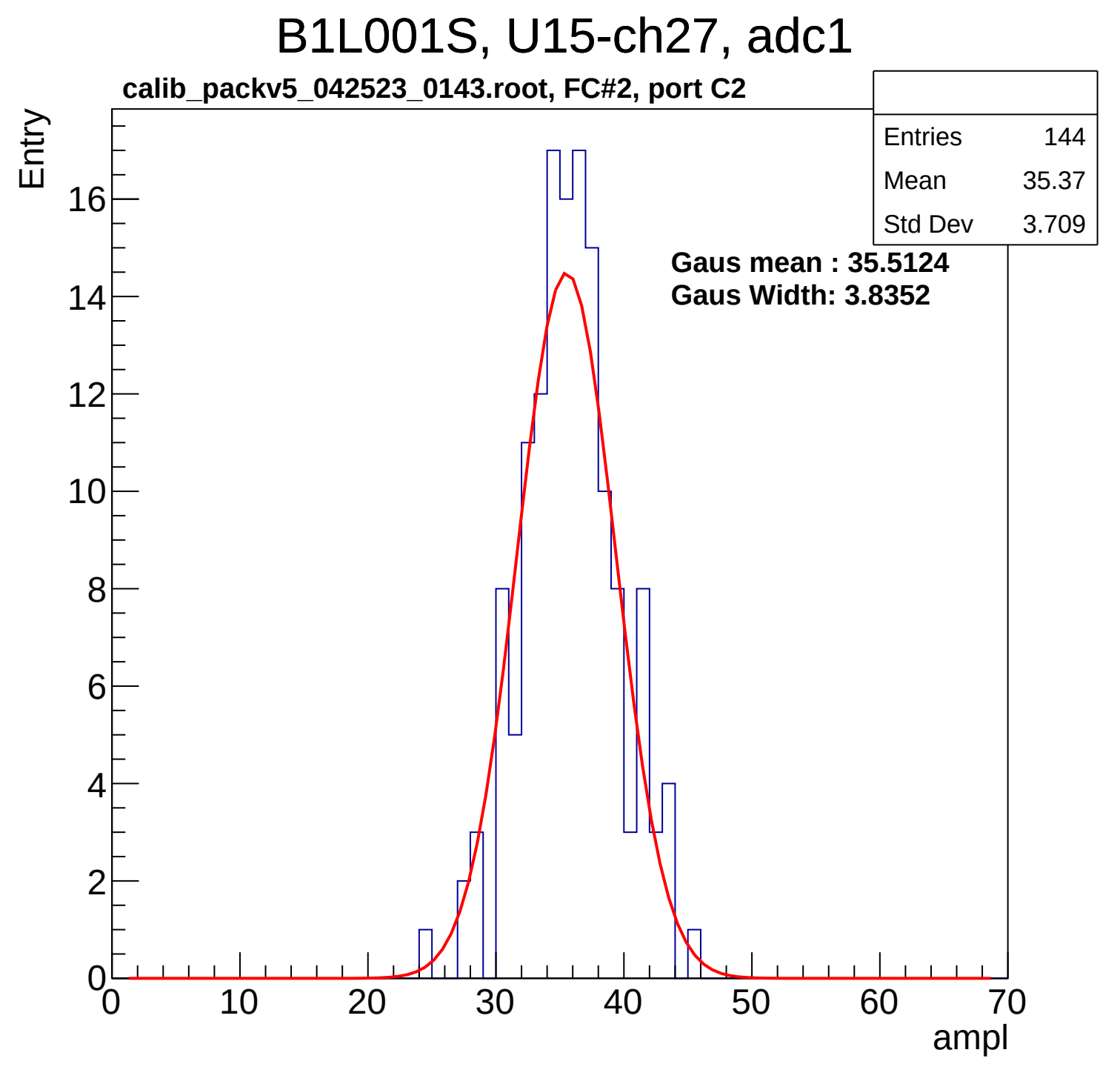
B1L001S, U15-ch27, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 35.37
Std Dev 3.709
Gaus mean : 35.5124
Gaus Width: 3.8352
Entry
ampl
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4
6
8
10
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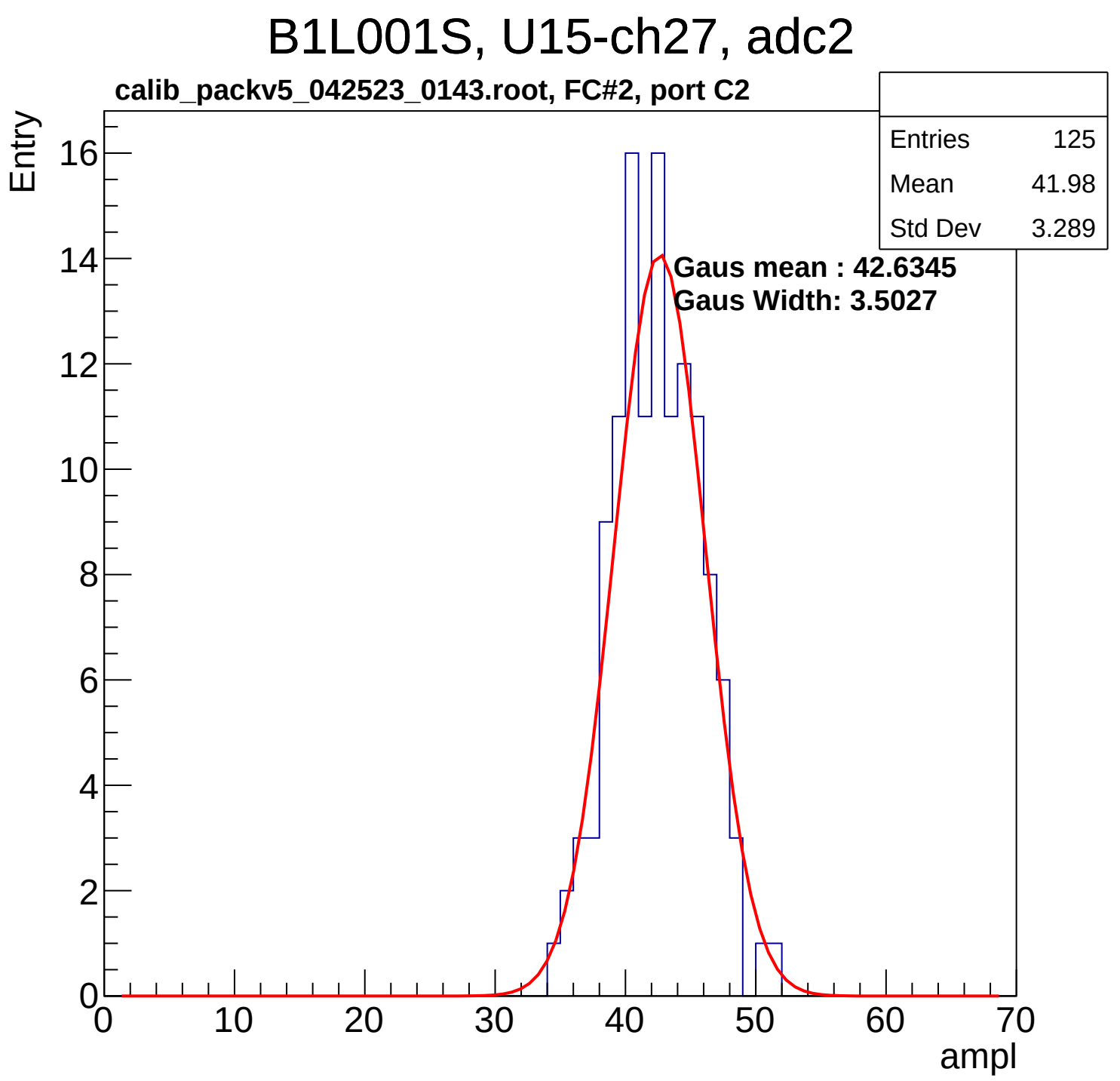
B1L001S, U15-ch27, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 41.98
Std Dev 3.289
Gaus mean : 42.6345
Gaus Width: 3.5027
Entry
ampl
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2
4
6
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10
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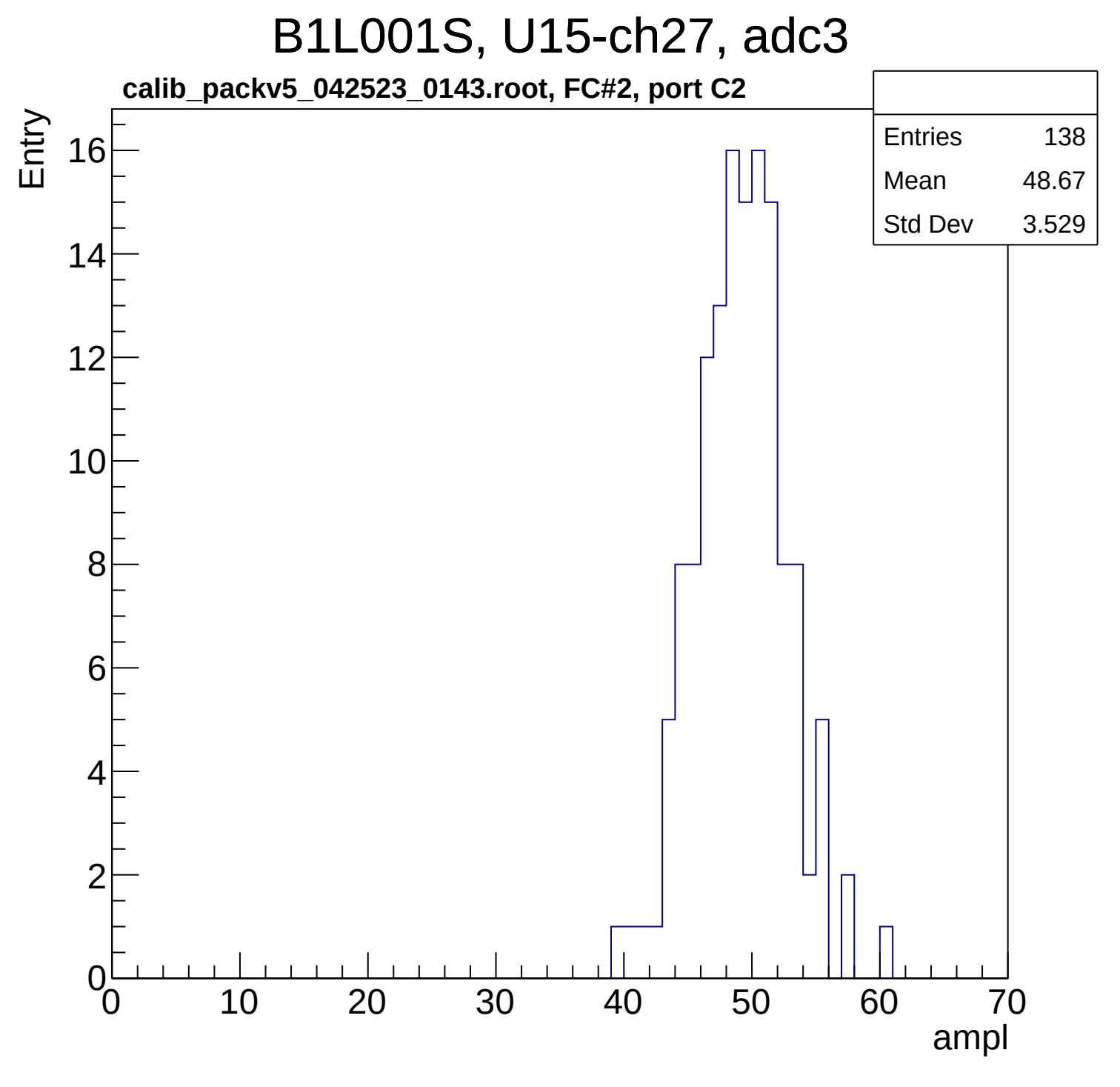
B1L001S, U15-ch27, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 48.67
Std Dev 3.529
Entry
ampl
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4
6
8
10
12
14
16
0
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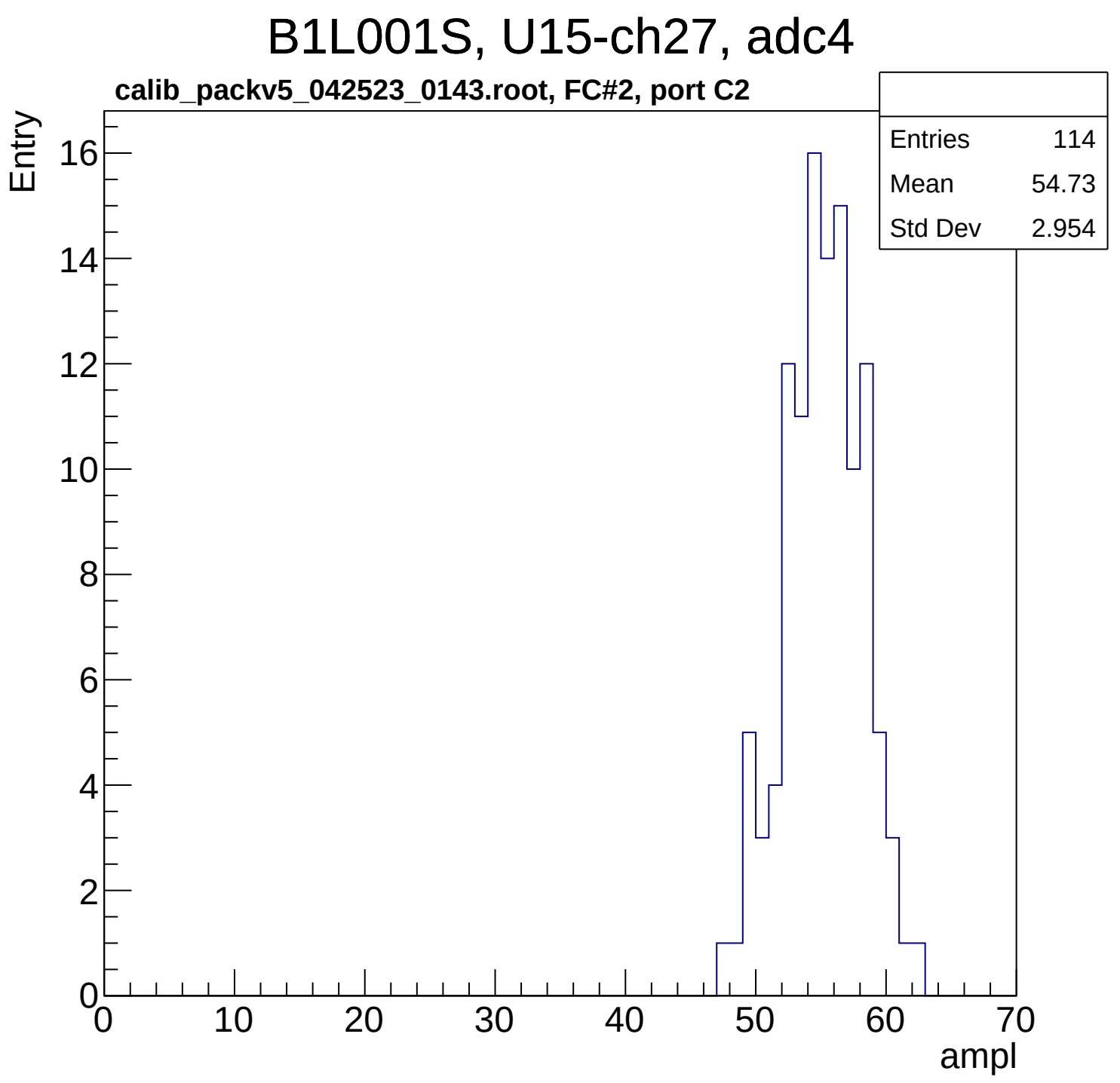
B1L001S, U15-ch27, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 54.73
Std Dev 2.954
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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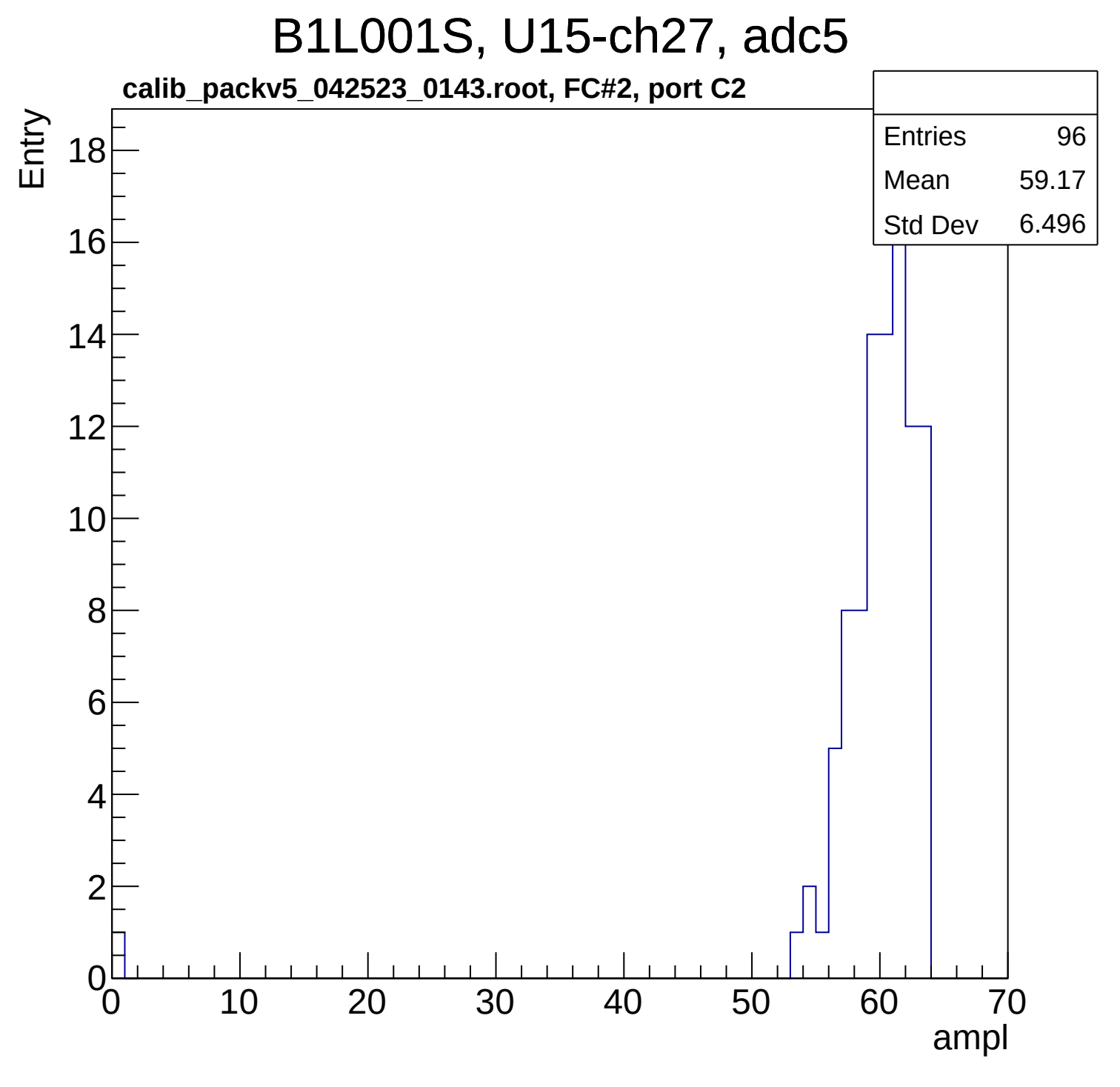
B1L001S, U15-ch27, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 59.17
Std Dev 6.496
Entry
ampl
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4
6
8
10
12
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16
18
0
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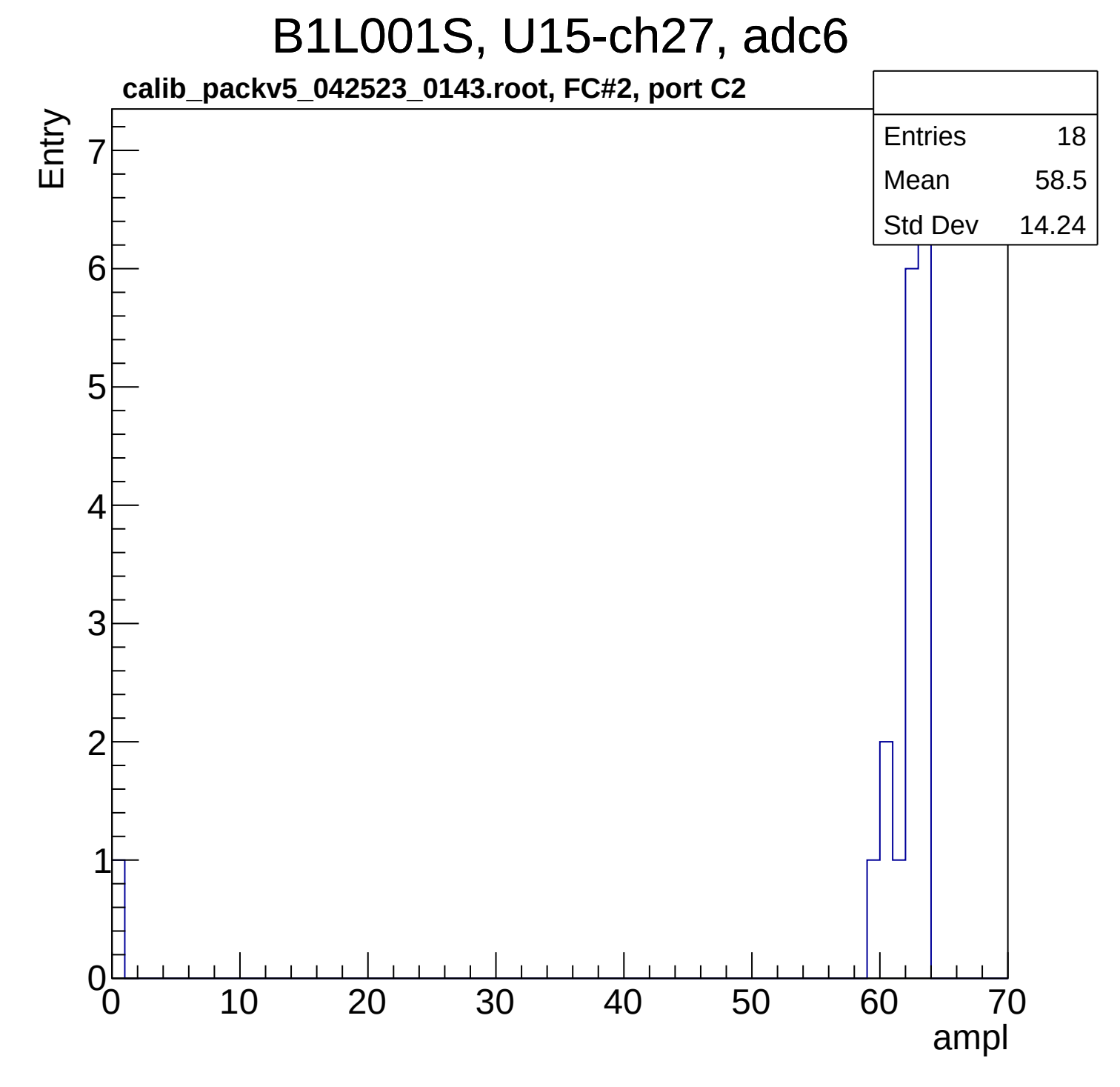
B1L001S, U15-ch27, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 58.5
Std Dev 14.24
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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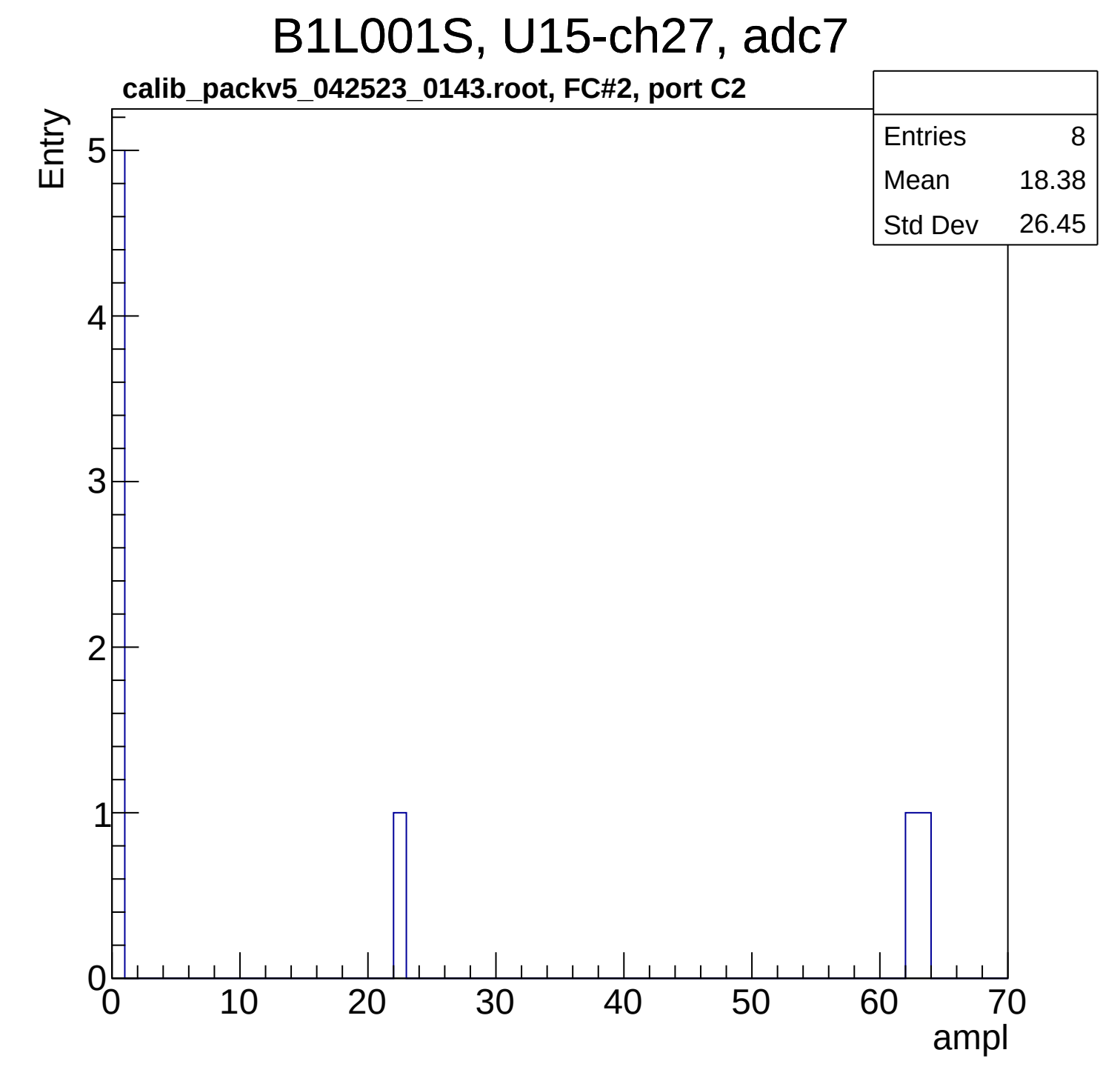
B1L001S, U15-ch27, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 18.38
Std Dev 26.45
Entry
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1
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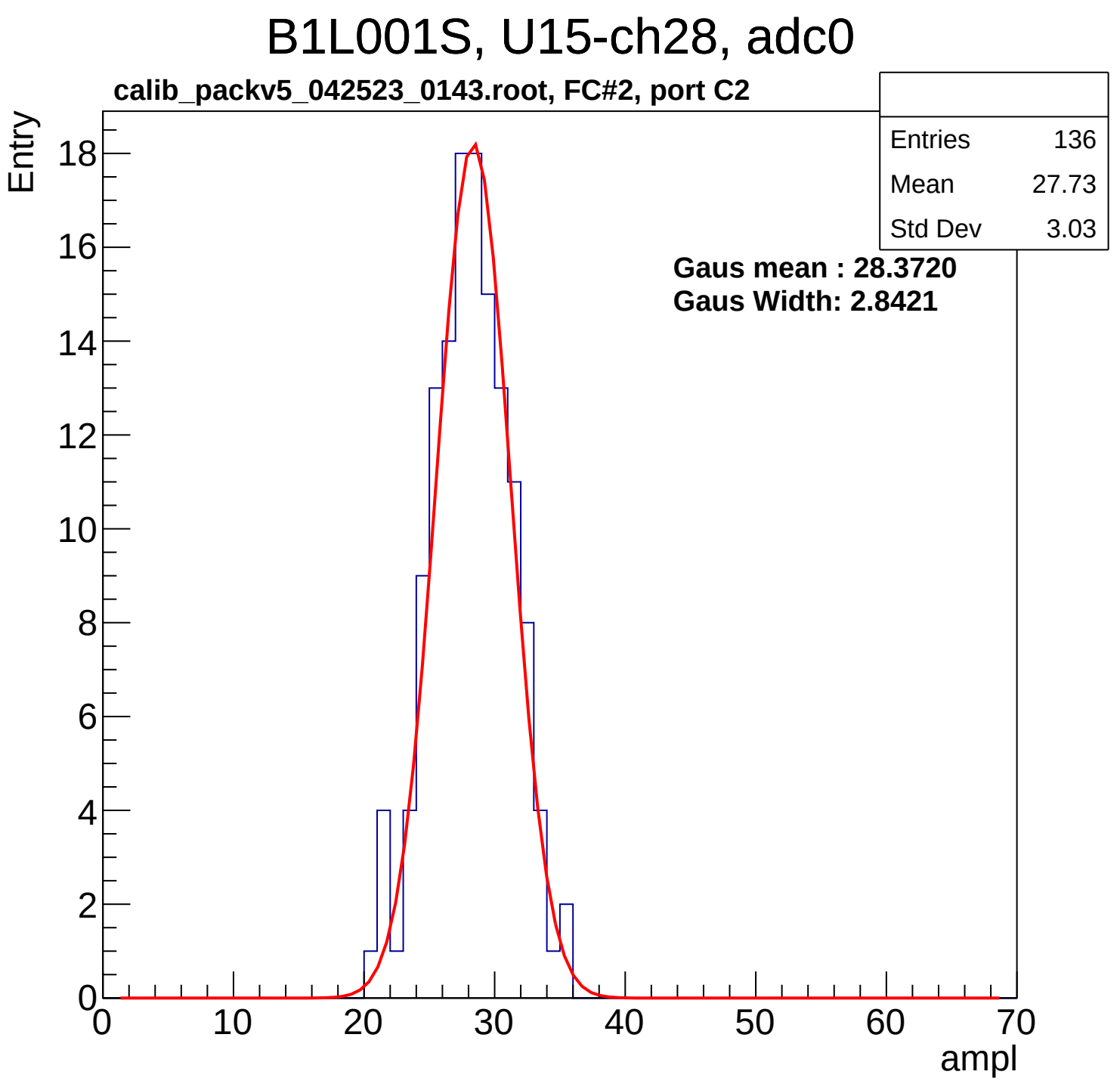
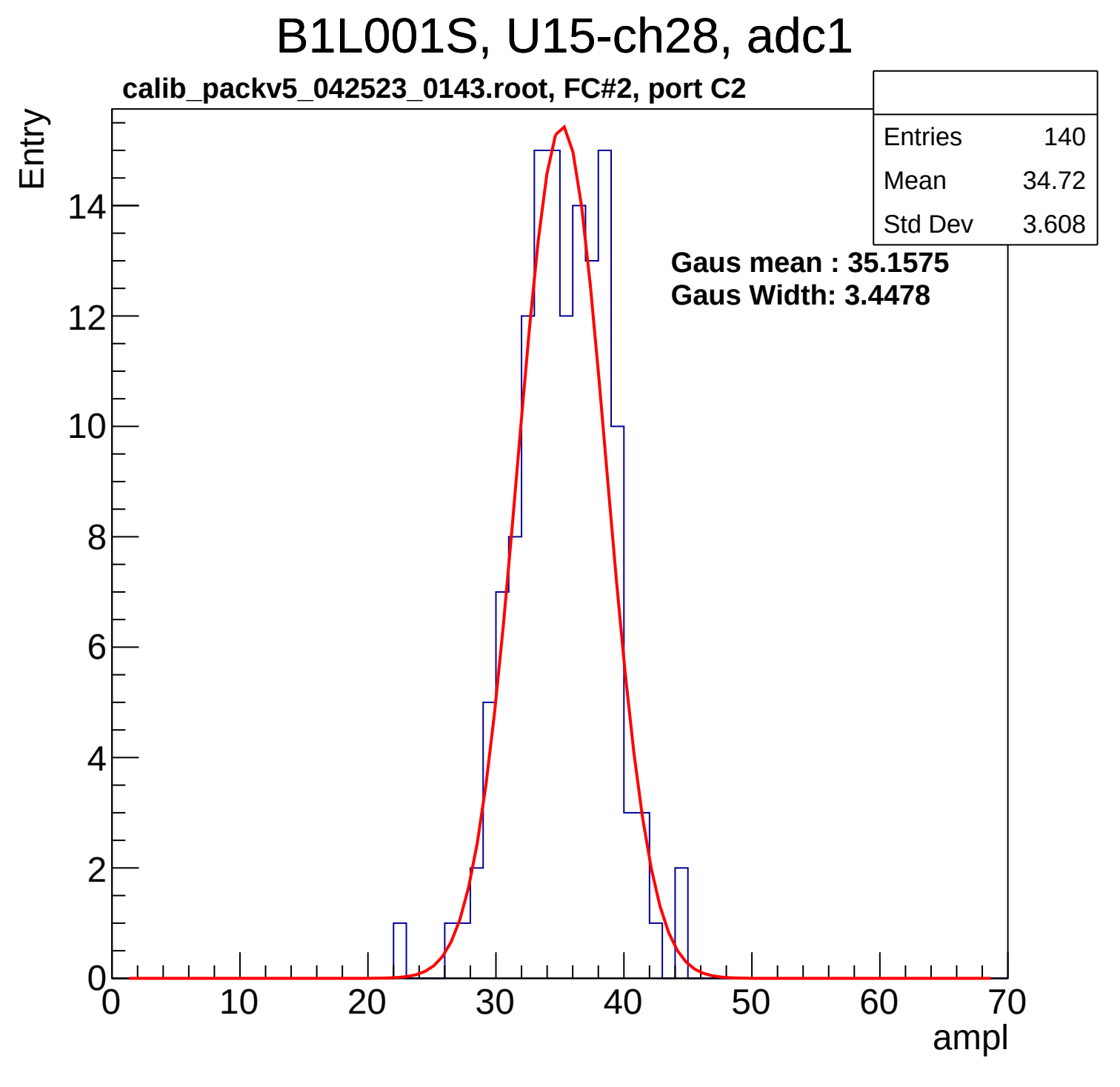
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch28, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 27.73
Std Dev 3.03
Gaus mean : 28.3720
Gaus Width: 2.8421
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch28, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 34.72
Std Dev 3.608
Gaus mean : 35.1575
Gaus Width: 3.4478
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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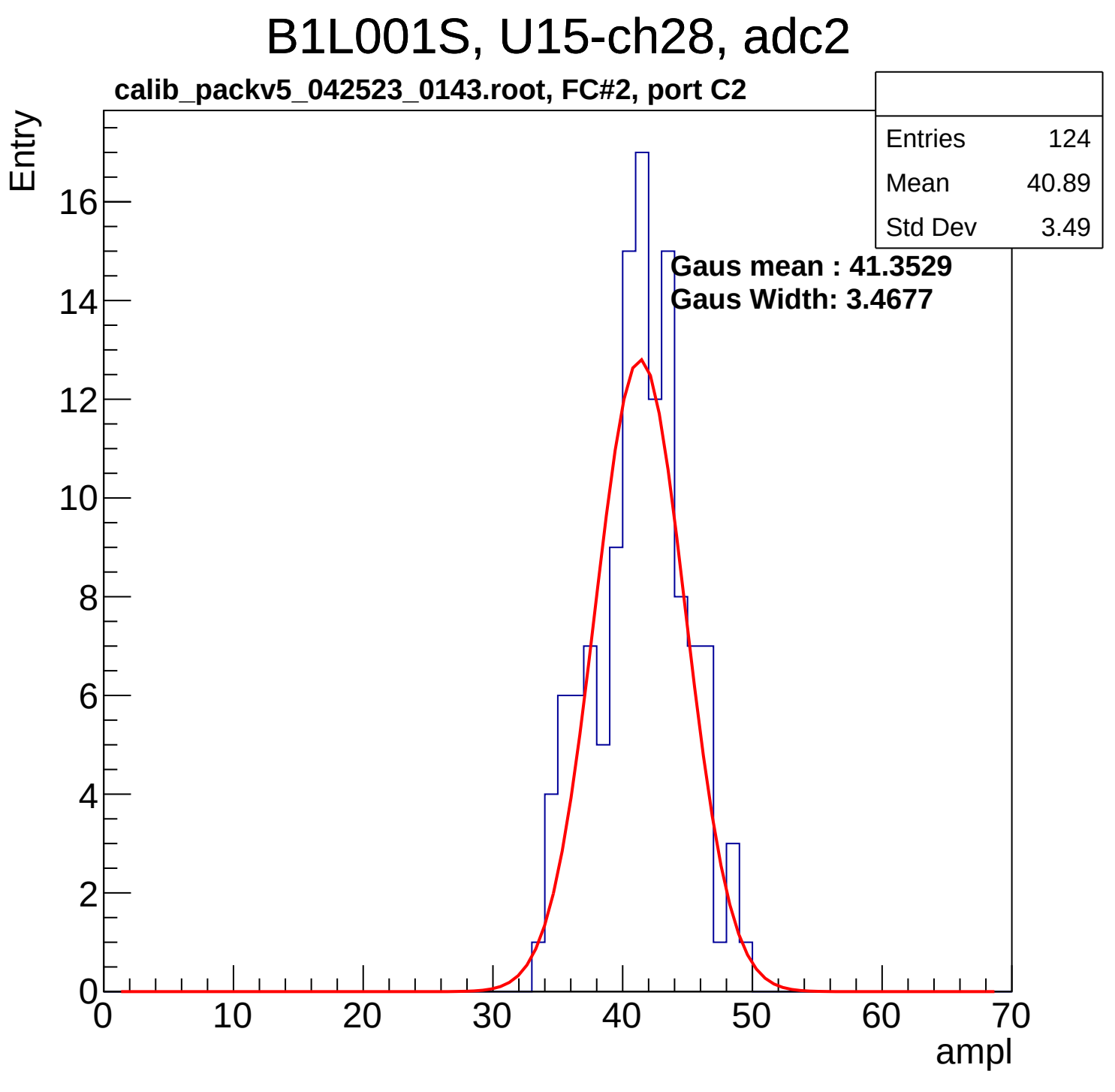
B1L001S, U15-ch28, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 40.89
Std Dev 3.49
Gaus mean : 41.3529
Gaus Width: 3.4677
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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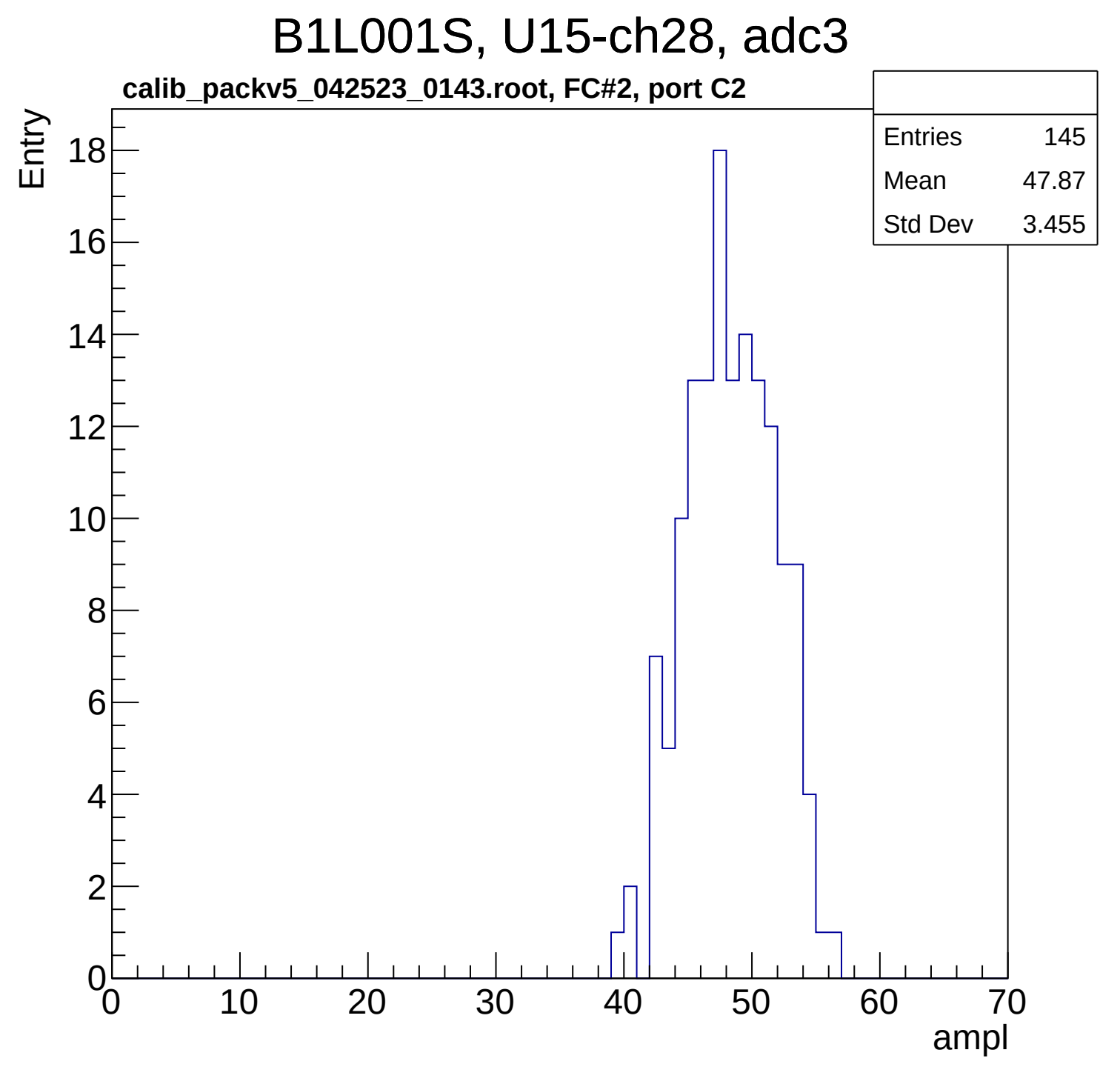
B1L001S, U15-ch28, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 47.87
Std Dev 3.455
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
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40
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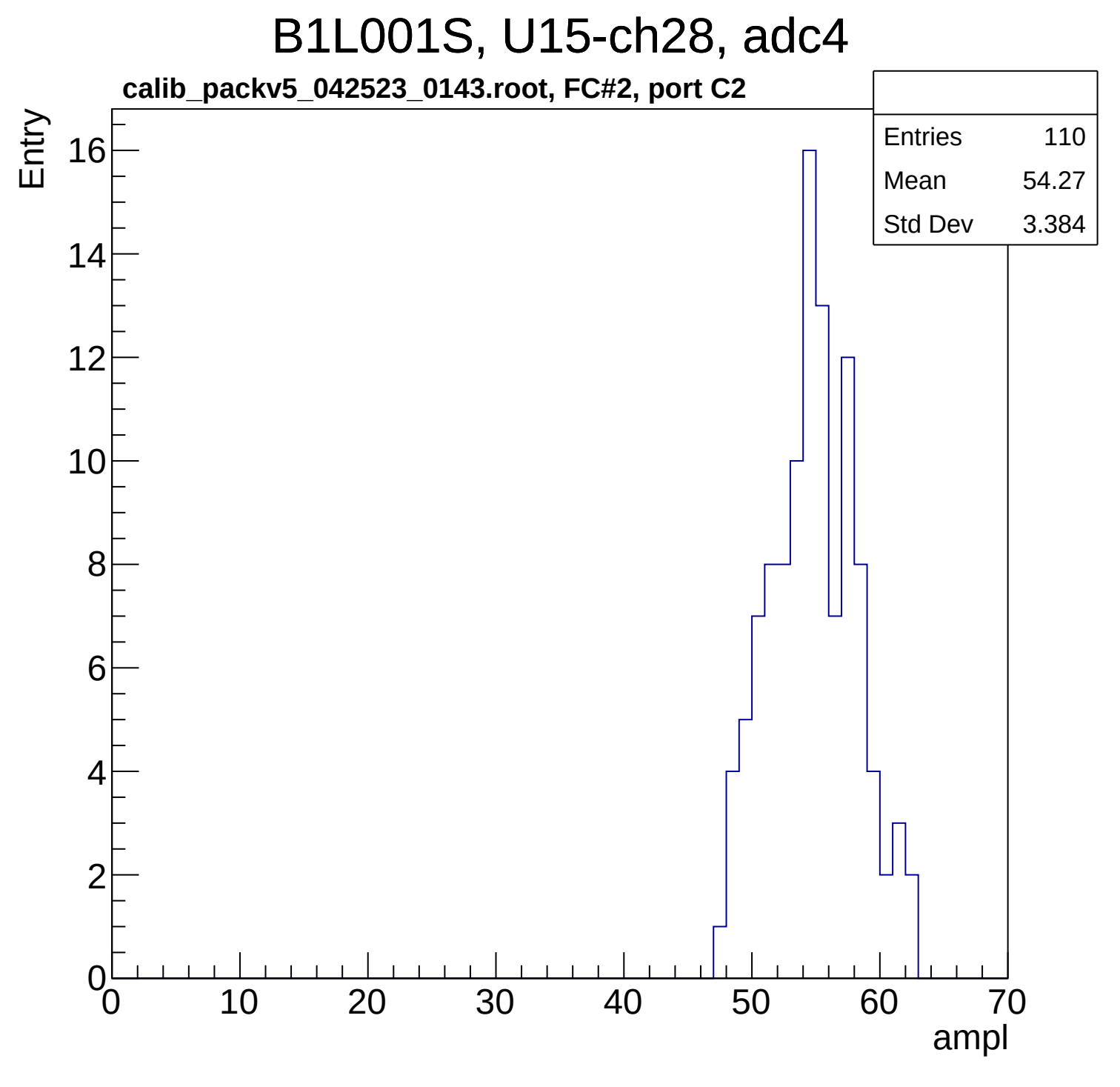
B1L001S, U15-ch28, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 54.27
Std Dev 3.384
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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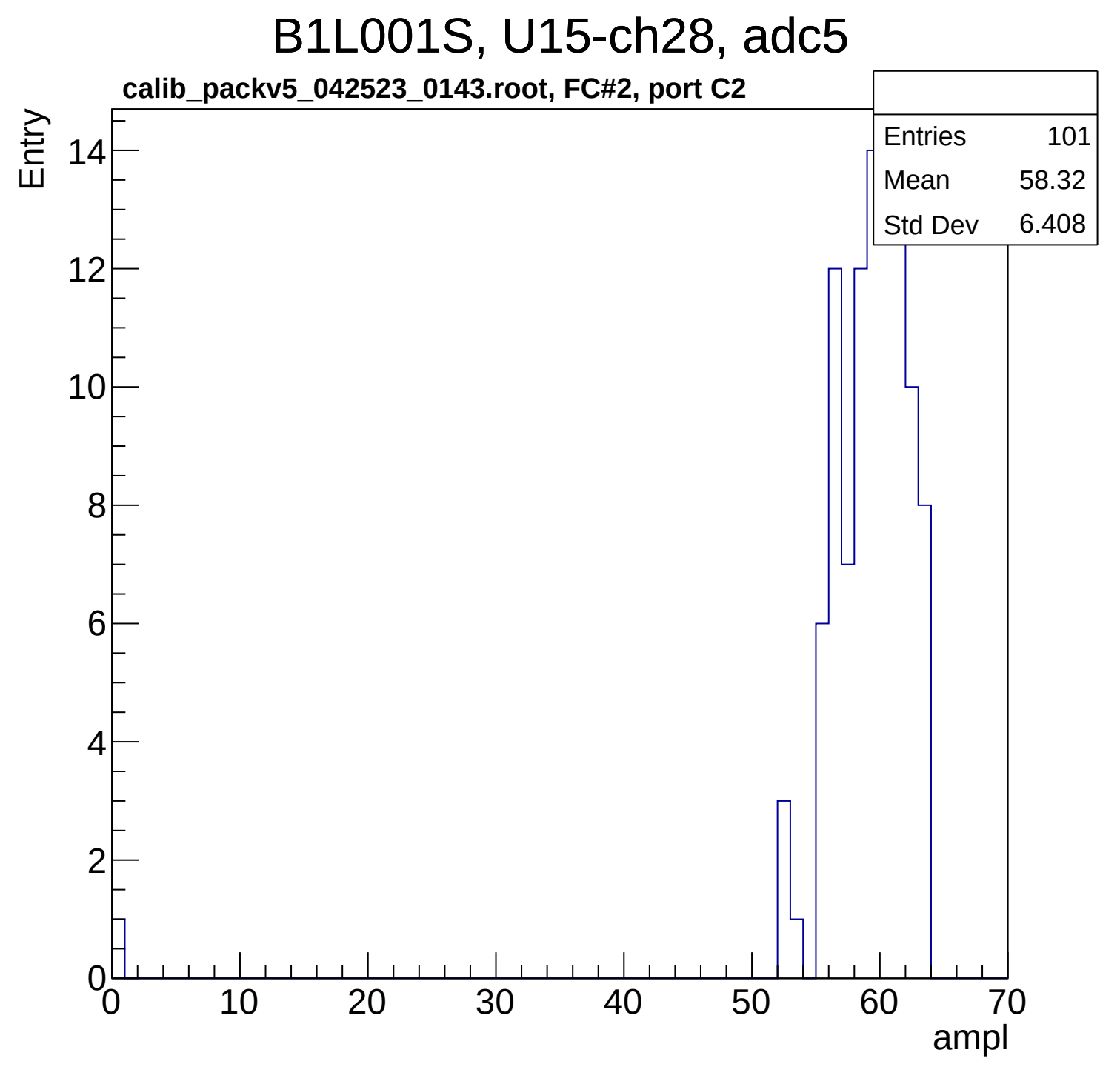
B1L001S, U15-ch28, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 58.32
Std Dev 6.408
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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60
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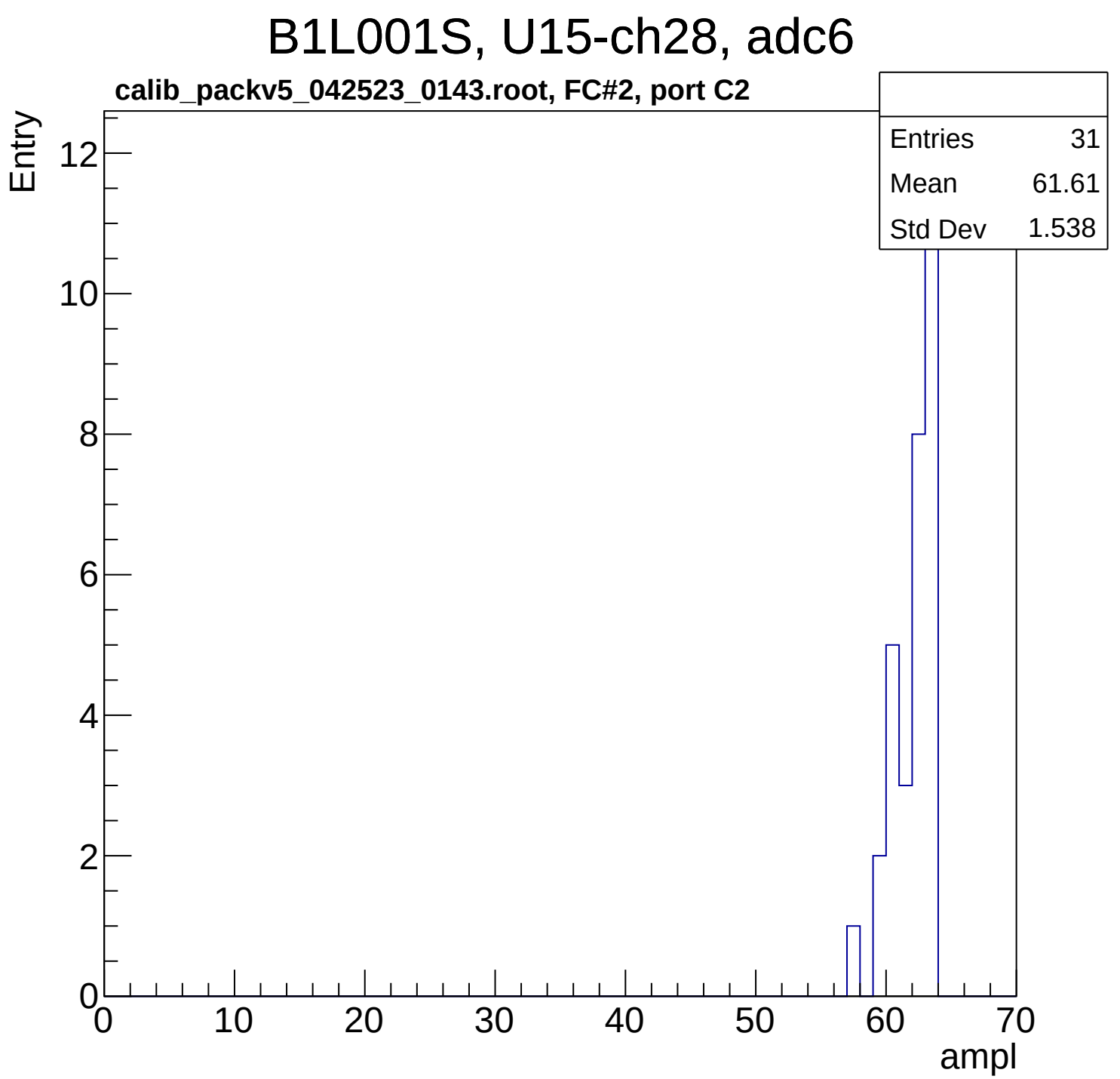
B1L001S, U15-ch28, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 31
Mean 61.61
Std Dev 1.538
Entry
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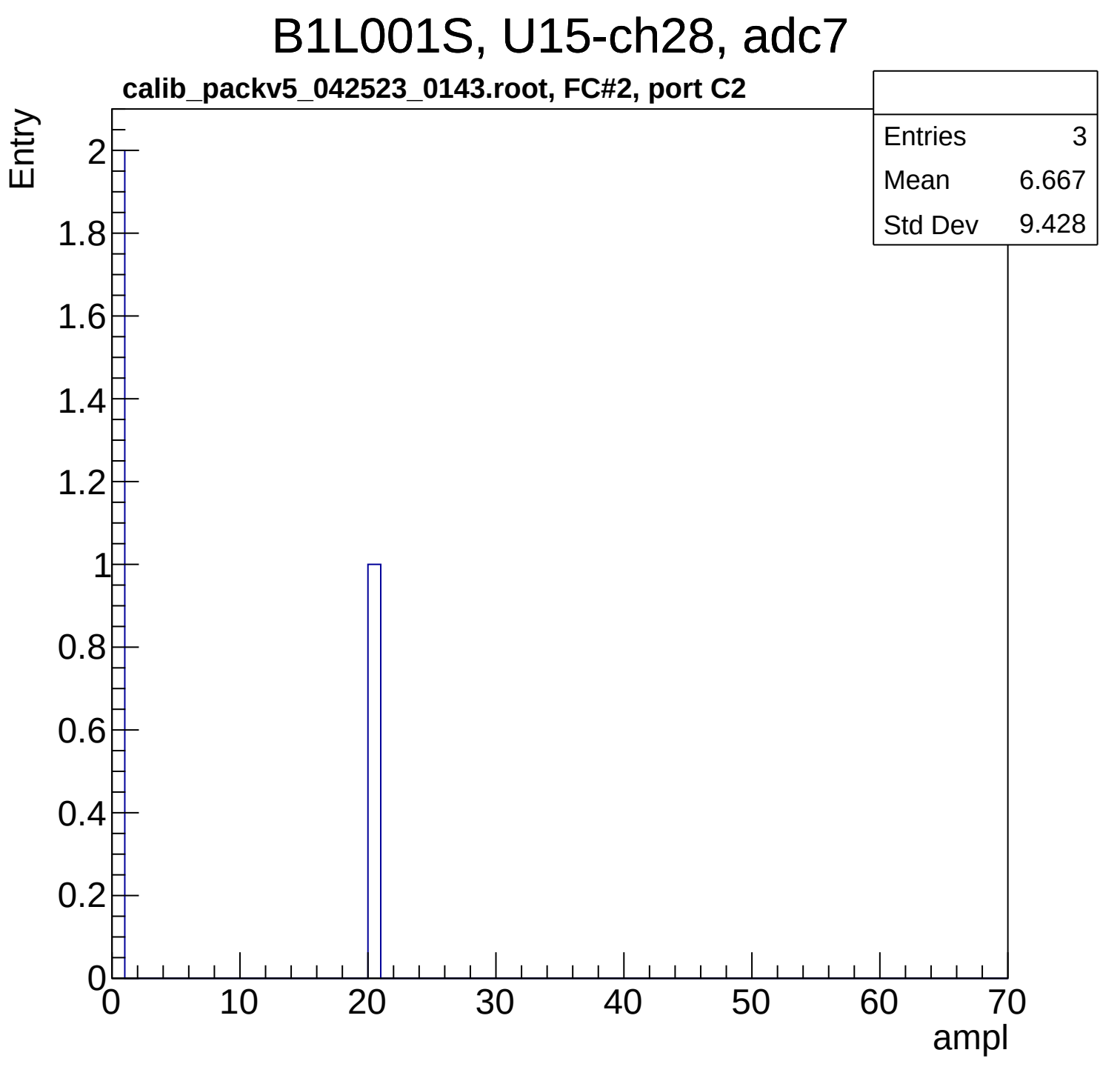
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch28, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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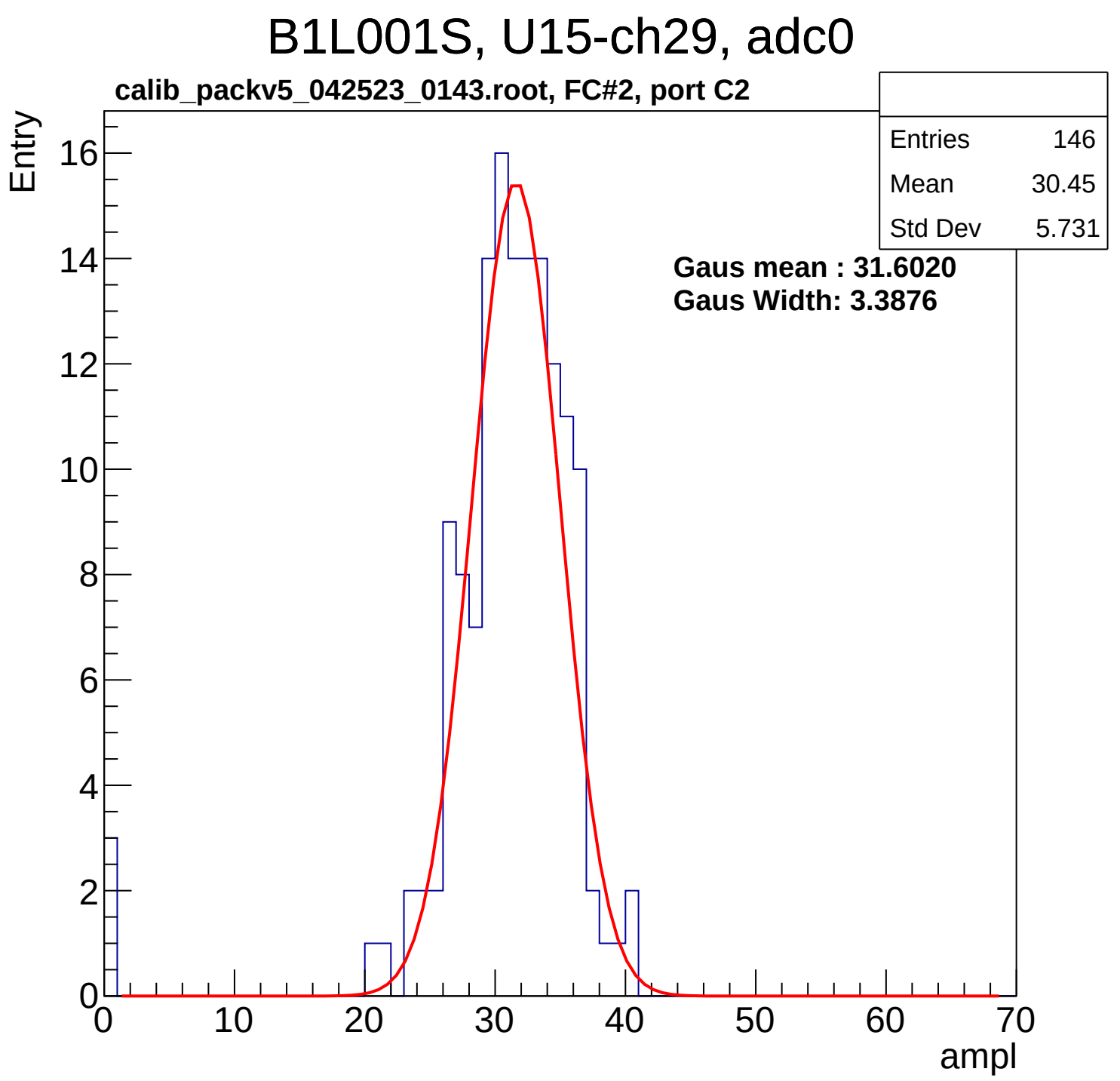
B1L001S, U15-ch29, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 30.45
Std Dev 5.731
Gaus mean : 31.6020
Gaus Width: 3.3876
Entry
ampl
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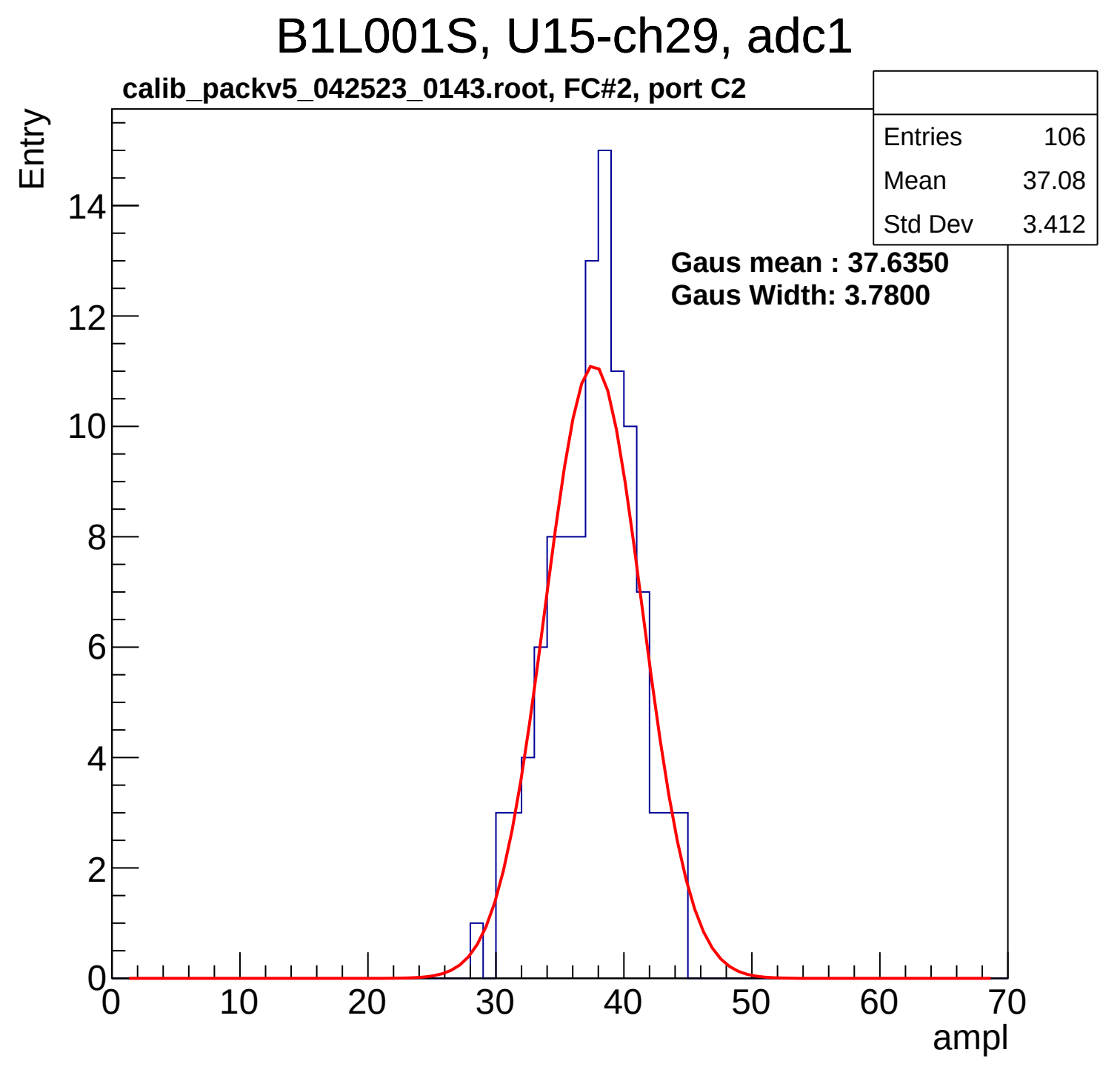
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch29, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 37.08
Std Dev 3.412
Gaus mean : 37.6350
Gaus Width: 3.7800
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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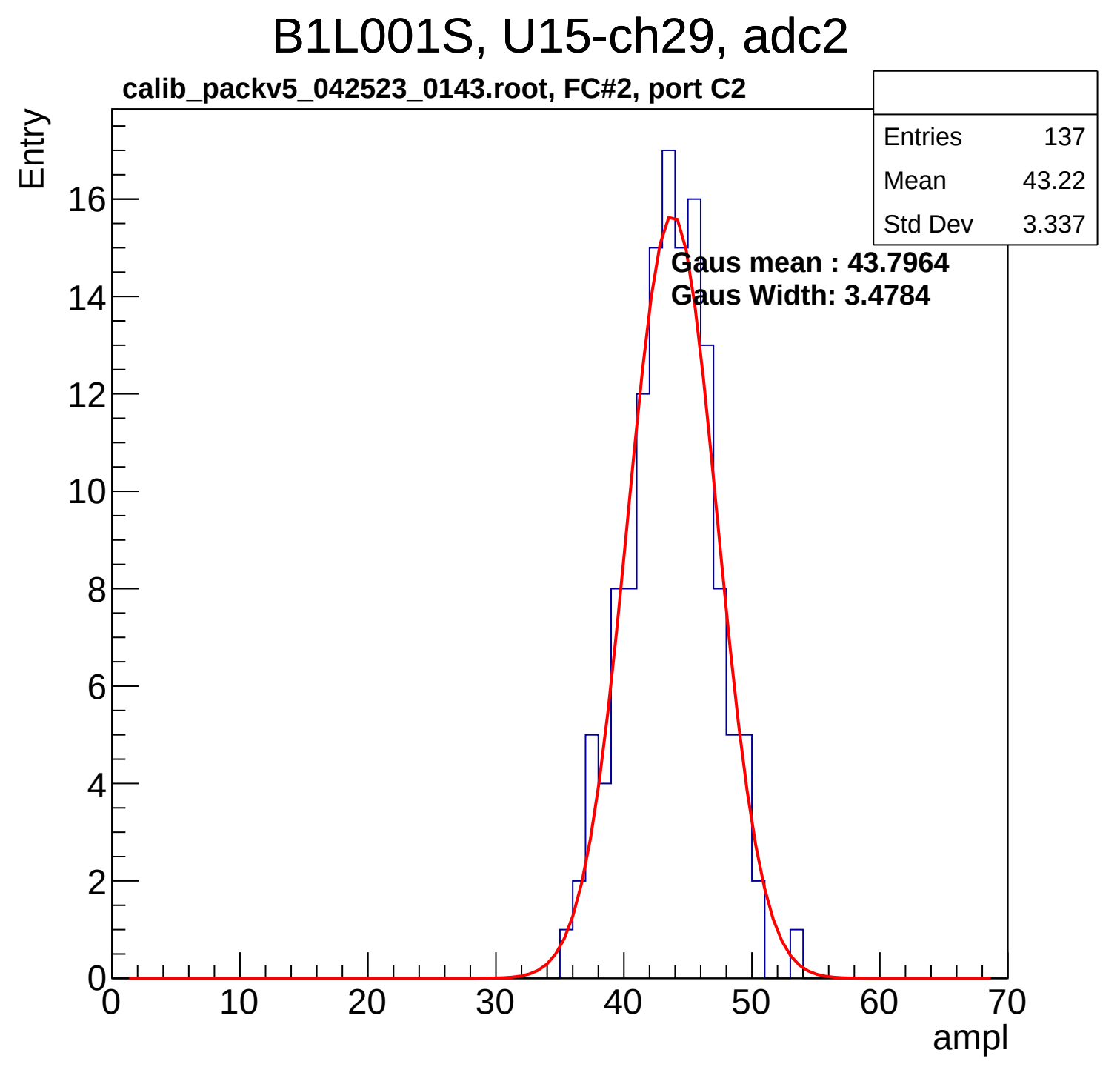
B1L001S, U15-ch29, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 43.22
Std Dev 3.337
Gaus mean : 43.7964
Gaus Width: 3.4784
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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40
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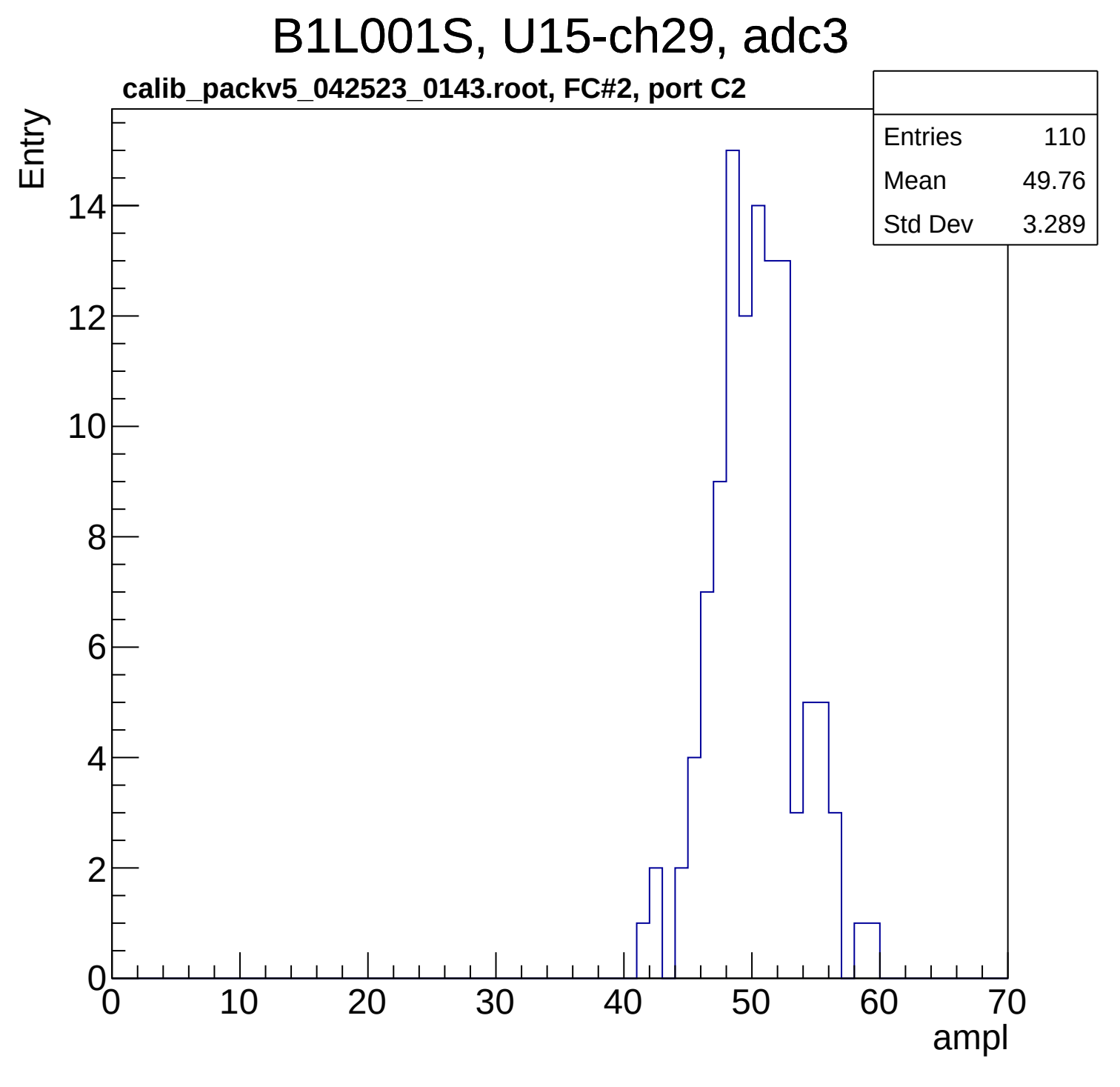
B1L001S, U15-ch29, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.76
Std Dev 3.289
Entry
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4
6
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10
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10
20
30
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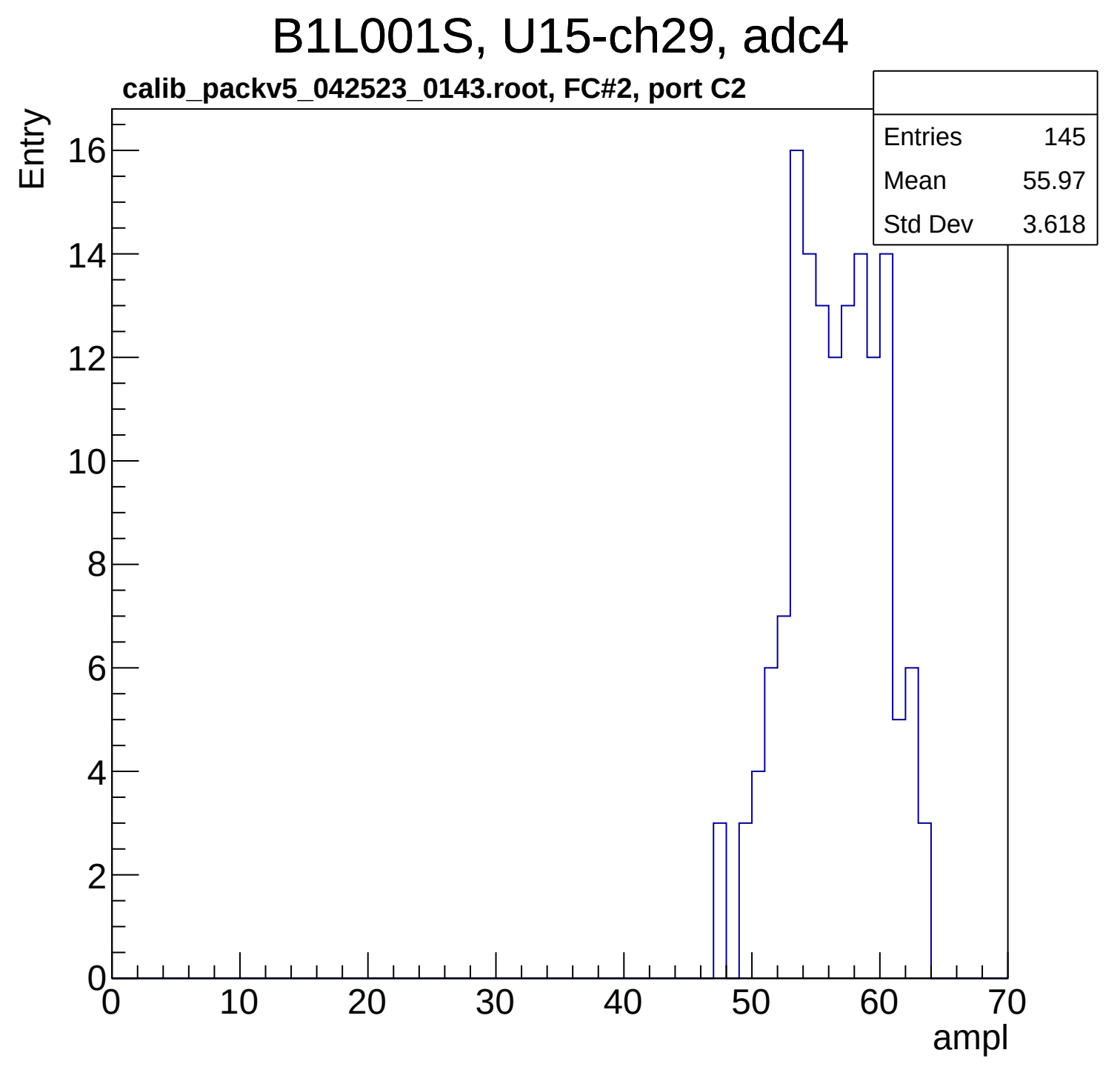
50
60
70
ampl


B1L001S, U15-ch29, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 55.97
Std Dev 3.618
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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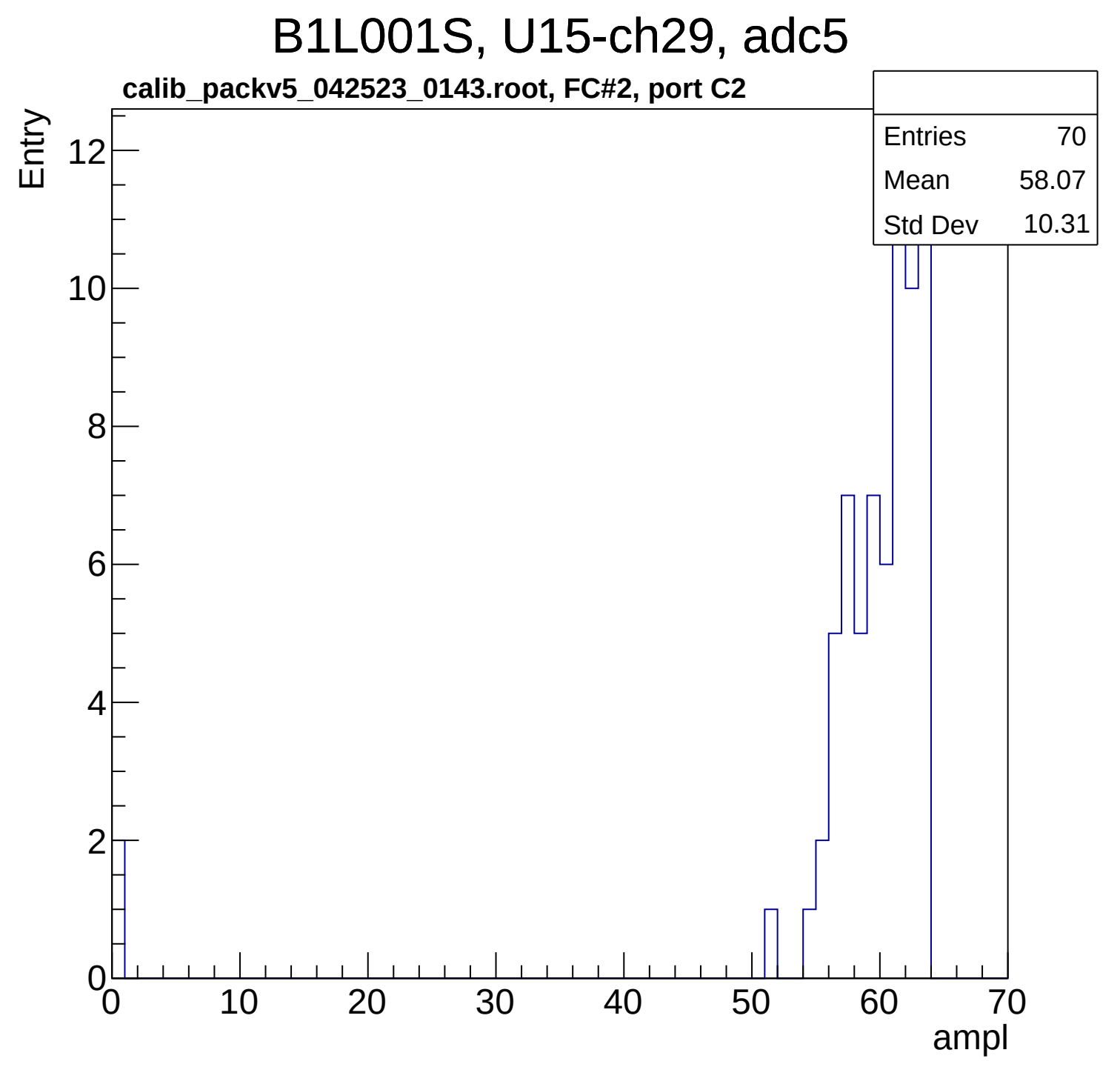
B1L001S, U15-ch29, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 70
Mean 58.07
Std Dev 10.31
Entry
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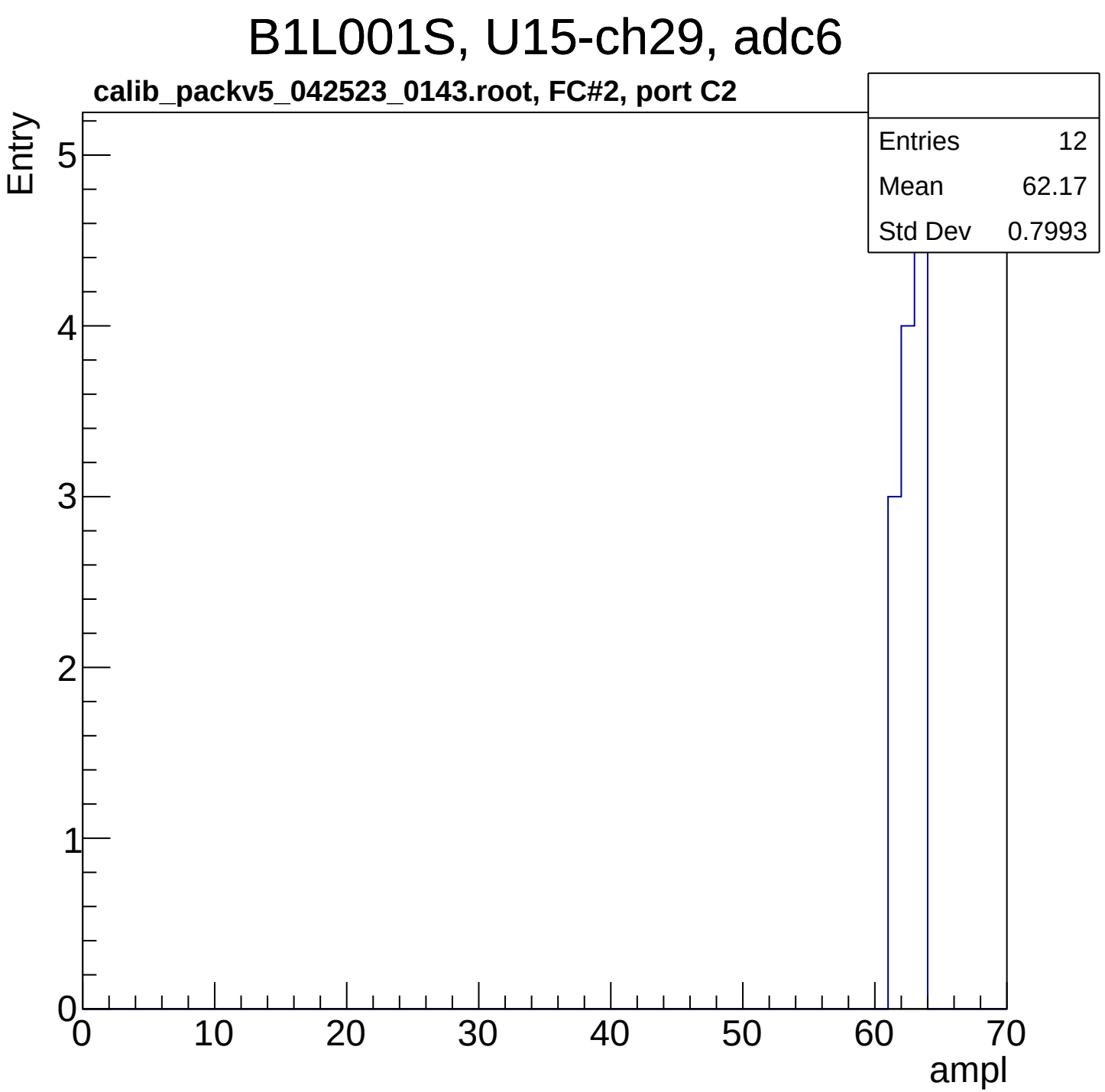
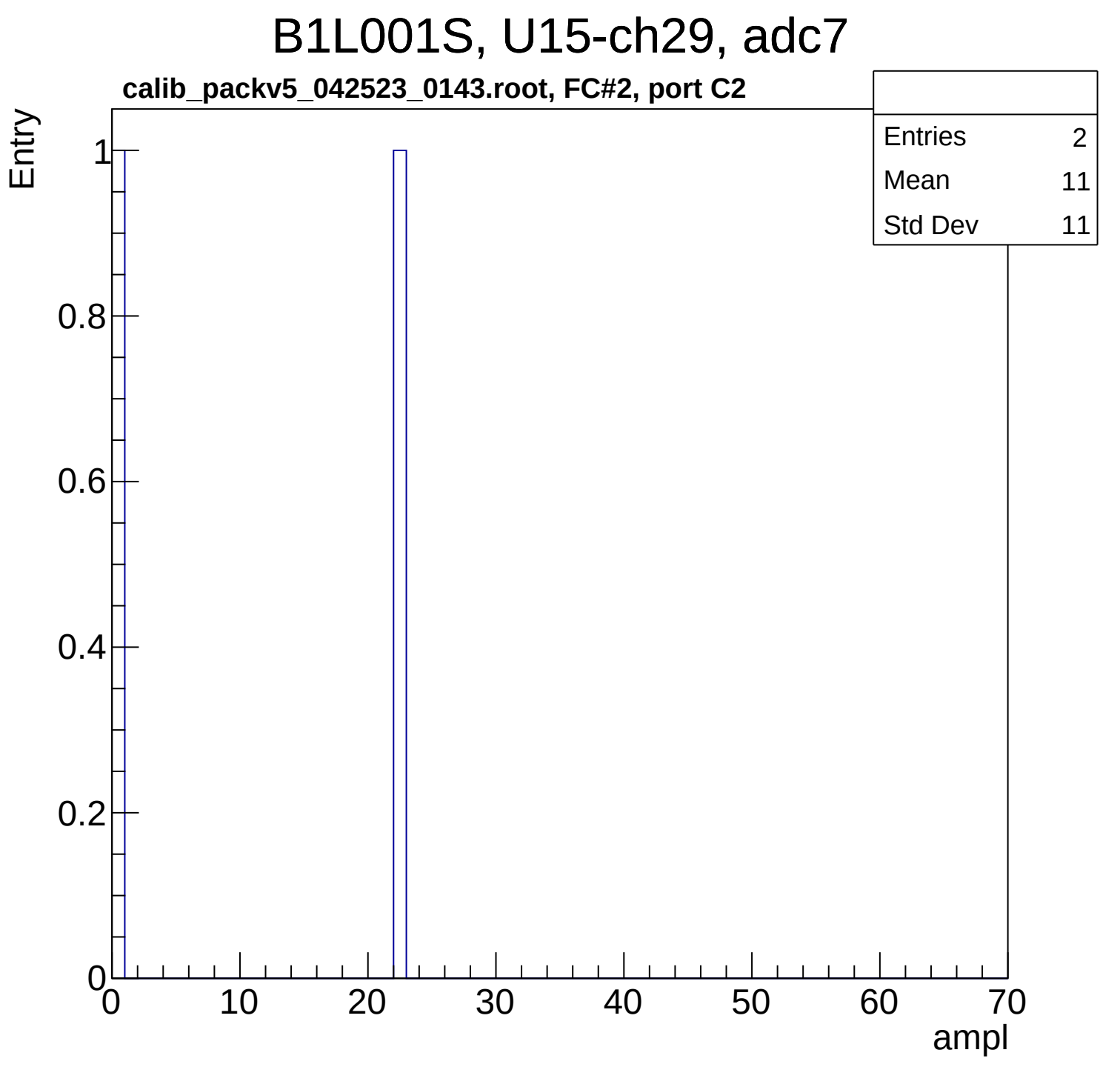
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch29, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 62.17
Std Dev 0.7993
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch29, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
0
0
10
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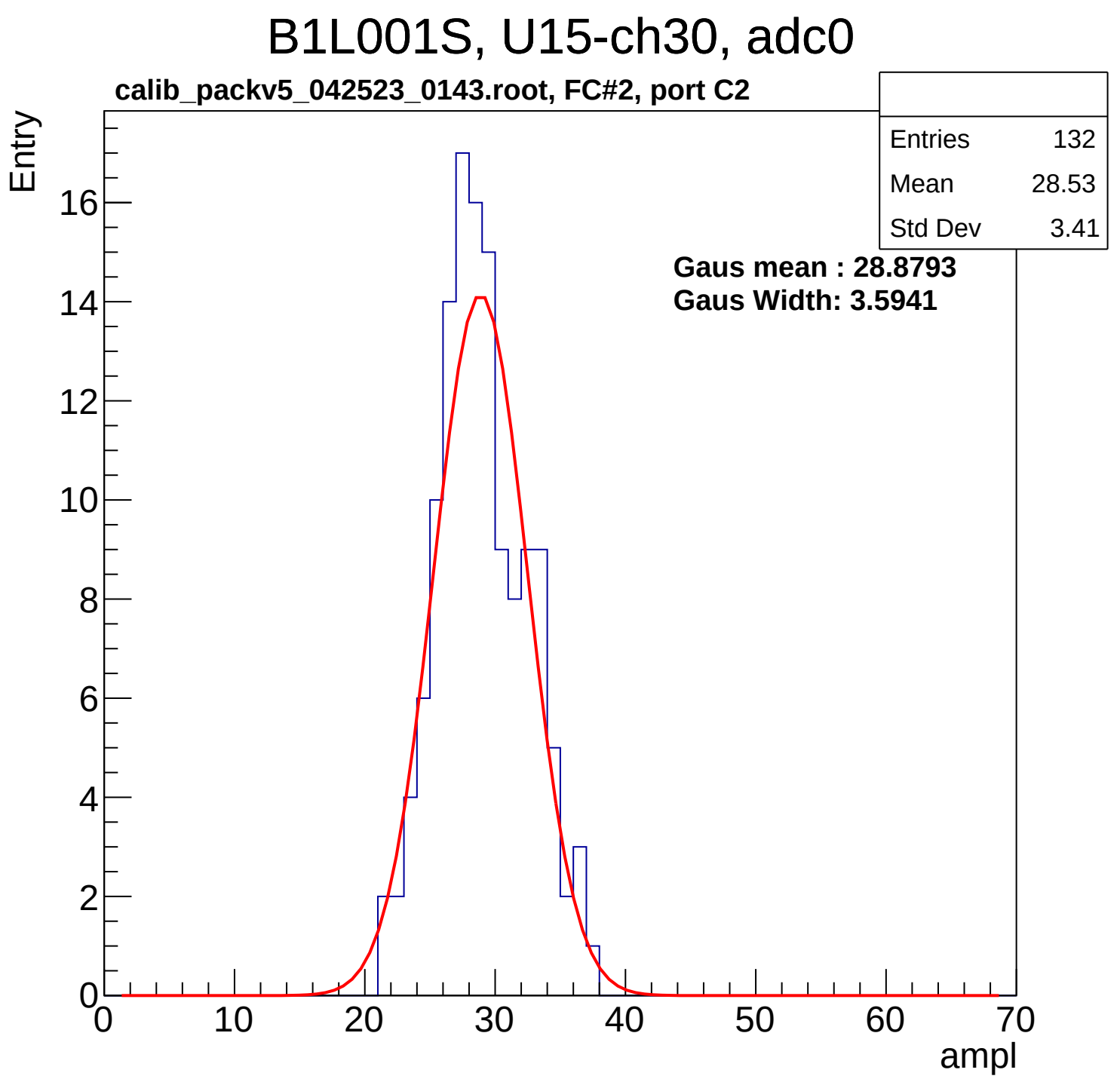
30
40
50
60
70
ampl


B1L001S, U15-ch30, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 28.53
Std Dev 3.41
Gaus mean : 28.8793
Gaus Width: 3.5941
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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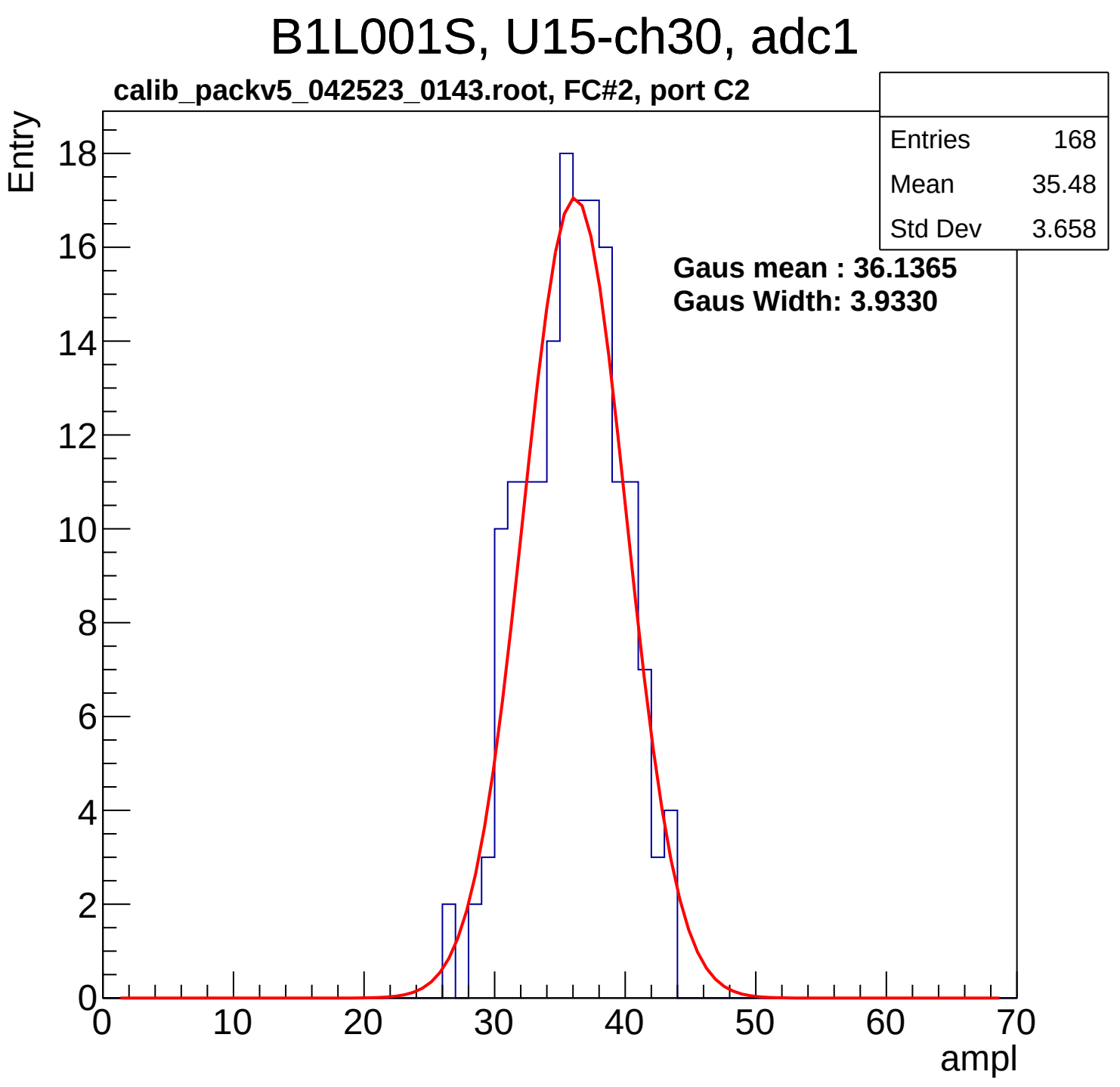
B1L001S, U15-ch30, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 168
Mean 35.48
Std Dev 3.658
Gaus mean : 36.1365
Gaus Width: 3.9330
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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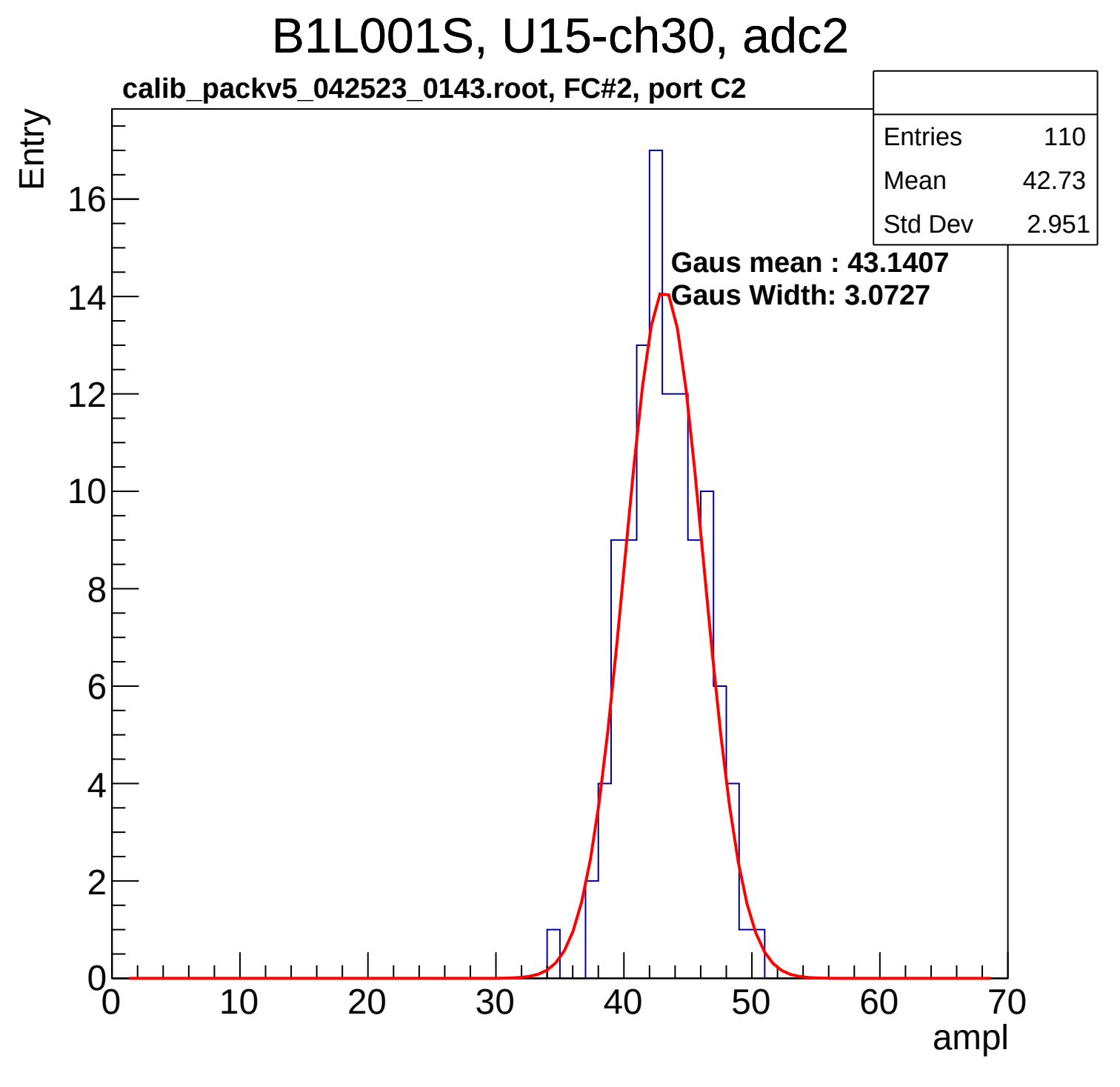
B1L001S, U15-ch30, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 42.73
Std Dev 2.951
Gaus mean : 43.1407
Gaus Width: 3.0727
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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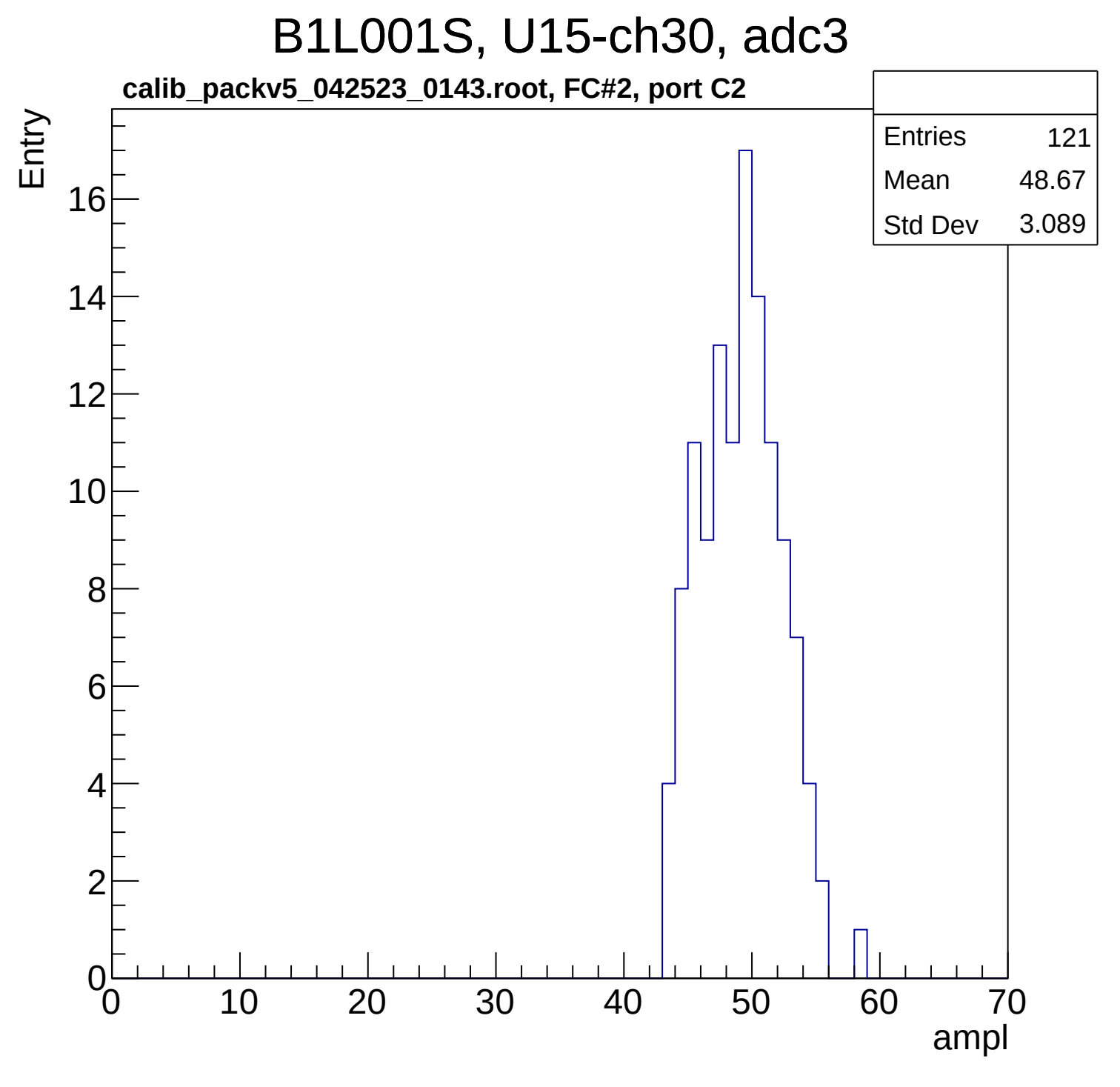
B1L001S, U15-ch30, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 48.67
Std Dev 3.089
Entry
ampl
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4
6
8
10
12
14
16
0
10
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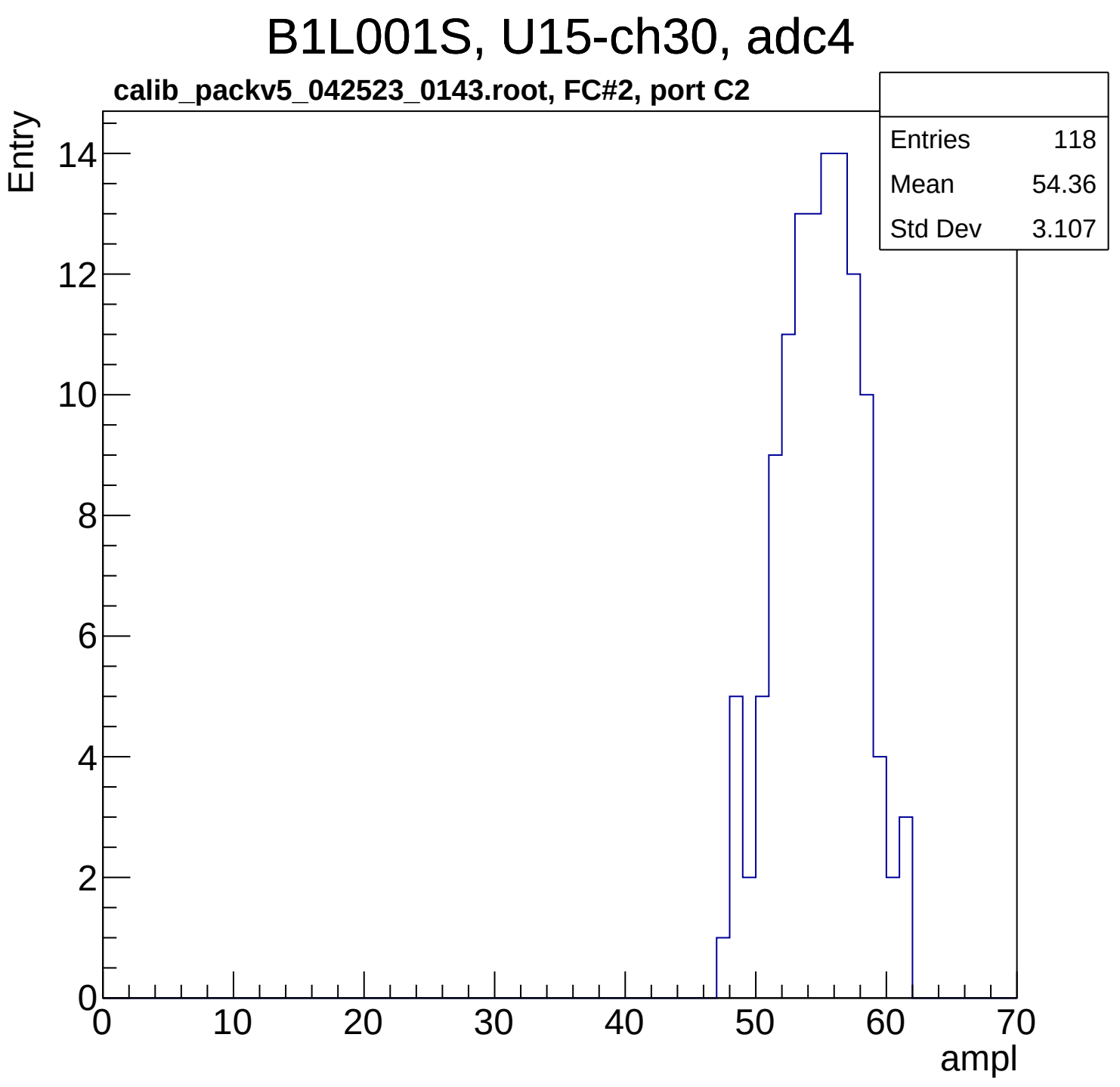
B1L001S, U15-ch30, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 54.36
Std Dev 3.107
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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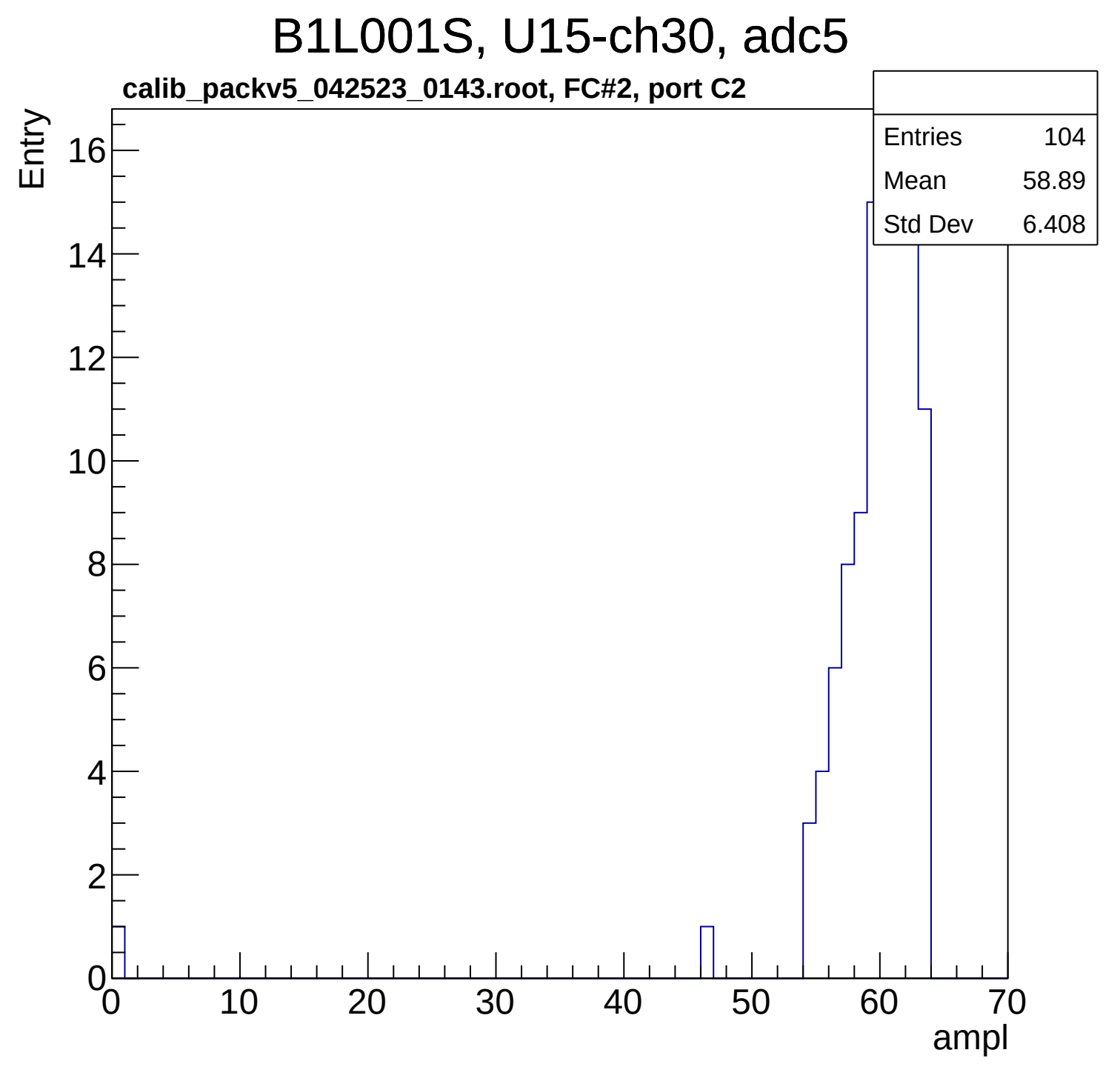
B1L001S, U15-ch30, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 58.89
Std Dev 6.408
Entry
ampl
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4
6
8
10
12
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10
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40
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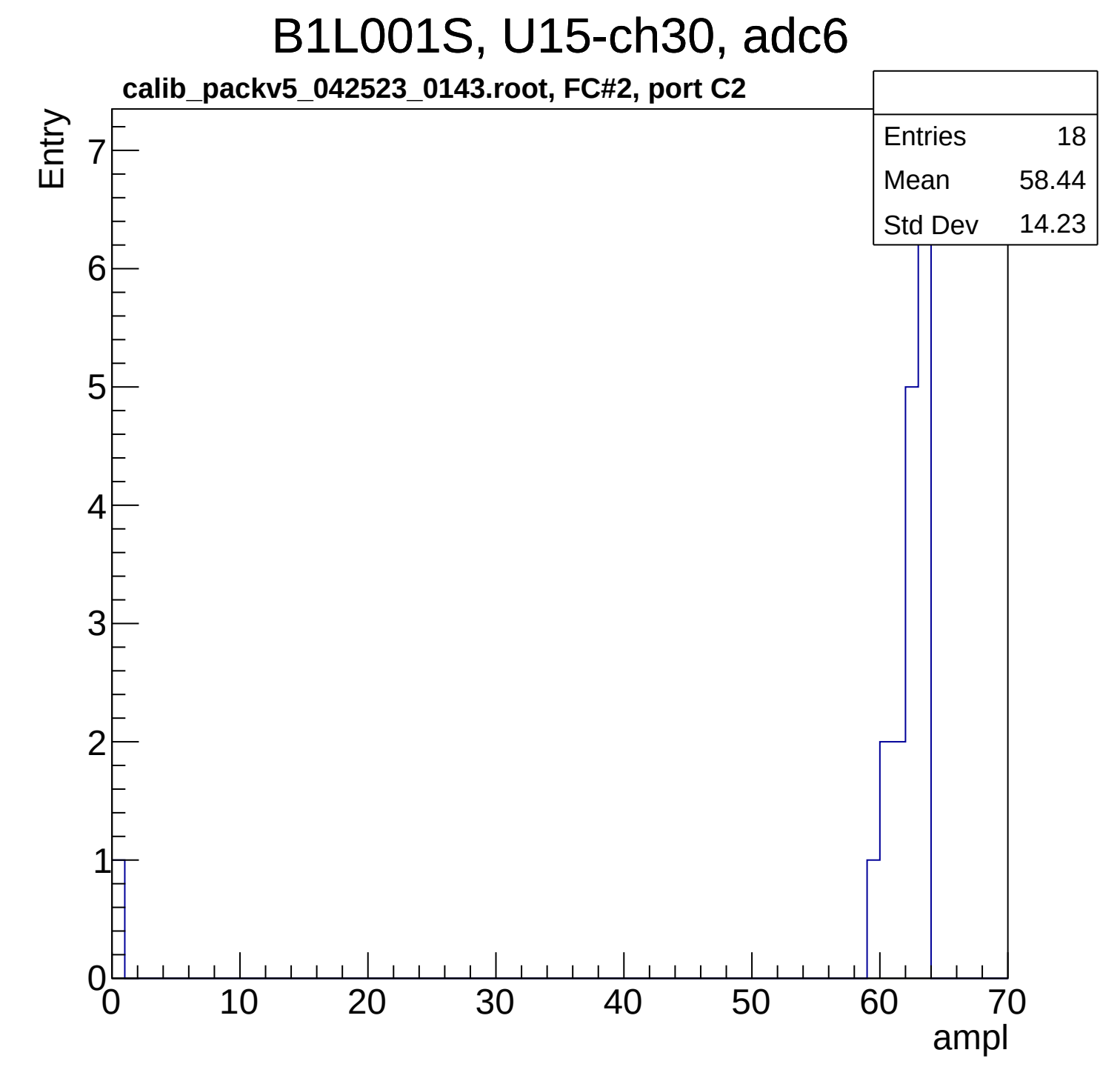
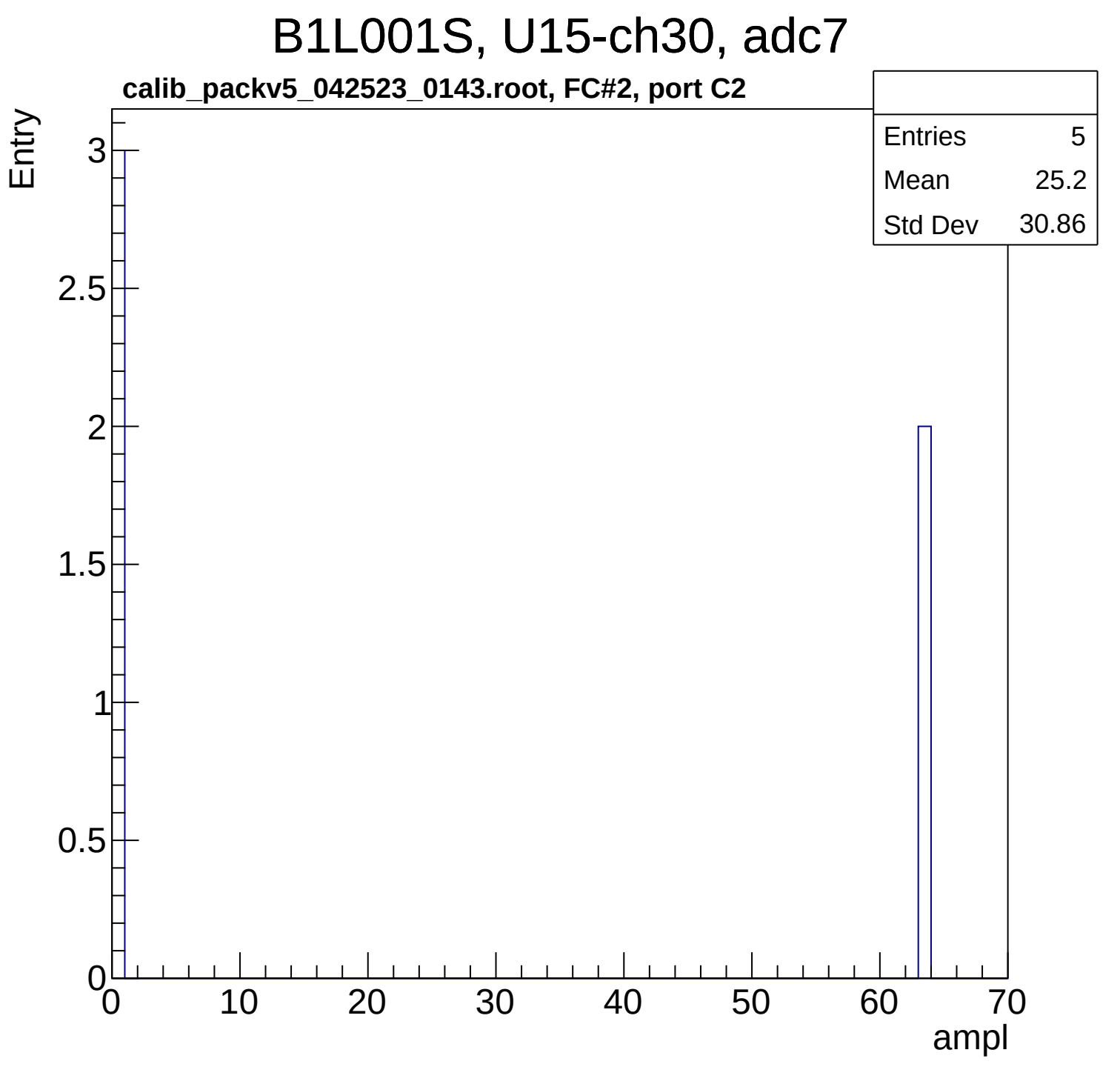

B1L001S, U15-ch30, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 58.44
Std Dev 14.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch30, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
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1
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2
2.5
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0
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60
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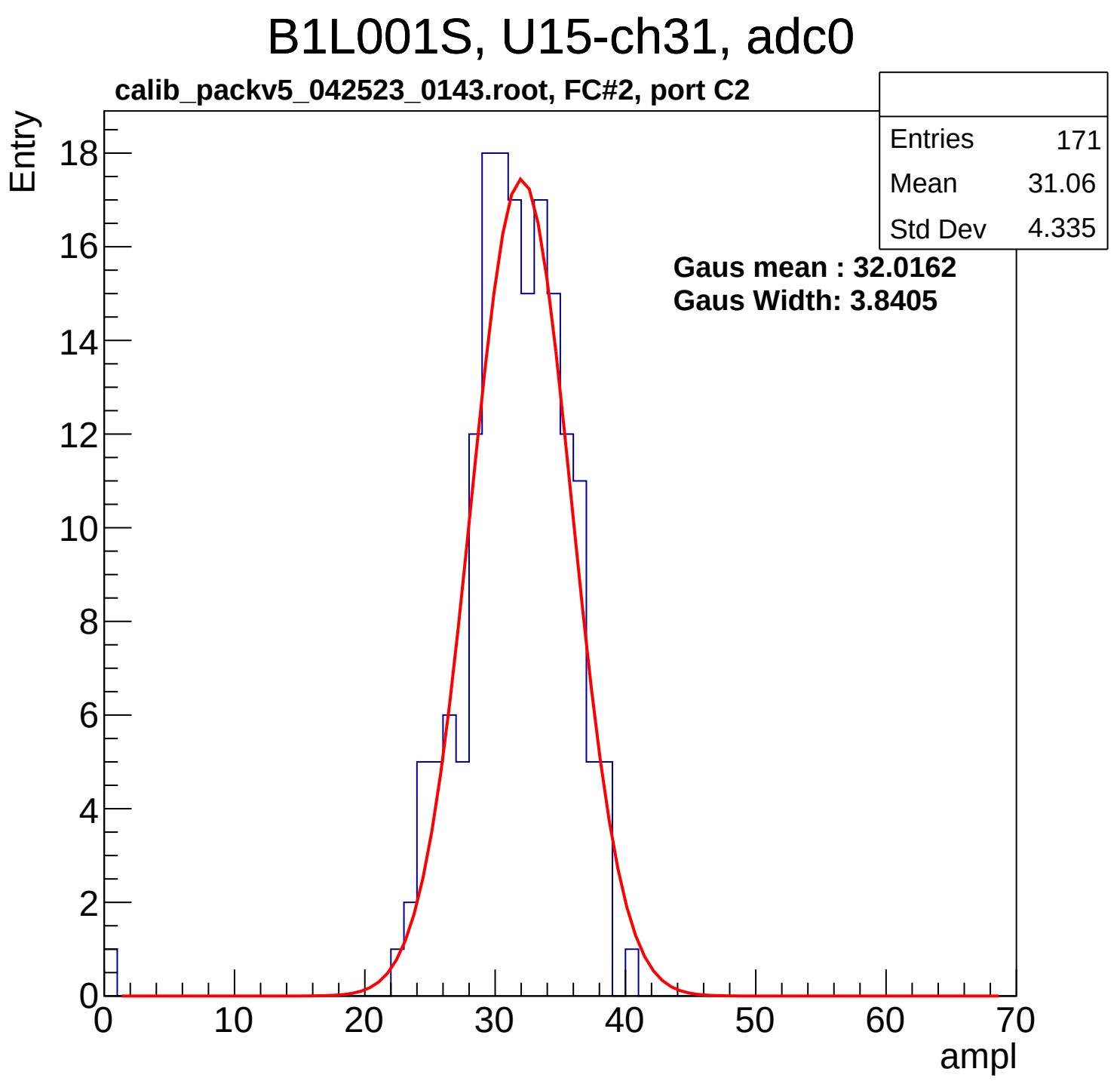
B1L001S, U15-ch31, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 171
Mean 31.06
Std Dev 4.335
Gaus mean : 32.0162
Gaus Width: 3.8405
Entry
ampl
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4
6
8
10
12
14
16
18
0
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40
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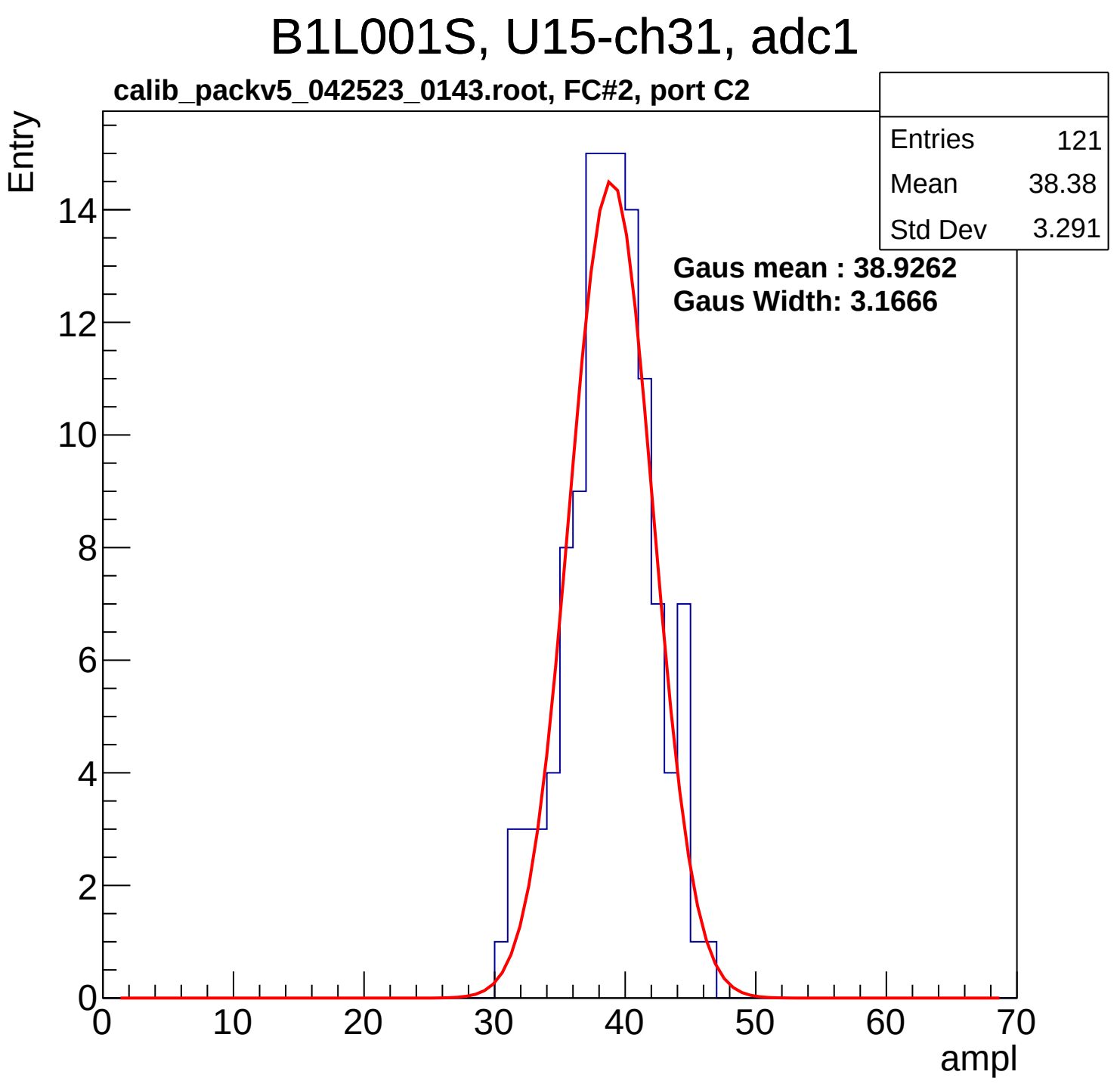
B1L001S, U15-ch31, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 38.38
Std Dev 3.291
Gaus mean : 38.9262
Gaus Width: 3.1666
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
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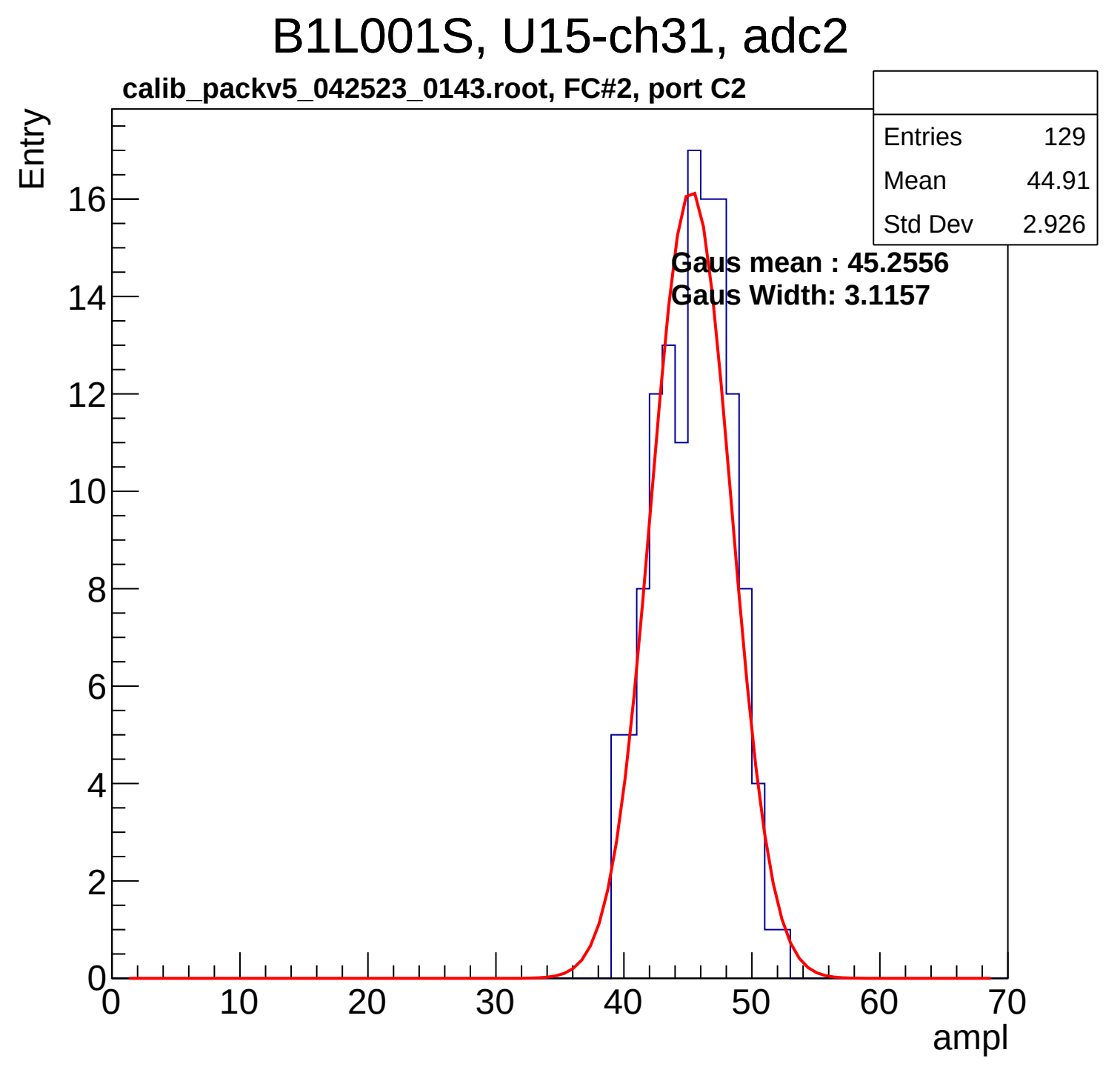
B1L001S, U15-ch31, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 44.91
Std Dev 2.926
Gaus mean : 45.2556
Gaus Width: 3.1157
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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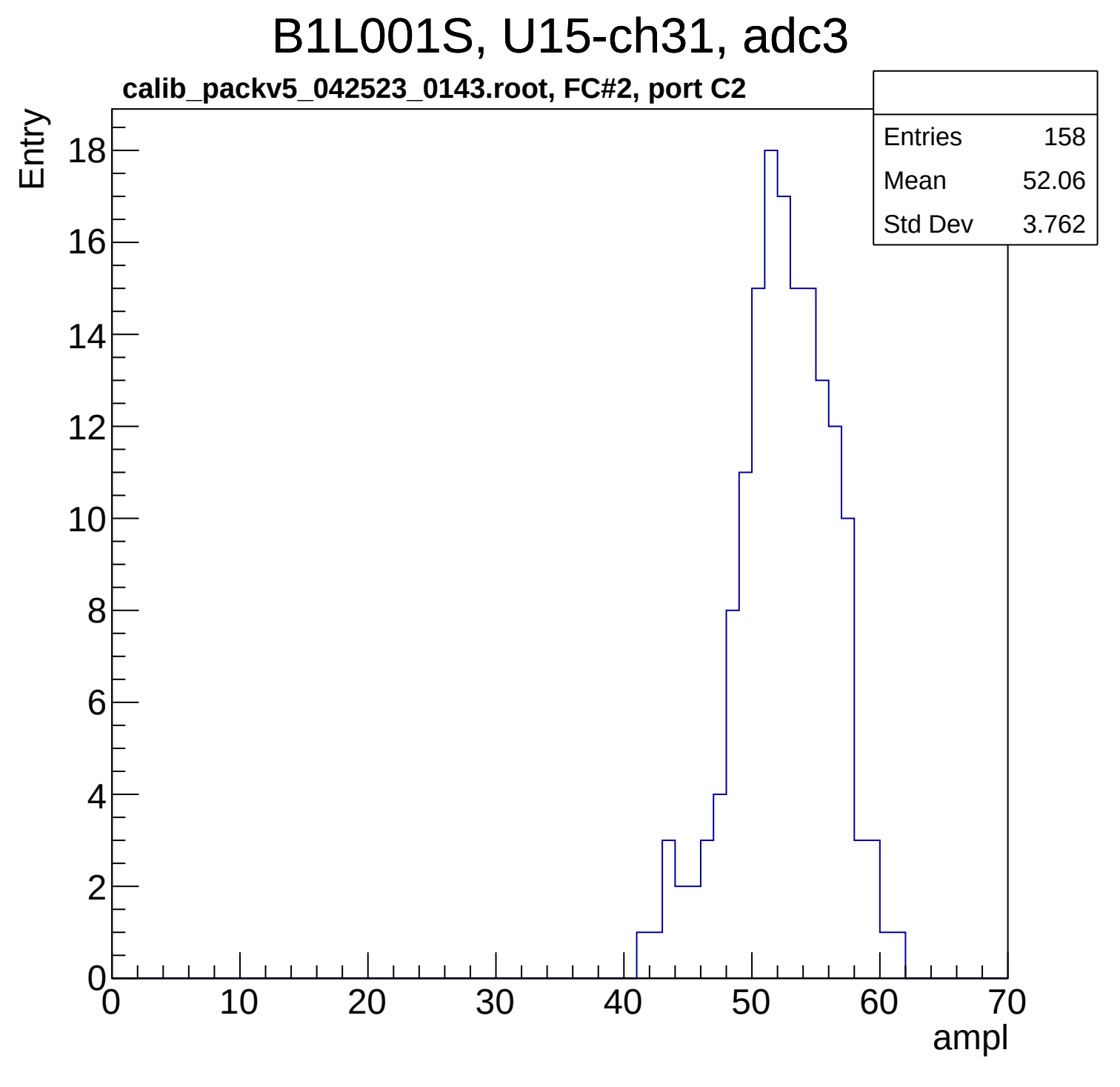
B1L001S, U15-ch31, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 52.06
Std Dev 3.762
Entry
ampl
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4
6
8
10
12
14
16
18
0
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60
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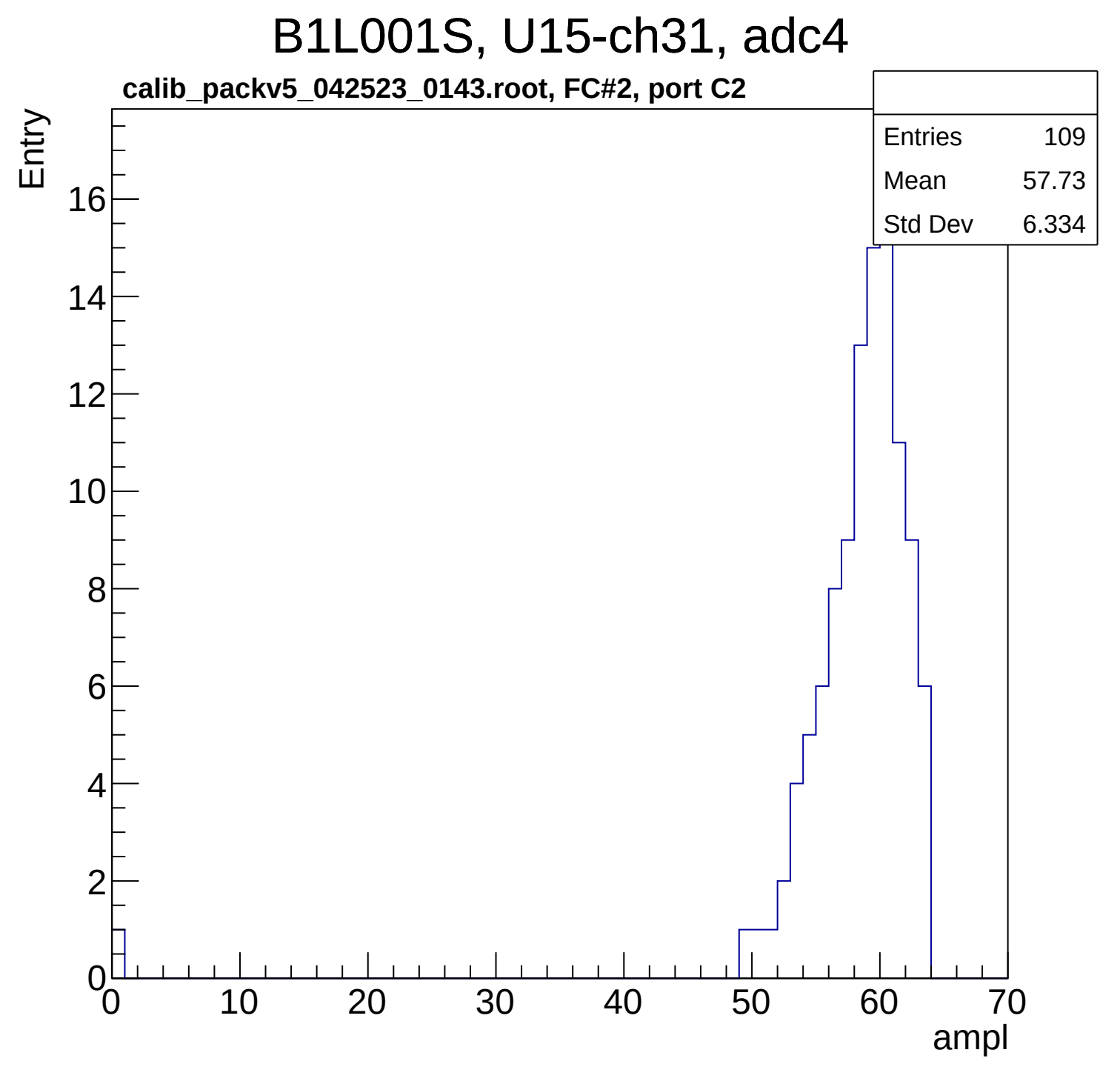
B1L001S, U15-ch31, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 57.73
Std Dev 6.334
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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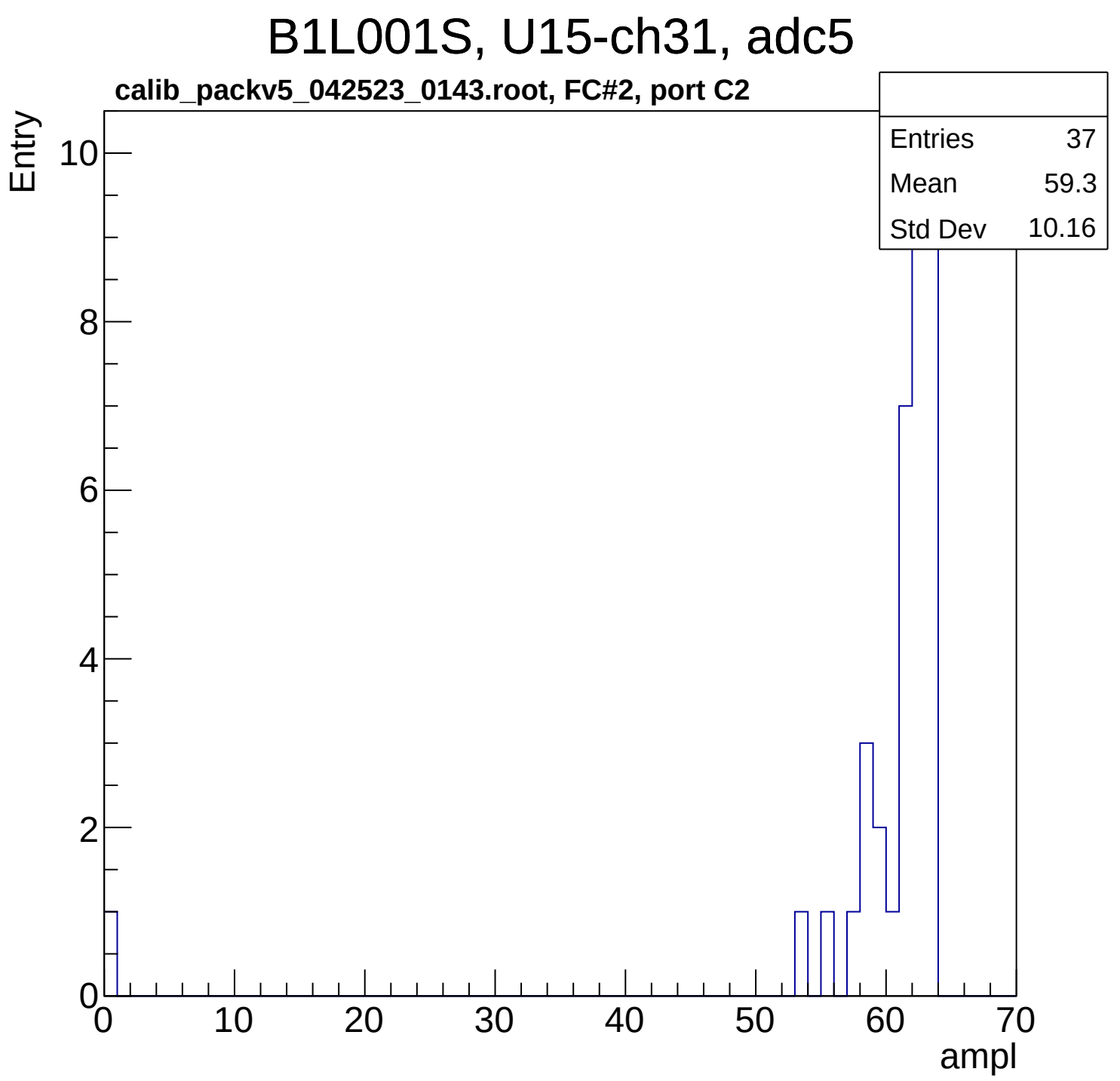
B1L001S, U15-ch31, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 37
Mean 59.3
Std Dev 10.16
Entry
ampl
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2
4
6
8
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0
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20
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40
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60
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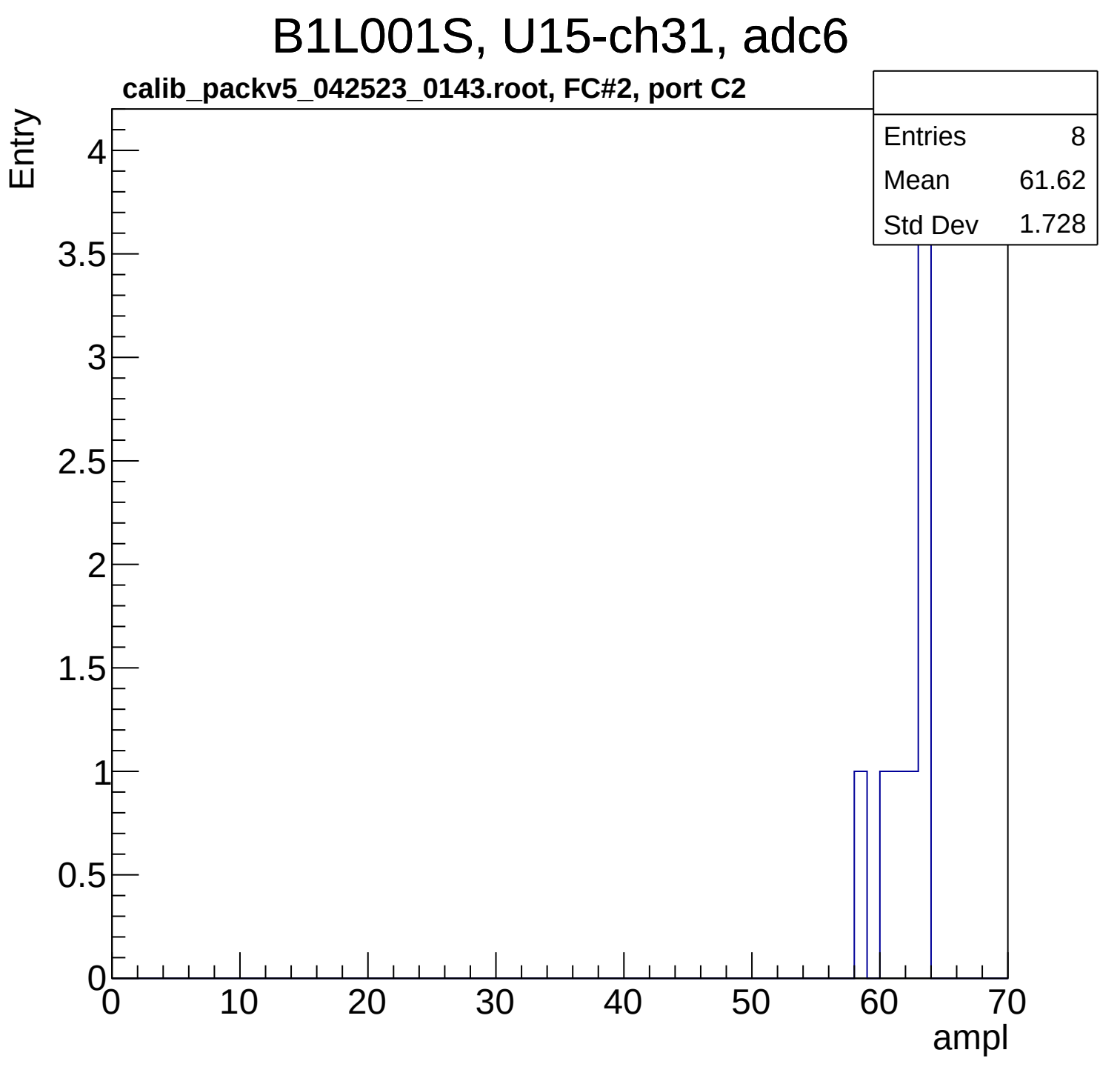
B1L001S, U15-ch31, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.62
Std Dev 1.728
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
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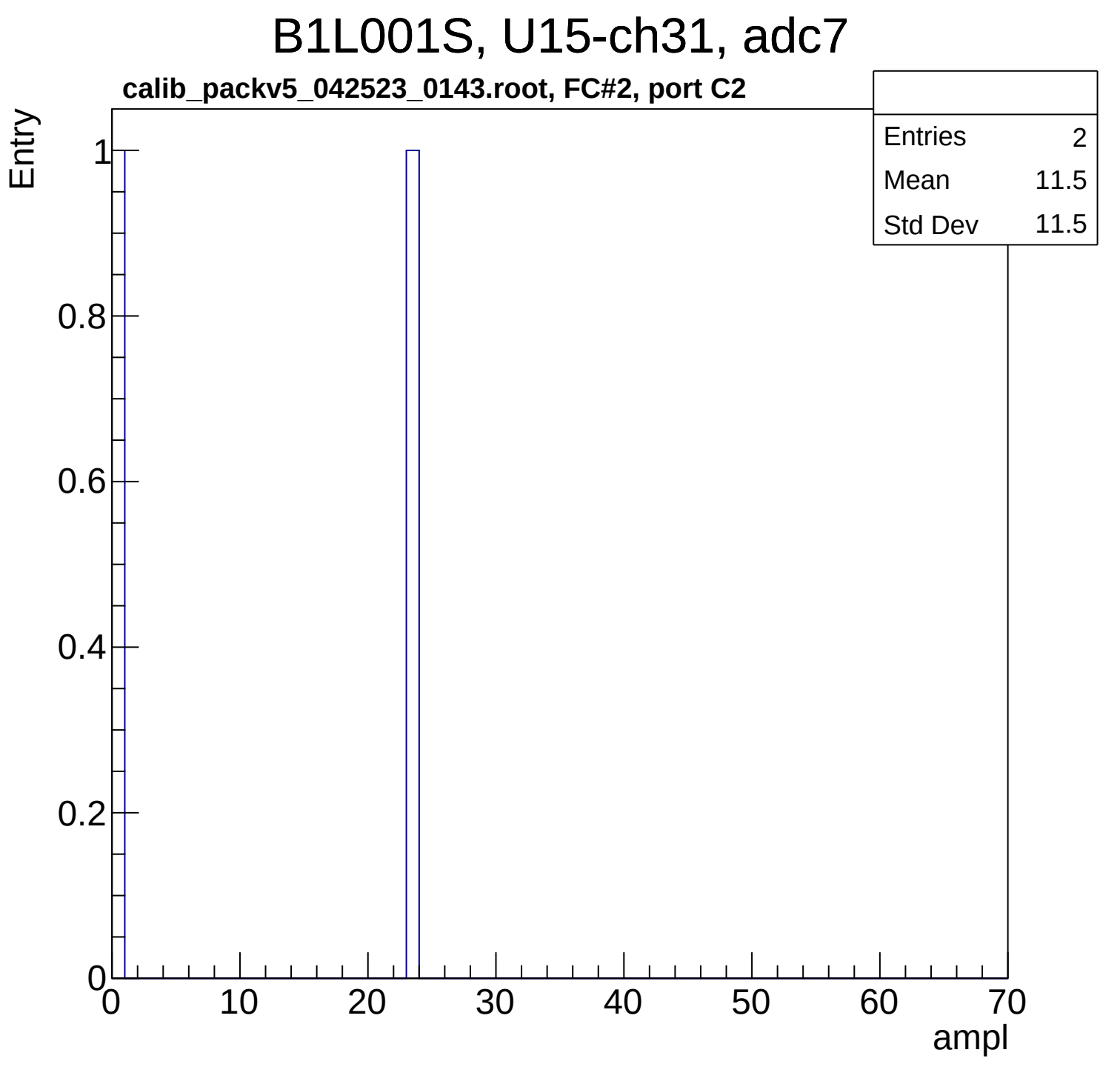
B1L001S, U15-ch31, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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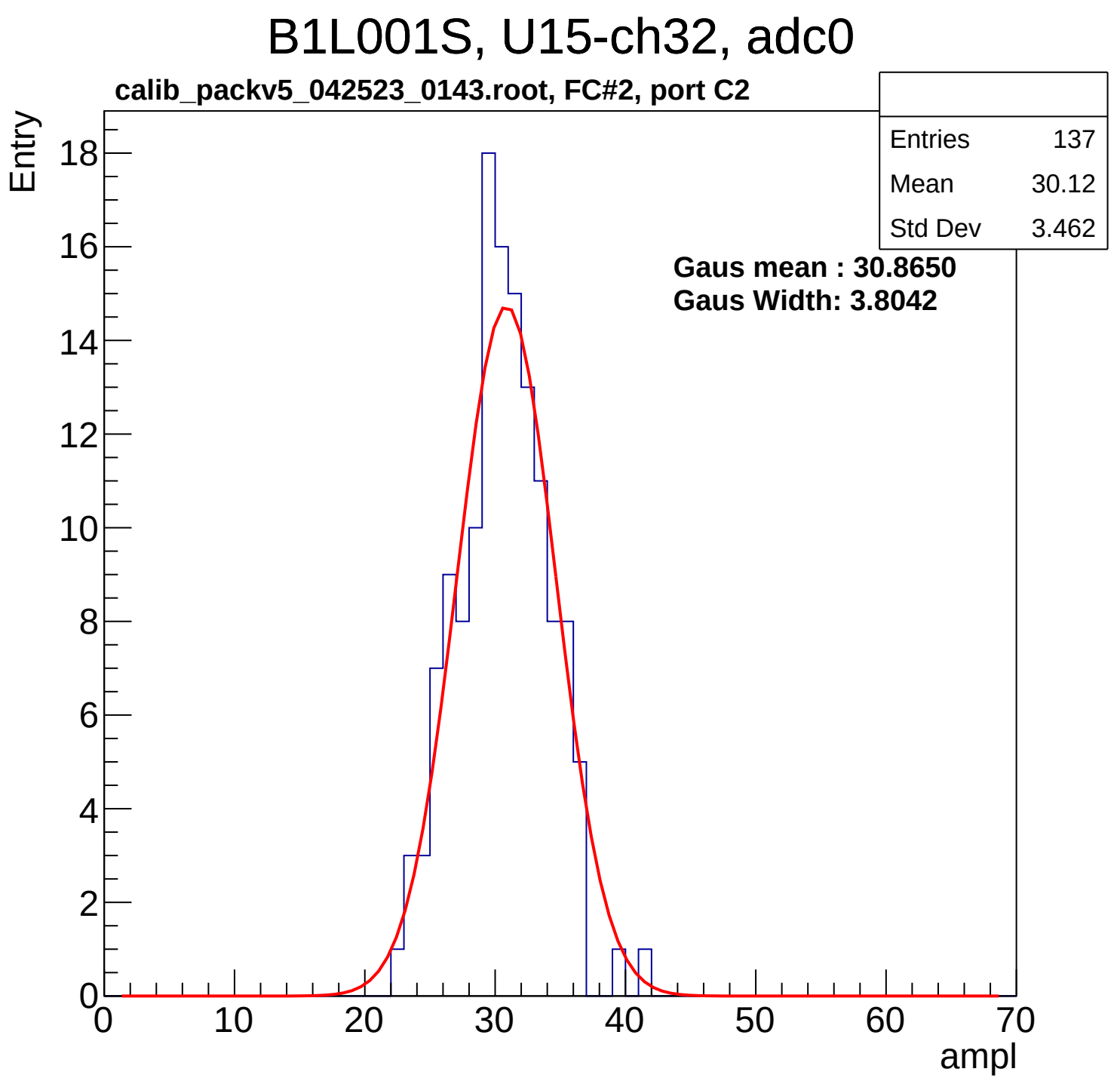
50
60
70
ampl


B1L001S, U15-ch32, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 30.12
Std Dev 3.462
Gaus mean : 30.8650
Gaus Width: 3.8042
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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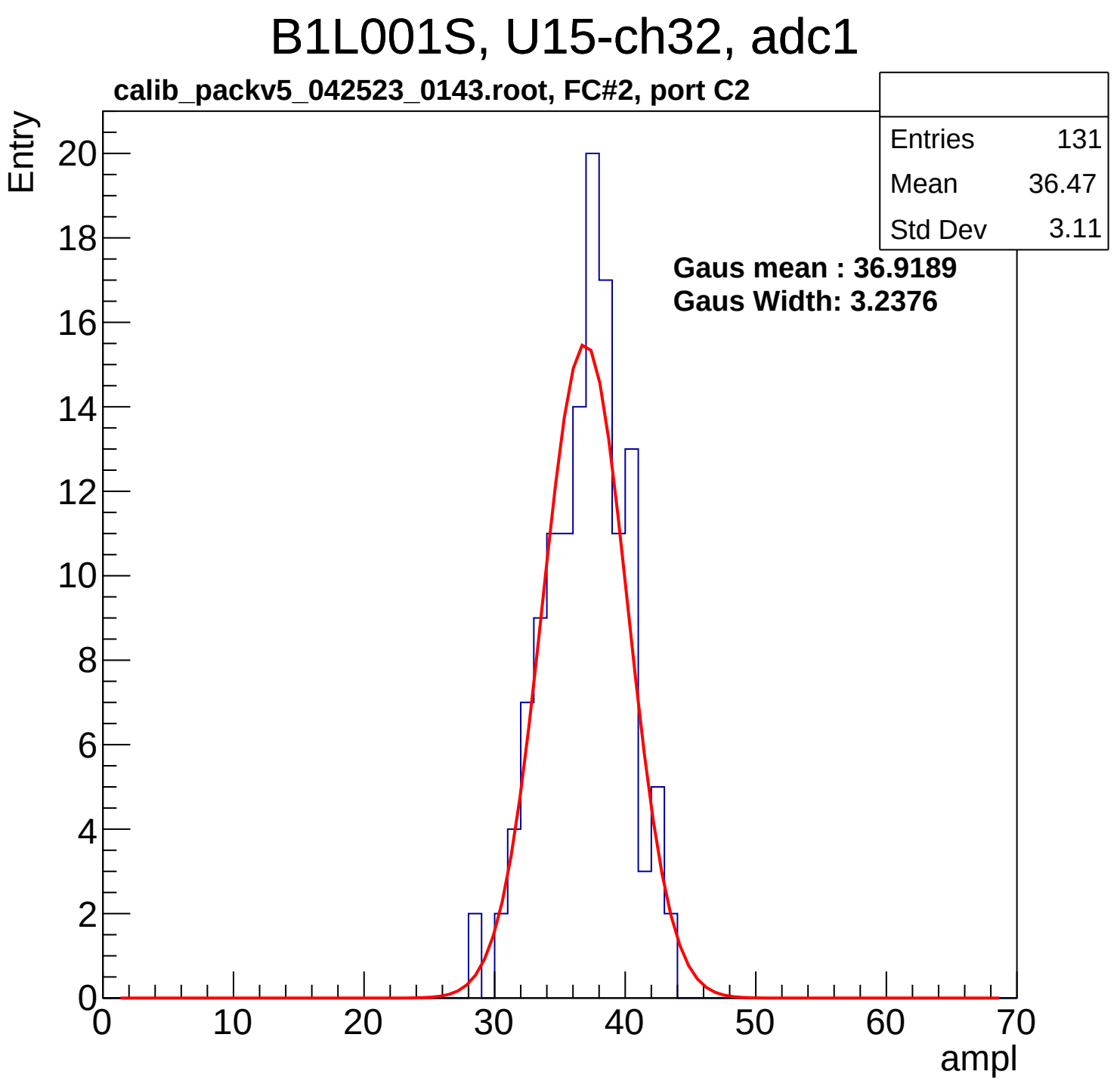
B1L001S, U15-ch32, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 36.47
Std Dev 3.11
Gaus mean : 36.9189
Gaus Width: 3.2376
Entry
ampl
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6
8
10
12
14
16
18
20
0
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20
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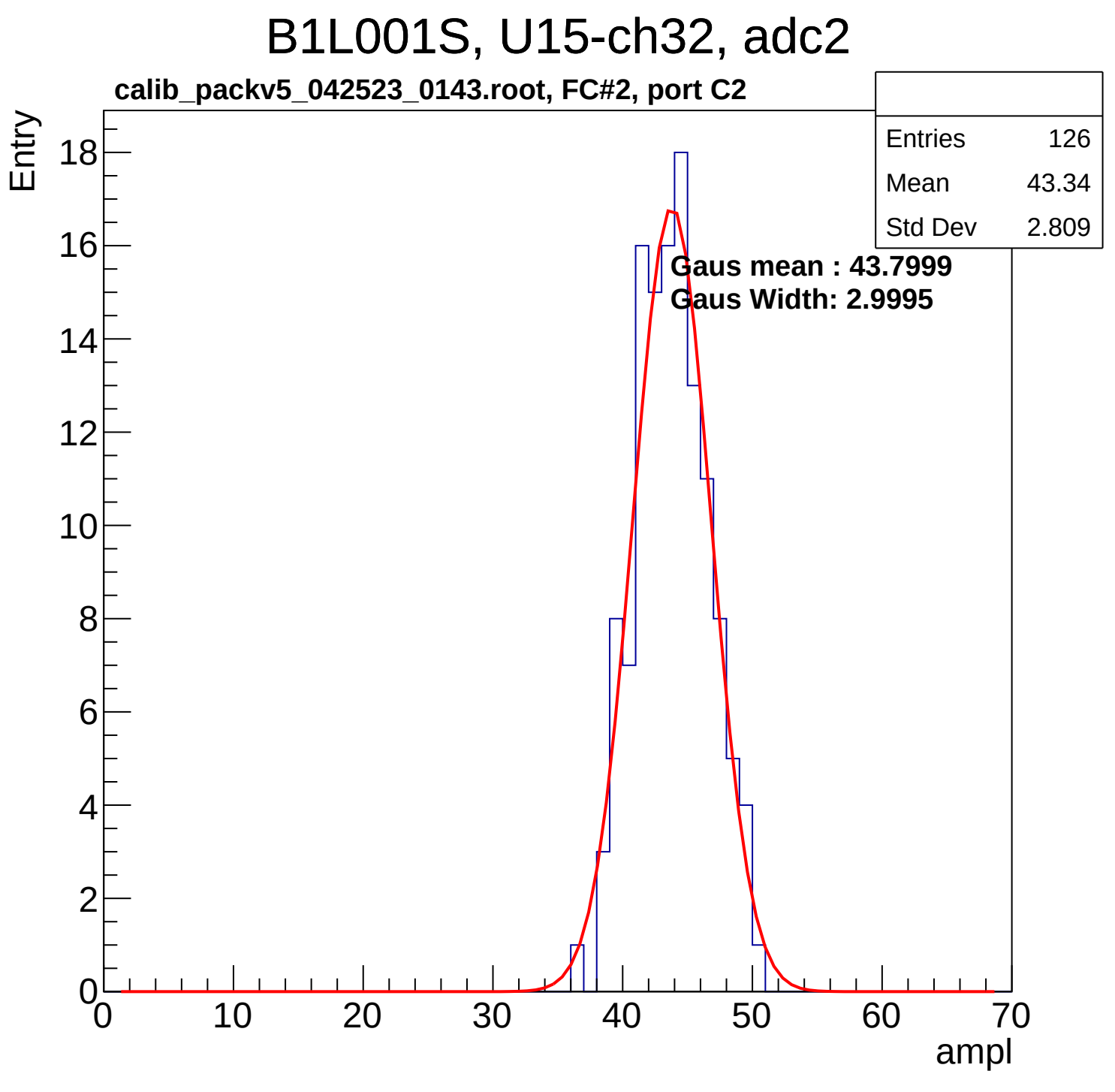
B1L001S, U15-ch32, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 43.34
Std Dev 2.809
Gaus mean : 43.7999
Gaus Width: 2.9995
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
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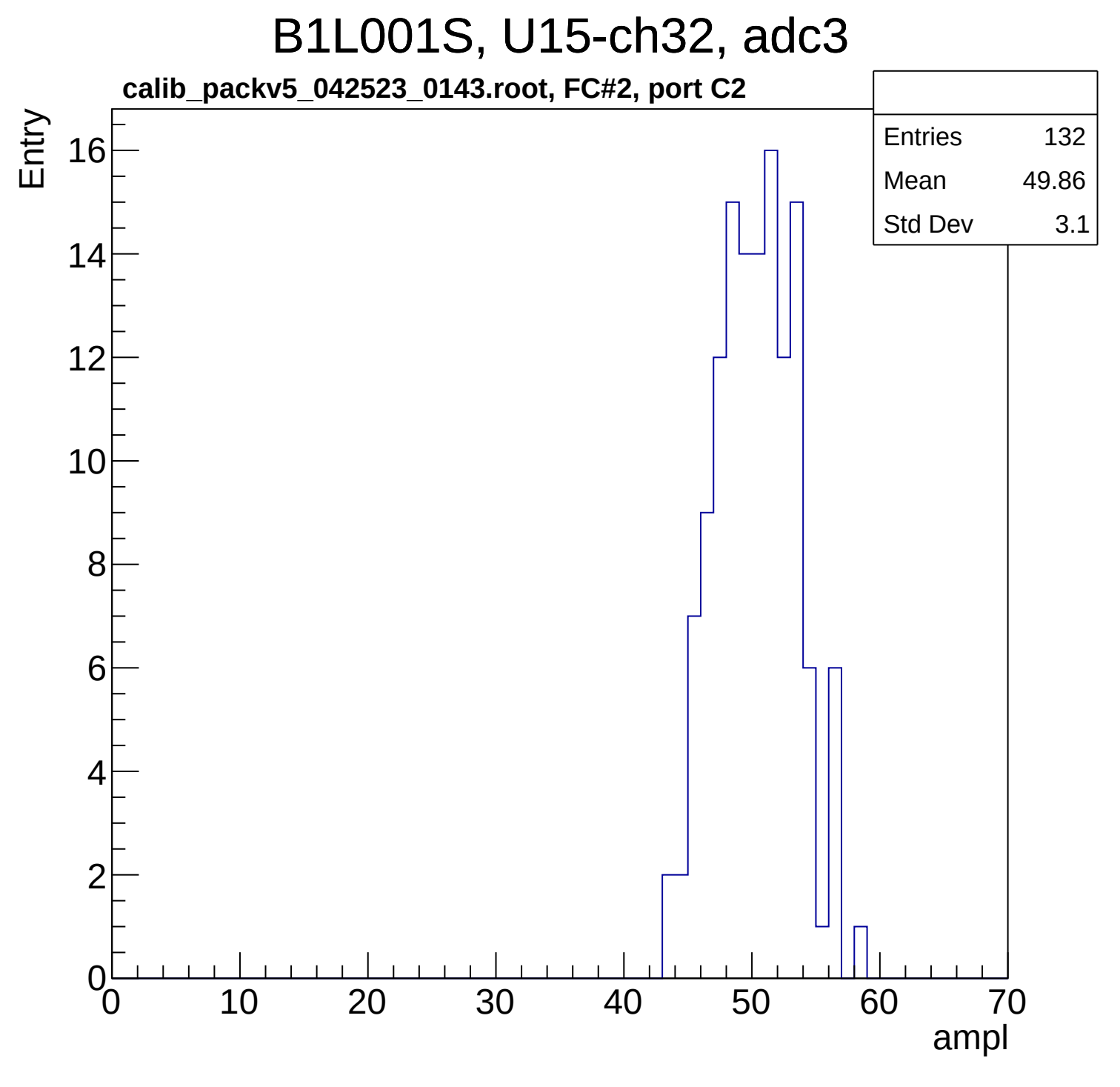
B1L001S, U15-ch32, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 49.86
Std Dev 3.1
Entry
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4
6
8
10
12
14
16
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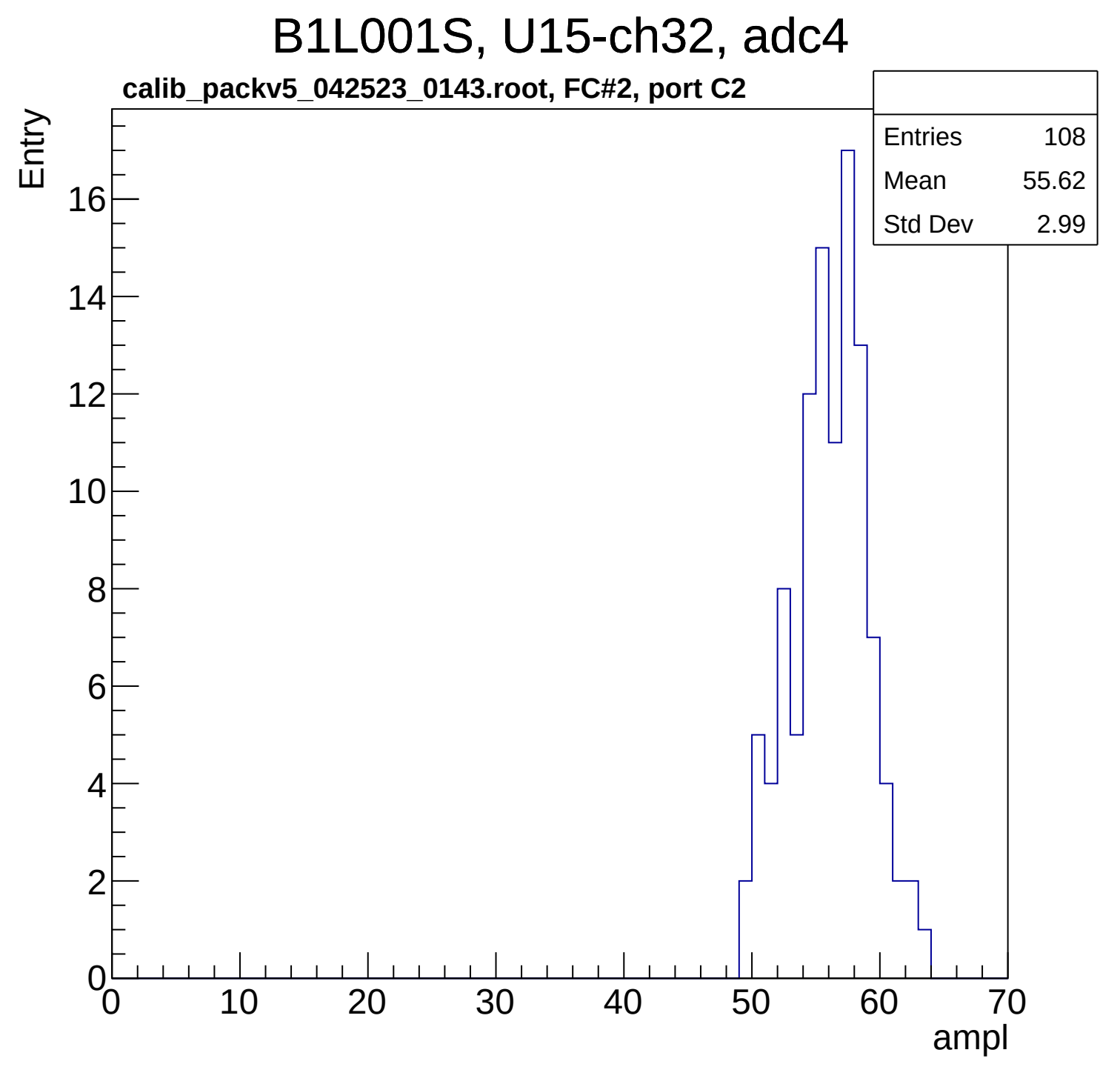
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch32, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 55.62
Std Dev 2.99
Entry
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4
6
8
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12
14
16
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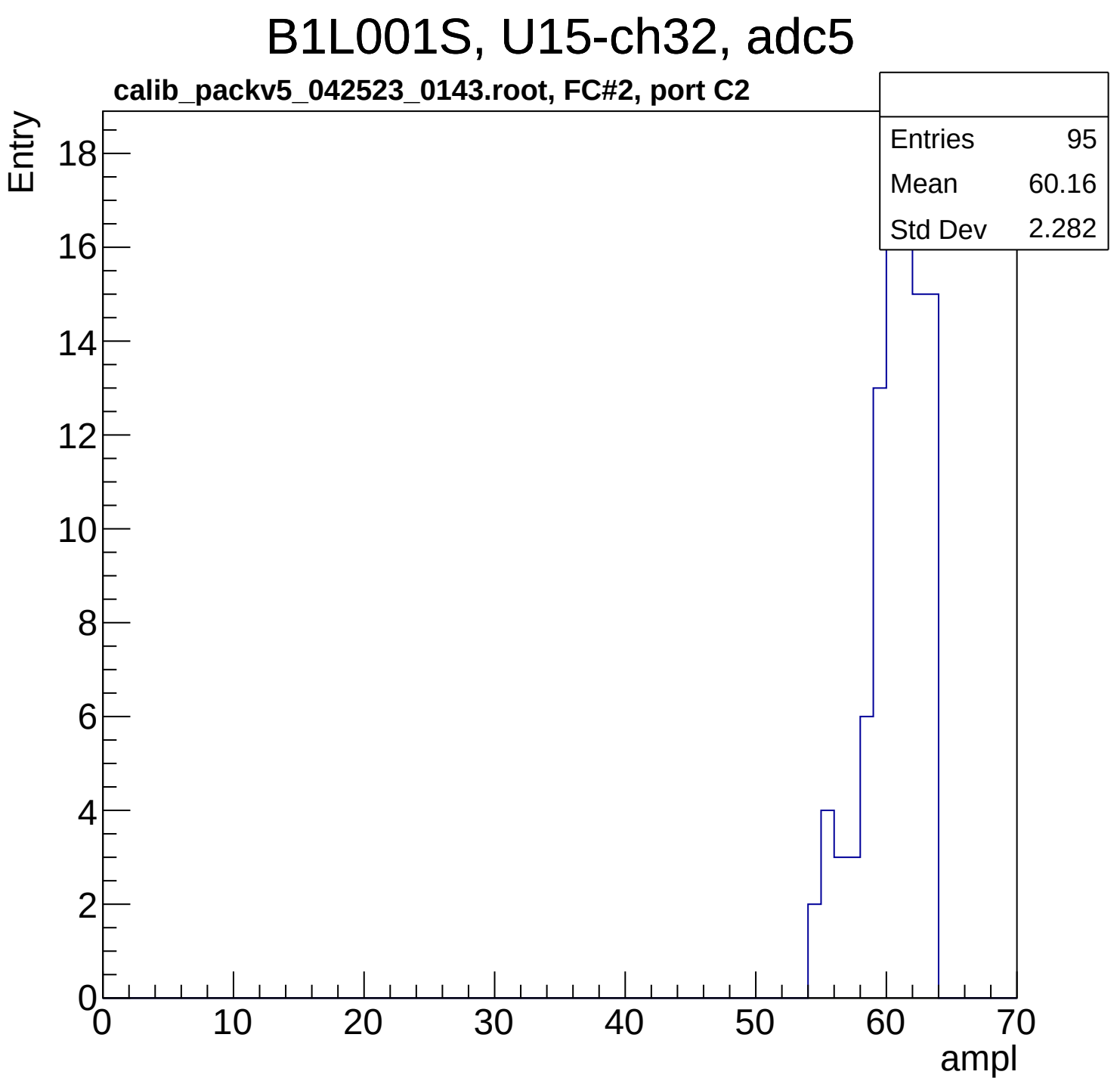
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch32, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 60.16
Std Dev 2.282
Entry
ampl
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6
8
10
12
14
16
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60
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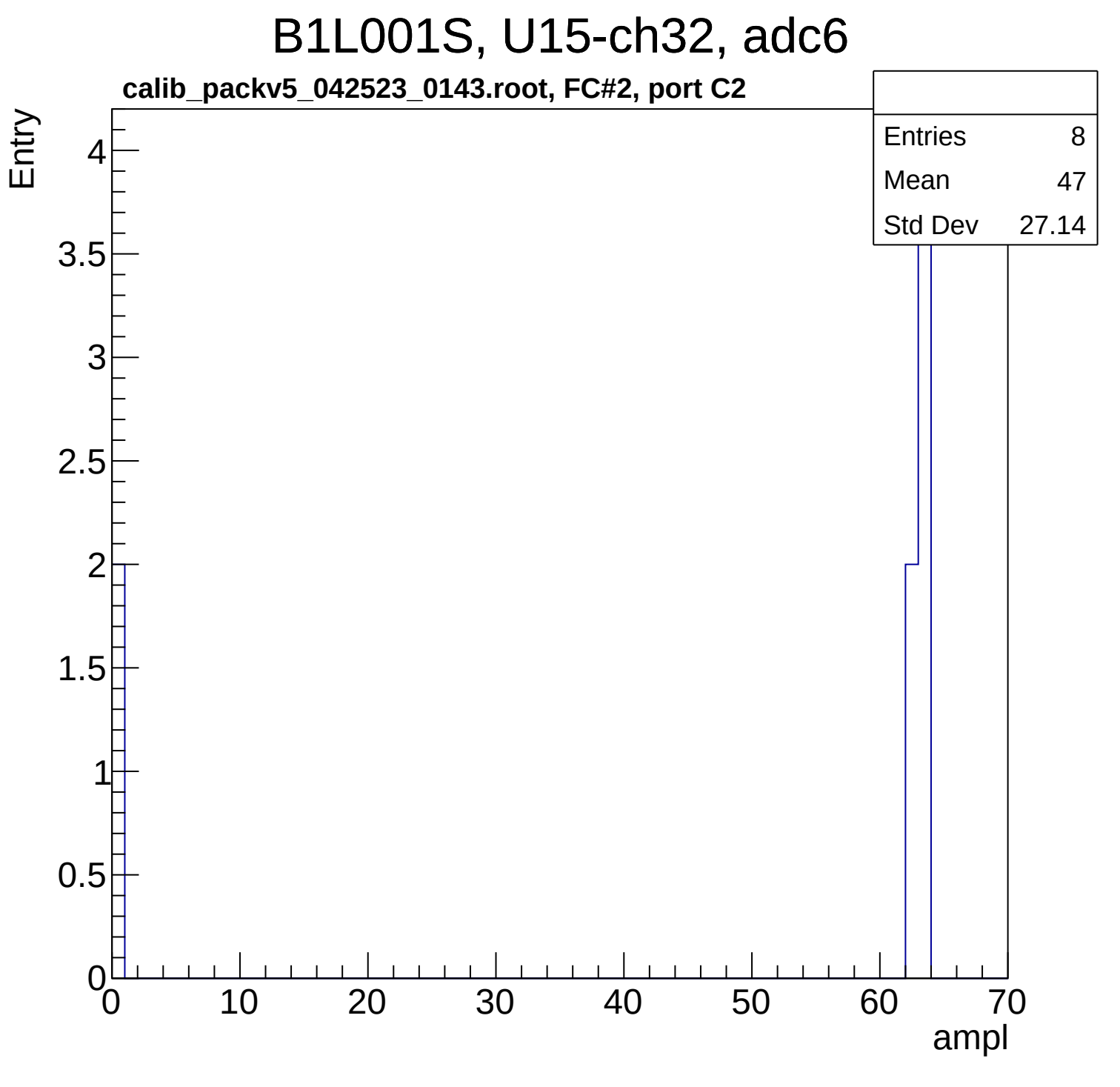
B1L001S, U15-ch32, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 47
Std Dev 27.14
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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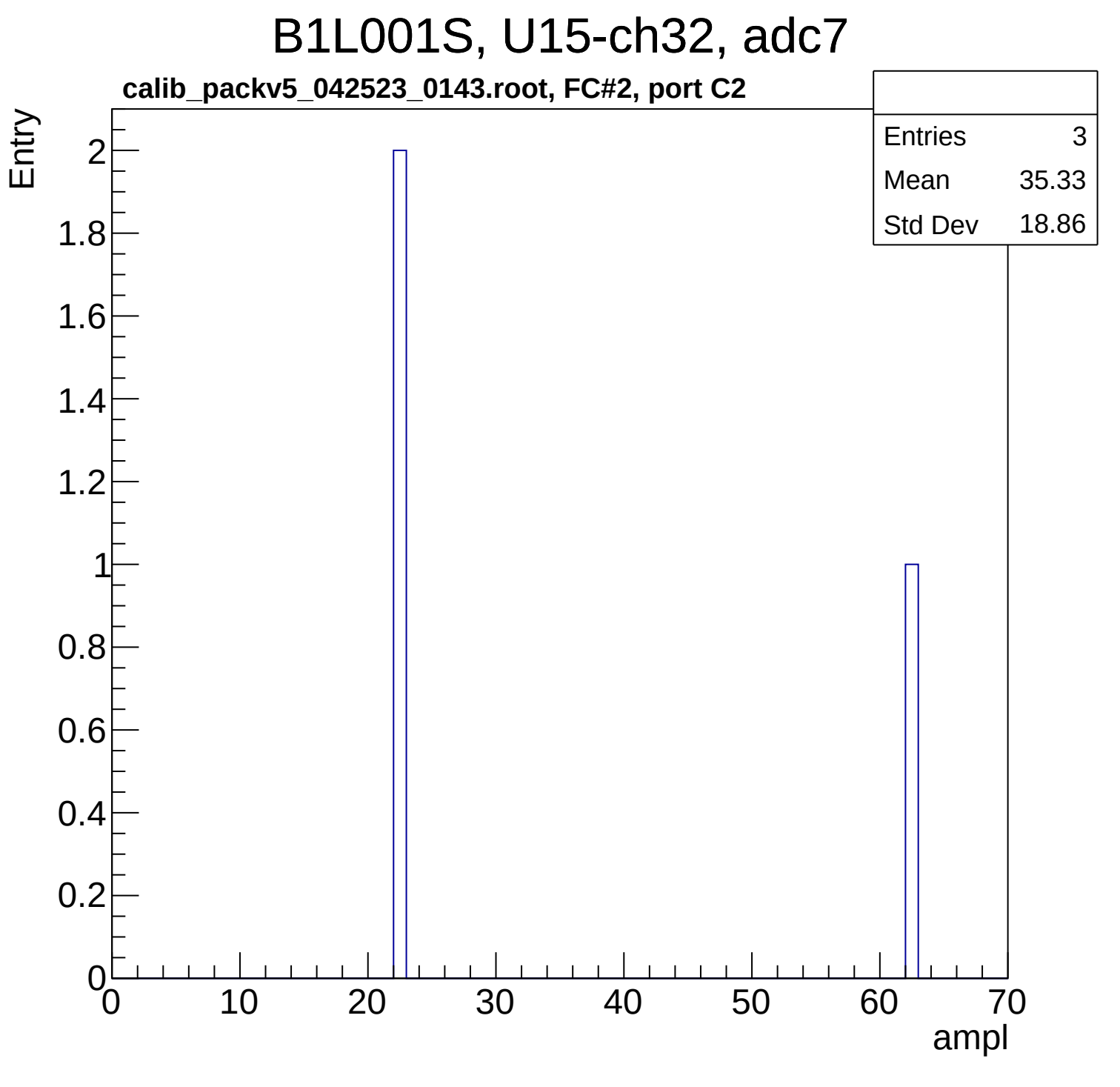
B1L001S, U15-ch32, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 35.33
Std Dev 18.86
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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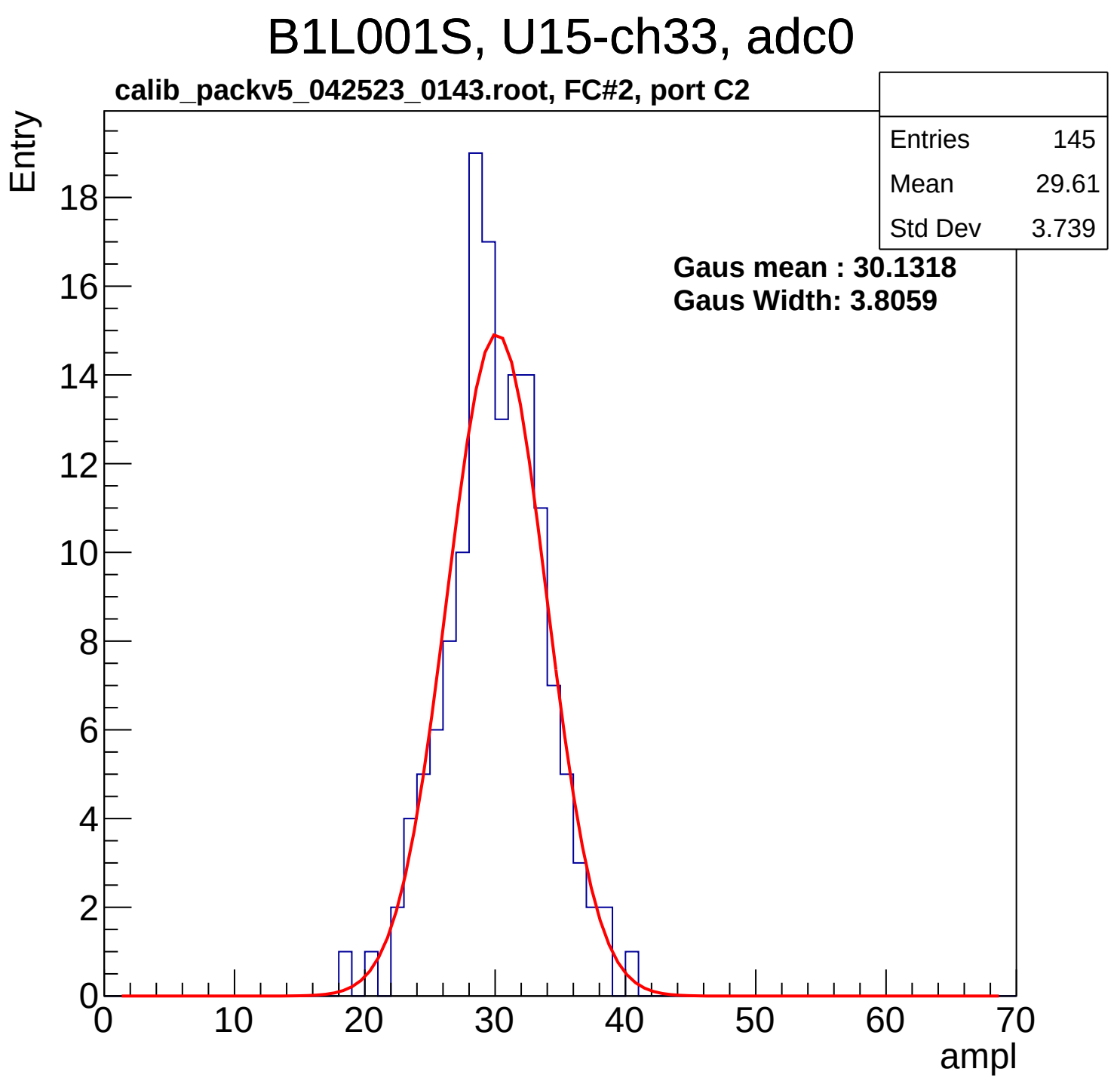
B1L001S, U15-ch33, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 29.61
Std Dev 3.739
Gaus mean : 30.1318
Gaus Width: 3.8059
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
40
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60
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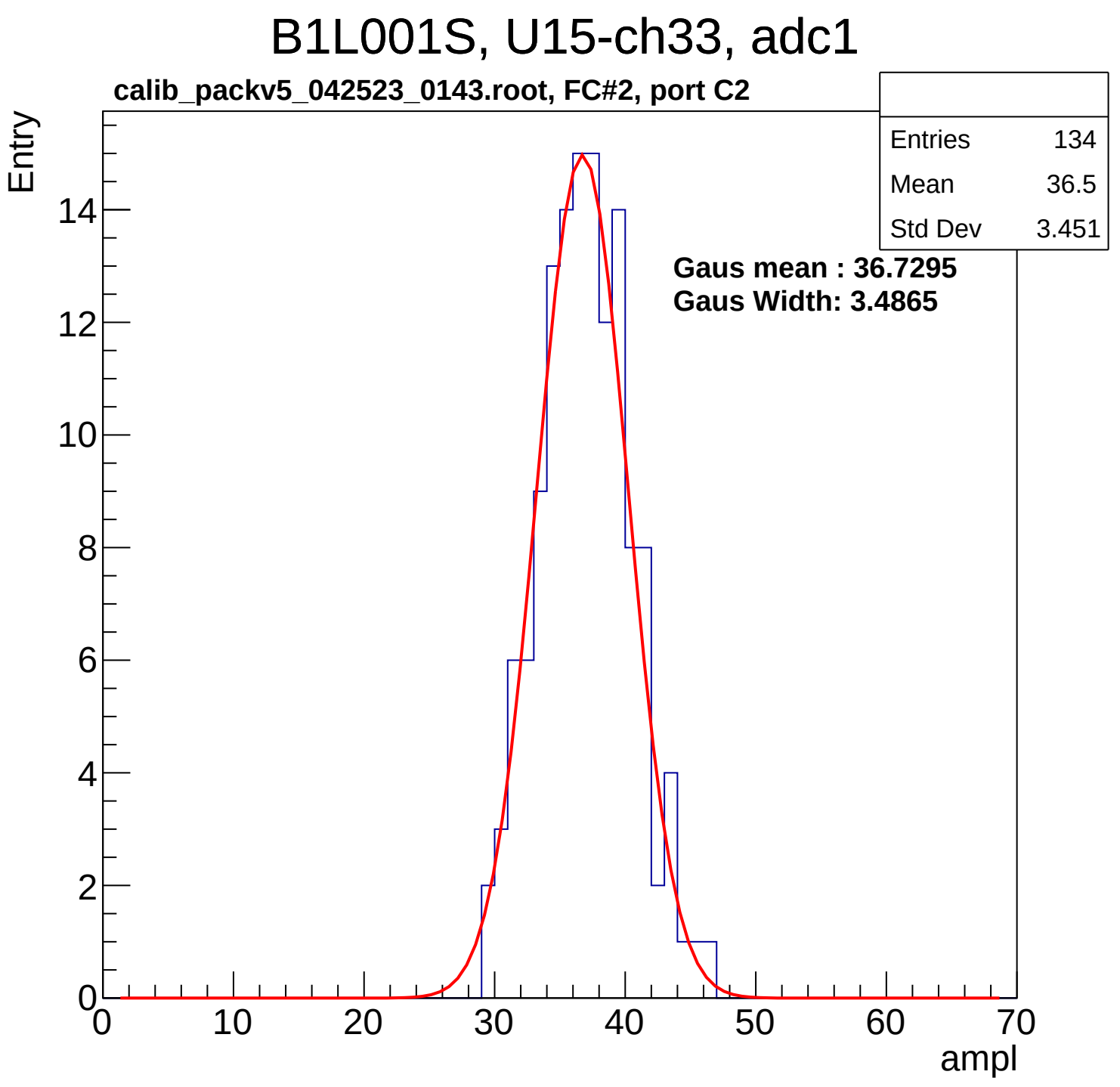
B1L001S, U15-ch33, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 36.5
Std Dev 3.451
Gaus mean : 36.7295
Gaus Width: 3.4865
Entry
ampl
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0
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60
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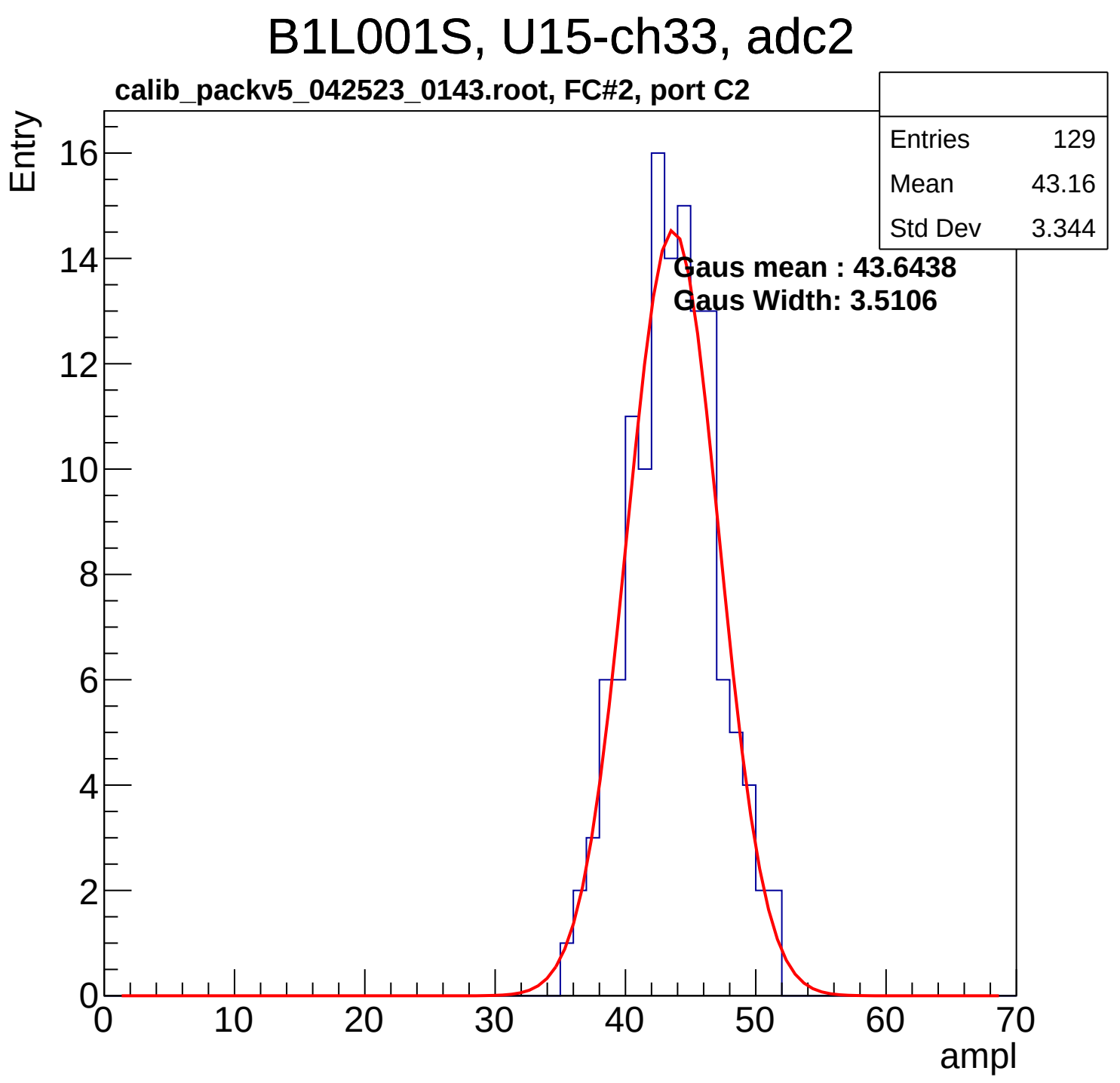
B1L001S, U15-ch33, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 43.16
Std Dev 3.344
Gaus mean : 43.6438
Gaus Width: 3.5106
Entry
ampl
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2
4
6
8
10
12
14
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0
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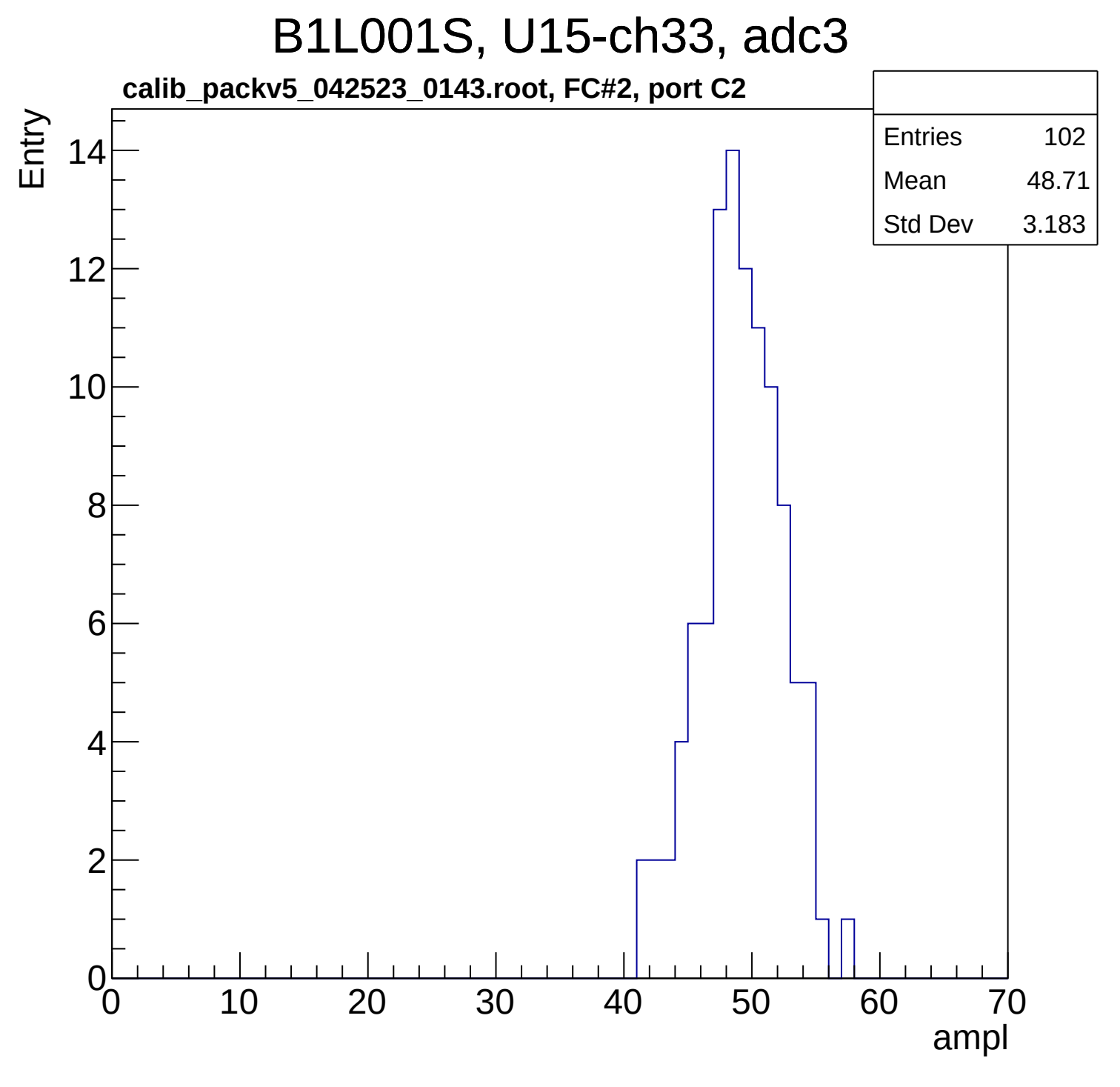
B1L001S, U15-ch33, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 48.71
Std Dev 3.183
Entry
ampl
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8
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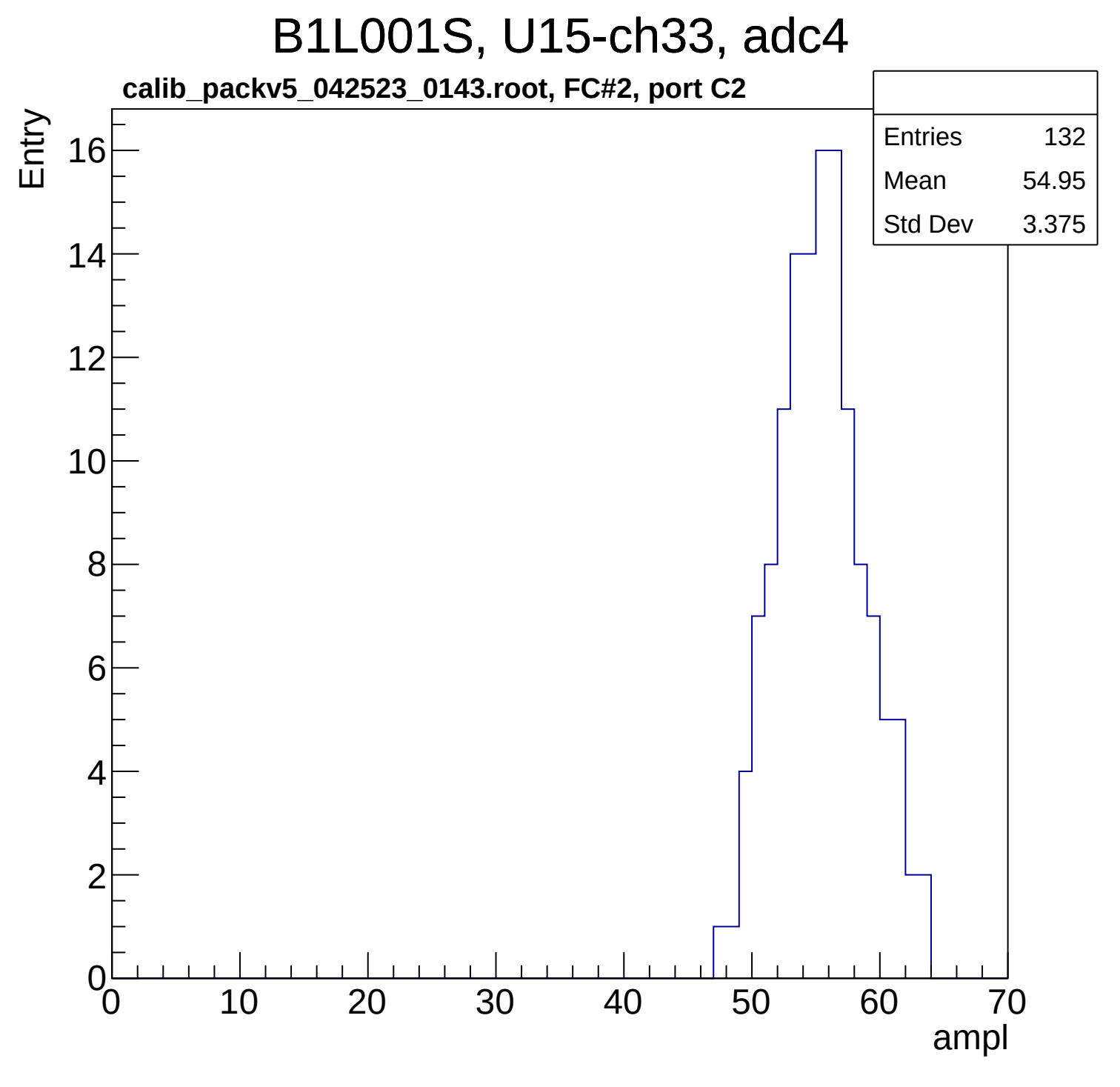
B1L001S, U15-ch33, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 54.95
Std Dev 3.375
Entry
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4
6
8
10
12
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16
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10
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50
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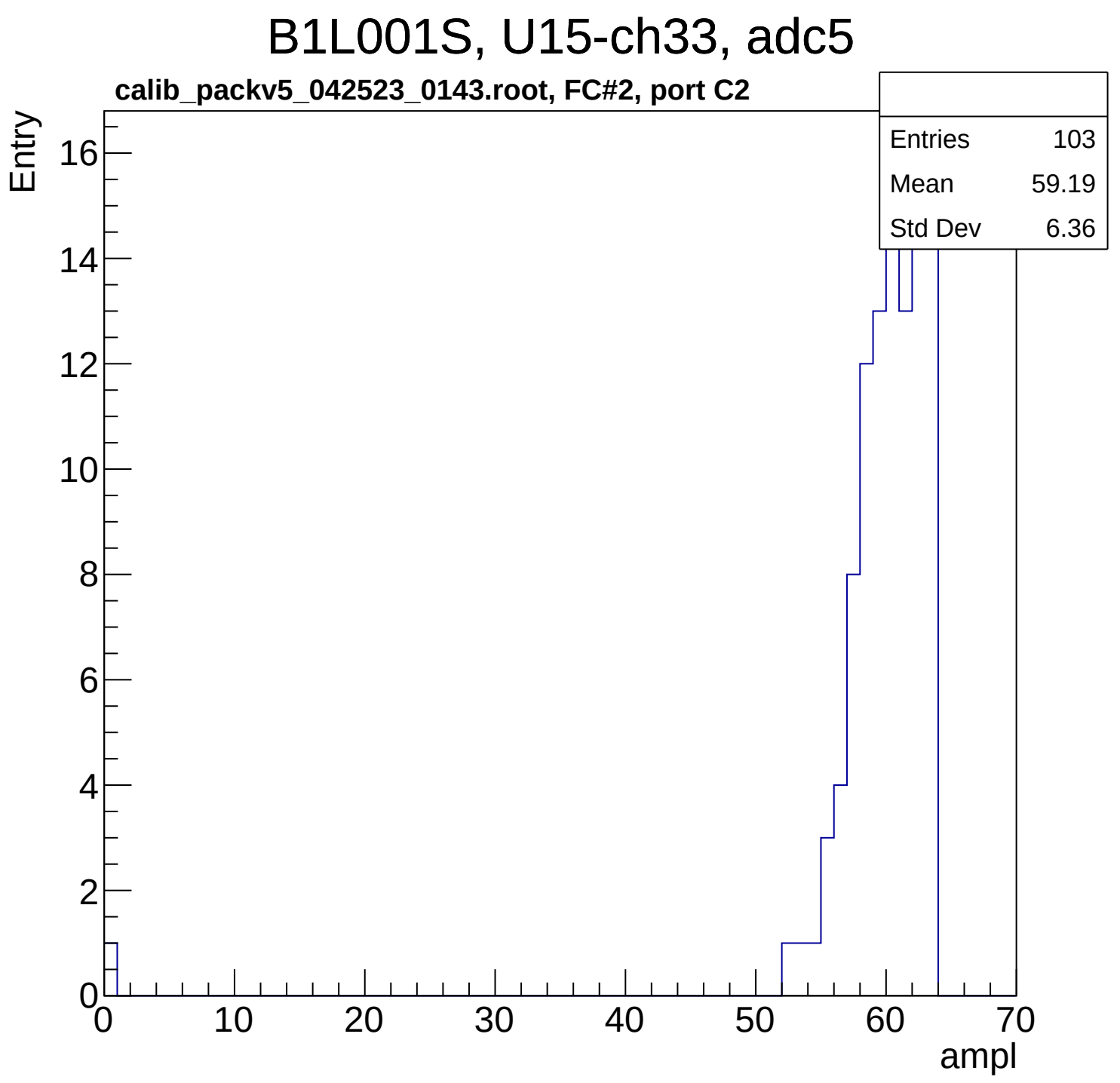
70
ampl


B1L001S, U15-ch33, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 59.19
Std Dev 6.36
Entry
ampl
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8
10
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0
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20
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60
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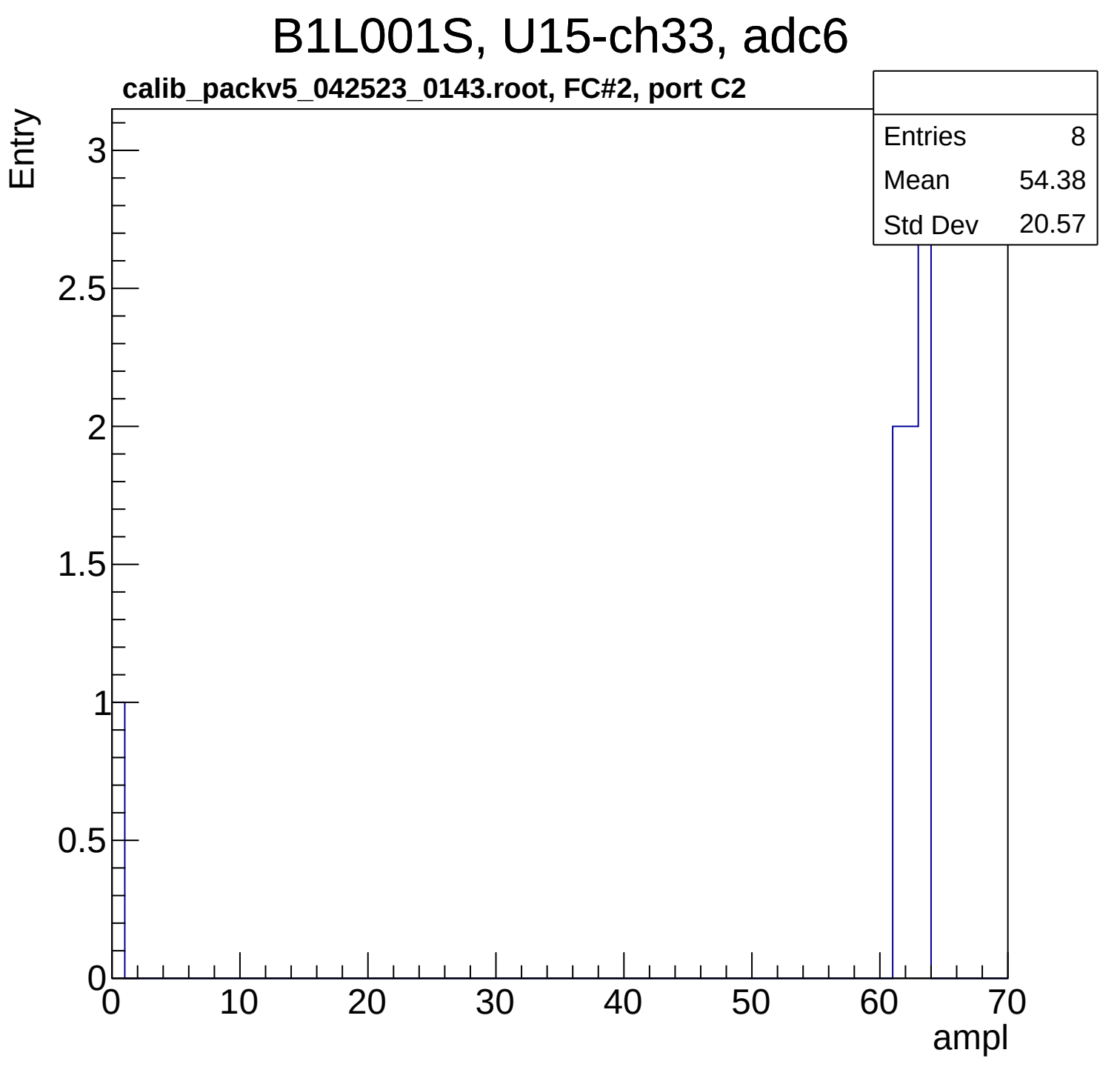
B1L001S, U15-ch33, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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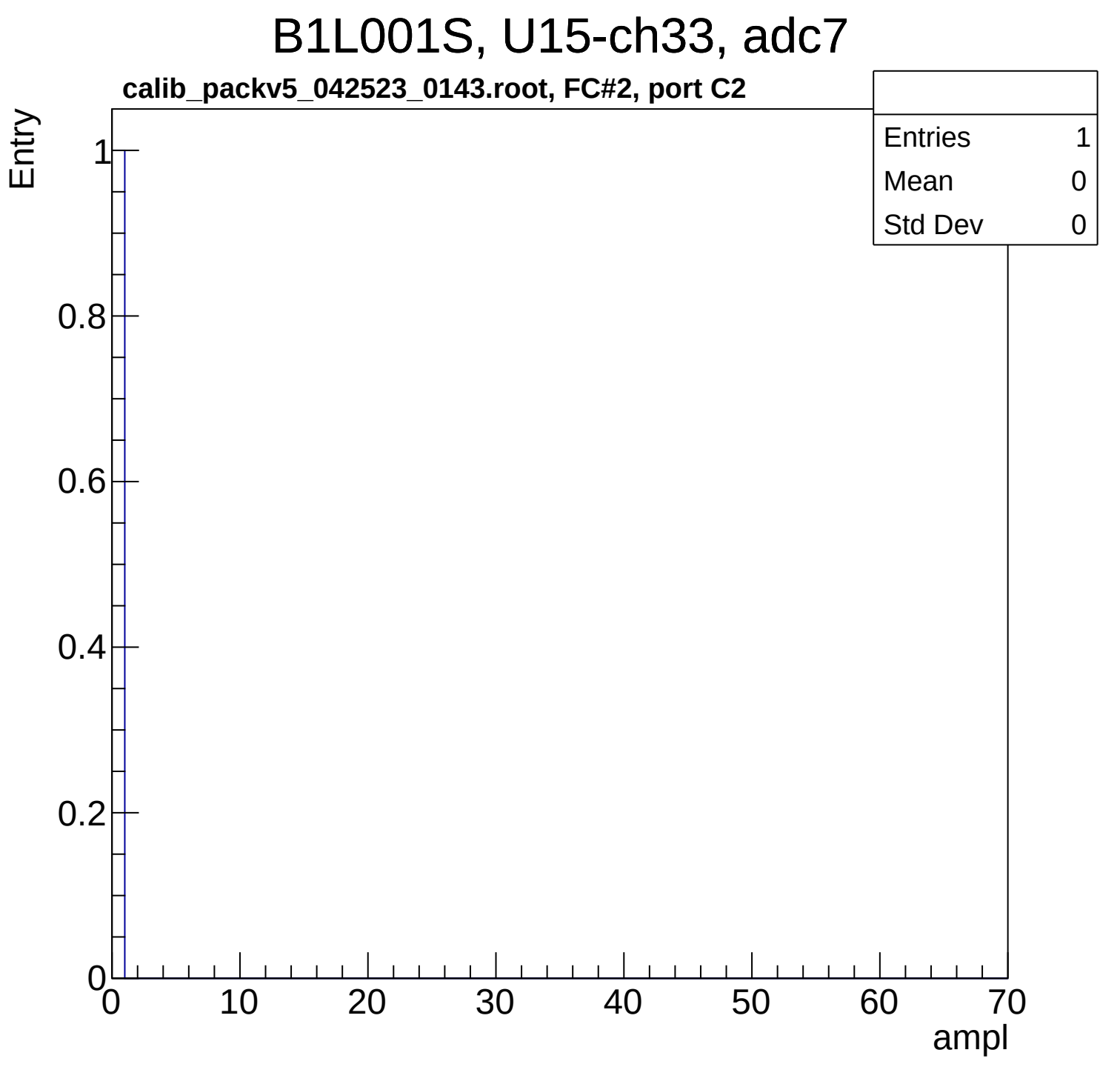
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch33, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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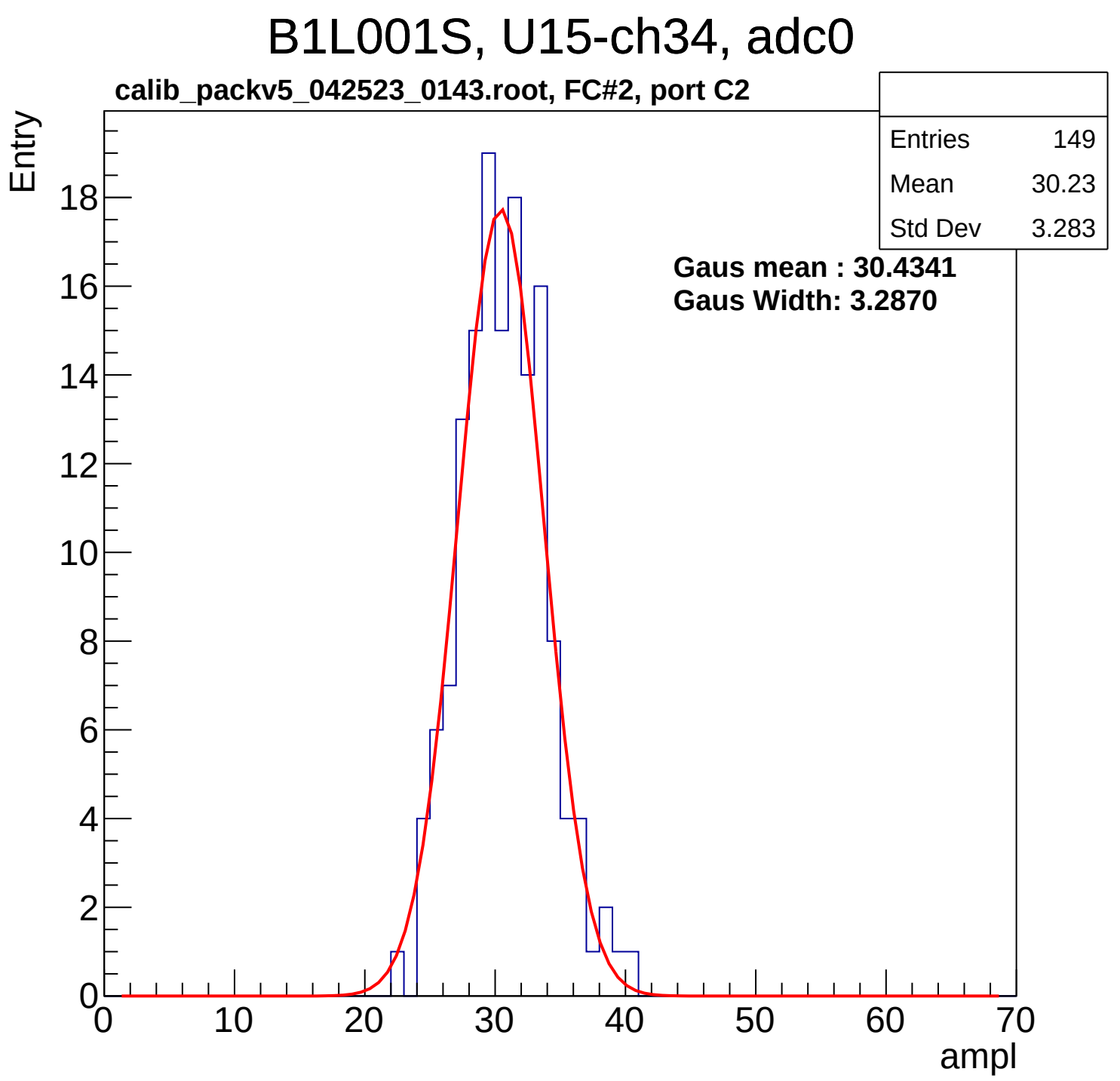
70
ampl


B1L001S, U15-ch34, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 30.23
Std Dev 3.283
Gaus mean : 30.4341
Gaus Width: 3.2870
Entry
ampl
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4
6
8
10
12
14
16
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0
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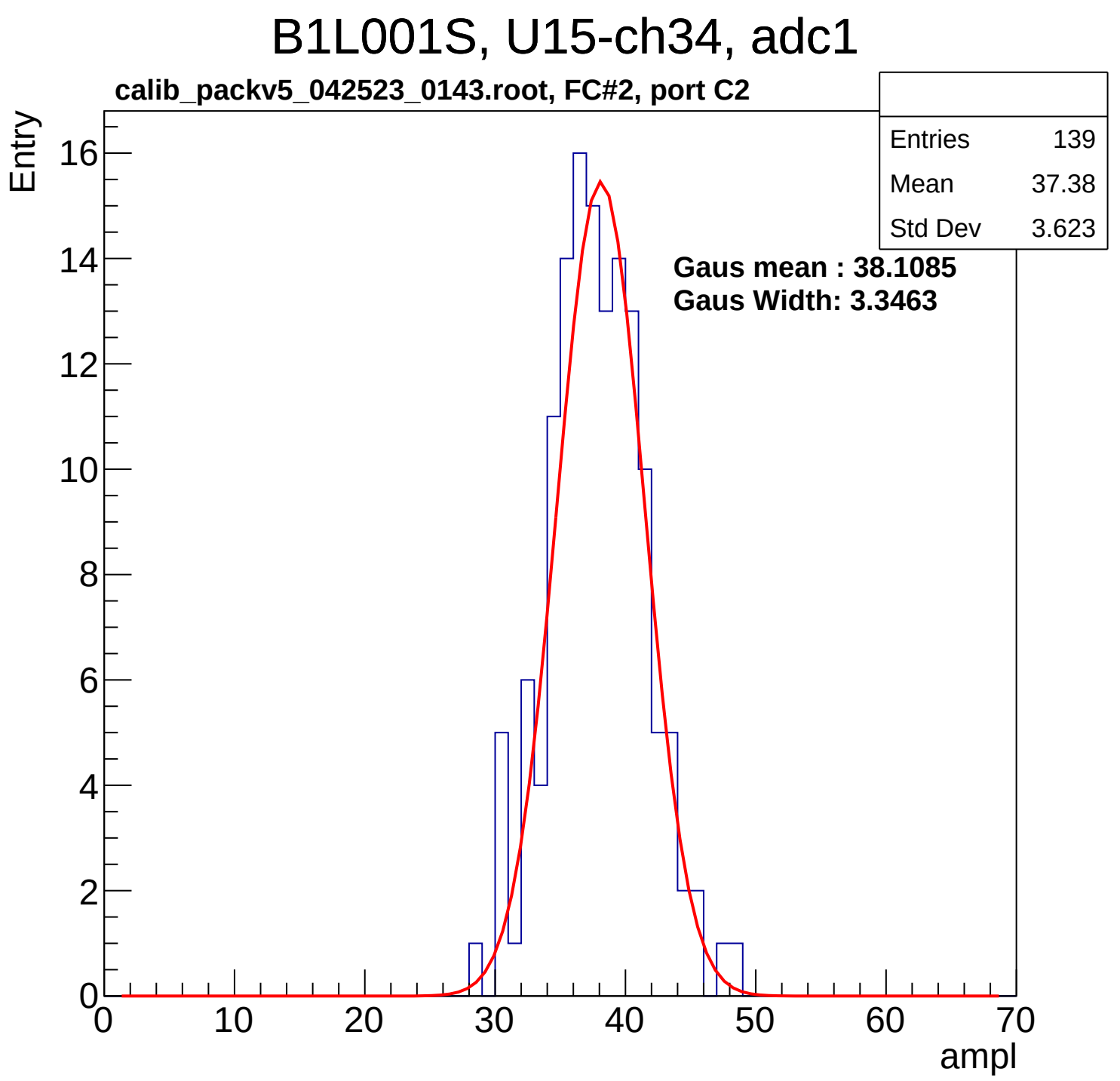
B1L001S, U15-ch34, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 37.38
Std Dev 3.623
Gaus mean : 38.1085
Gaus Width: 3.3463
Entry
ampl
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4
6
8
10
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14
16
0
10
20
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40
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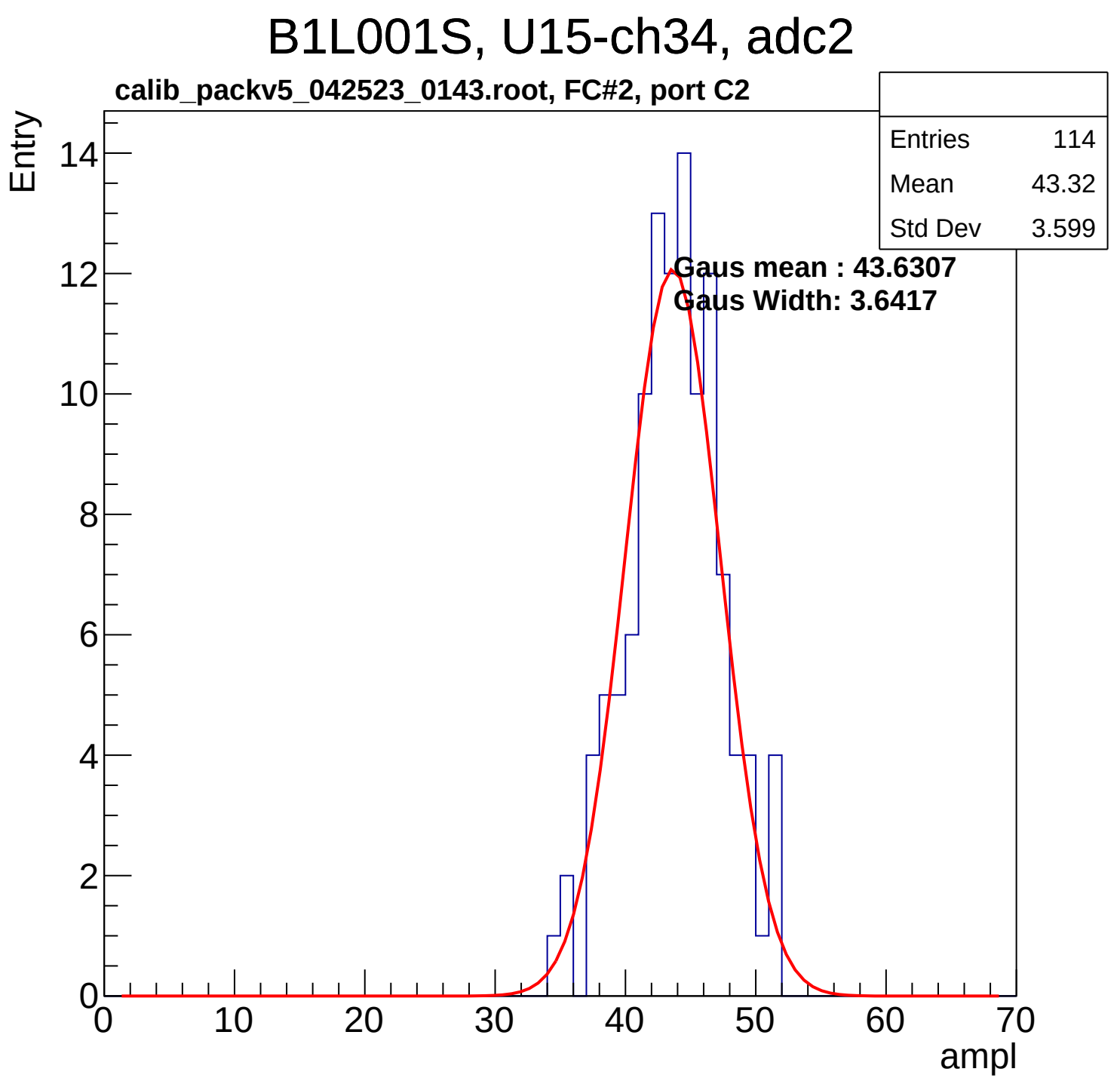
B1L001S, U15-ch34, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 43.32
Std Dev 3.599
Gaus mean : 43.6307
Gaus Width: 3.6417
Entry
ampl
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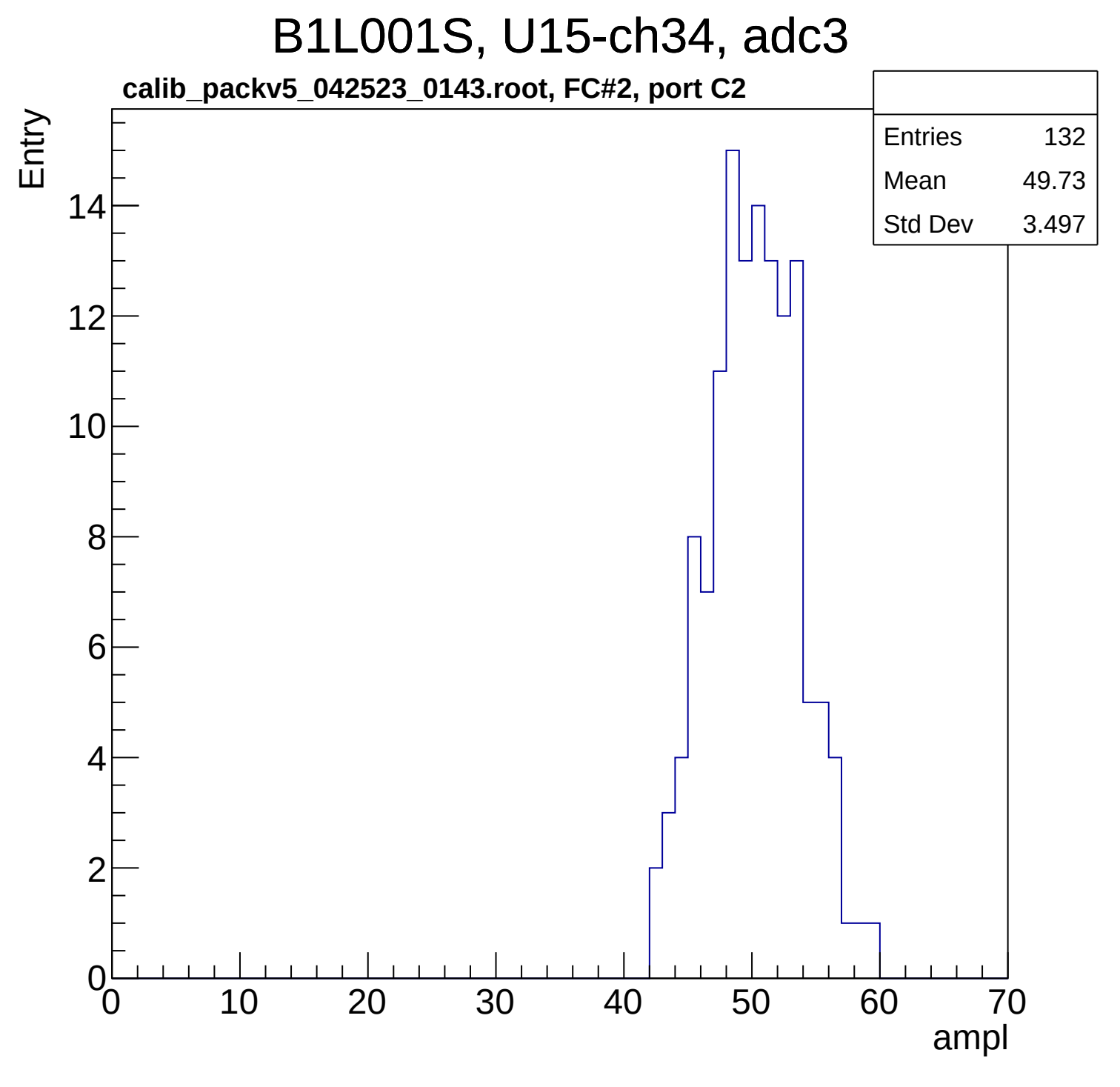
B1L001S, U15-ch34, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 49.73
Std Dev 3.497
Entry
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6
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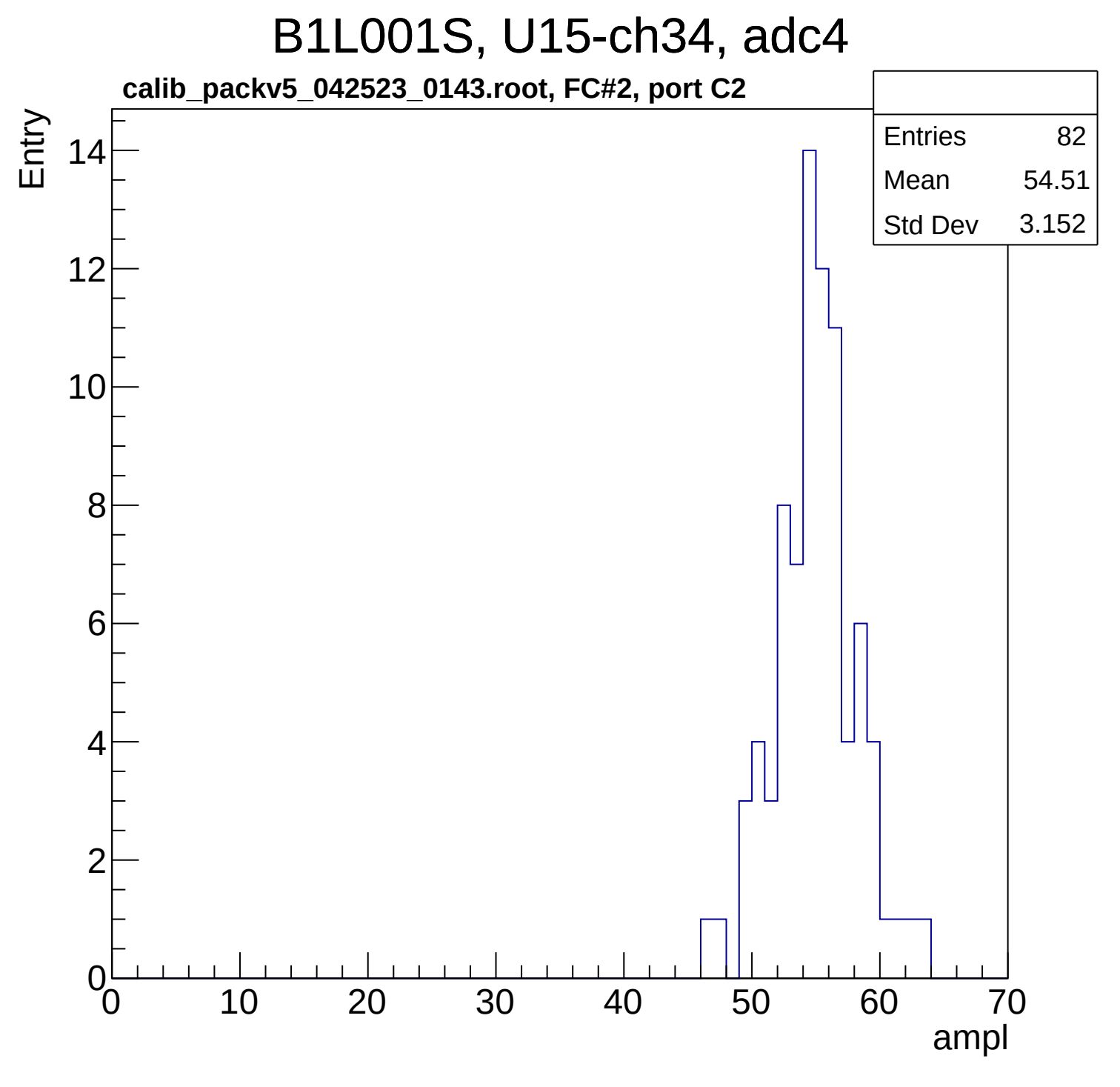
70
ampl


B1L001S, U15-ch34, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 54.51
Std Dev 3.152
Entry
ampl
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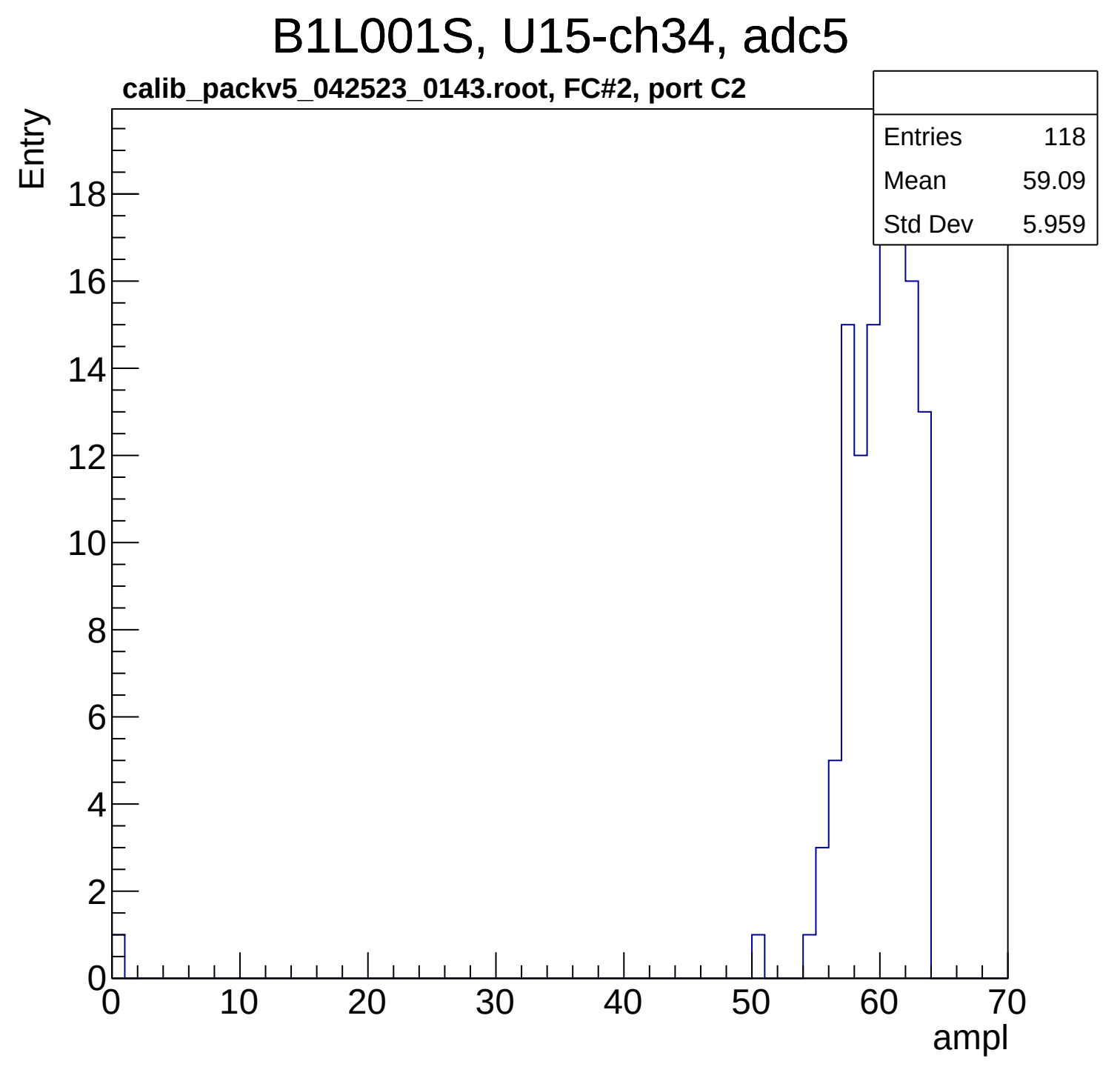
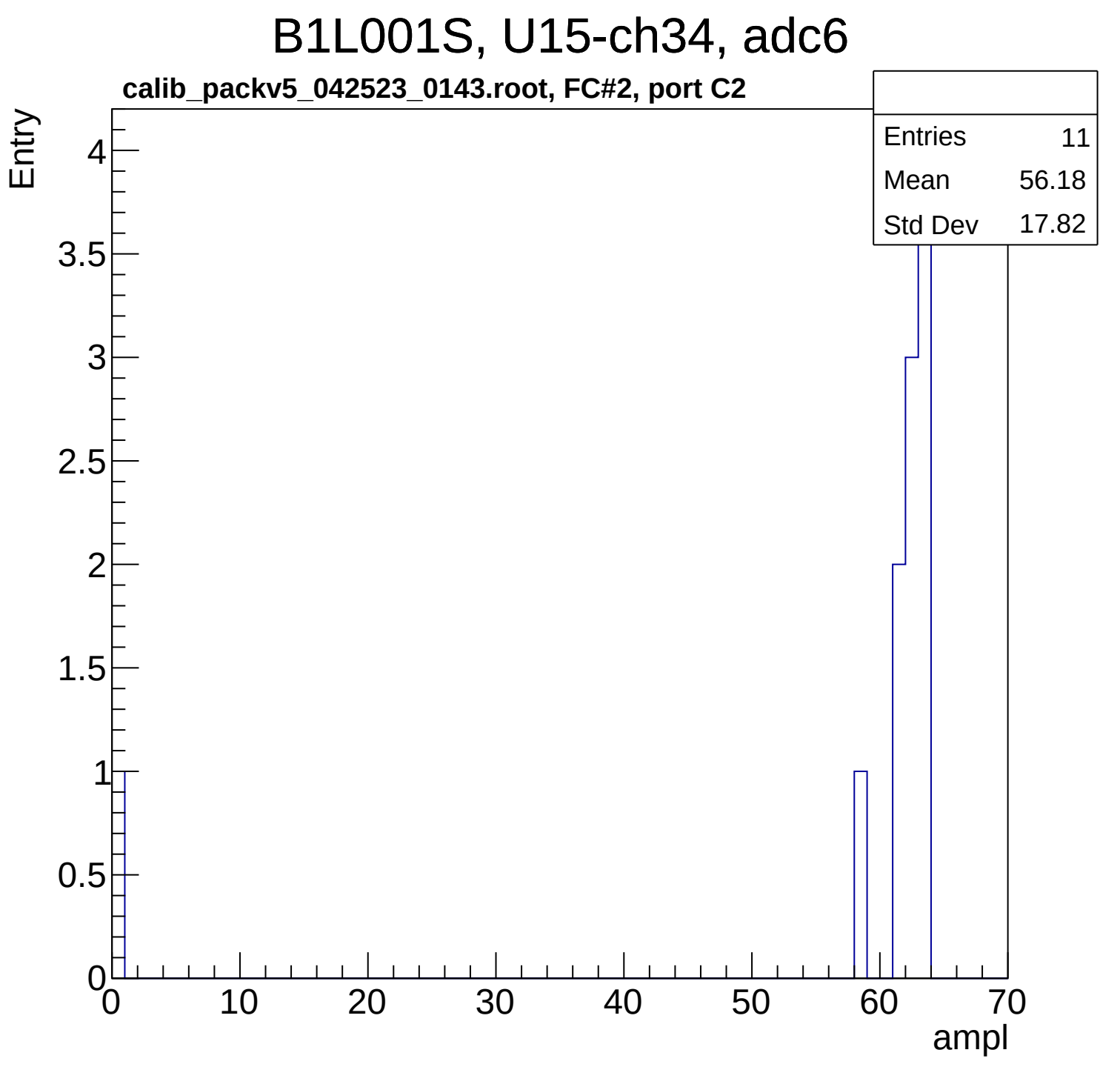

B1L001S, U15-ch34, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 59.09
Std Dev 5.959
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch34, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 56.18
Std Dev 17.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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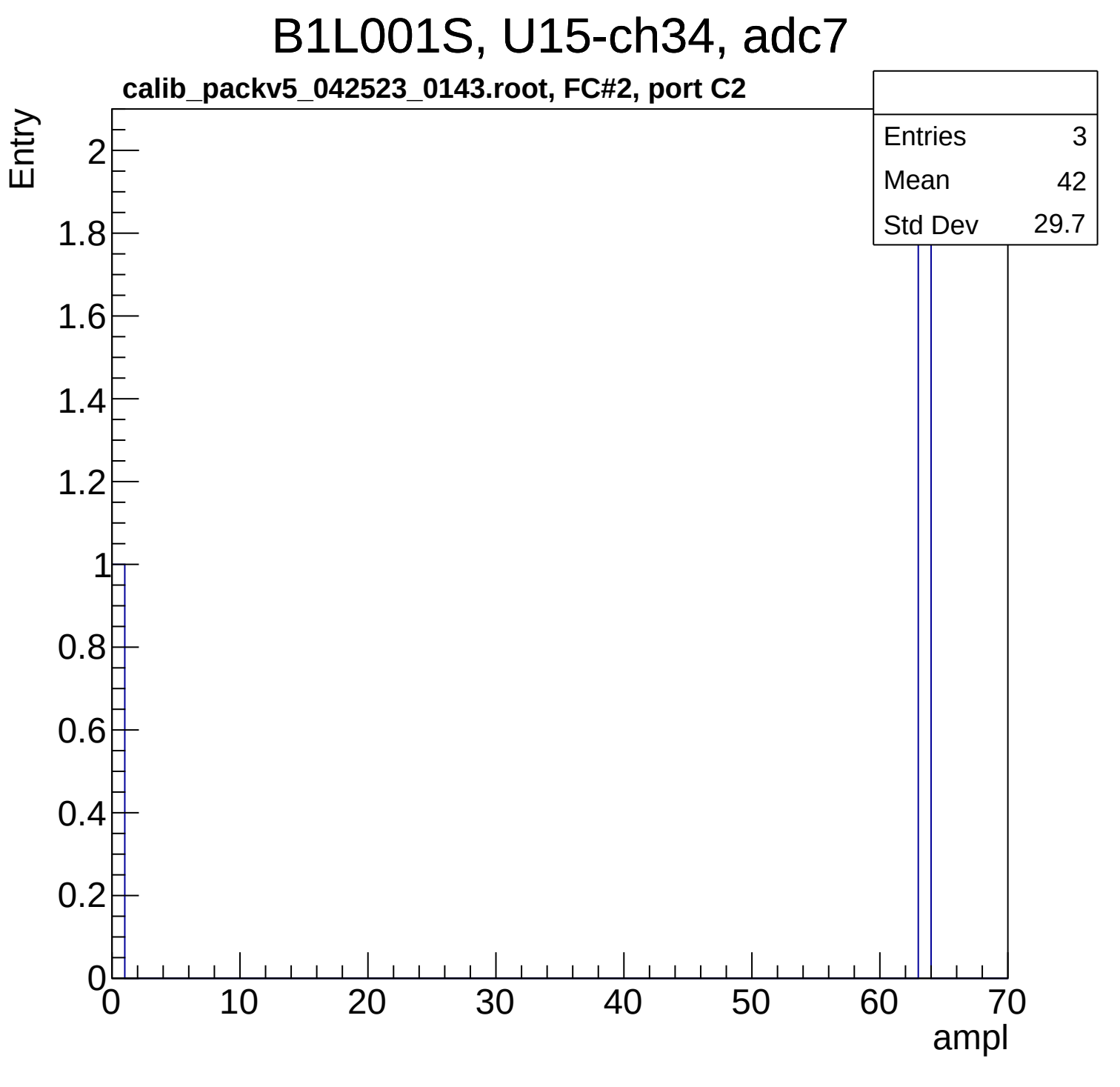
B1L001S, U15-ch34, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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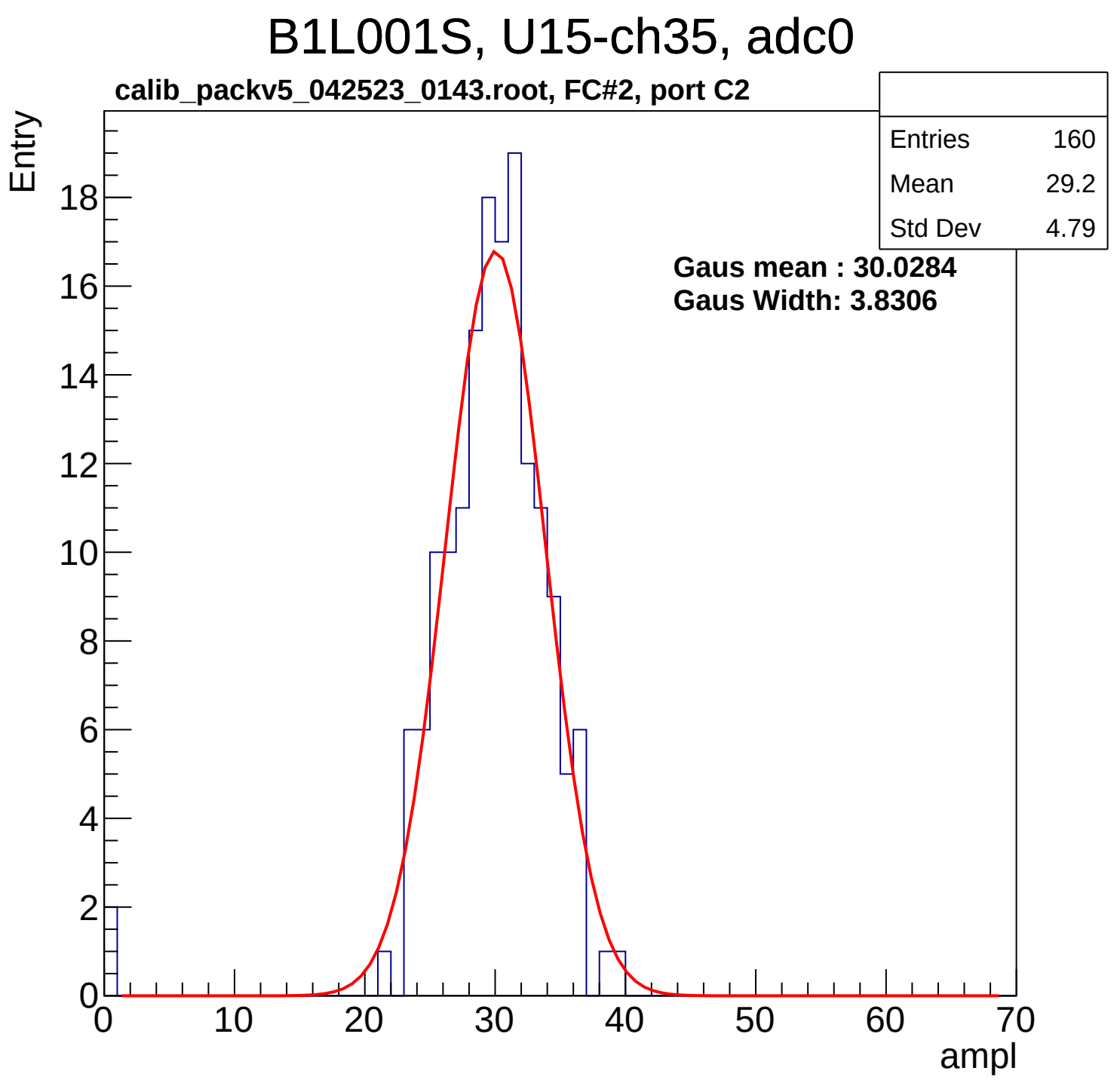
B1L001S, U15-ch35, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 29.2
Std Dev 4.79
Gaus mean : 30.0284
Gaus Width: 3.8306
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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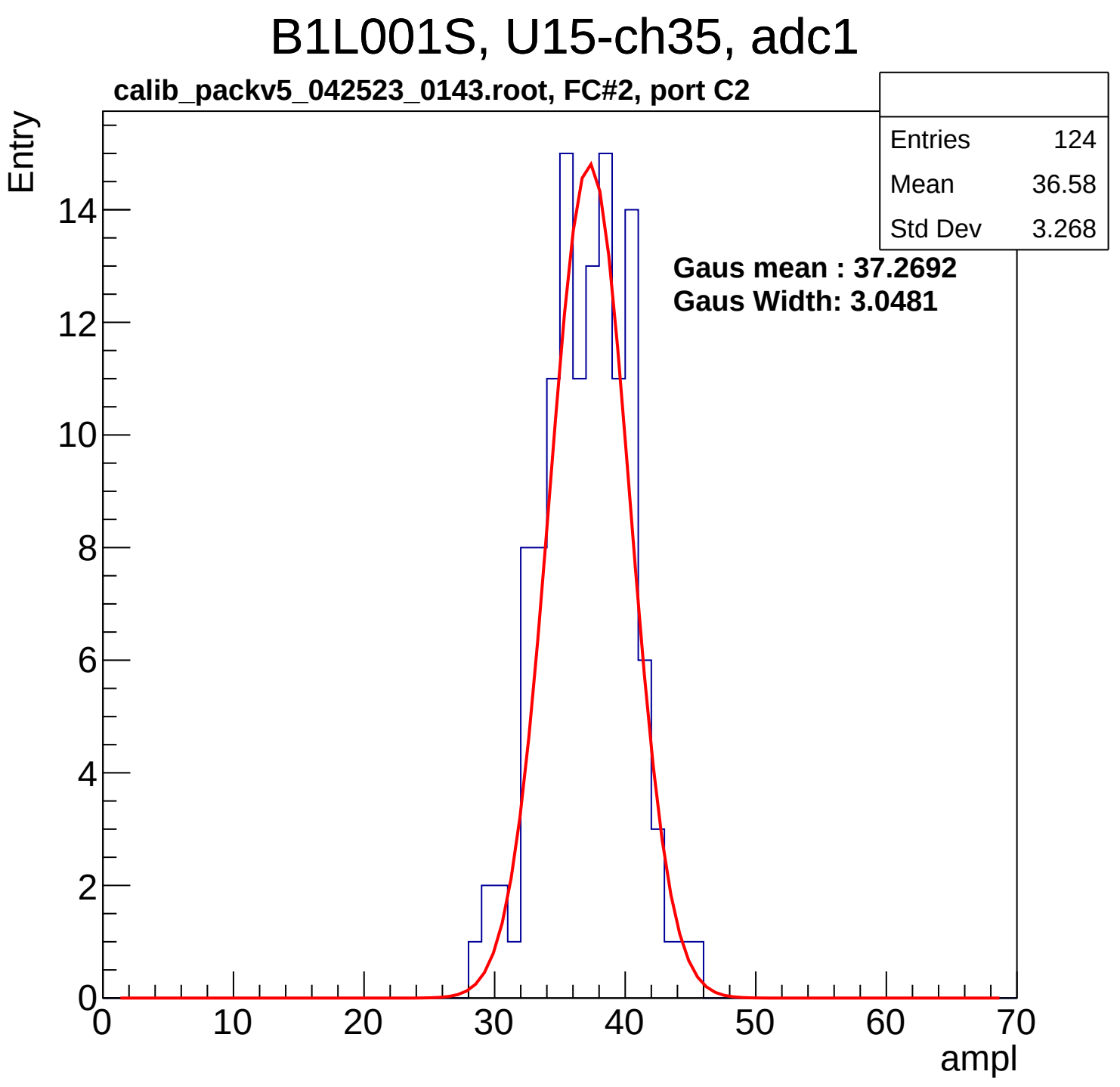
B1L001S, U15-ch35, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 36.58
Std Dev 3.268
Gaus mean : 37.2692
Gaus Width: 3.0481
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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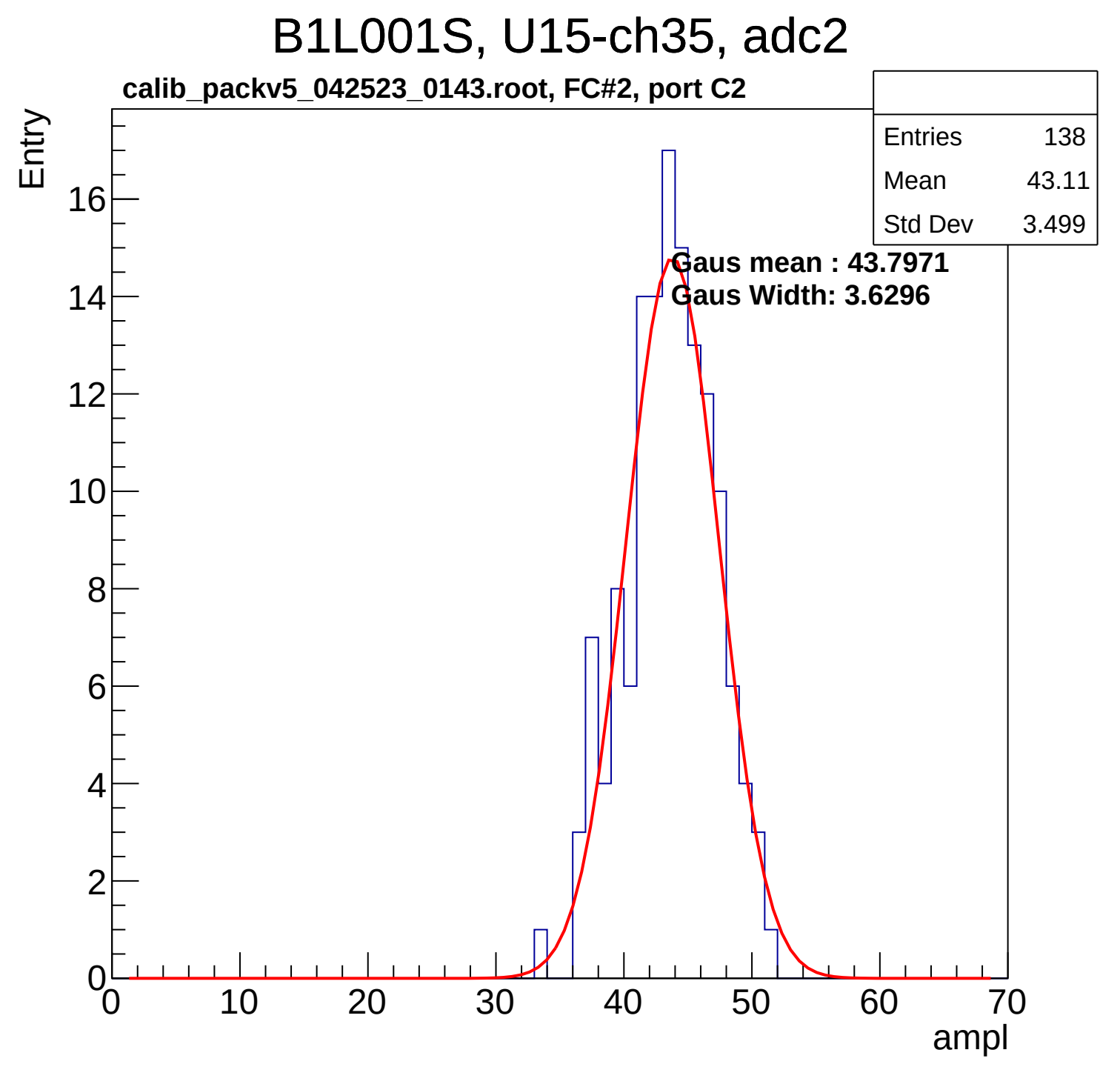
B1L001S, U15-ch35, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.11
Std Dev 3.499
Gaus mean : 43.7971
Gaus Width: 3.6296
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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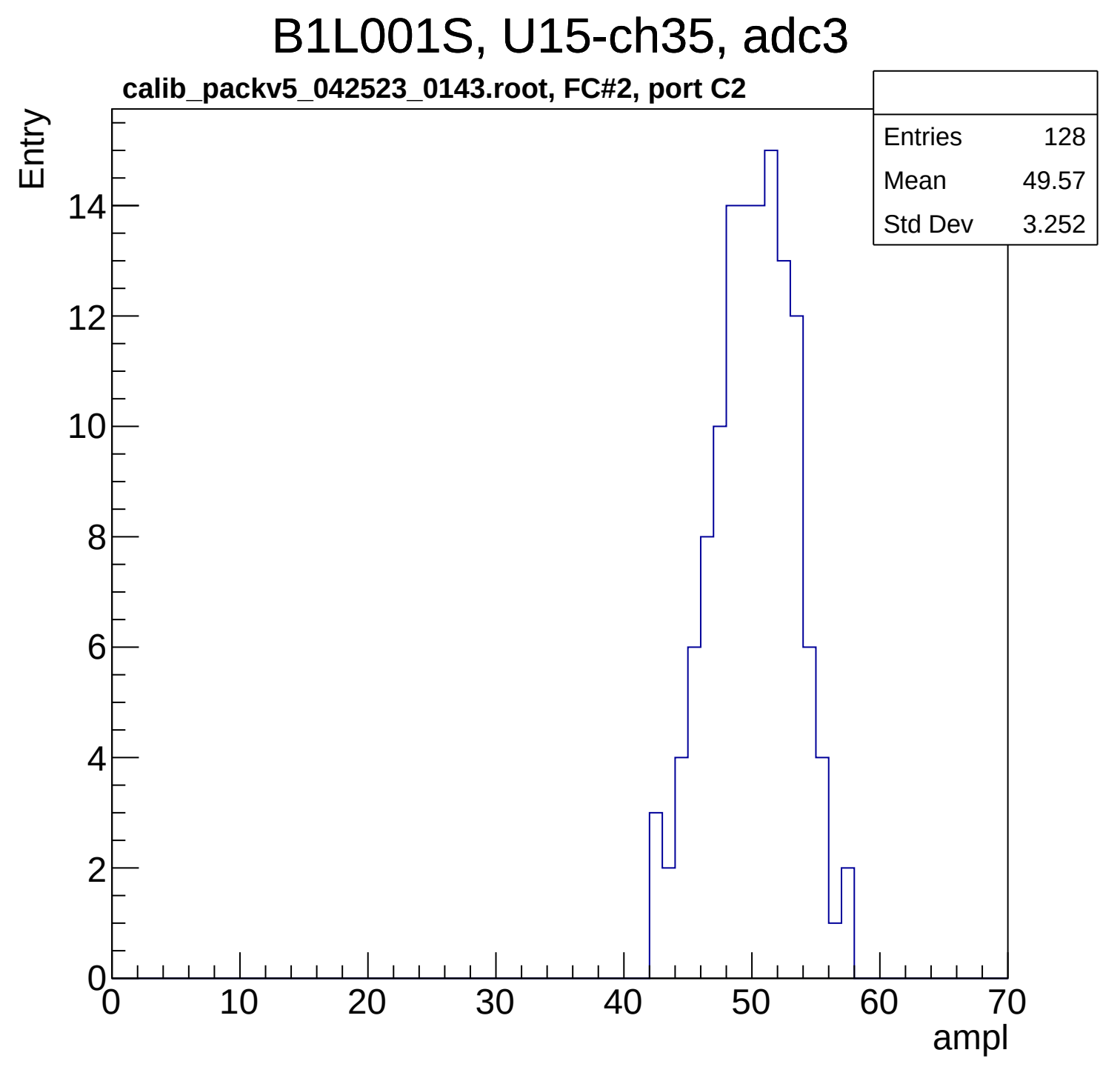
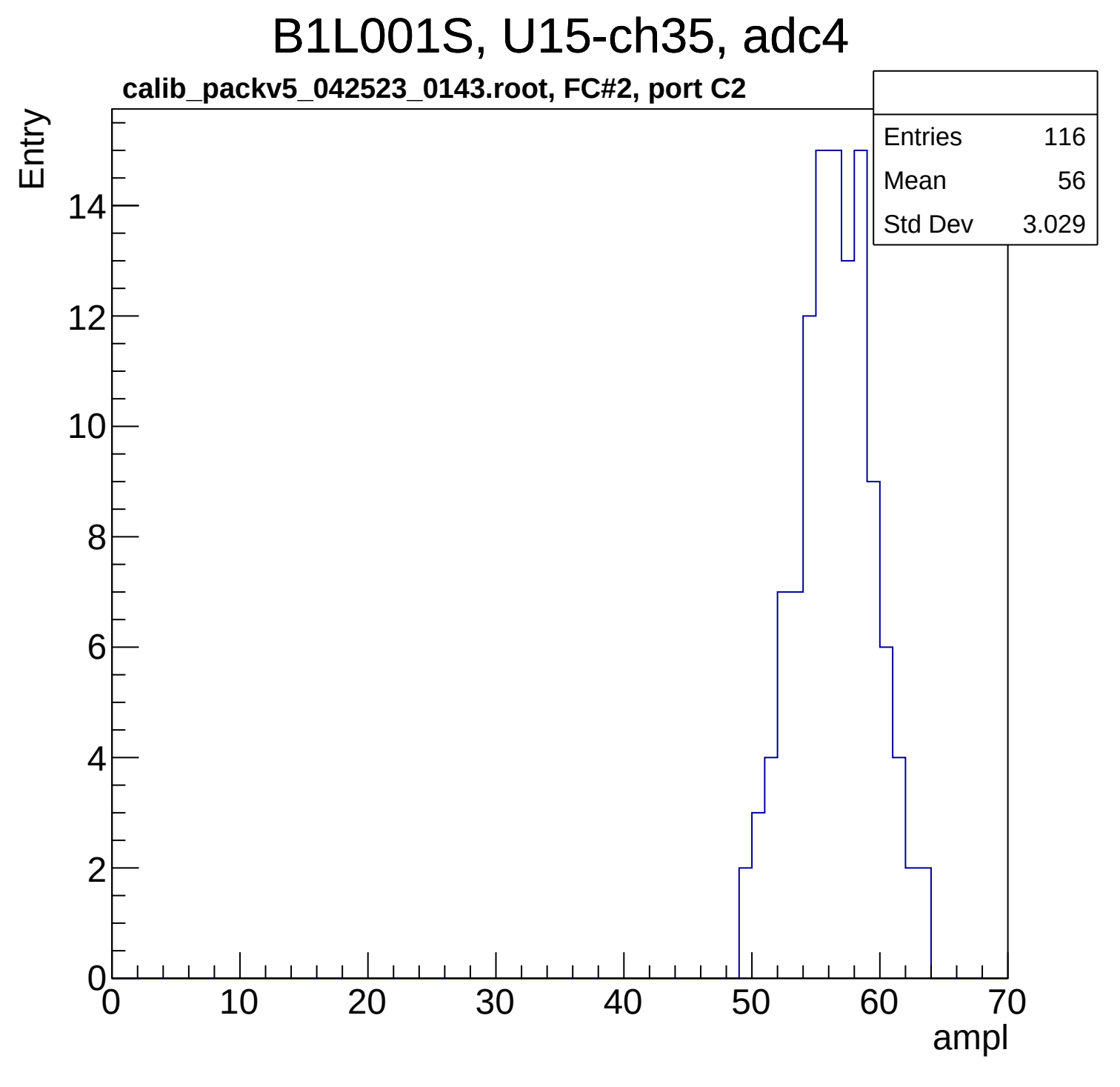
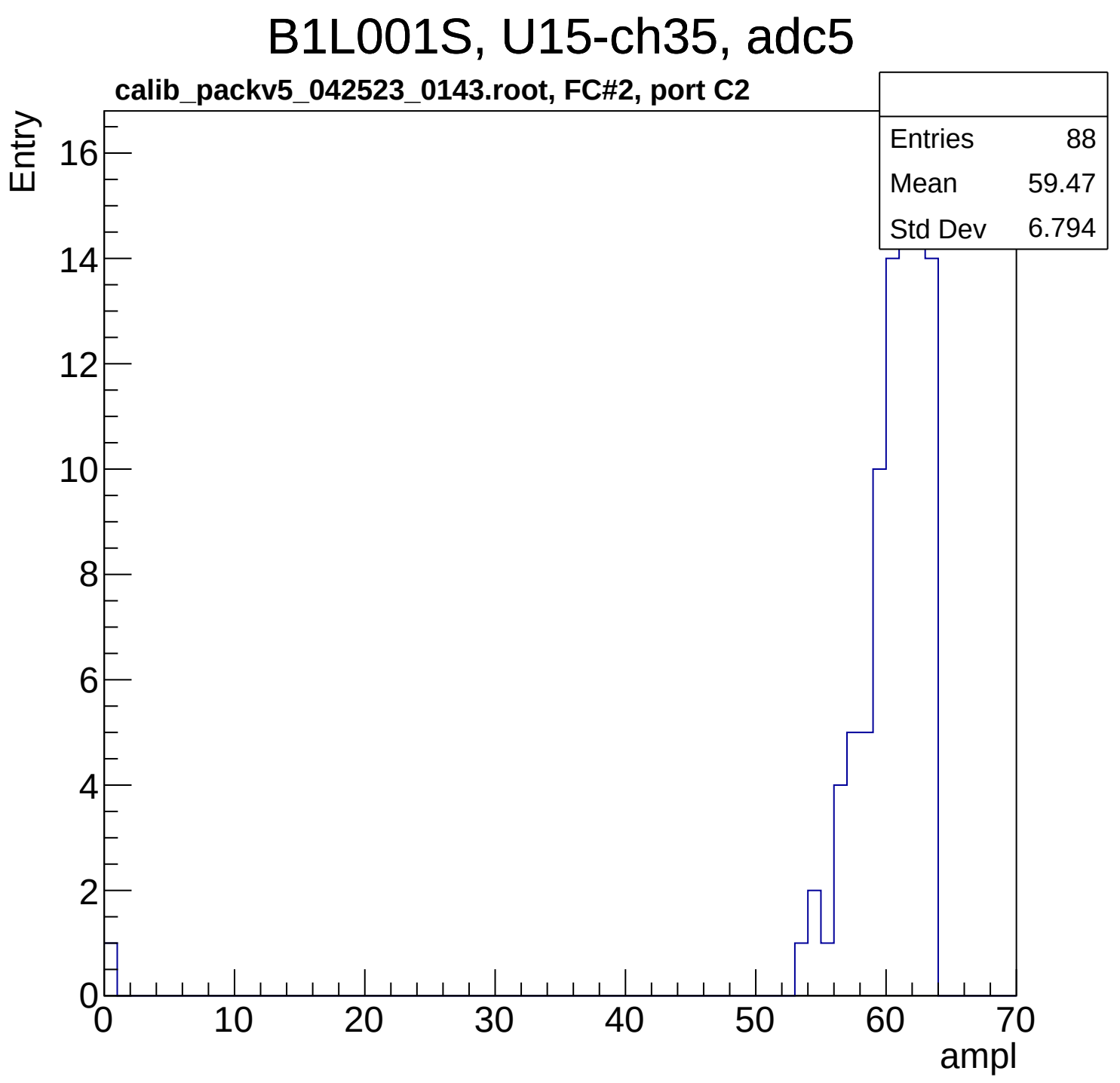
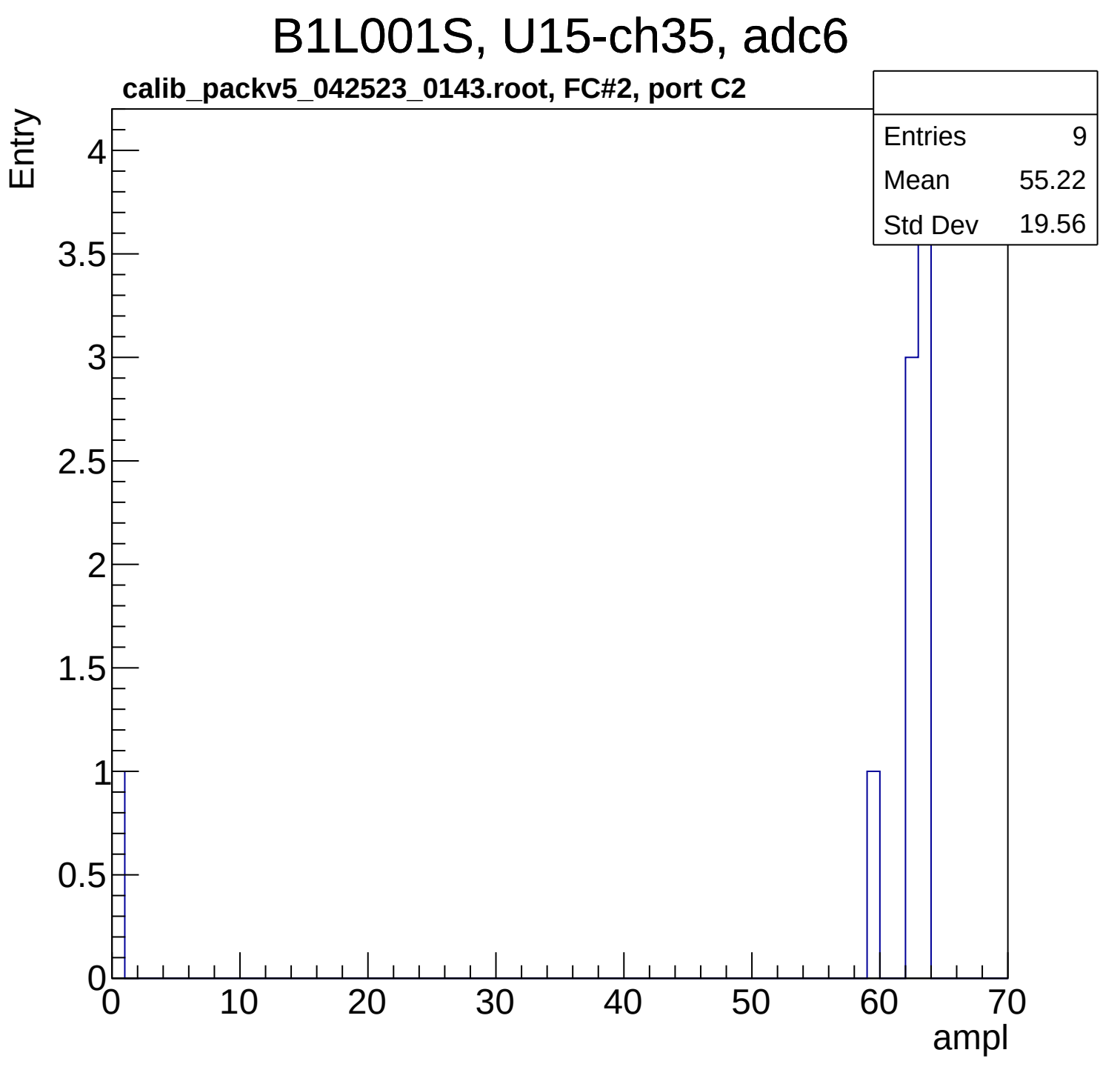

B1L001S, U15-ch35, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 49.57
Std Dev 3.252
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch35, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 56
Std Dev 3.029
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch35, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.47
Std Dev 6.794
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch35, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 55.22
Std Dev 19.56
Entry
ampl
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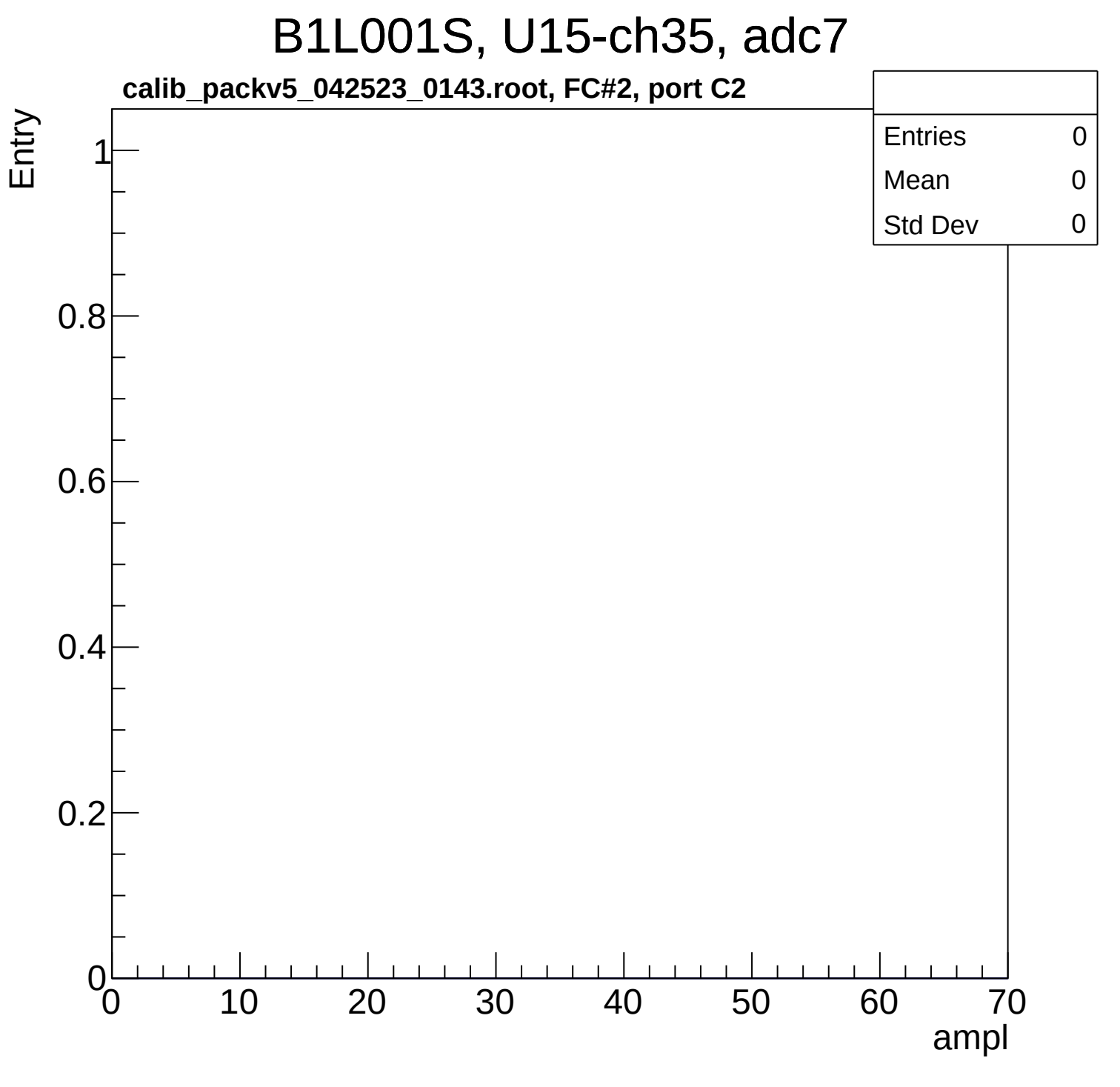
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch35, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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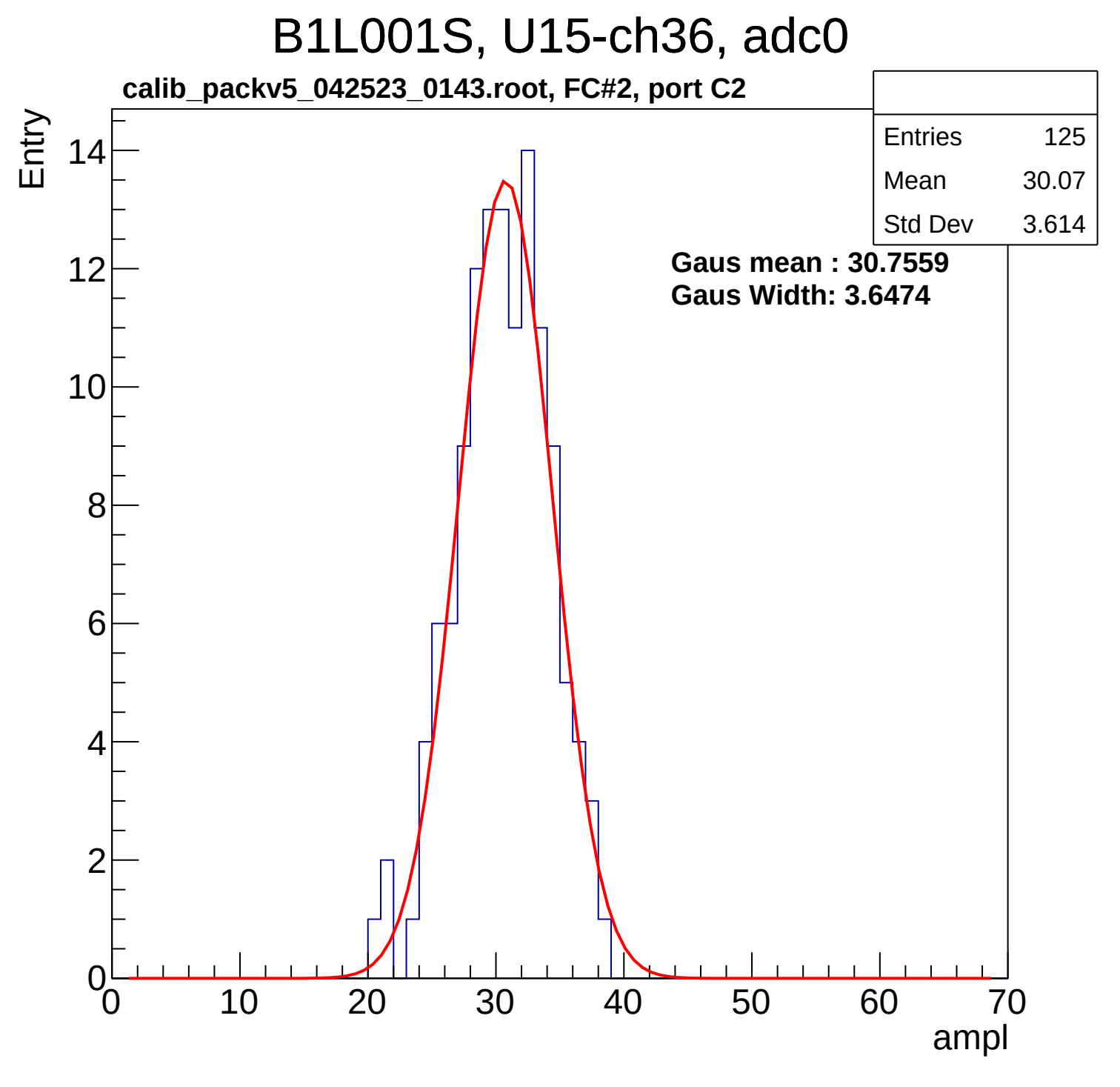
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch36, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 30.07
Std Dev 3.614
Gaus mean : 30.7559
Gaus Width: 3.6474
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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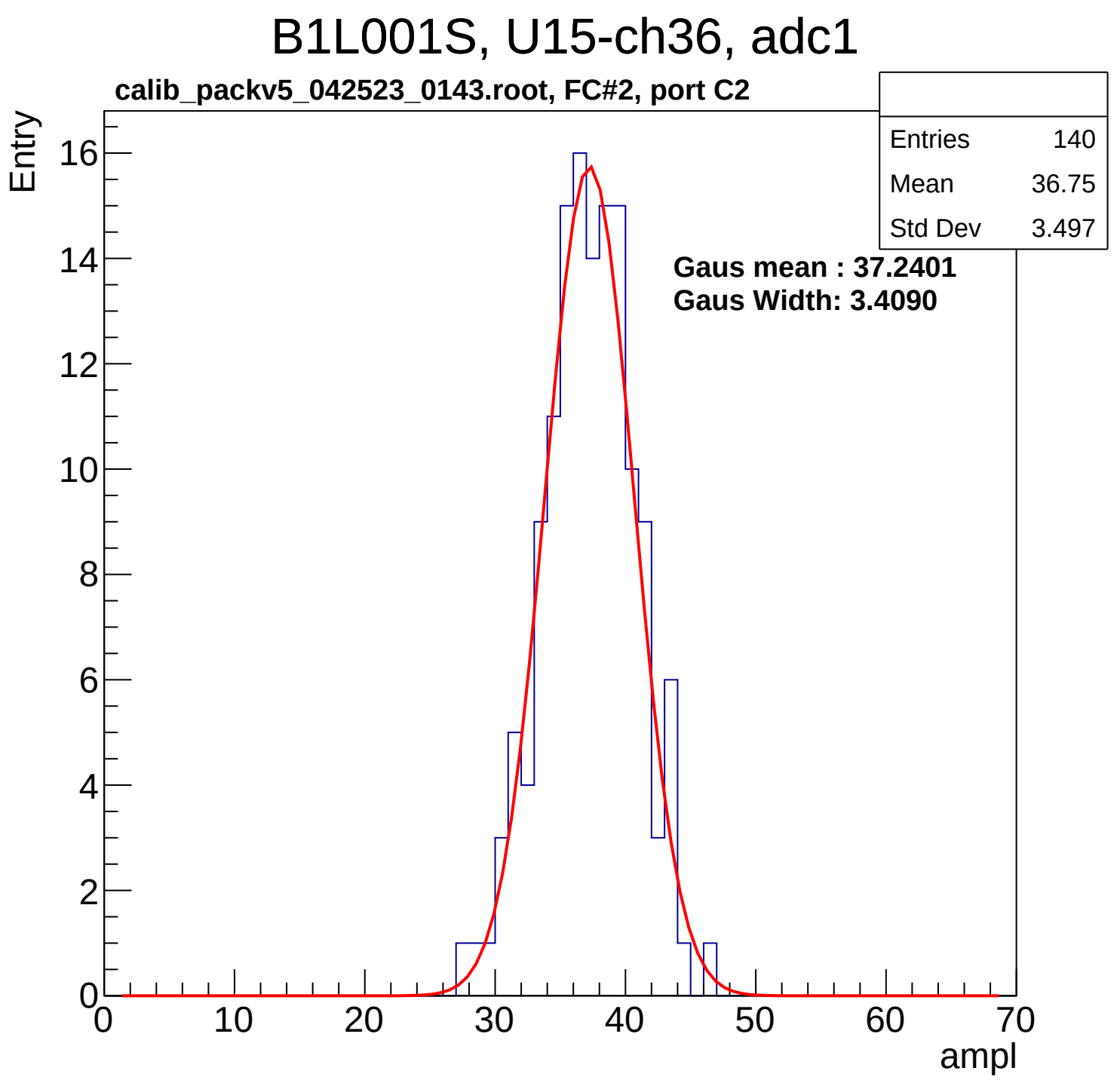
B1L001S, U15-ch36, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 36.75
Std Dev 3.497
Gaus mean : 37.2401
Gaus Width: 3.4090
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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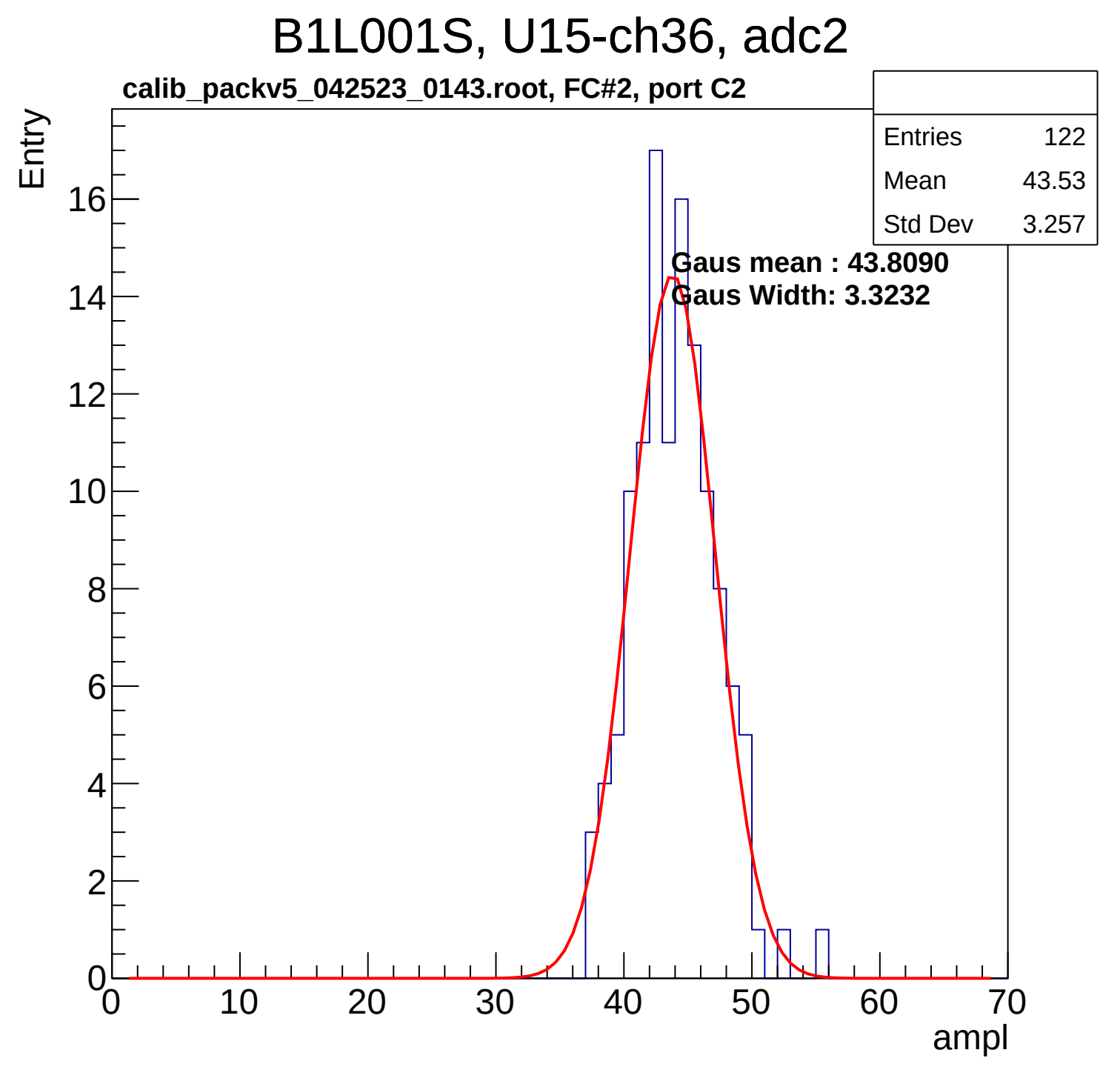
B1L001S, U15-ch36, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 43.53
Std Dev 3.257
Gaus mean : 43.8090
Gaus Width: 3.3232
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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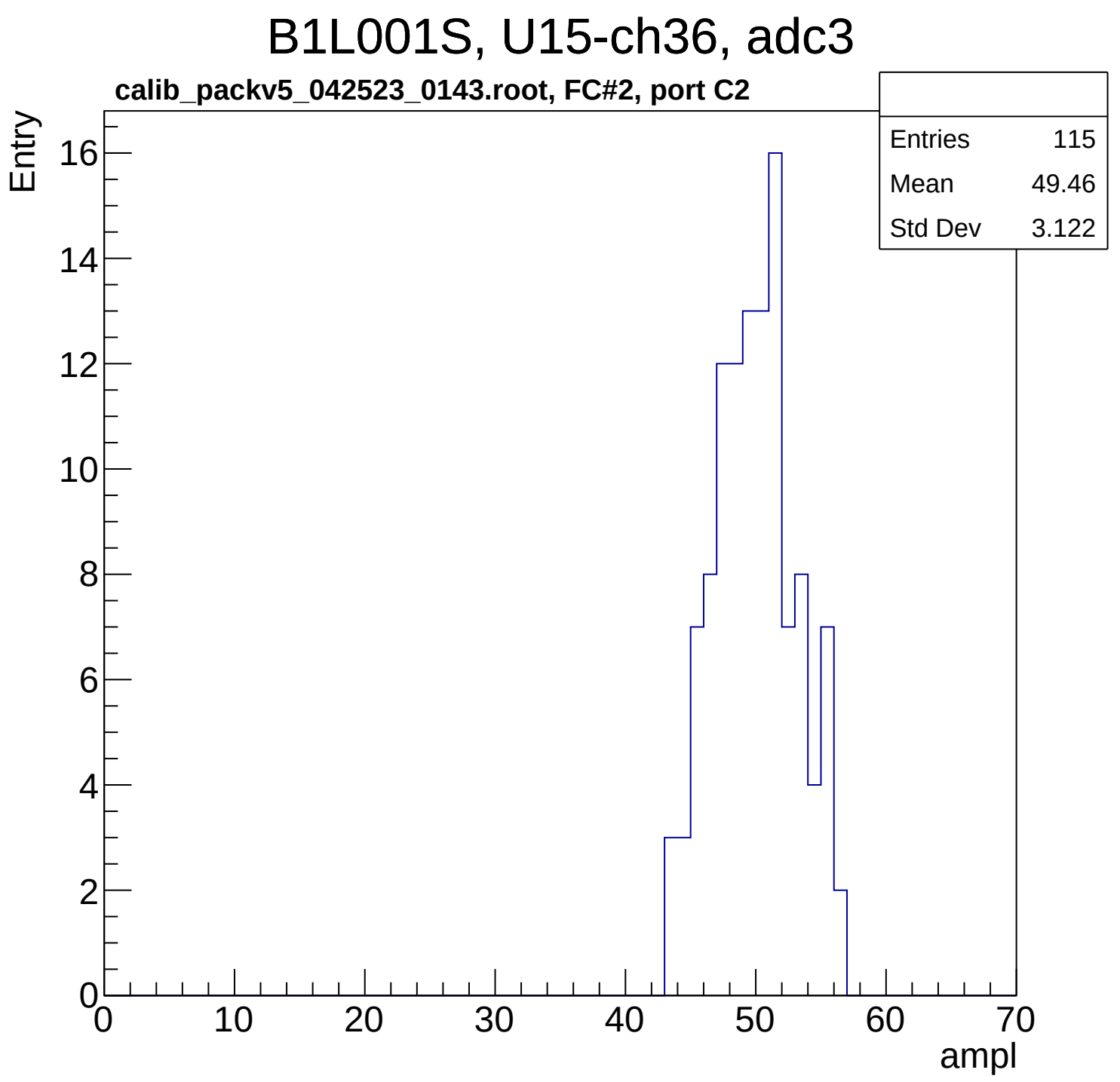
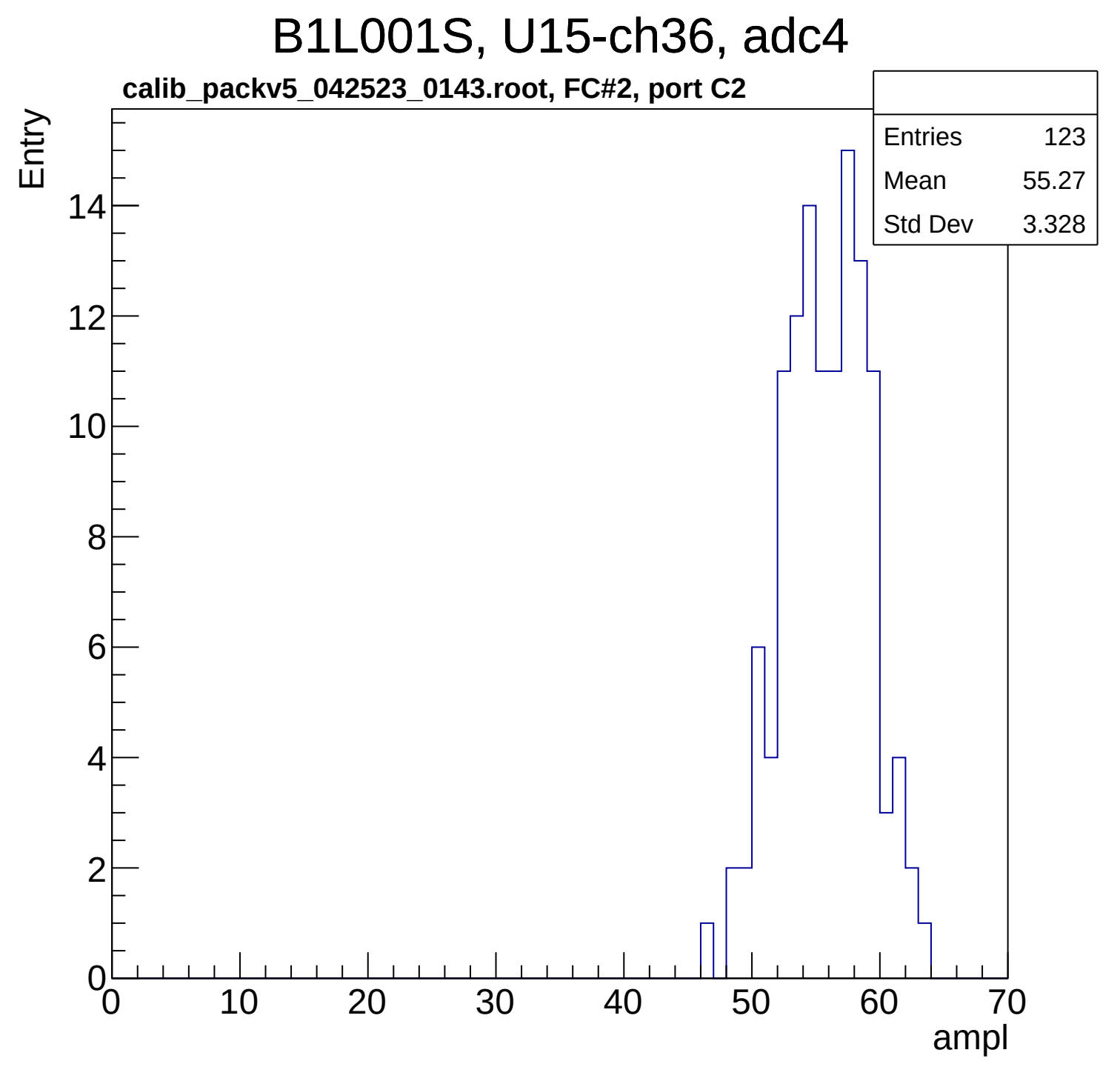
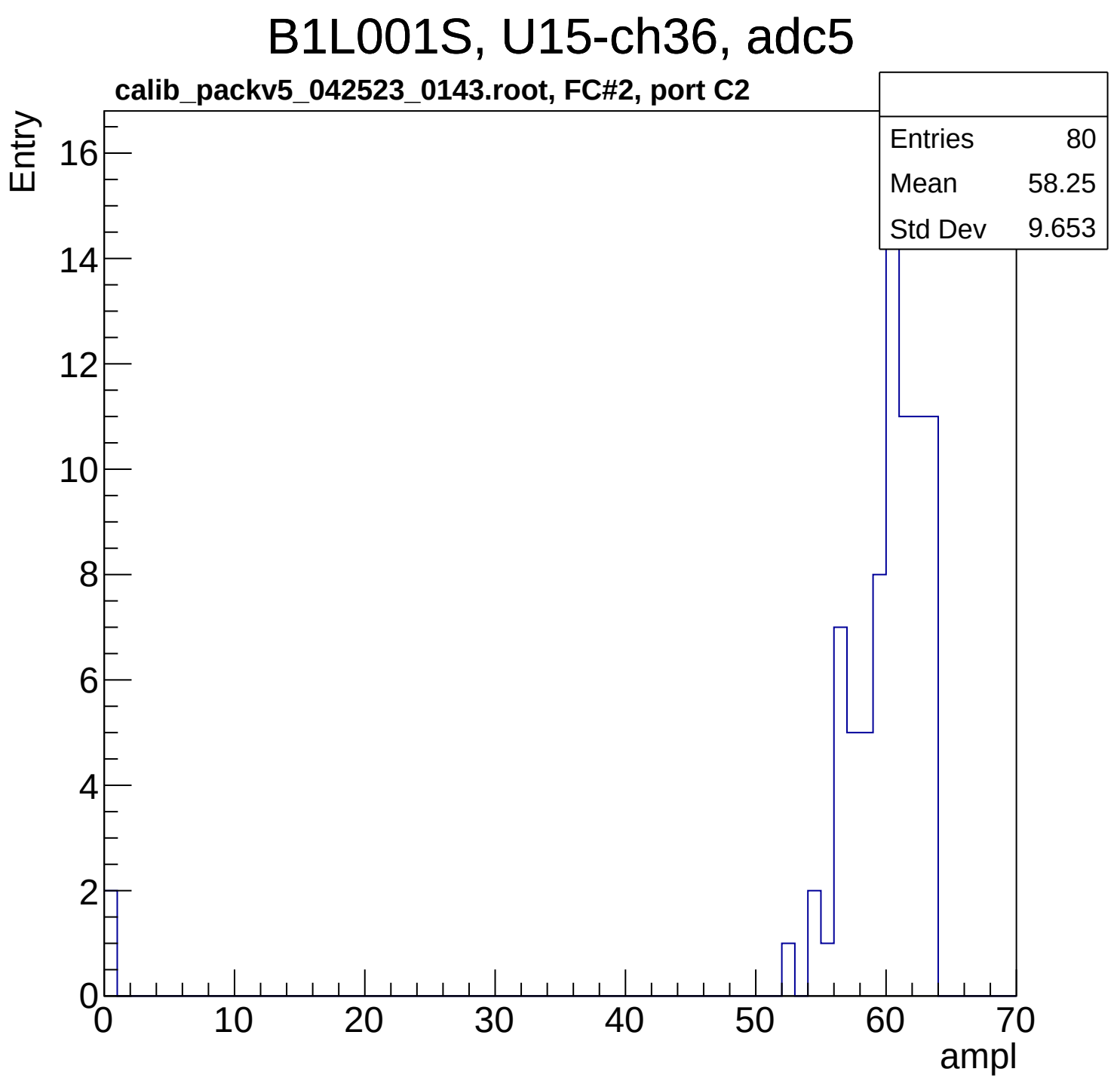

B1L001S, U15-ch36, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 49.46
Std Dev 3.122
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch36, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 55.27
Std Dev 3.328
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch36, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 58.25
Std Dev 9.653
Entry
ampl
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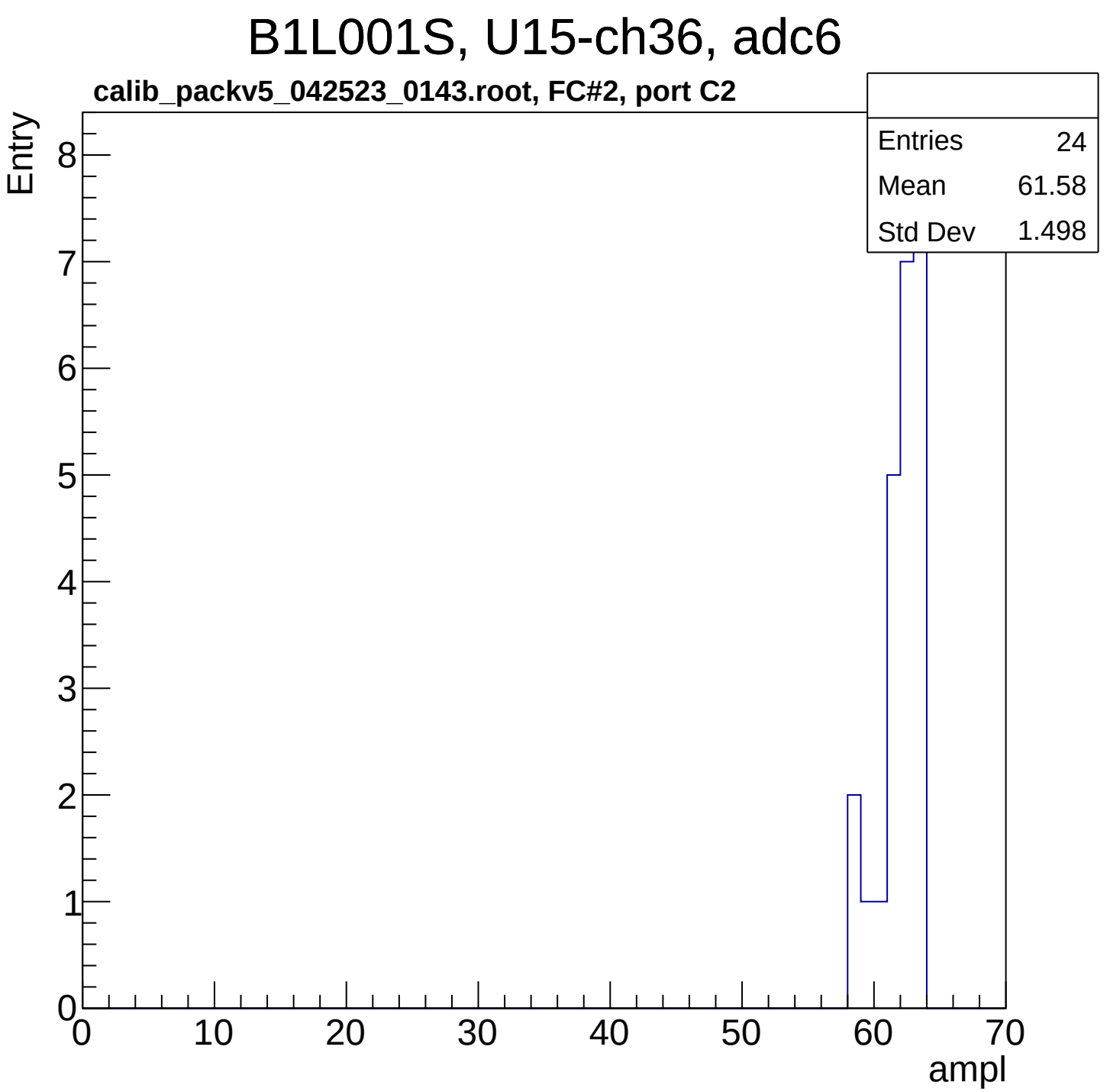
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch36, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 24
Mean 61.58
Std Dev 1.498
Entry
ampl
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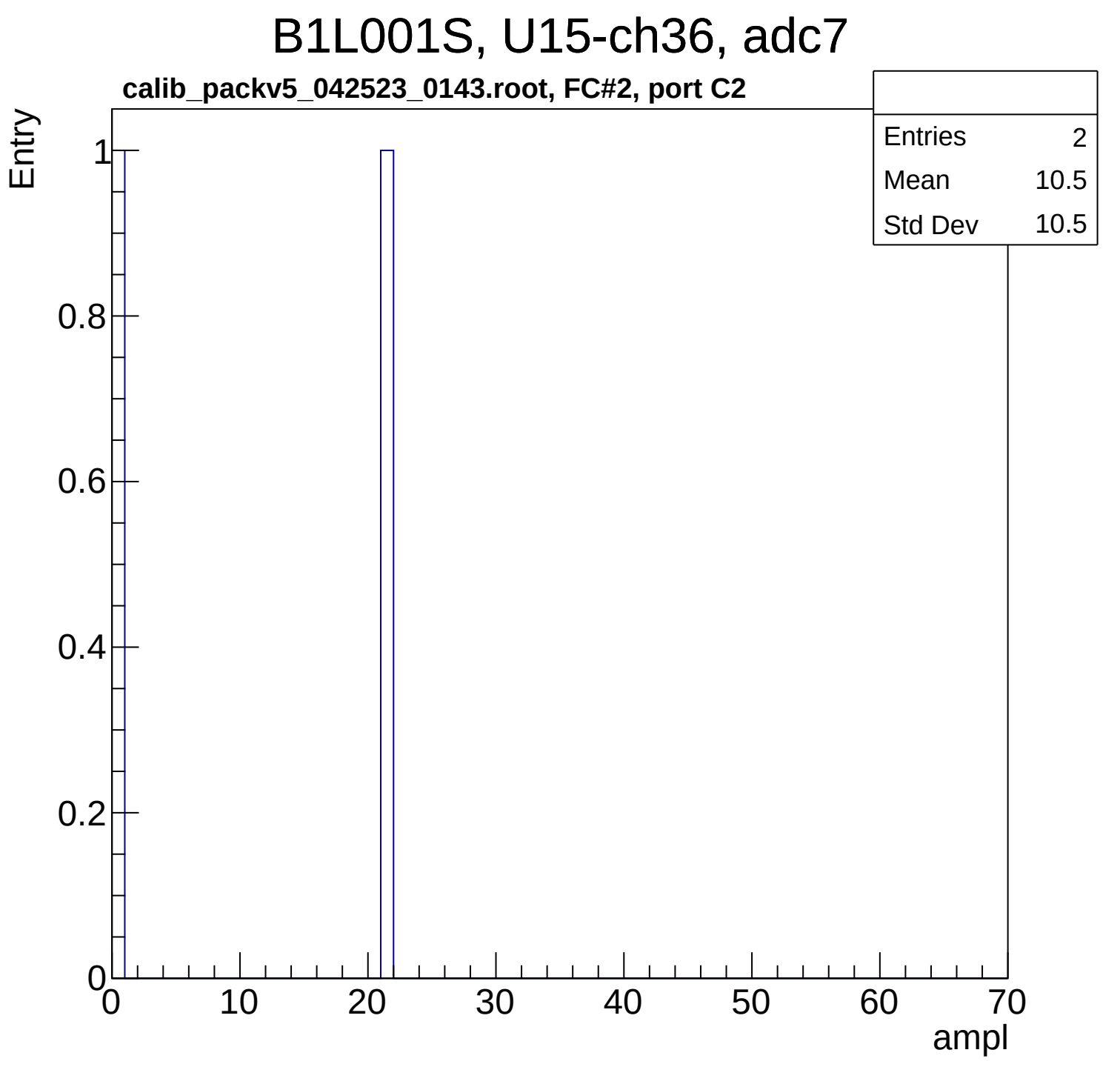
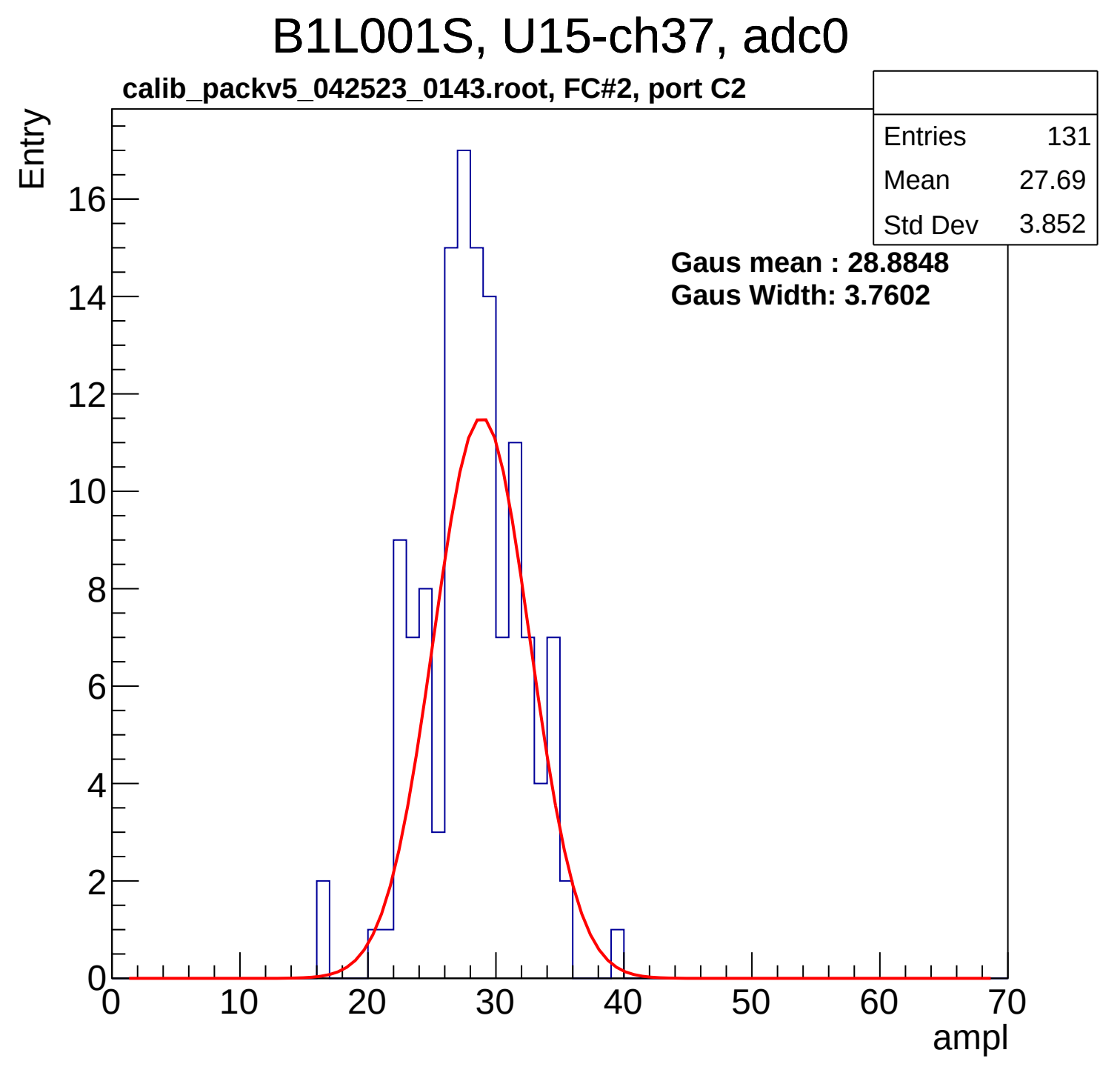
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch36, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch37, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 27.69
Std Dev 3.852
Gaus mean : 28.8848
Gaus Width: 3.7602
Entry
ampl
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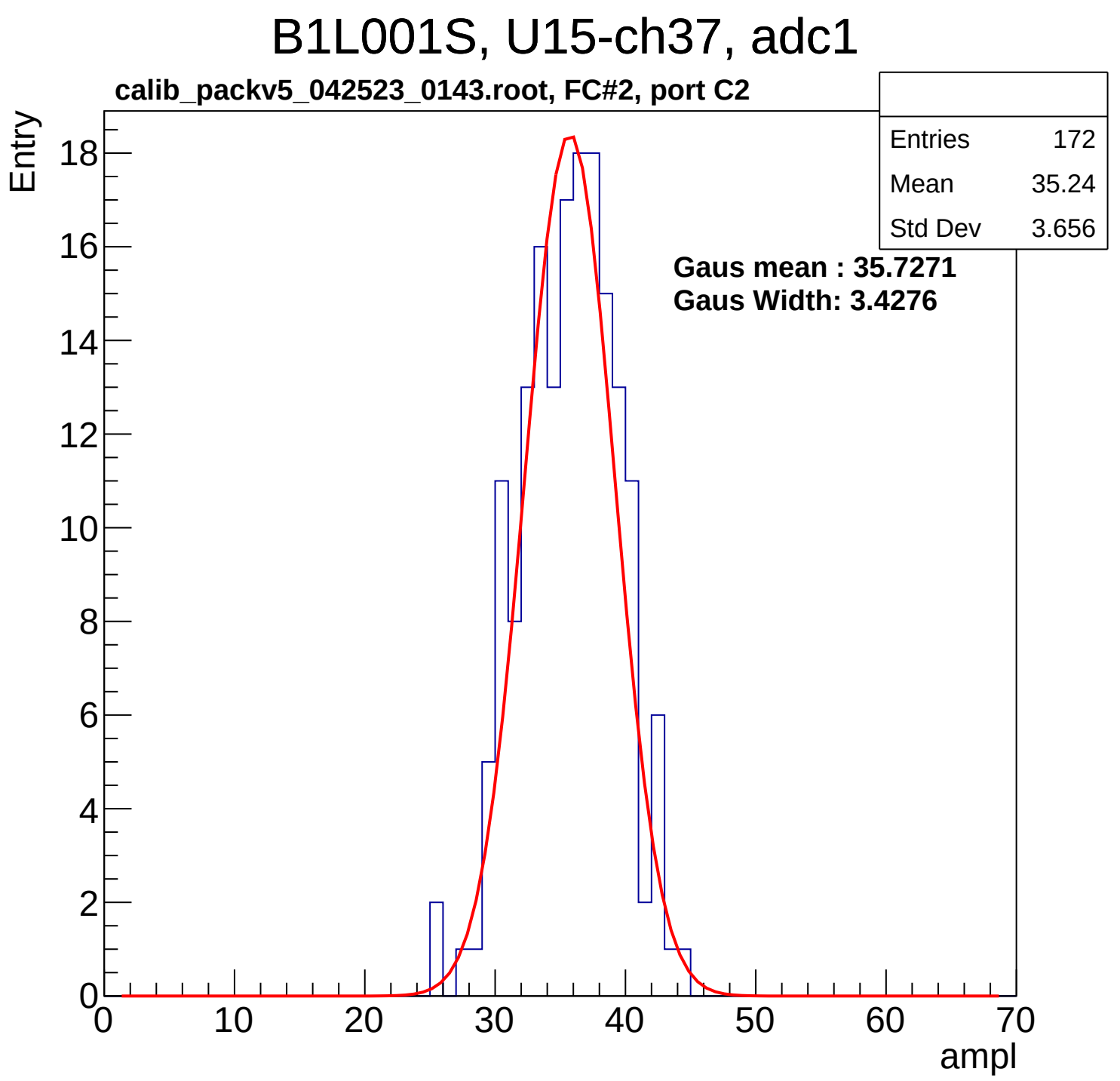
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch37, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 172
Mean 35.24
Std Dev 3.656
Gaus mean : 35.7271
Gaus Width: 3.4276
Entry
ampl
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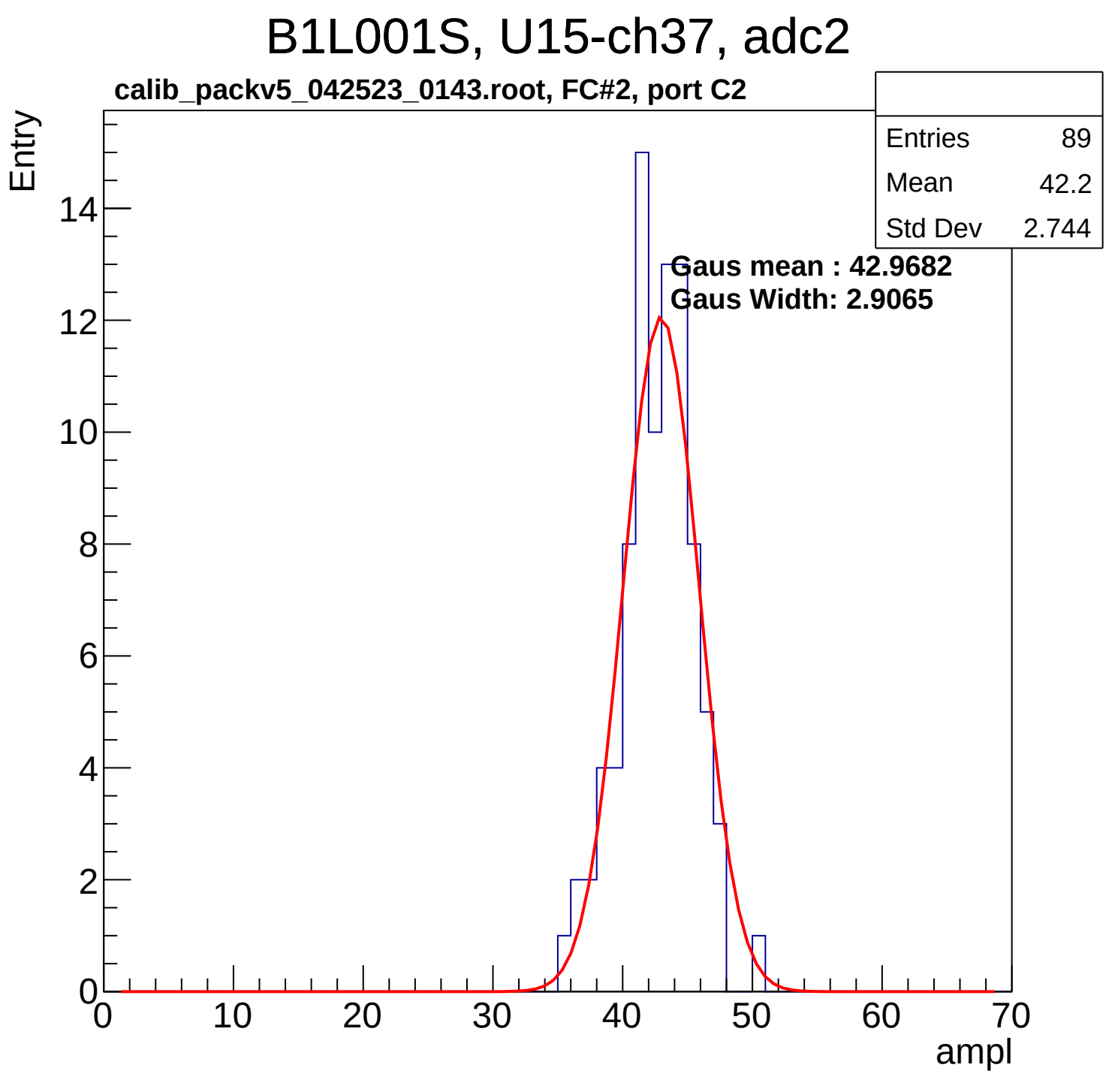
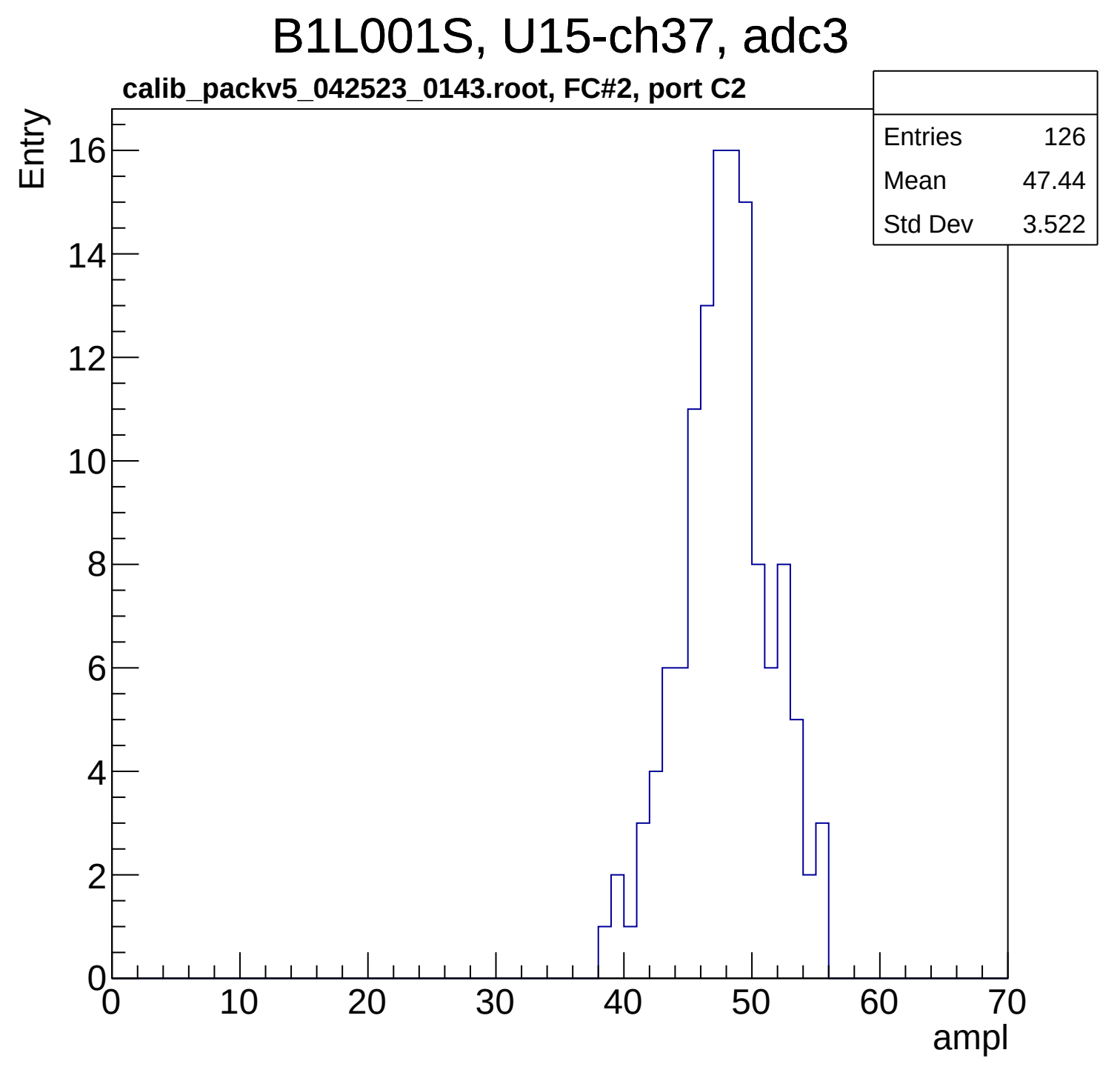
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch37, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 42.2
Std Dev 2.744
Gaus mean : 42.9682
Gaus Width: 2.9065
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch37, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 47.44
Std Dev 3.522
Entry
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4
6
8
10
12
14
16
0
10
20
30
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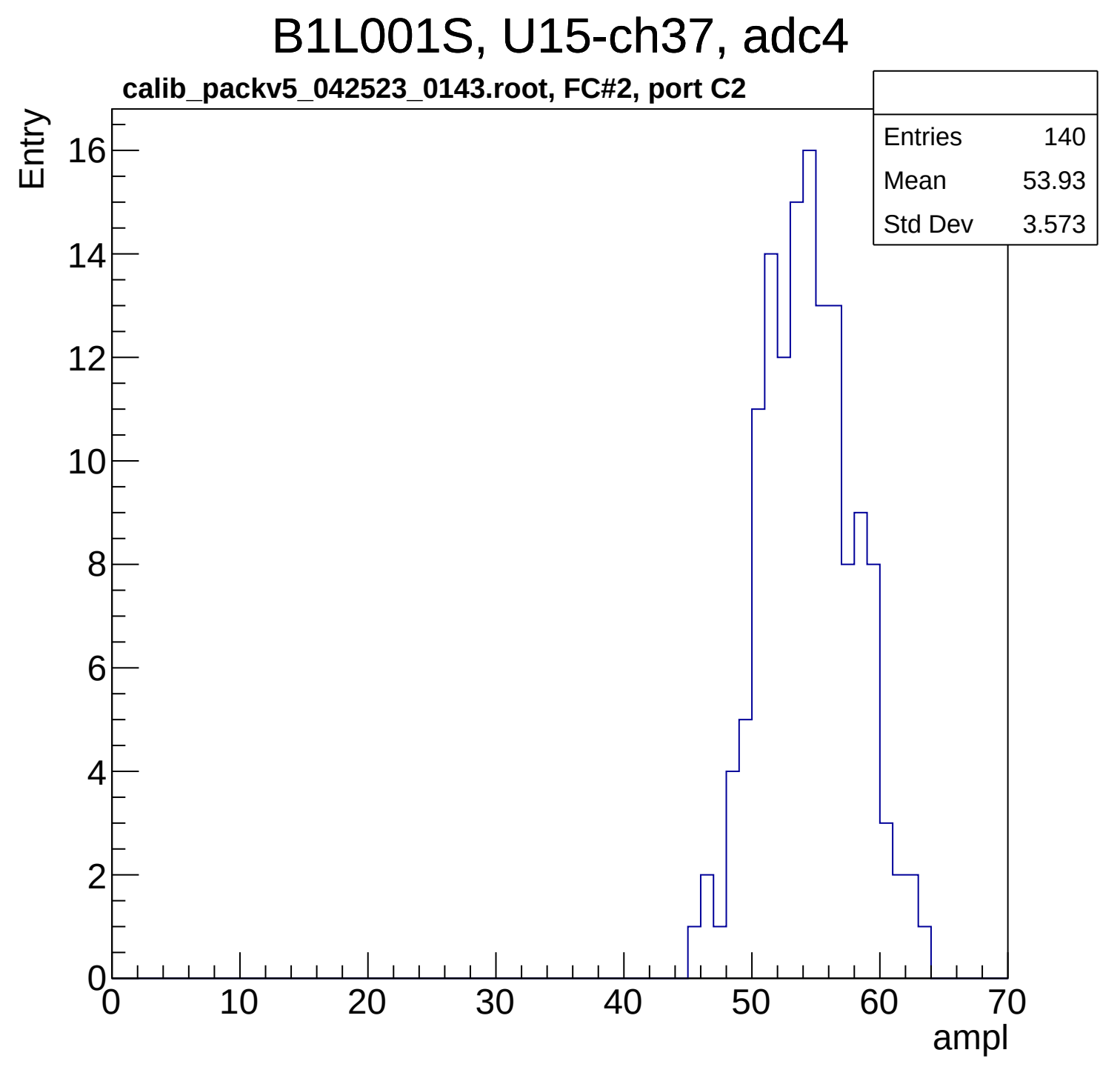
50
60
70
ampl


B1L001S, U15-ch37, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 53.93
Std Dev 3.573
Entry
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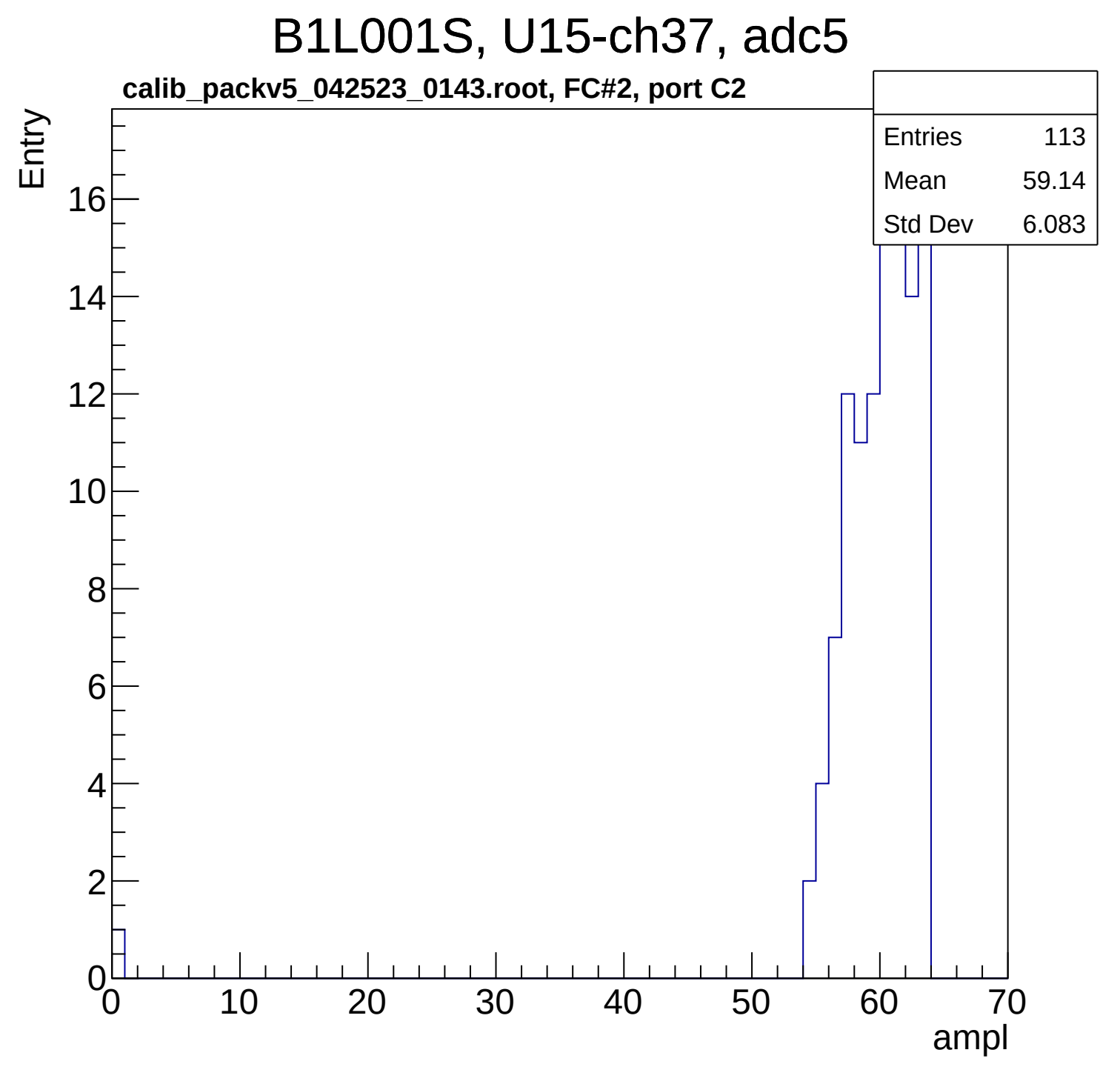
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch37, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 59.14
Std Dev 6.083
Entry
ampl
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4
6
8
10
12
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16
0
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20
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60
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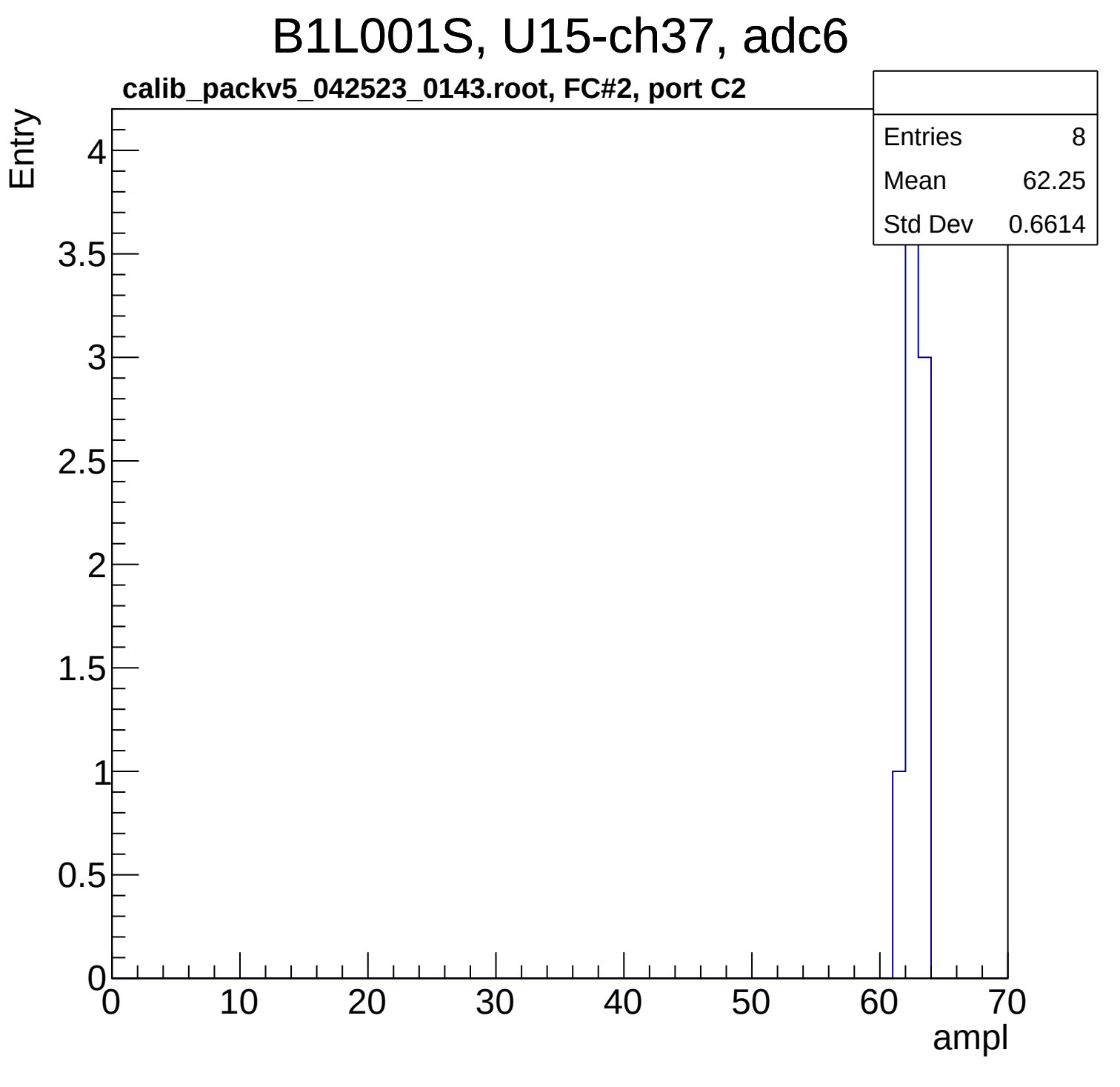
B1L001S, U15-ch37, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 62.25
Std Dev 0.6614
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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50
60
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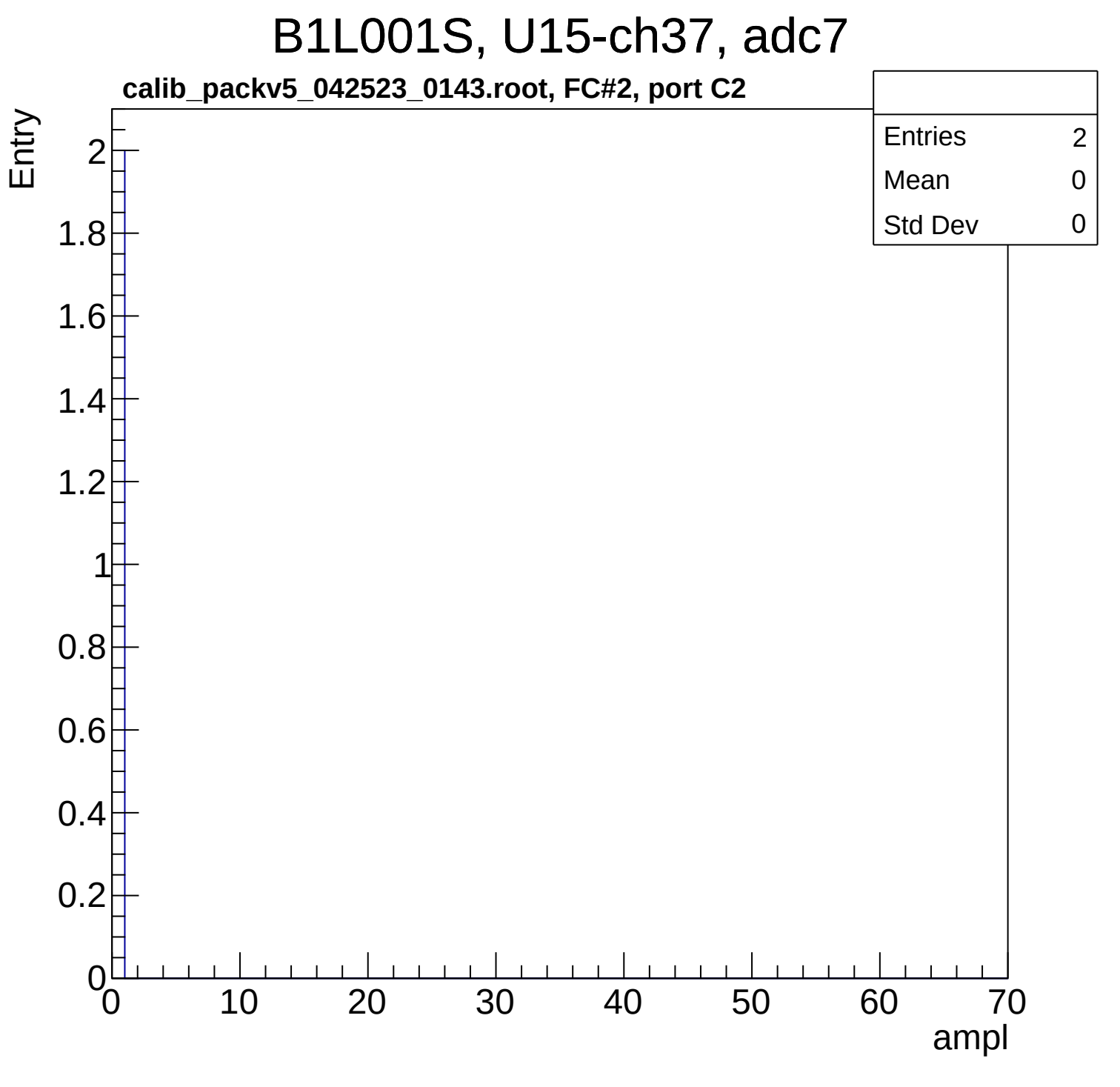
B1L001S, U15-ch37, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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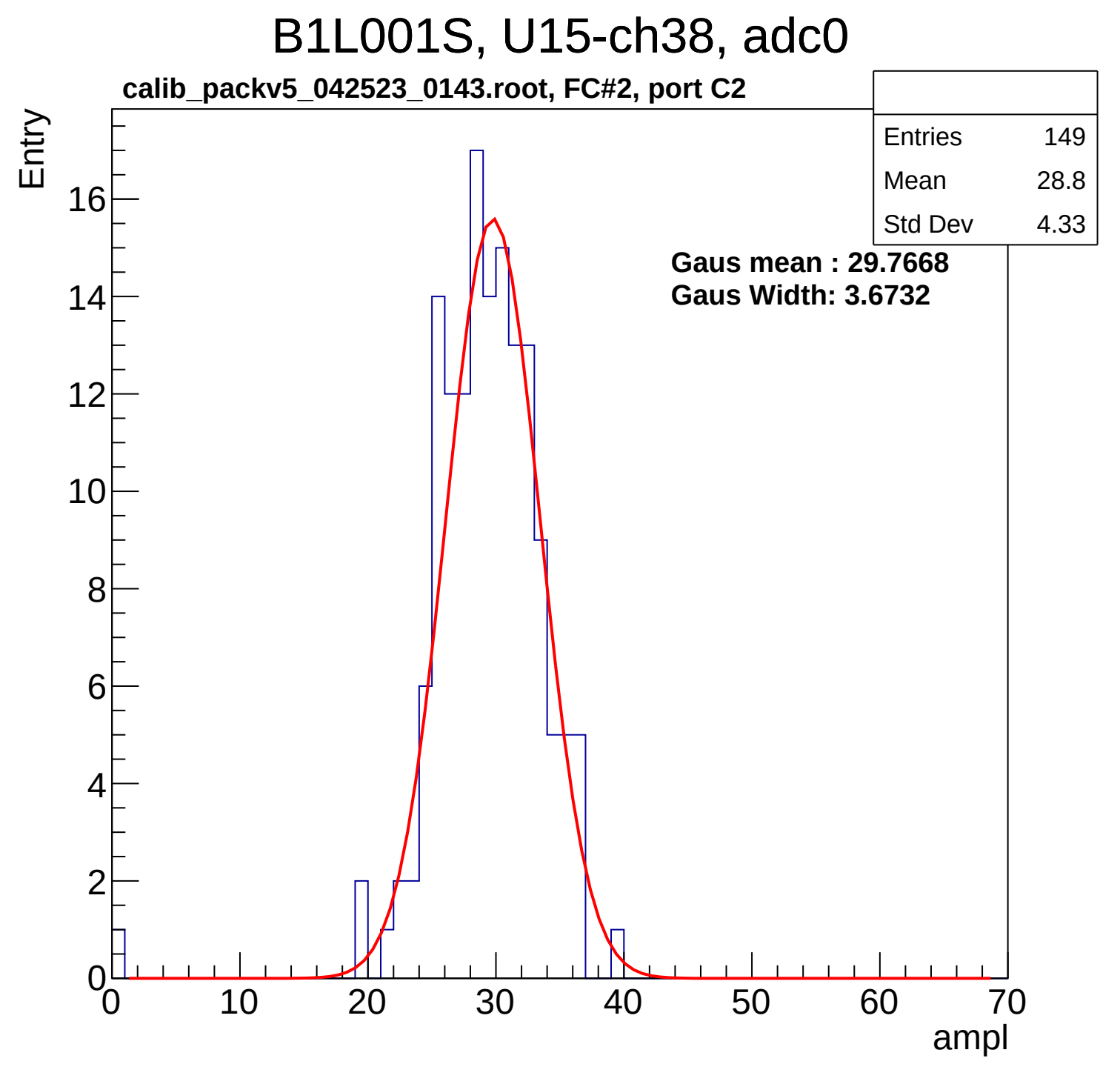
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch38, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 28.8
Std Dev 4.33
Gaus mean : 29.7668
Gaus Width: 3.6732
Entry
ampl
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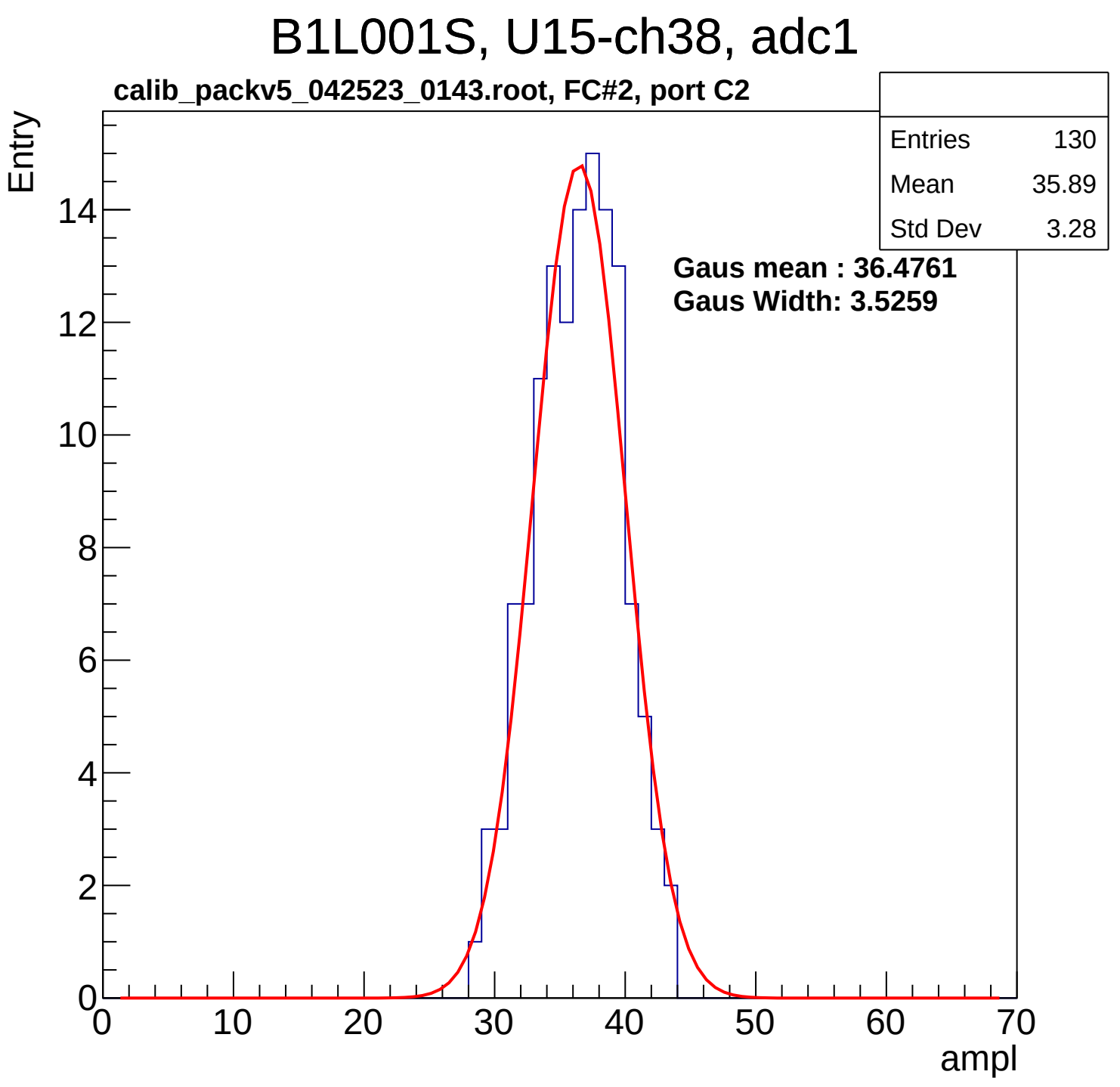
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch38, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 35.89
Std Dev 3.28
Gaus mean : 36.4761
Gaus Width: 3.5259
Entry
ampl
0
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0
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60
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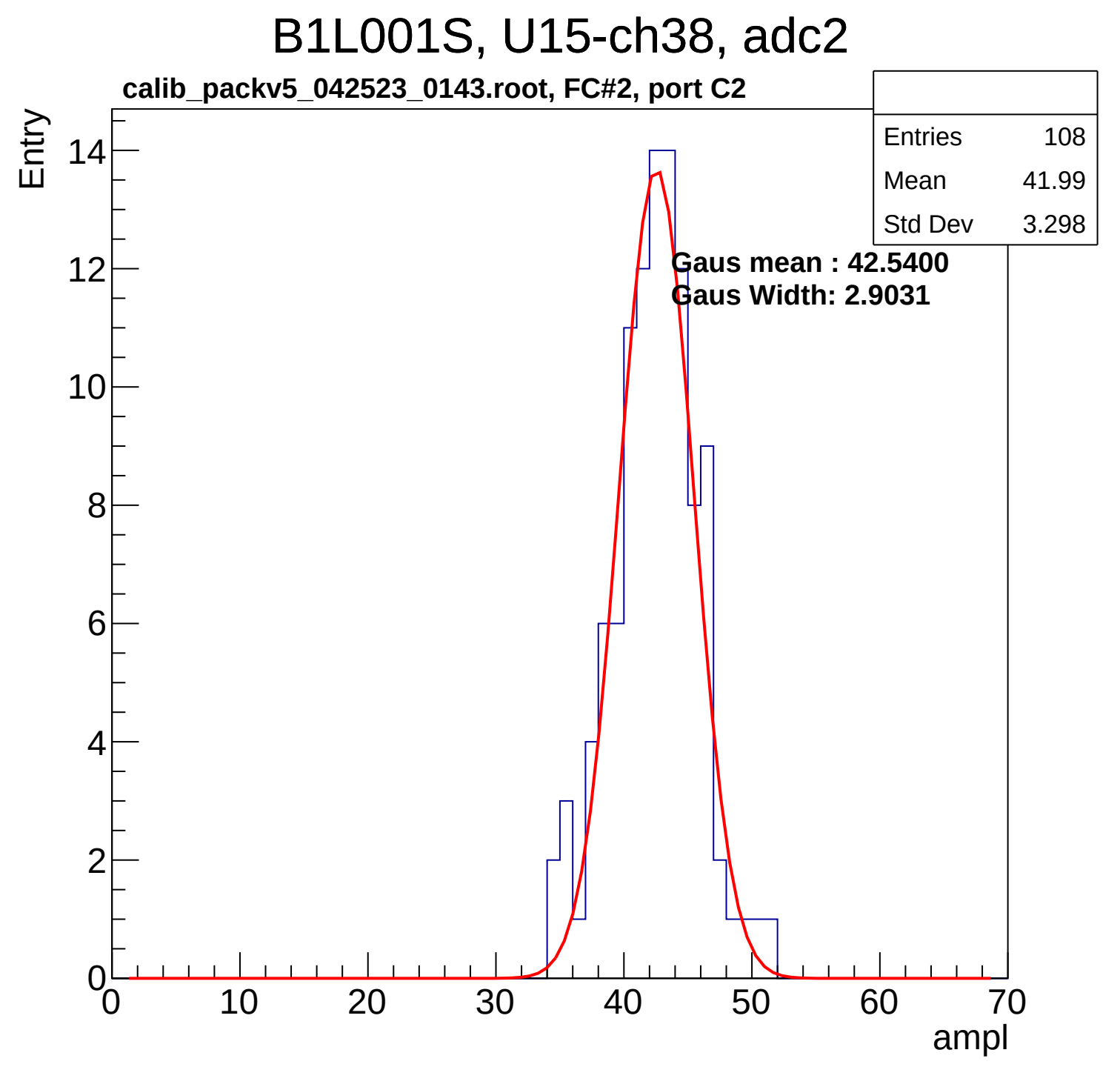
B1L001S, U15-ch38, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 41.99
Std Dev 3.298
Gaus mean : 42.5400
Gaus Width: 2.9031
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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60
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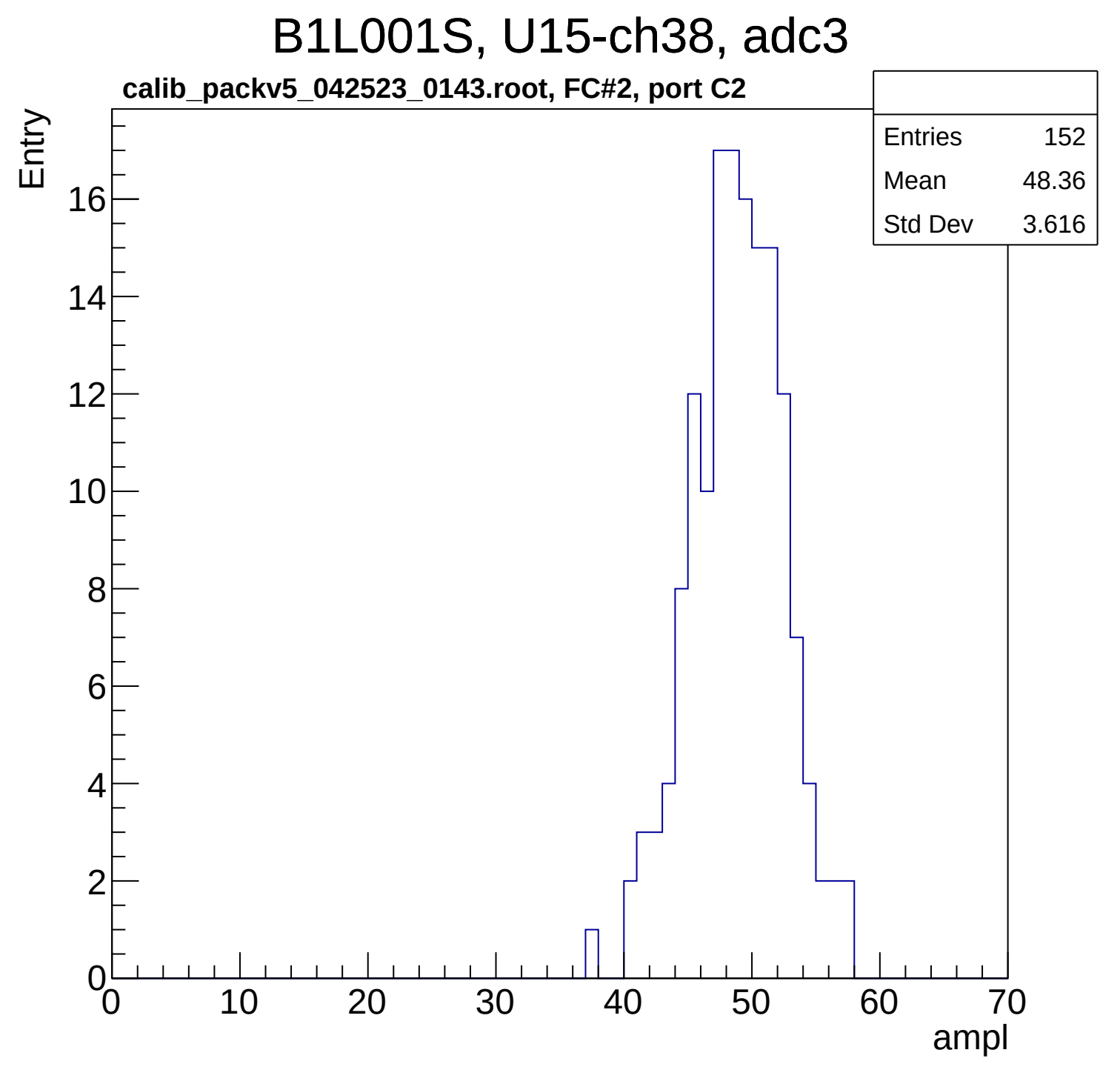
B1L001S, U15-ch38, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 48.36
Std Dev 3.616
Entry
ampl
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10
12
14
16
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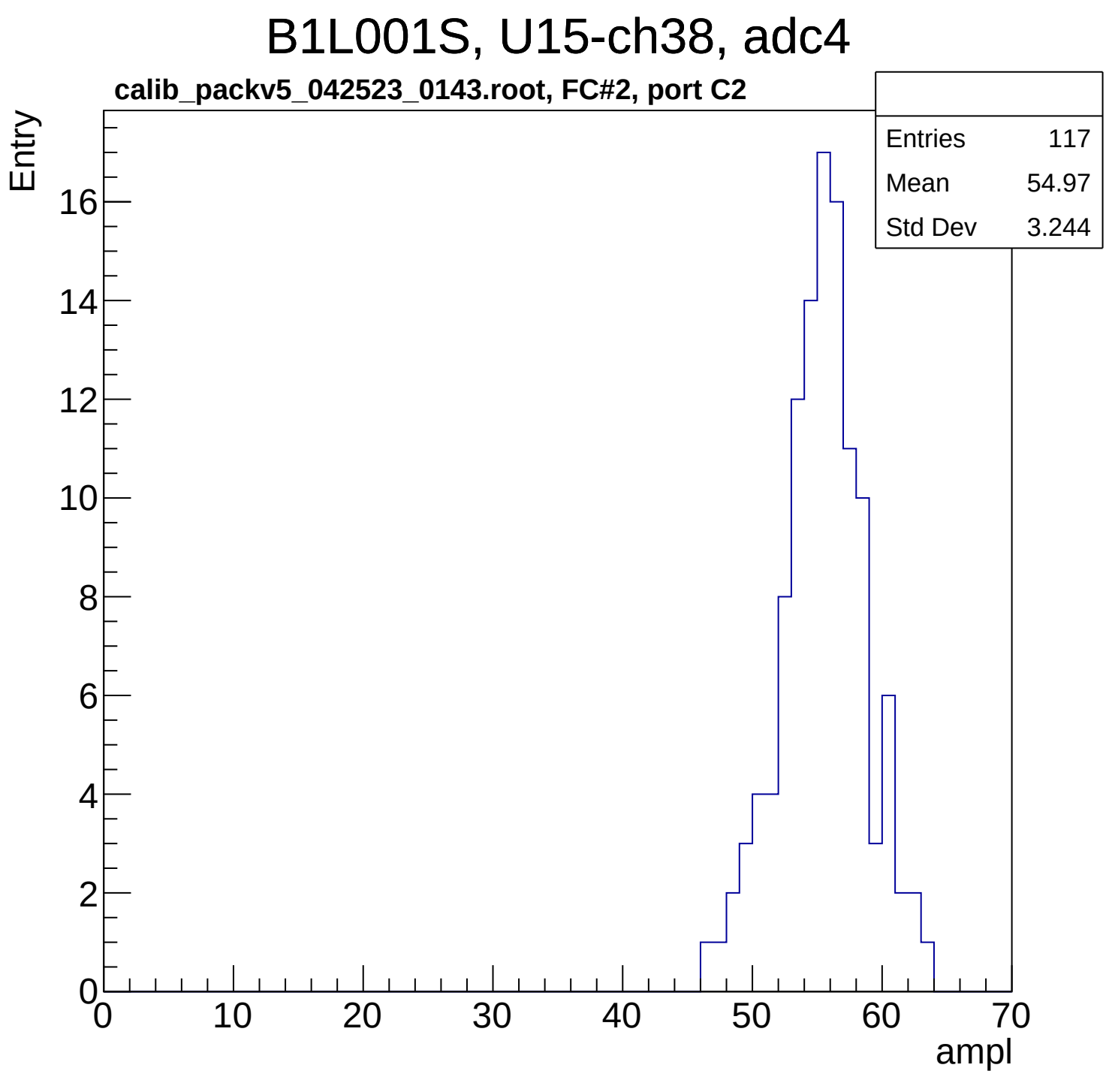
B1L001S, U15-ch38, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 54.97
Std Dev 3.244
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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20
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40
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60
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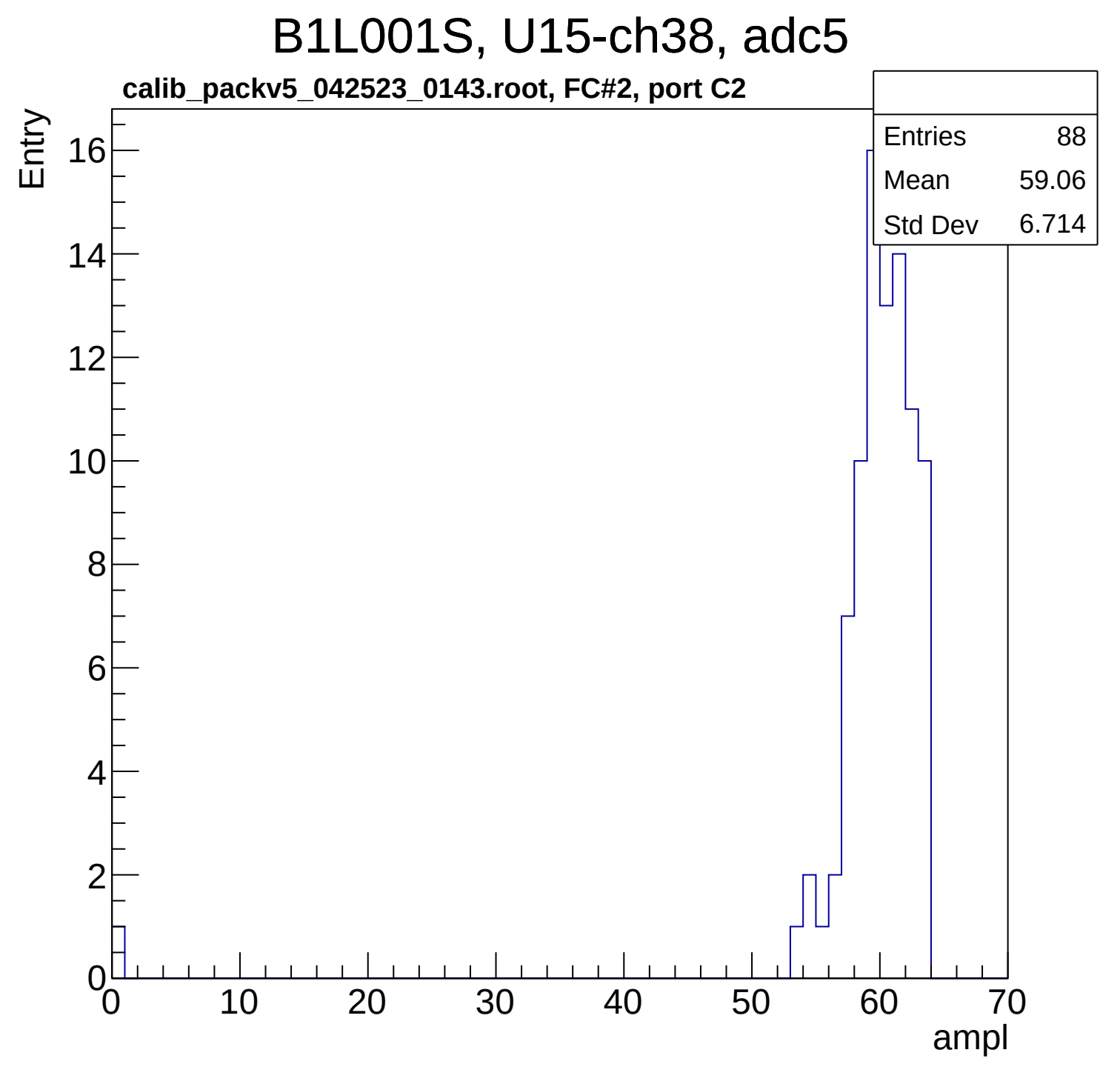
B1L001S, U15-ch38, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.06
Std Dev 6.714
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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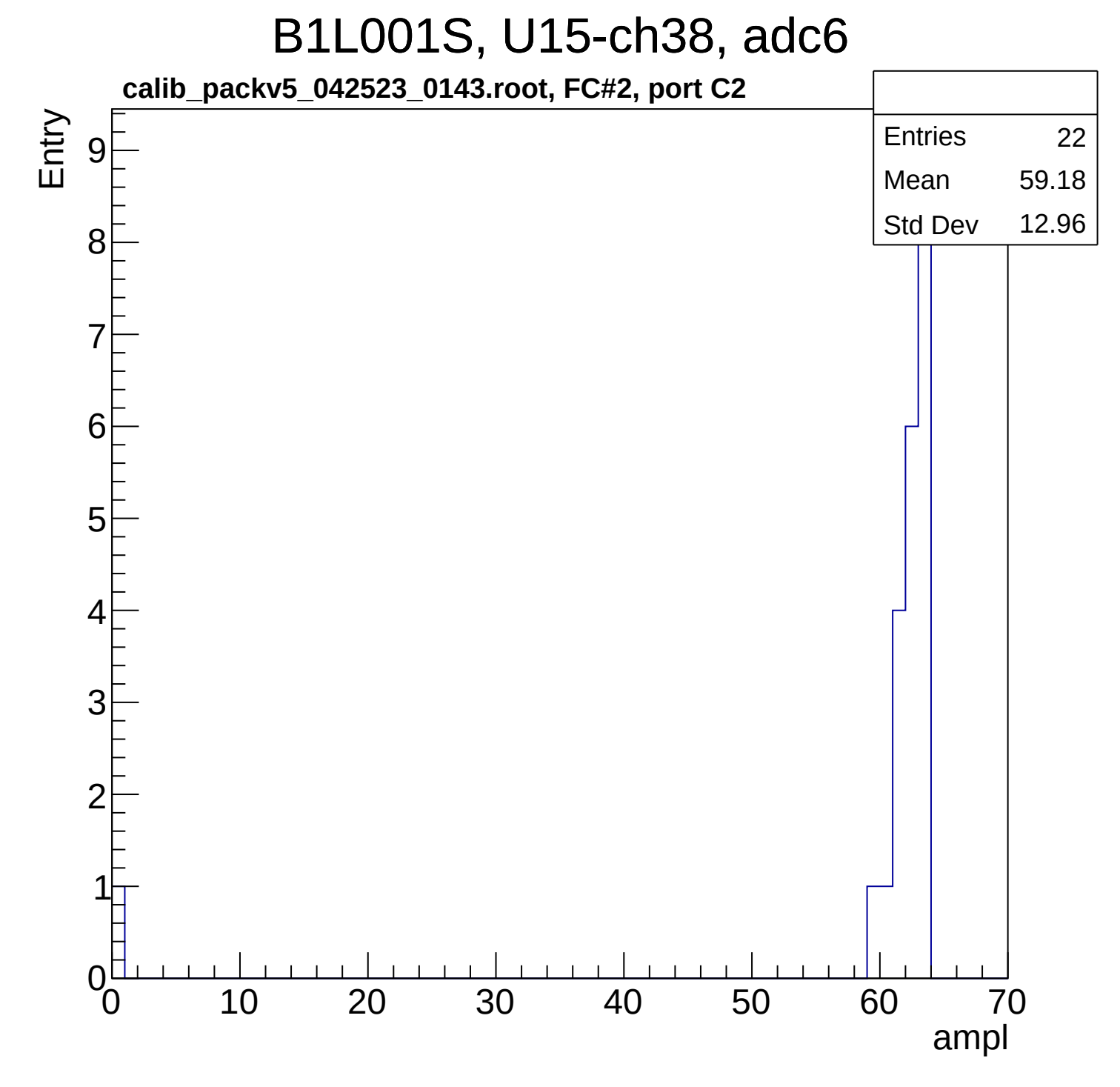
B1L001S, U15-ch38, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 22
Mean 59.18
Std Dev 12.96
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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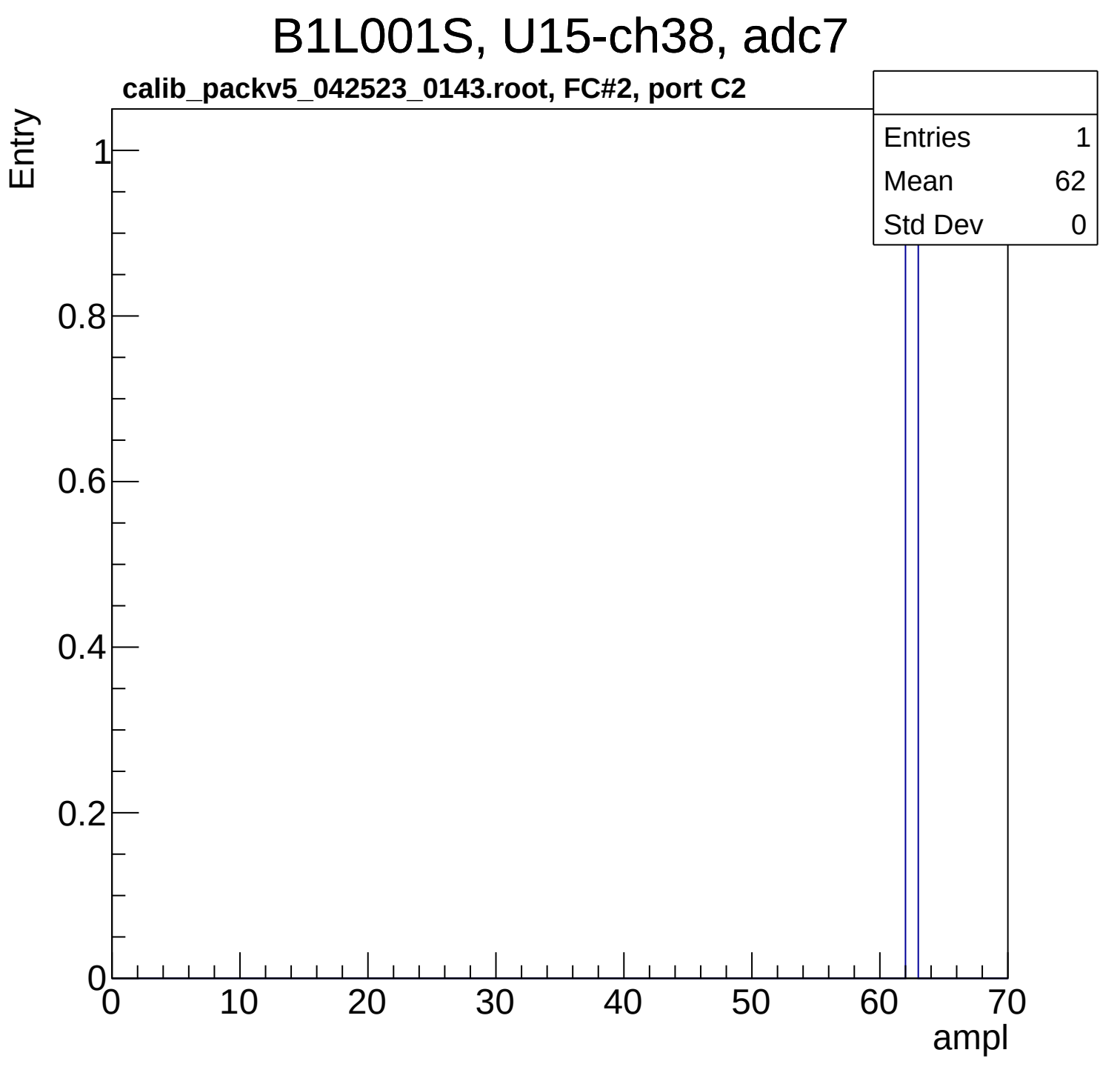
B1L001S, U15-ch38, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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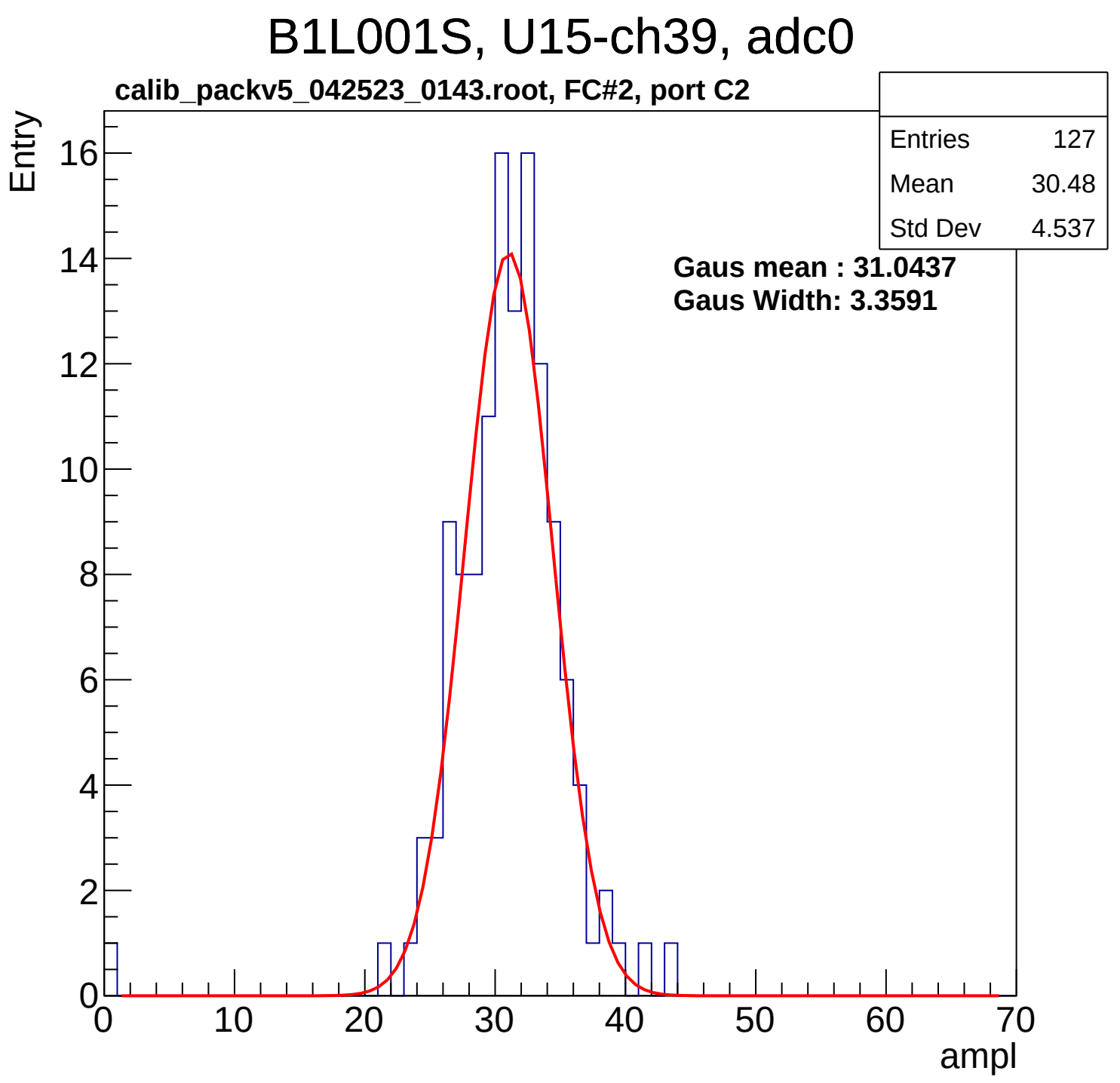
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch39, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 30.48
Std Dev 4.537
Gaus mean : 31.0437
Gaus Width: 3.3591
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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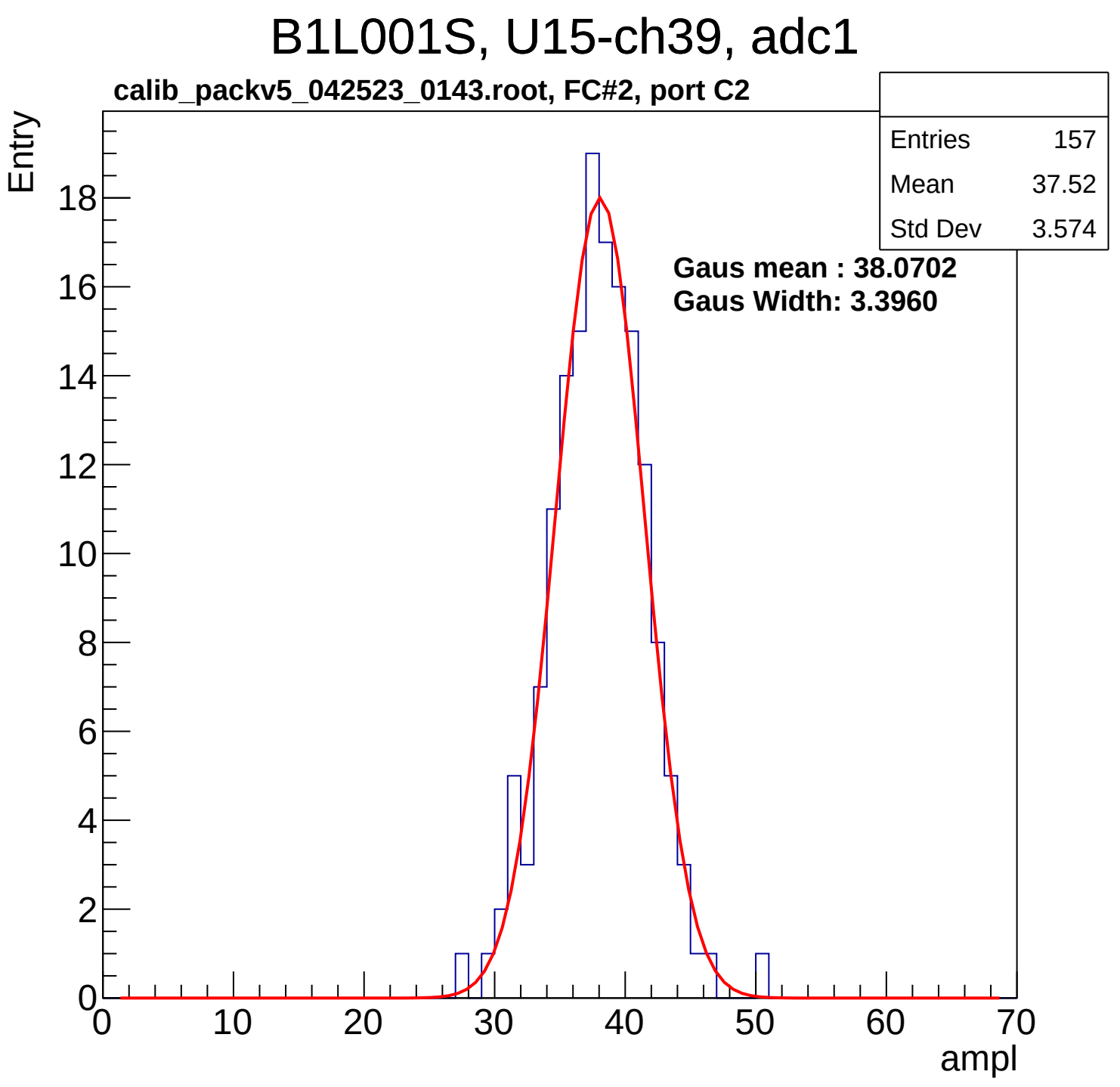
B1L001S, U15-ch39, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 37.52
Std Dev 3.574
Gaus mean : 38.0702
Gaus Width: 3.3960
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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40
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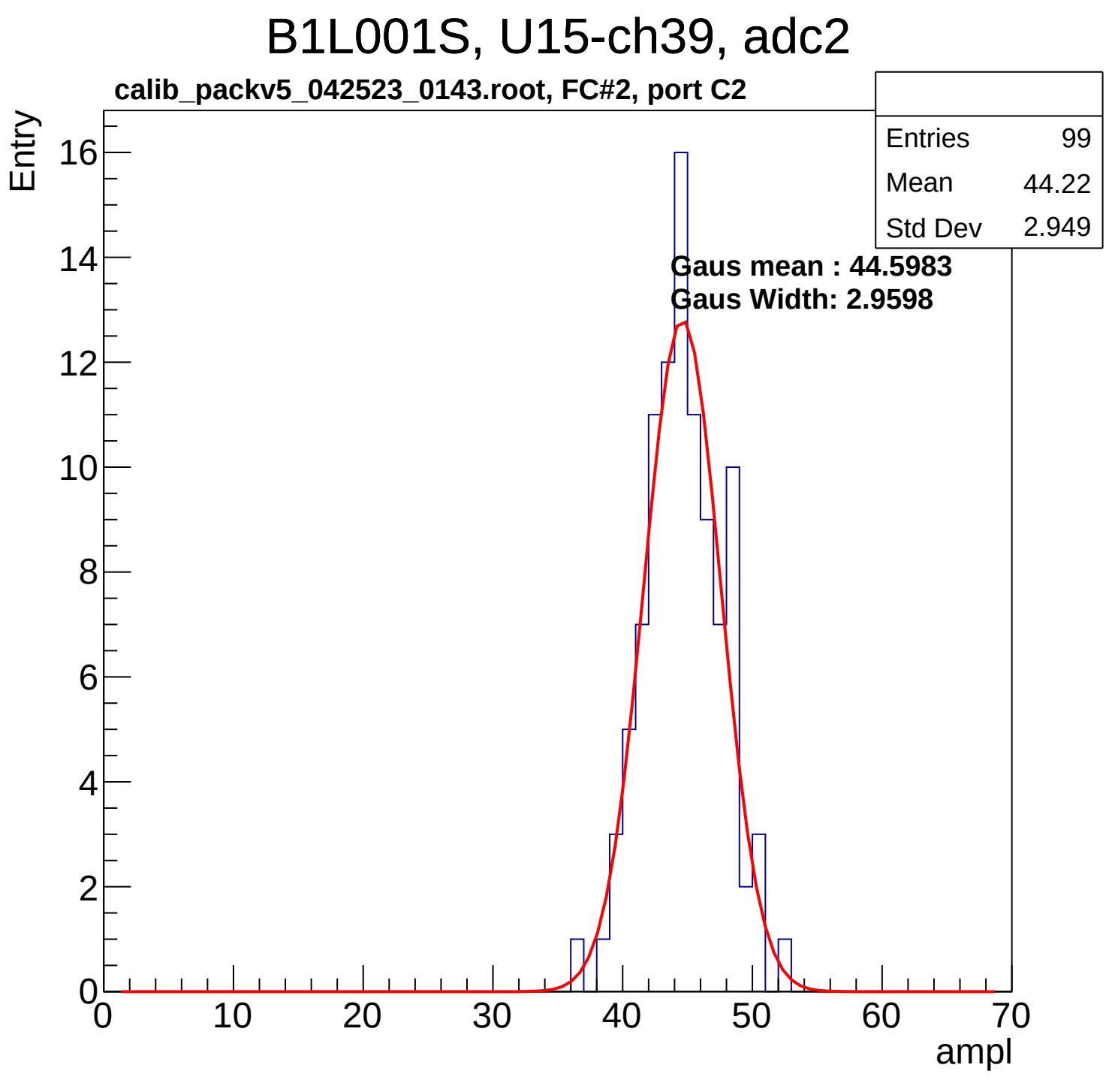
B1L001S, U15-ch39, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 44.22
Std Dev 2.949
Gaus mean : 44.5983
Gaus Width: 2.9598
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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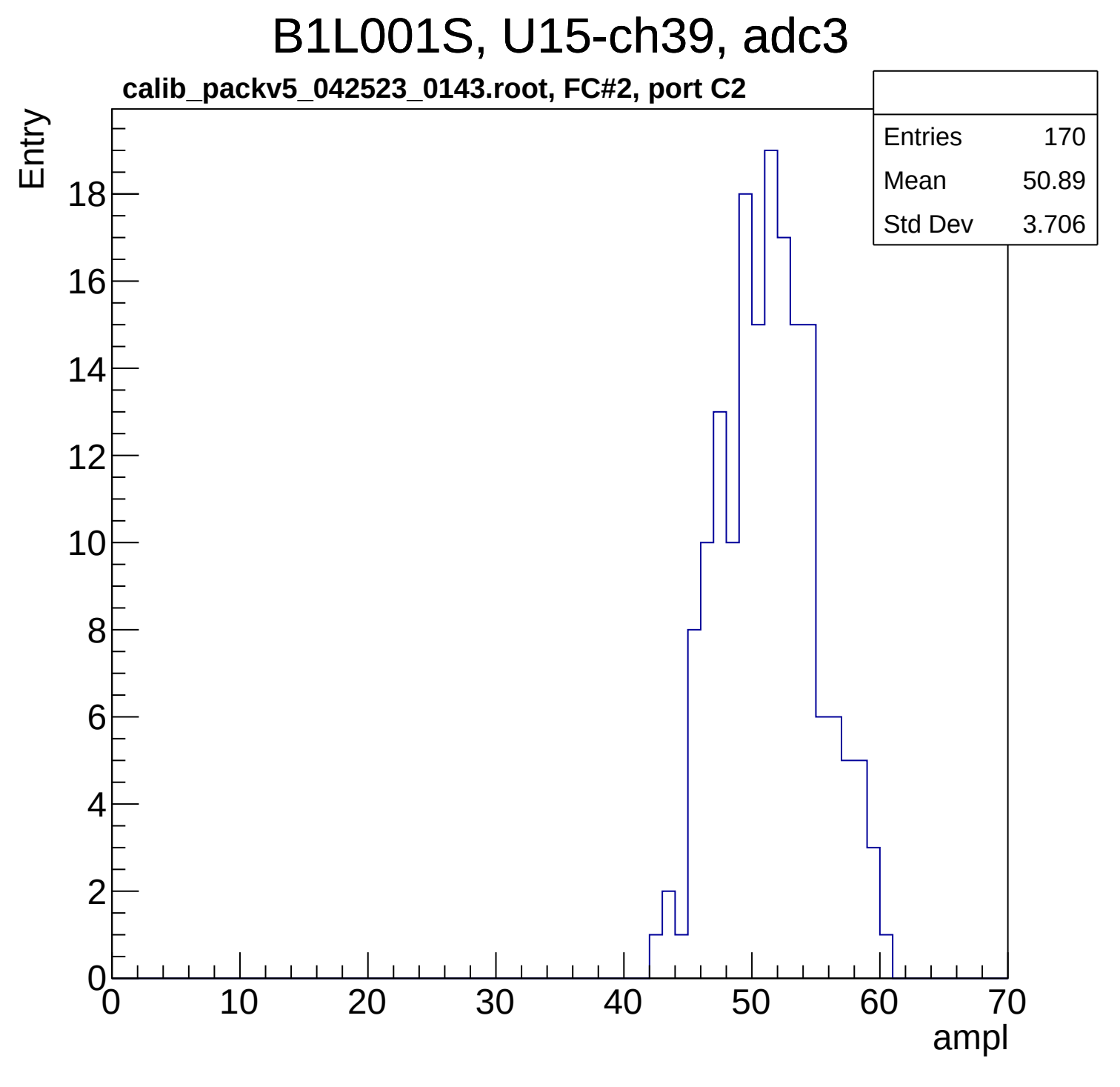
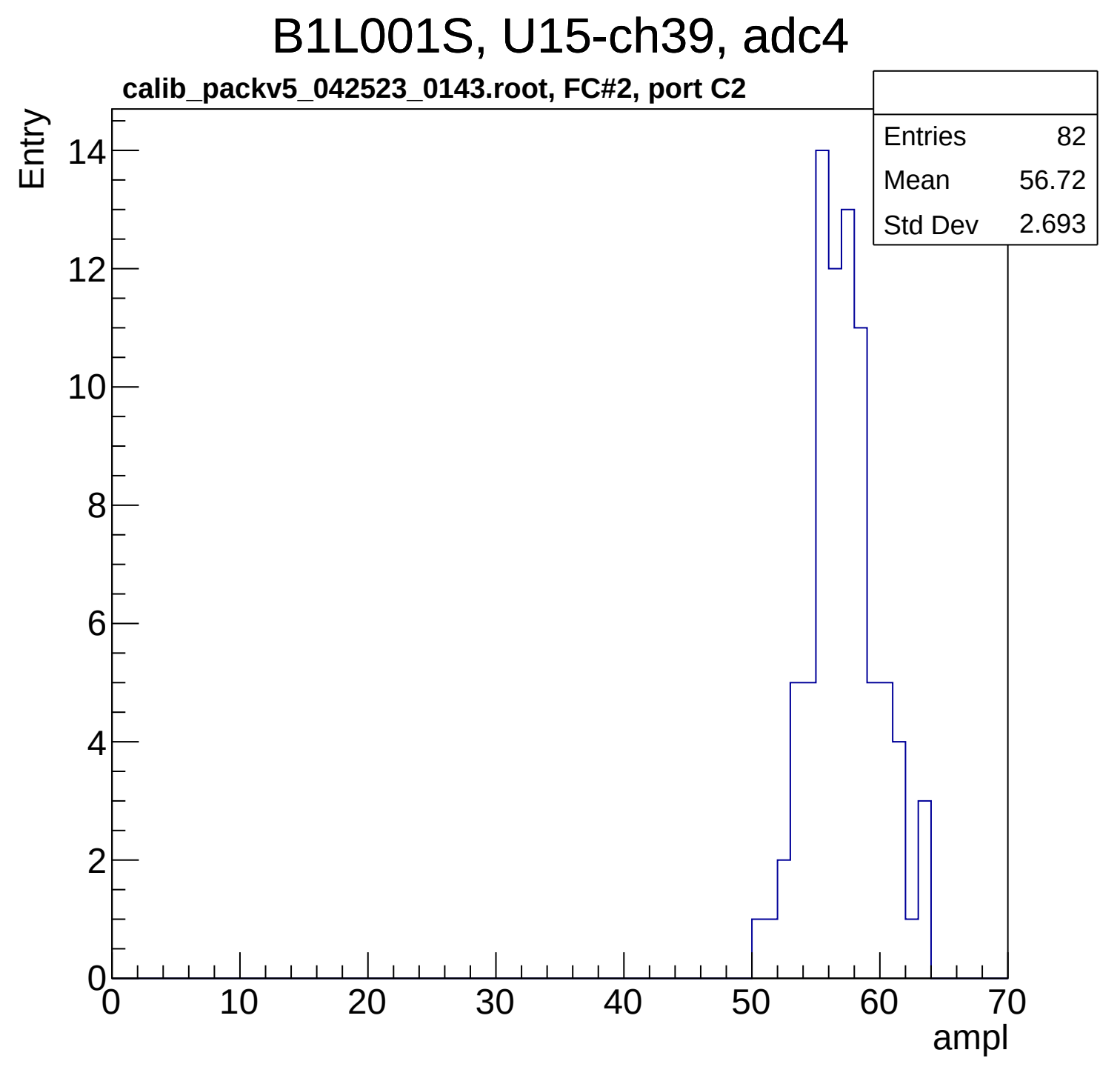

B1L001S, U15-ch39, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 170
Mean 50.89
Std Dev 3.706
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch39, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 56.72
Std Dev 2.693
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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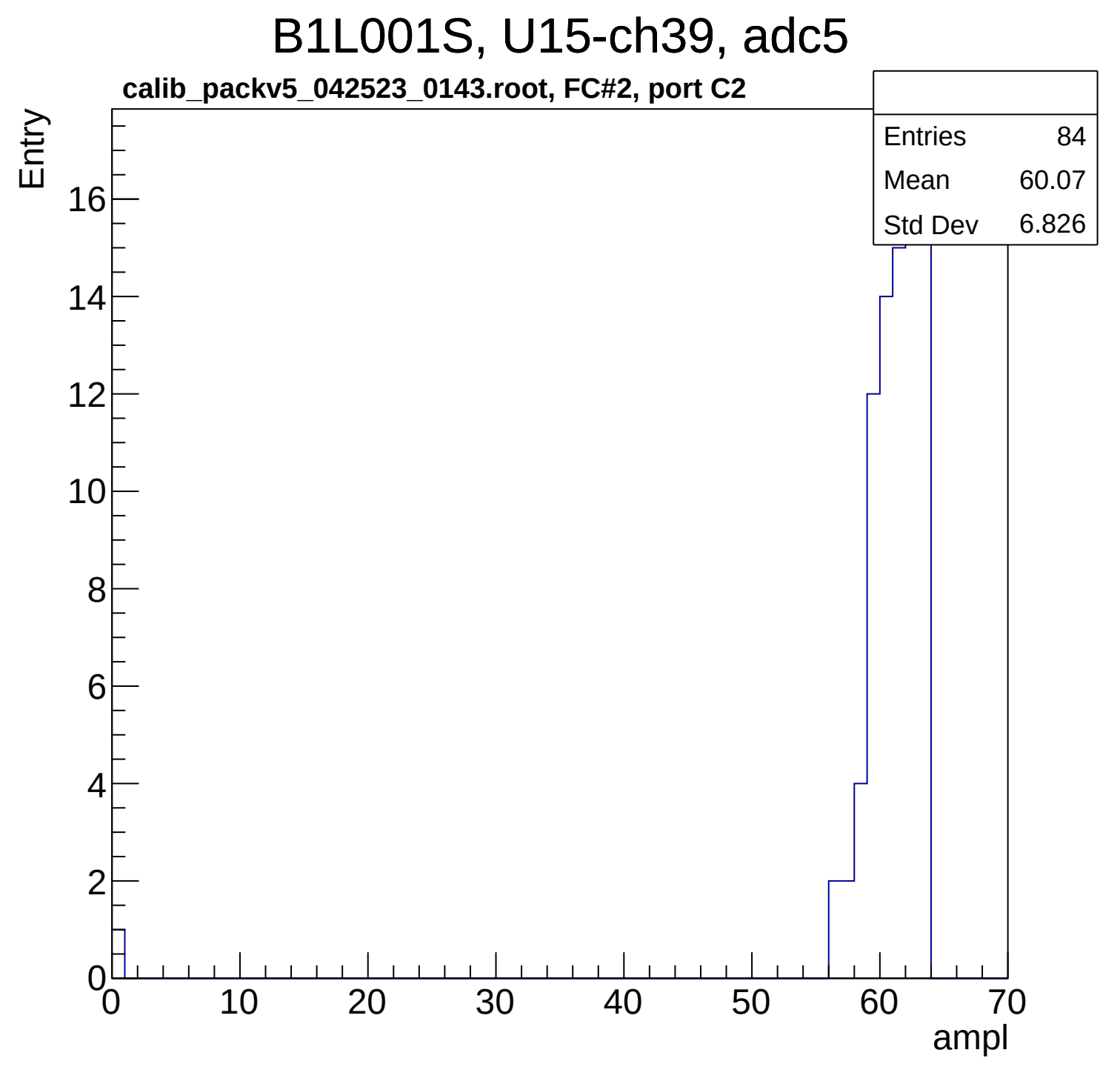
B1L001S, U15-ch39, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 60.07
Std Dev 6.826
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
50
60
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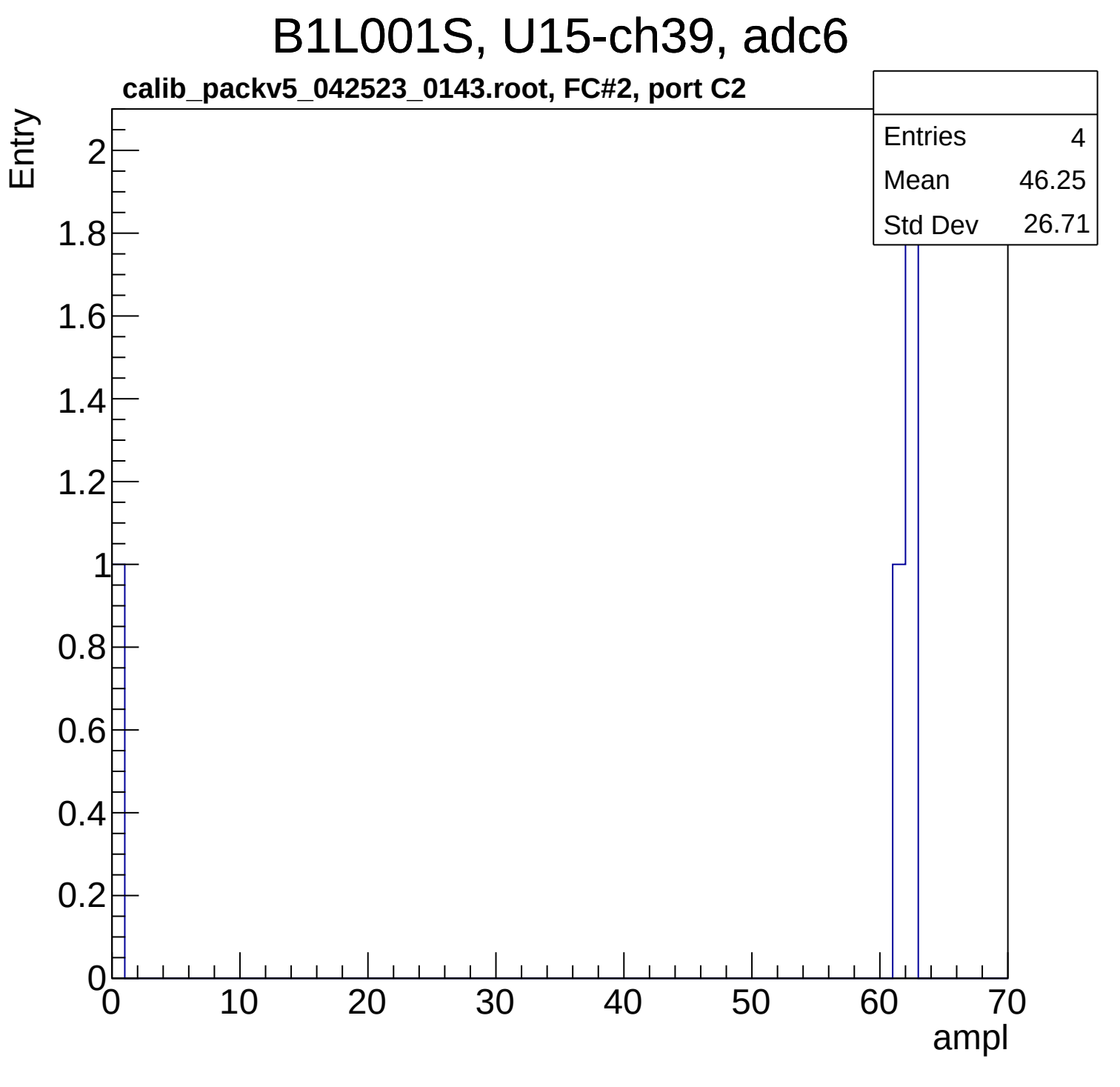
B1L001S, U15-ch39, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 46.25
Std Dev 26.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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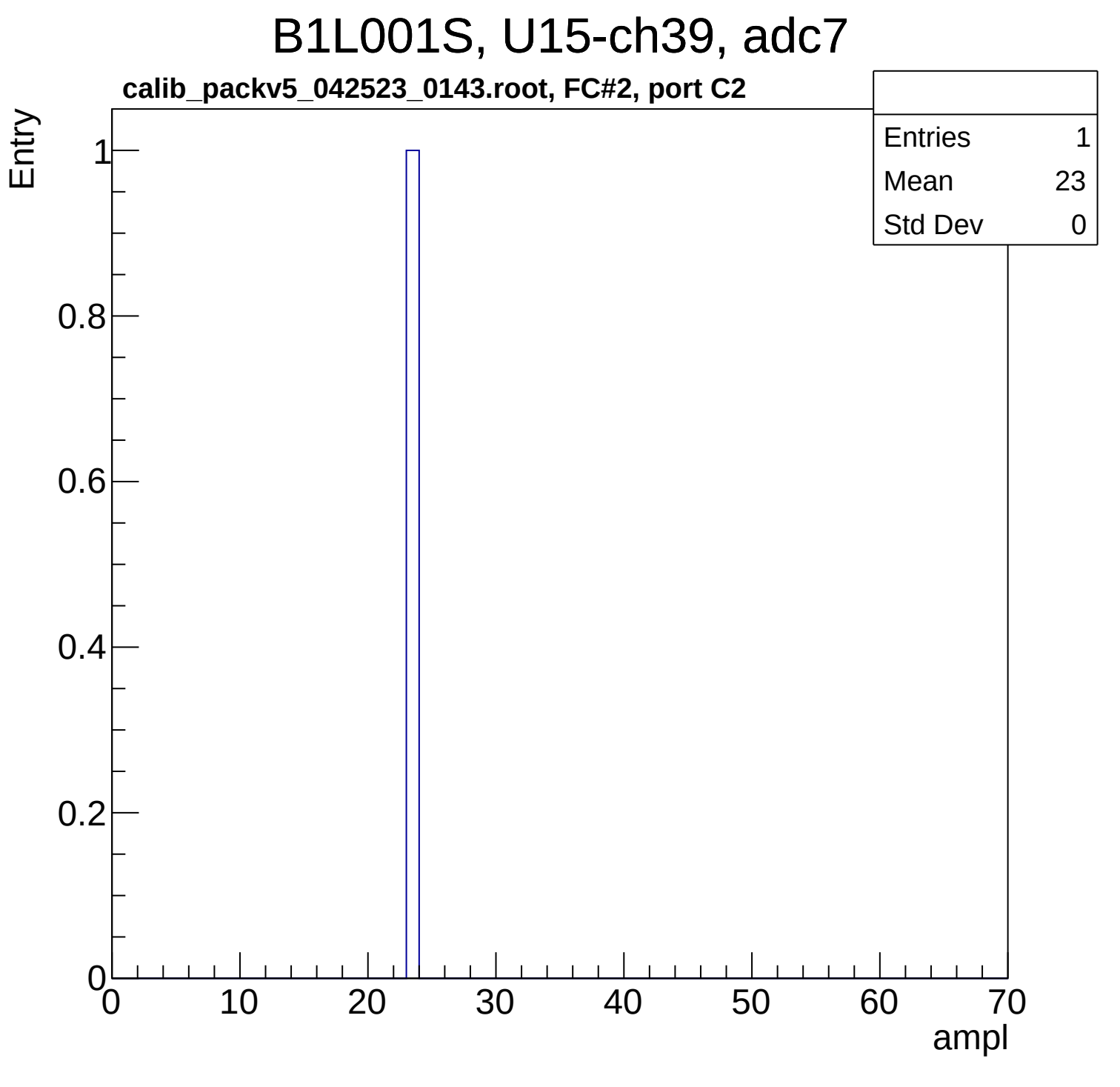
B1L001S, U15-ch39, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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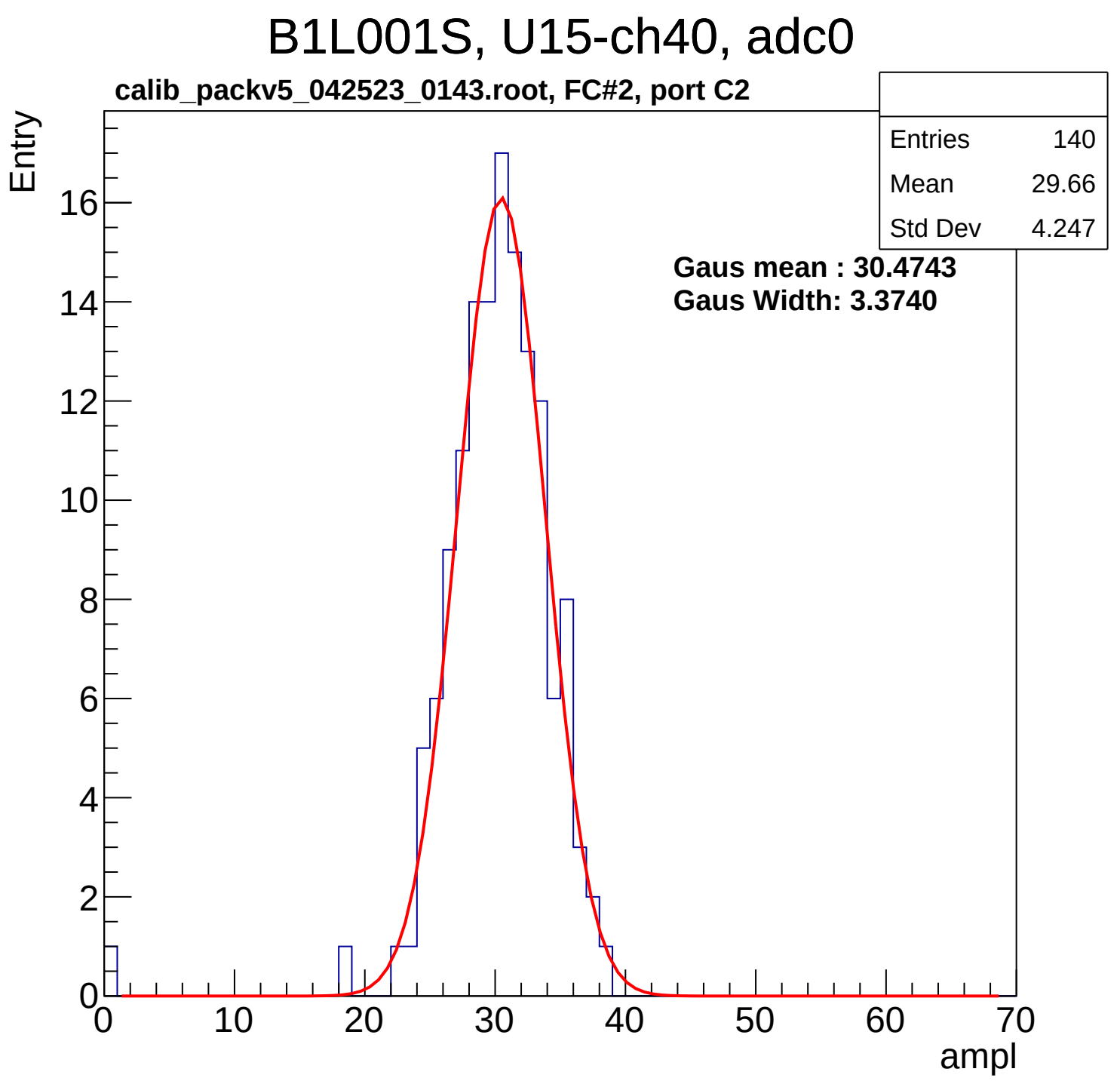
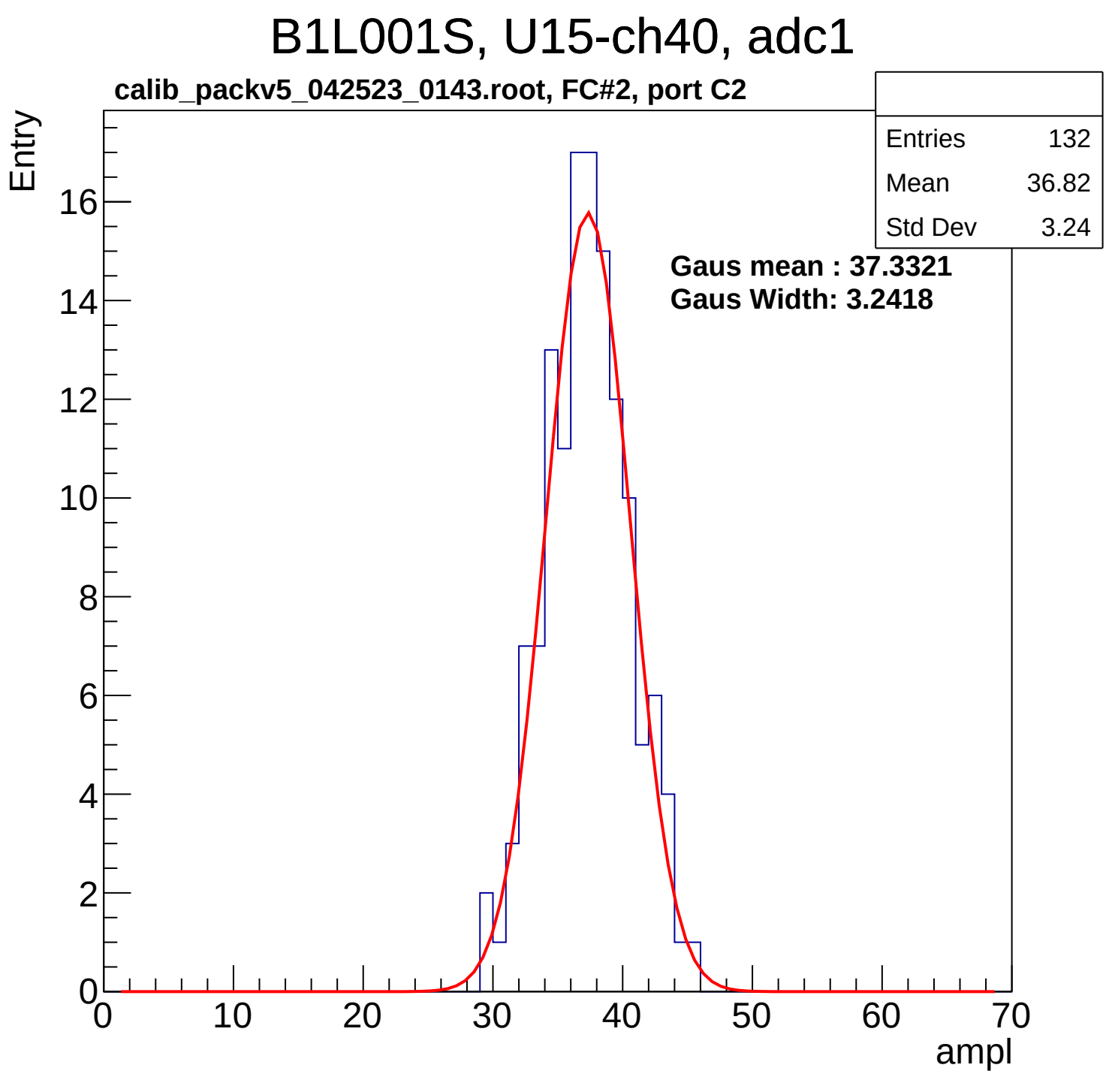
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch40, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 29.66
Std Dev 4.247
Gaus mean : 30.4743
Gaus Width: 3.3740
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch40, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 36.82
Std Dev 3.24
Gaus mean : 37.3321
Gaus Width: 3.2418
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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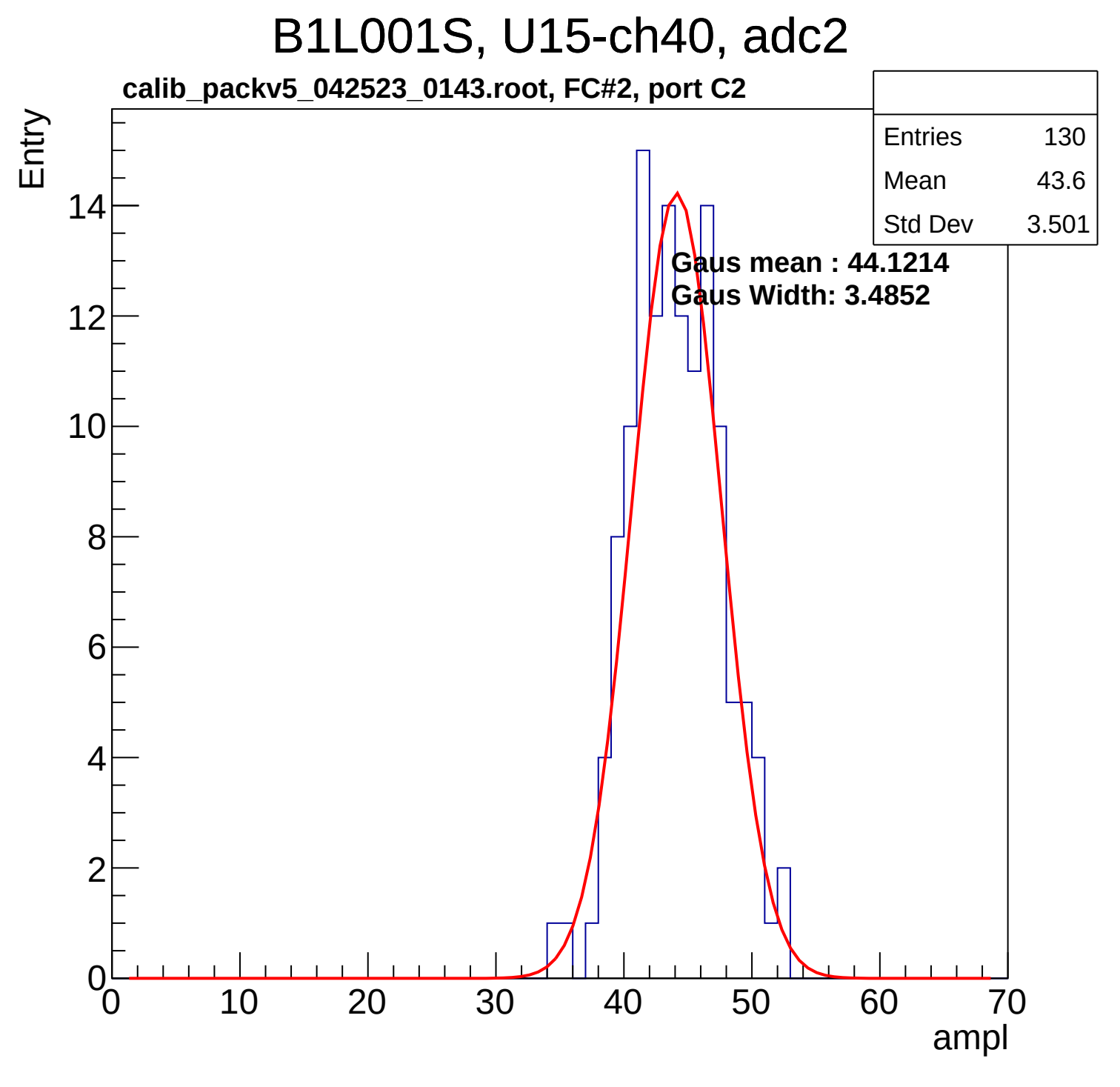
B1L001S, U15-ch40, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 43.6
Std Dev 3.501
Gaus mean : 44.1214
Gaus Width: 3.4852
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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60
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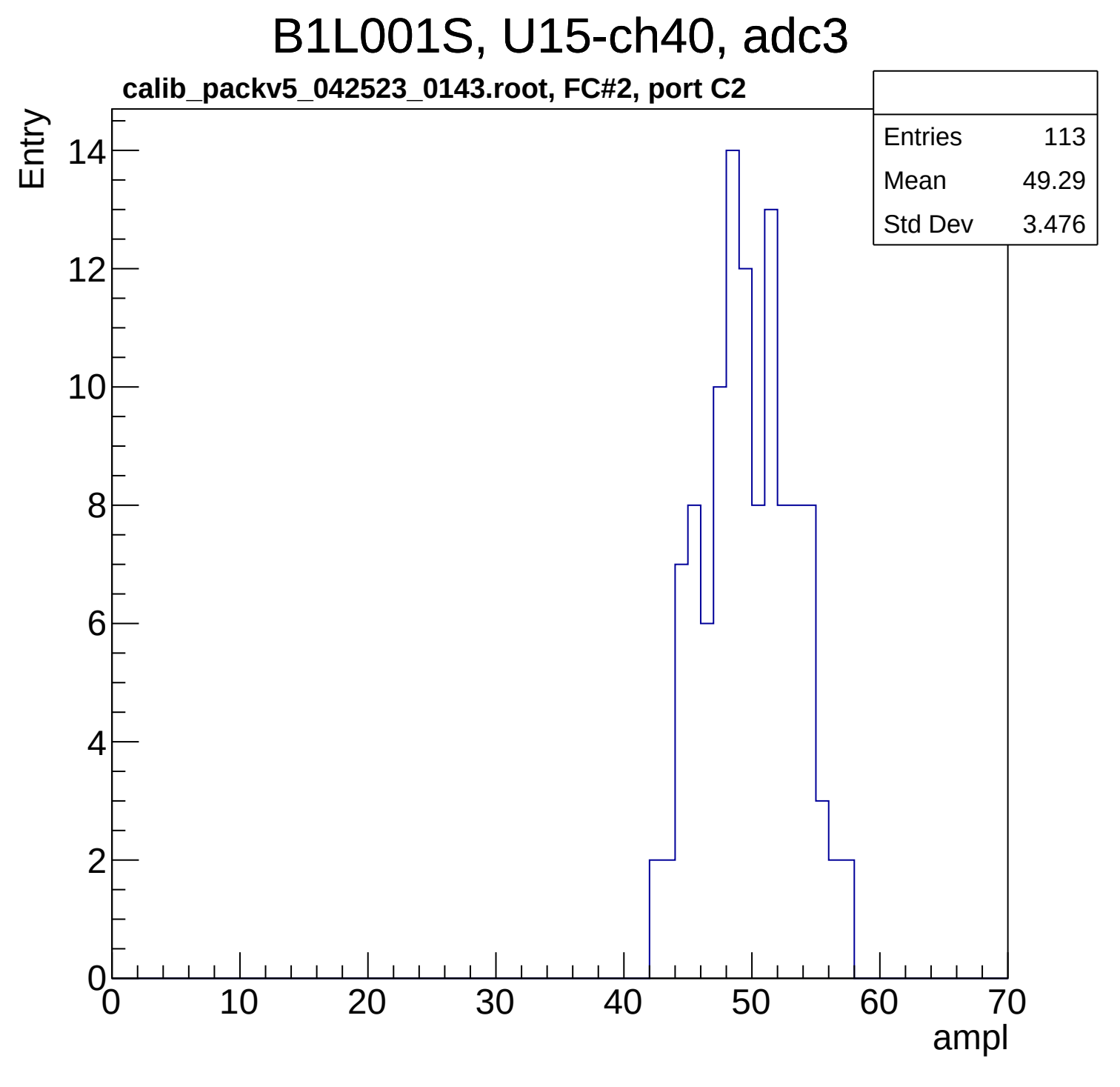
B1L001S, U15-ch40, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.29
Std Dev 3.476
Entry
ampl
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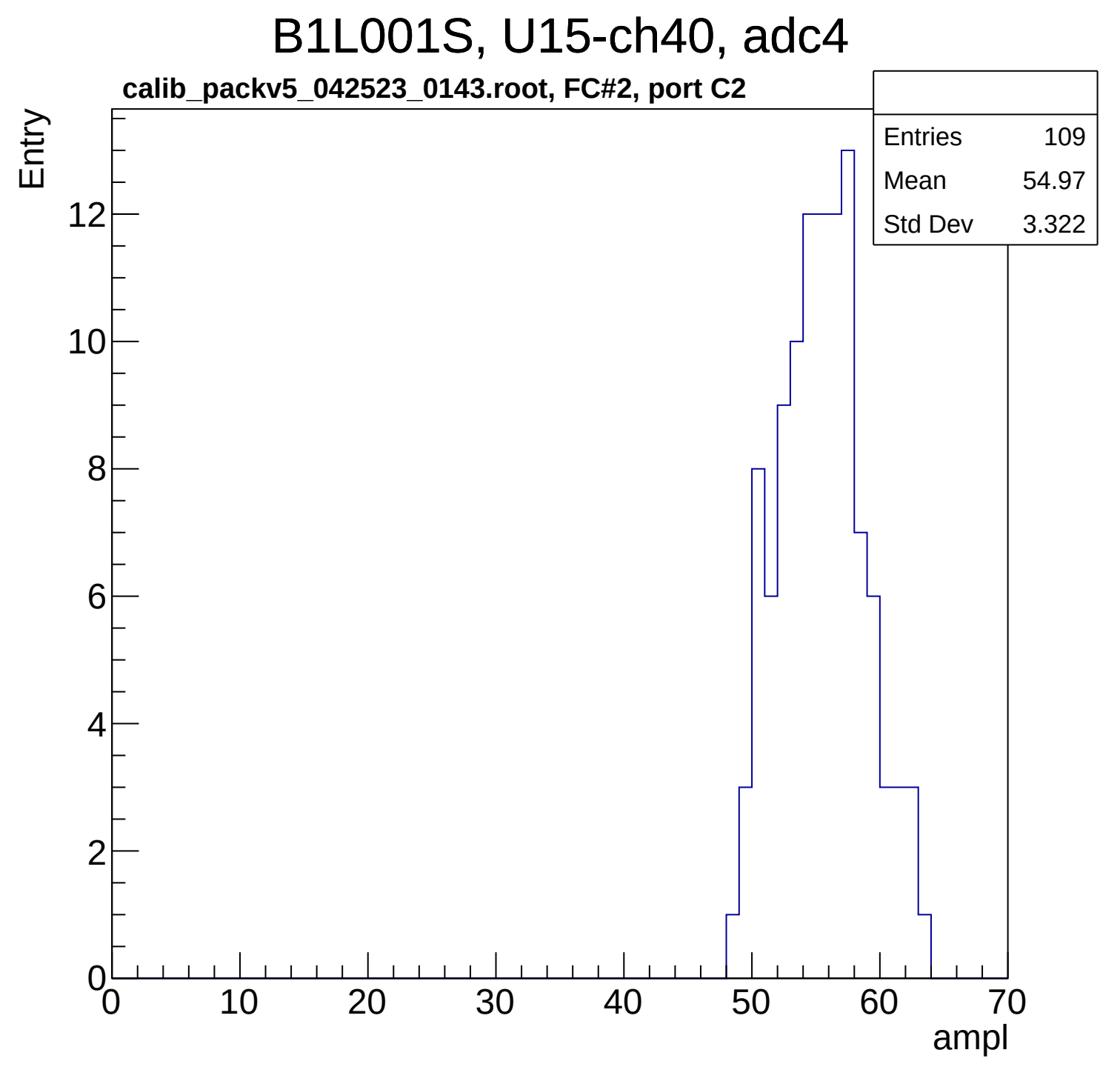
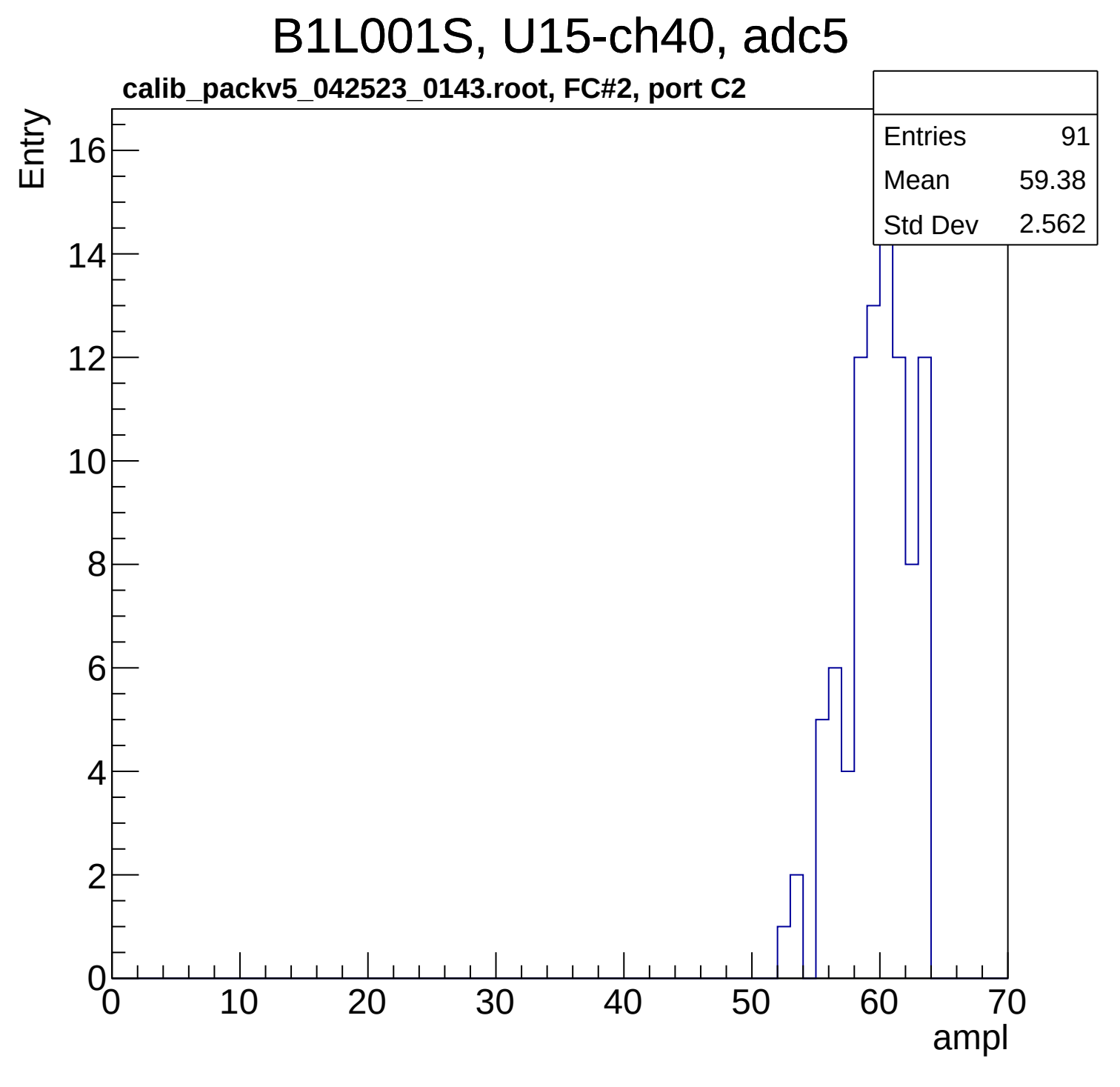
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch40, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 54.97
Std Dev 3.322
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch40, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.38
Std Dev 2.562
Entry
ampl
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8
10
12
14
16
0
10
20
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60
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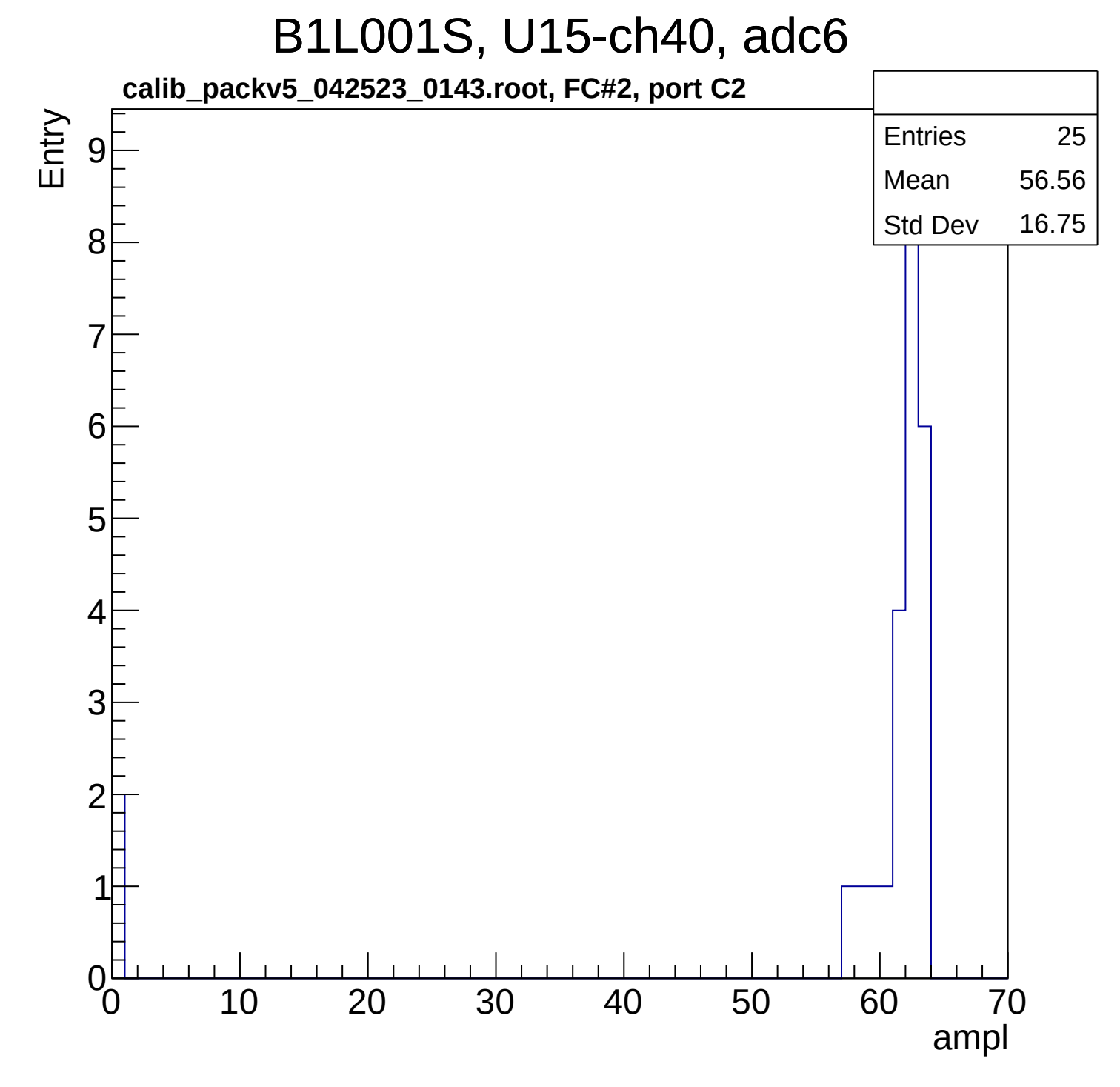
B1L001S, U15-ch40, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 25
Mean 56.56
Std Dev 16.75
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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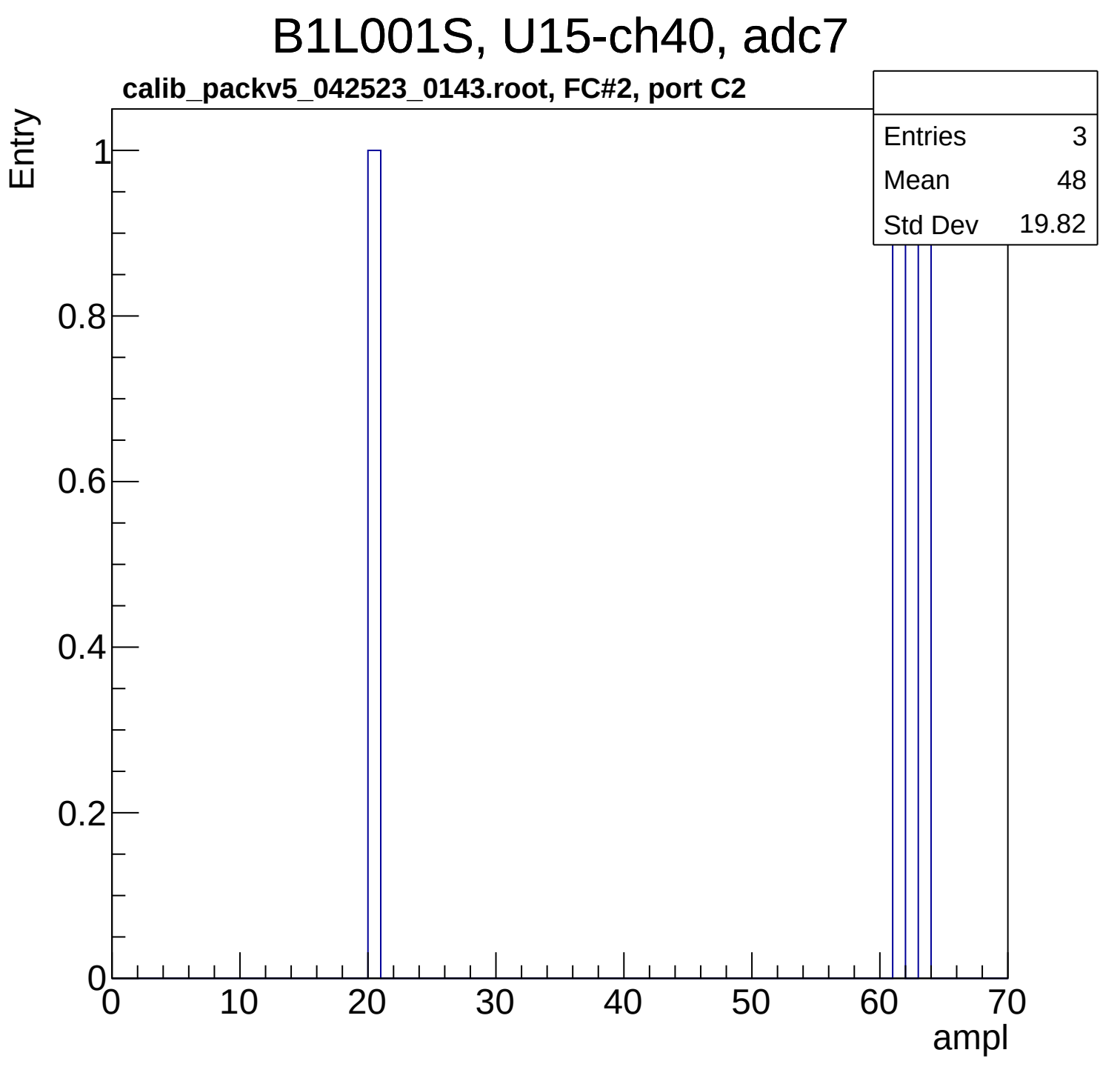
B1L001S, U15-ch40, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 48
Std Dev 19.82
Entry
ampl
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0.4
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0
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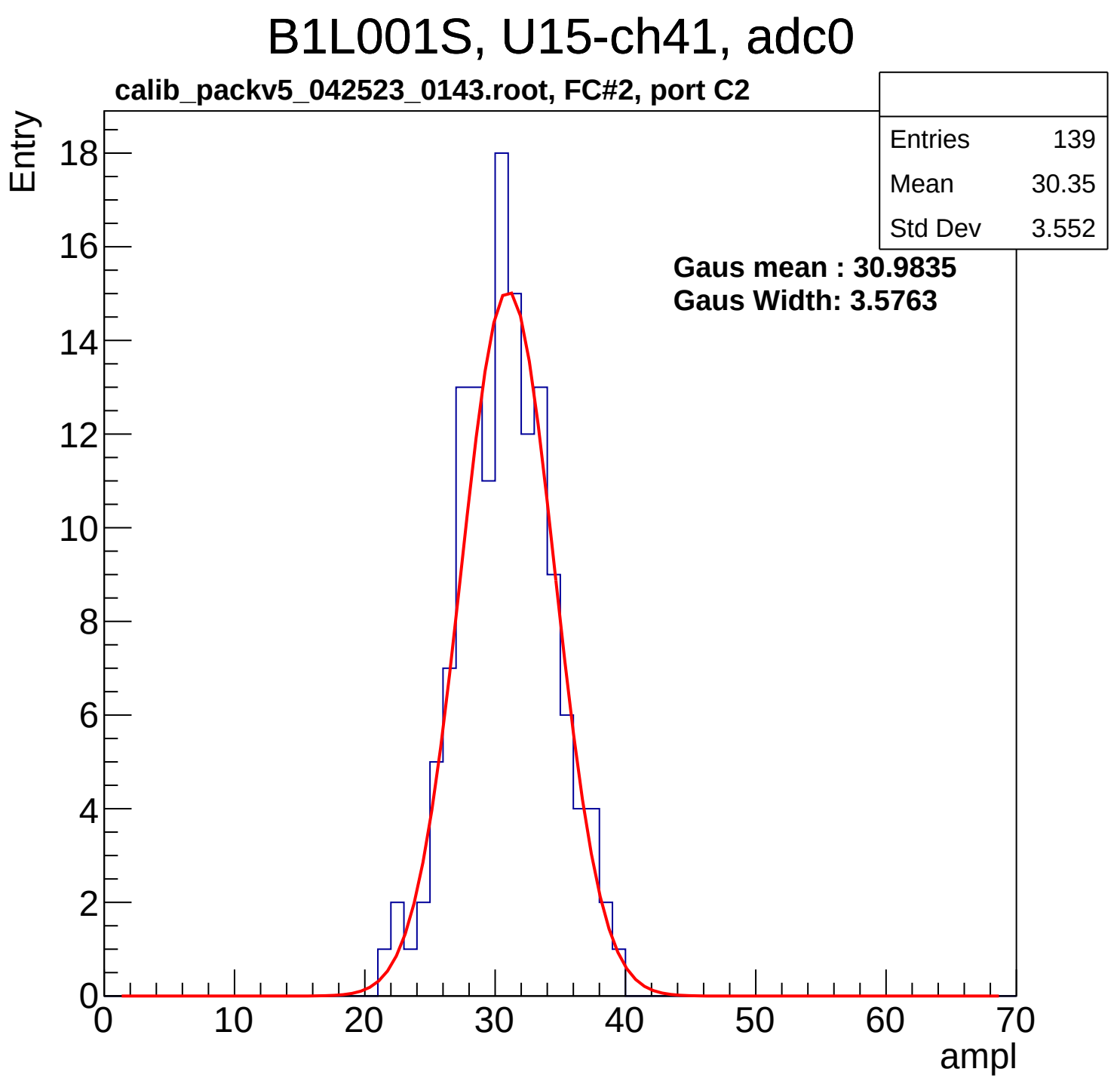
B1L001S, U15-ch41, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 30.35
Std Dev 3.552
Gaus mean : 30.9835
Gaus Width: 3.5763
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
40
50
60
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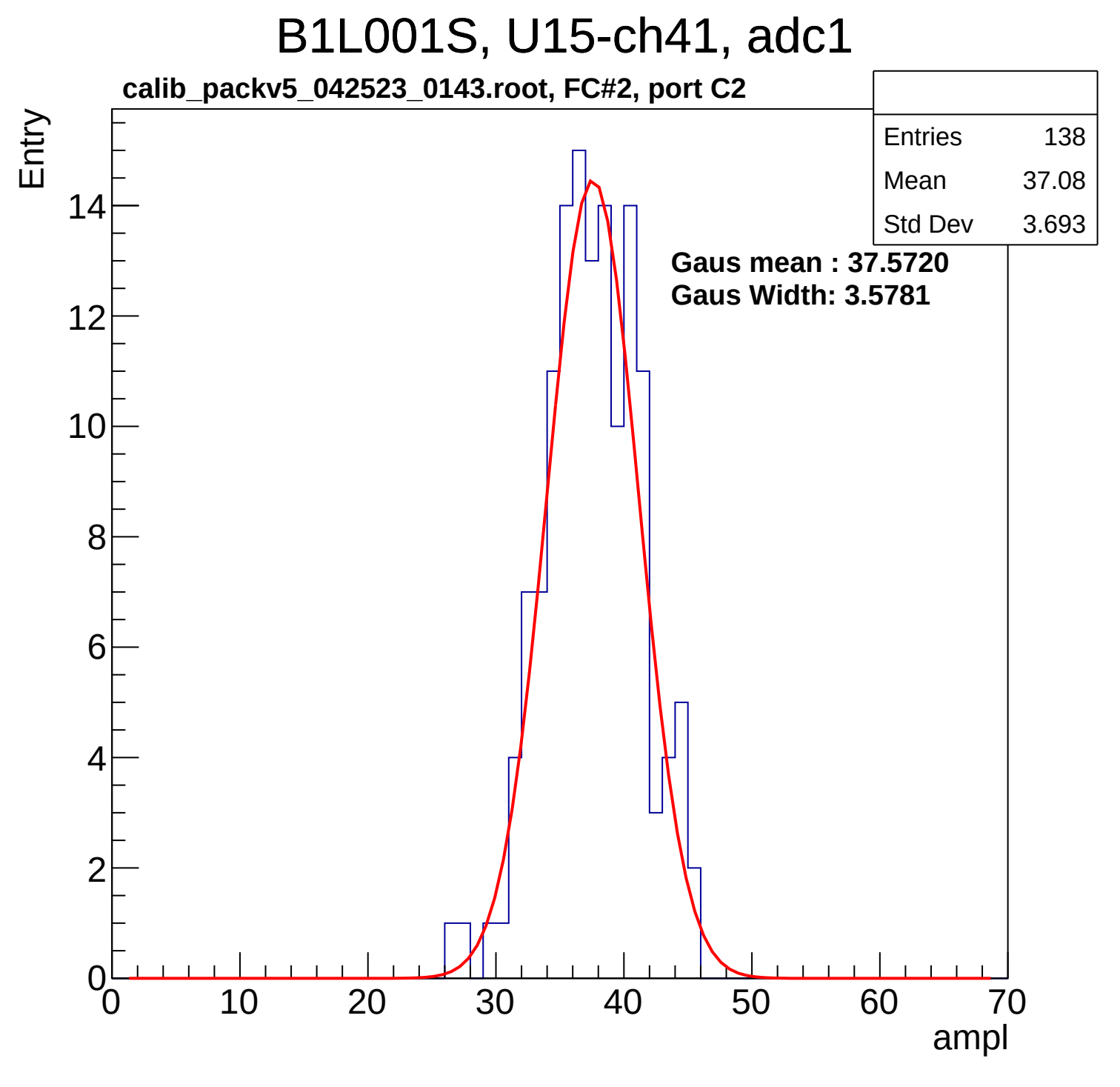
B1L001S, U15-ch41, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 37.08
Std Dev 3.693
Gaus mean : 37.5720
Gaus Width: 3.5781
Entry
ampl
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2
4
6
8
10
12
14
0
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20
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60
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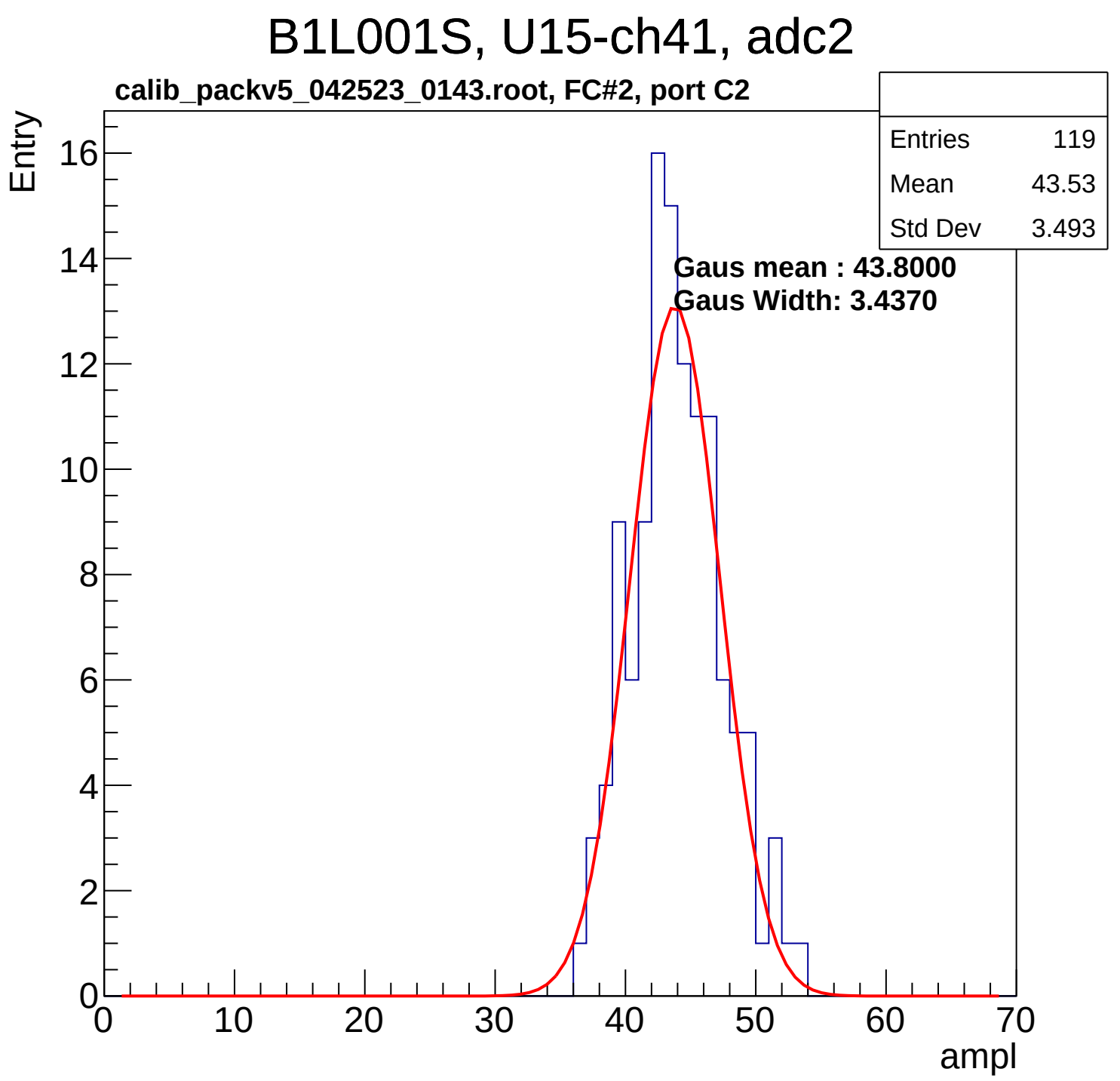
B1L001S, U15-ch41, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 43.53
Std Dev 3.493
Gaus mean : 43.8000
Gaus Width: 3.4370
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
50
60
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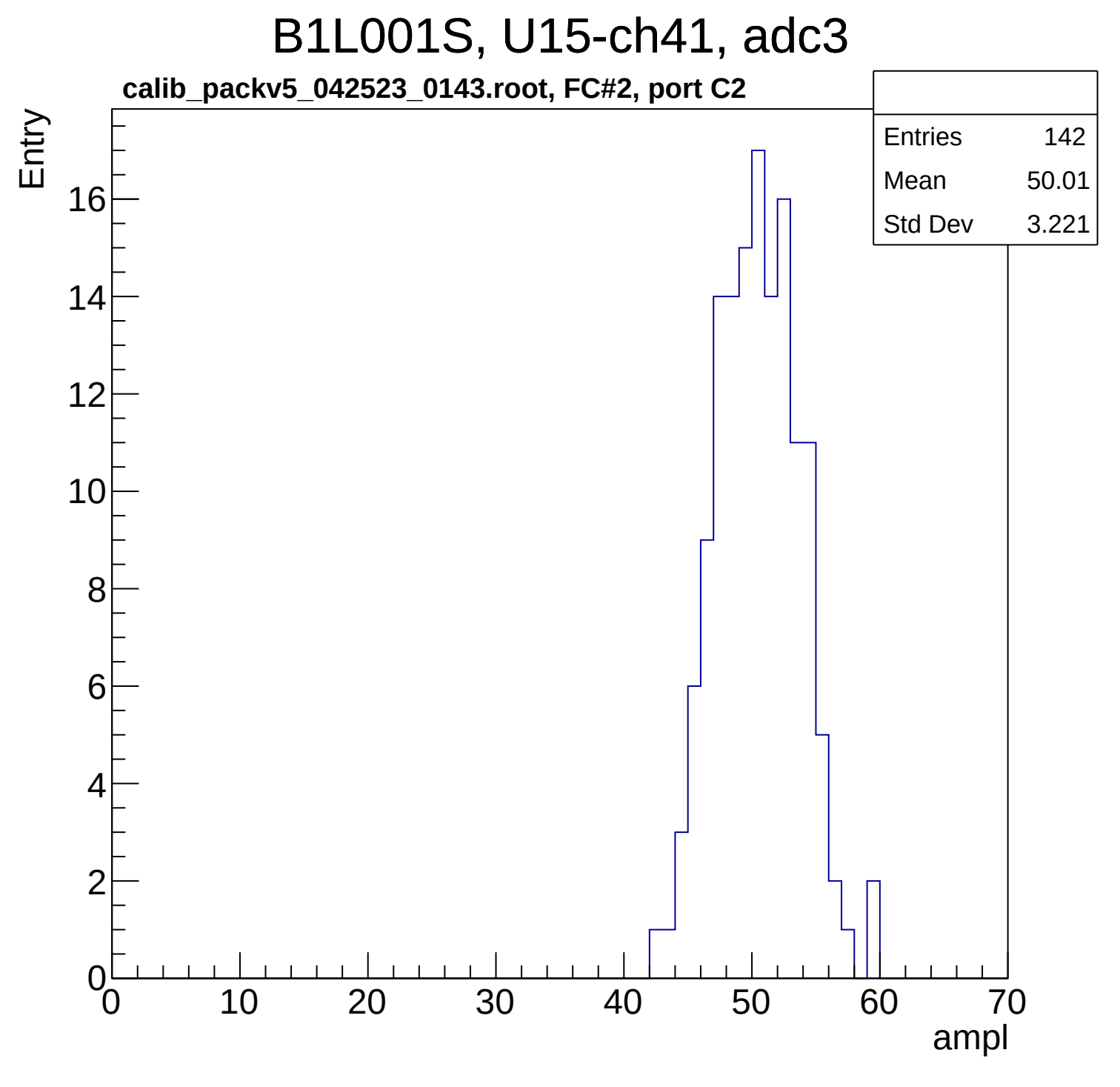
B1L001S, U15-ch41, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 50.01
Std Dev 3.221
Entry
ampl
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8
10
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14
16
0
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60
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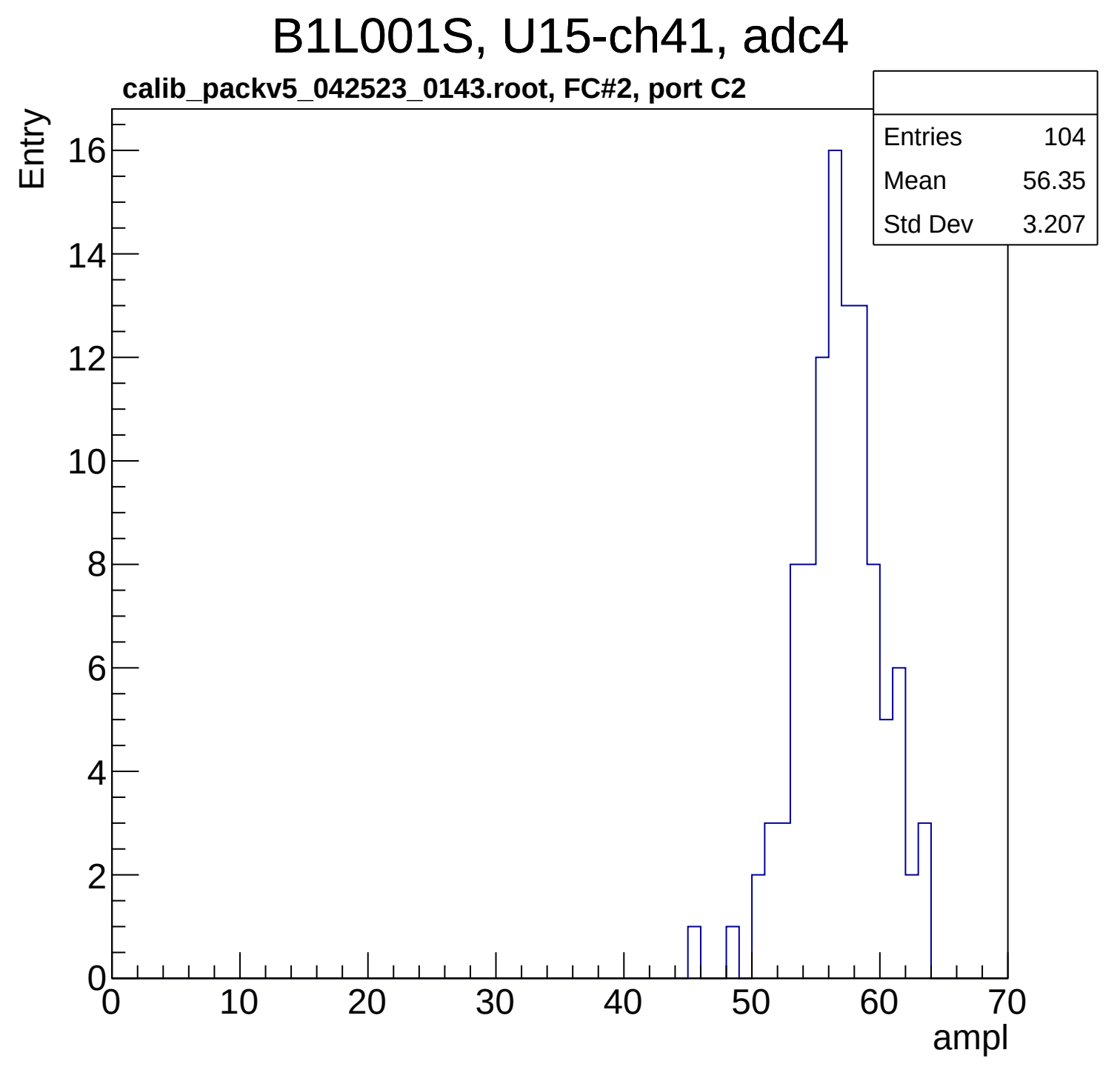
B1L001S, U15-ch41, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 56.35
Std Dev 3.207
Entry
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4
6
8
10
12
14
16
0
10
20
30
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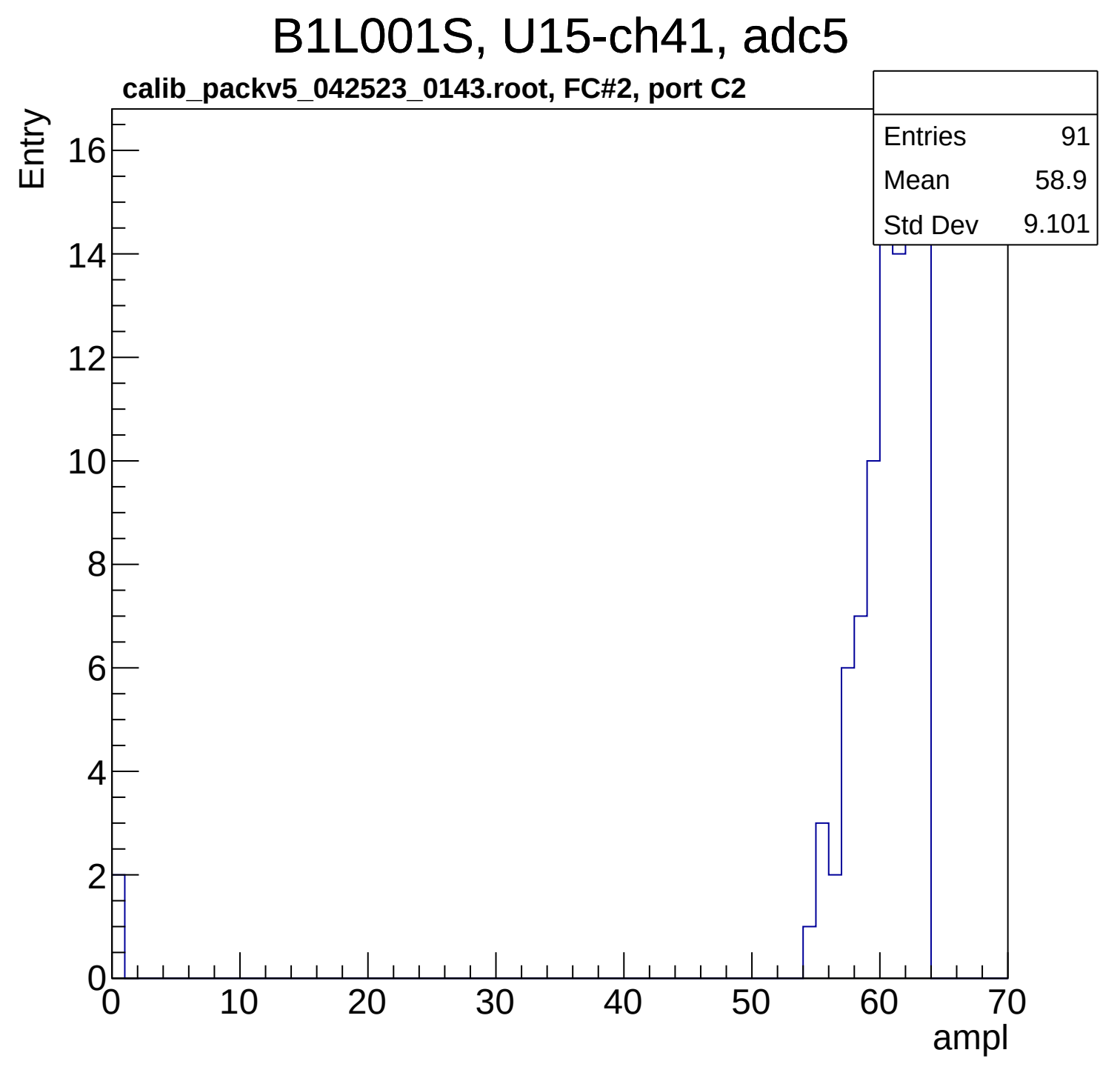
50
60
70
ampl


B1L001S, U15-ch41, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 58.9
Std Dev 9.101
Entry
ampl
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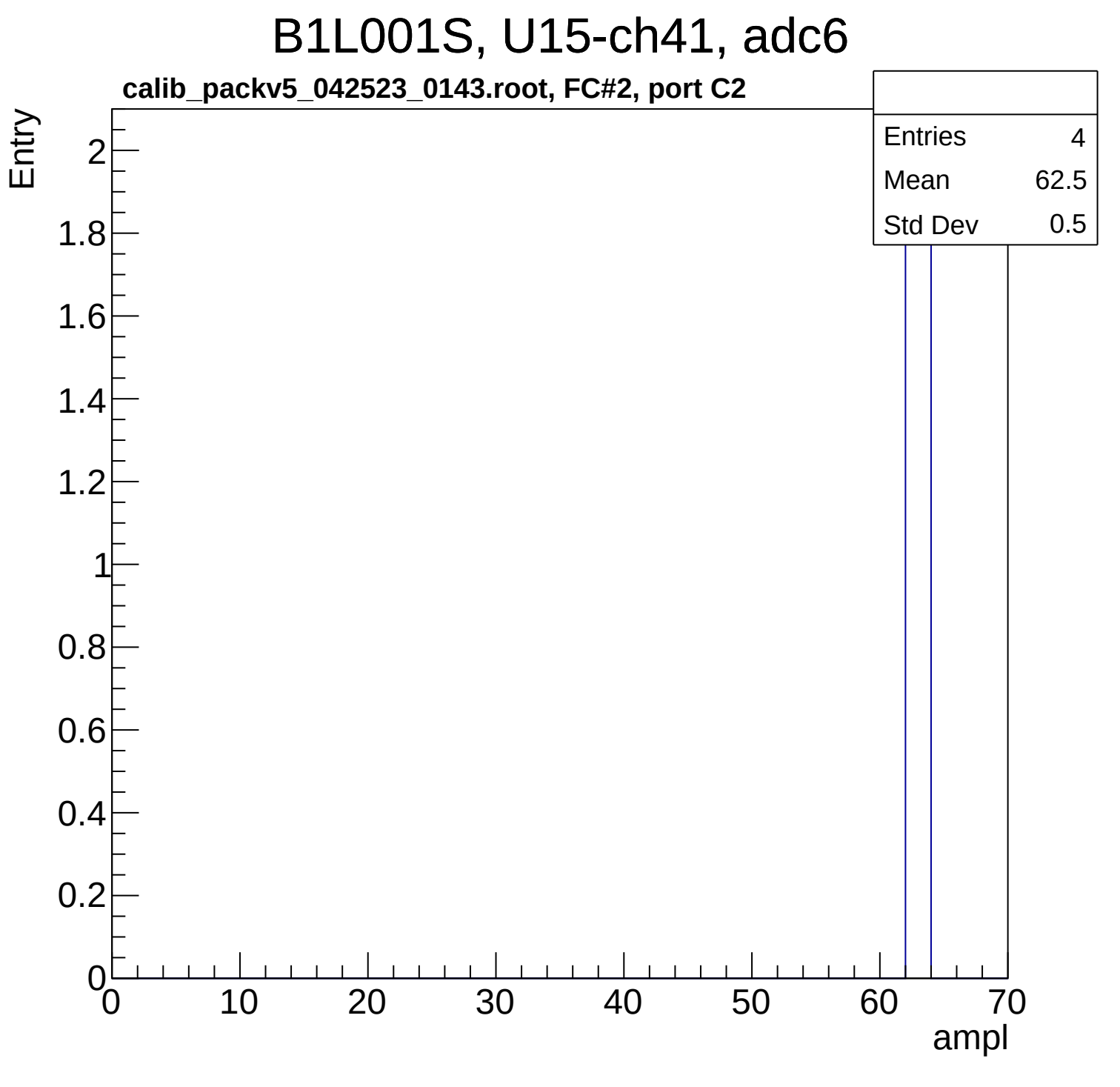
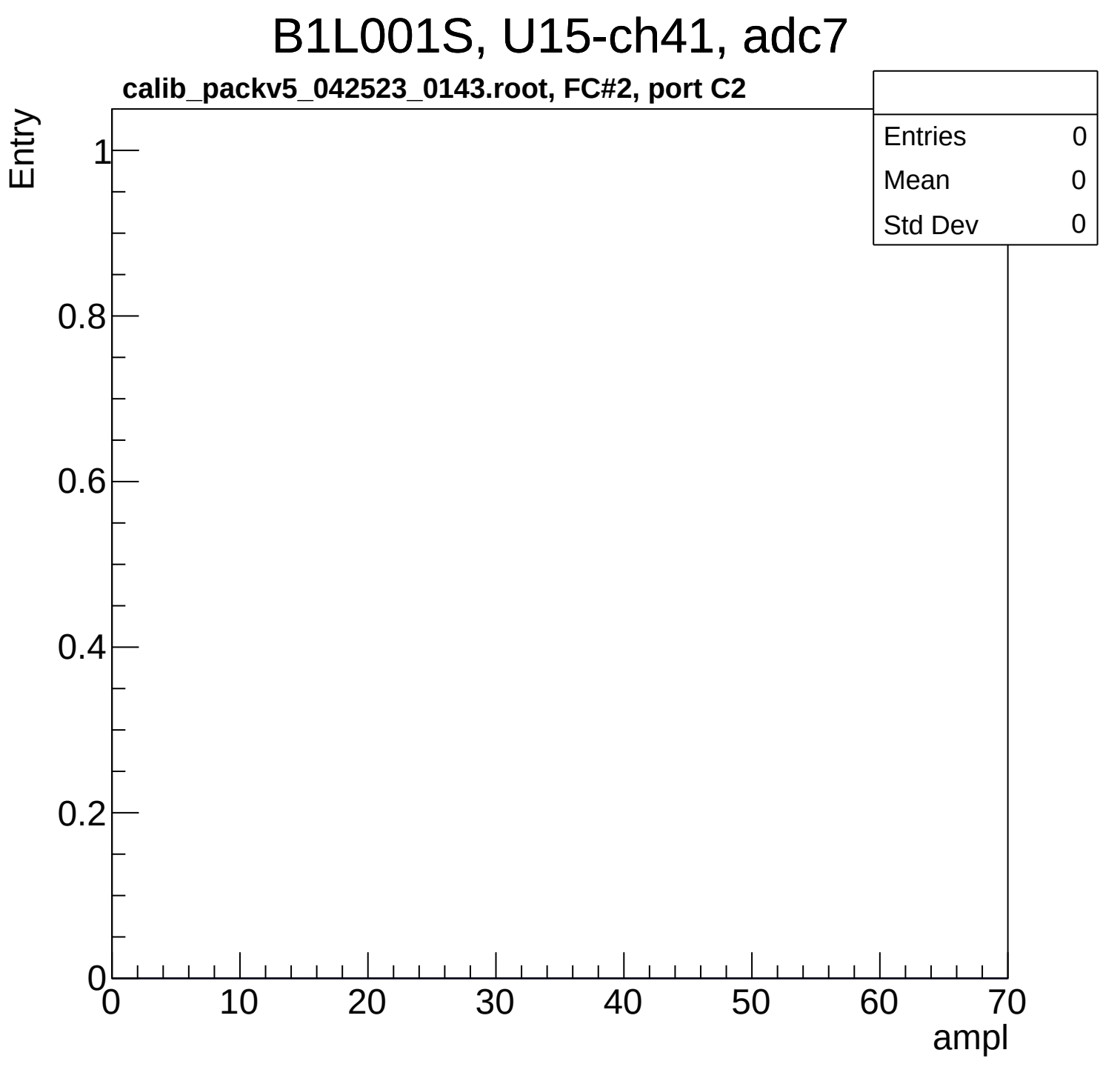
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch41, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch41, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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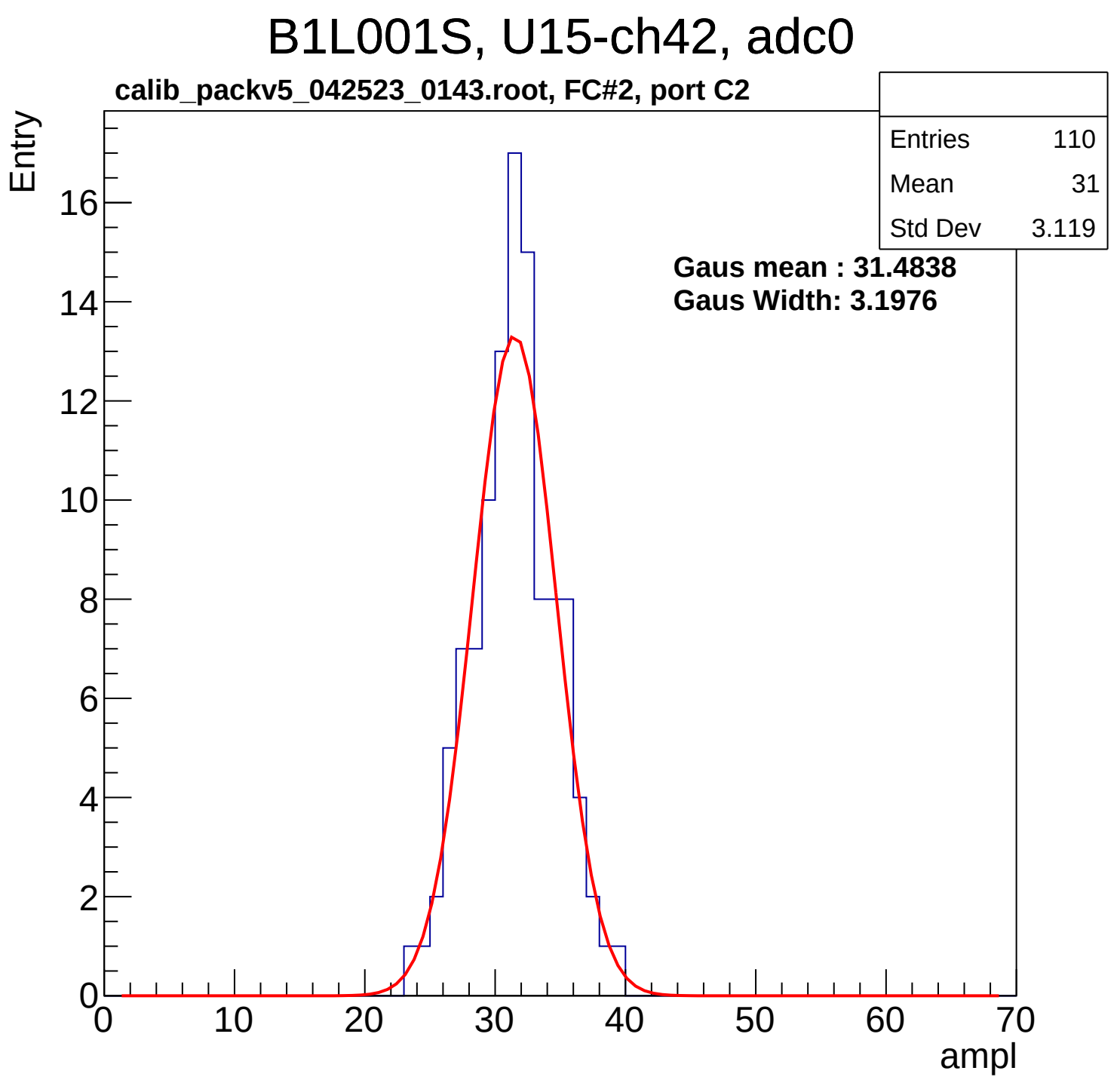
50
60
70
ampl


B1L001S, U15-ch42, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 31
Std Dev 3.119
Gaus mean : 31.4838
Gaus Width: 3.1976
Entry
ampl
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4
6
8
10
12
14
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0
10
20
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60
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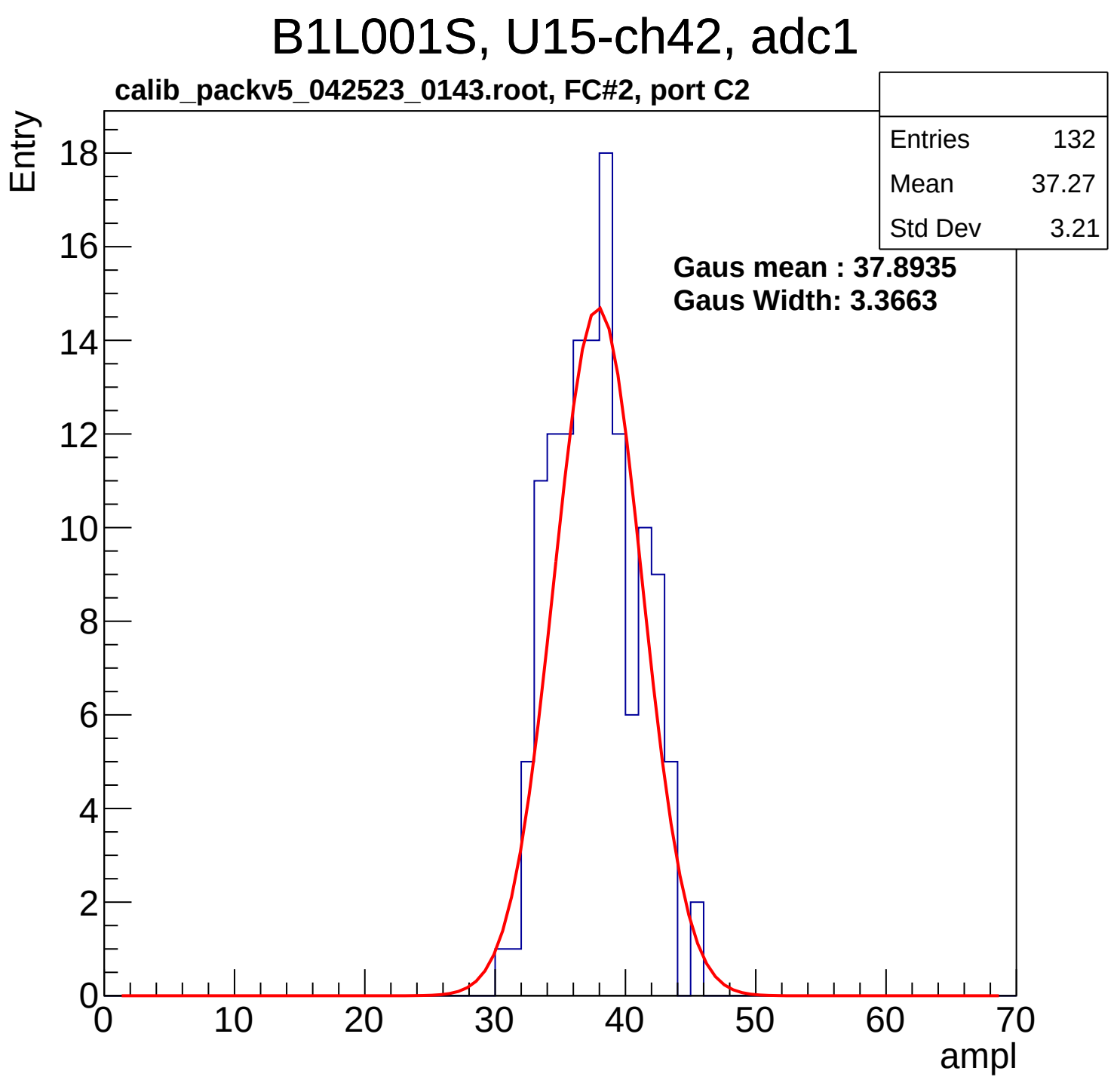
B1L001S, U15-ch42, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 37.27
Std Dev 3.21
Gaus mean : 37.8935
Gaus Width: 3.3663
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
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60
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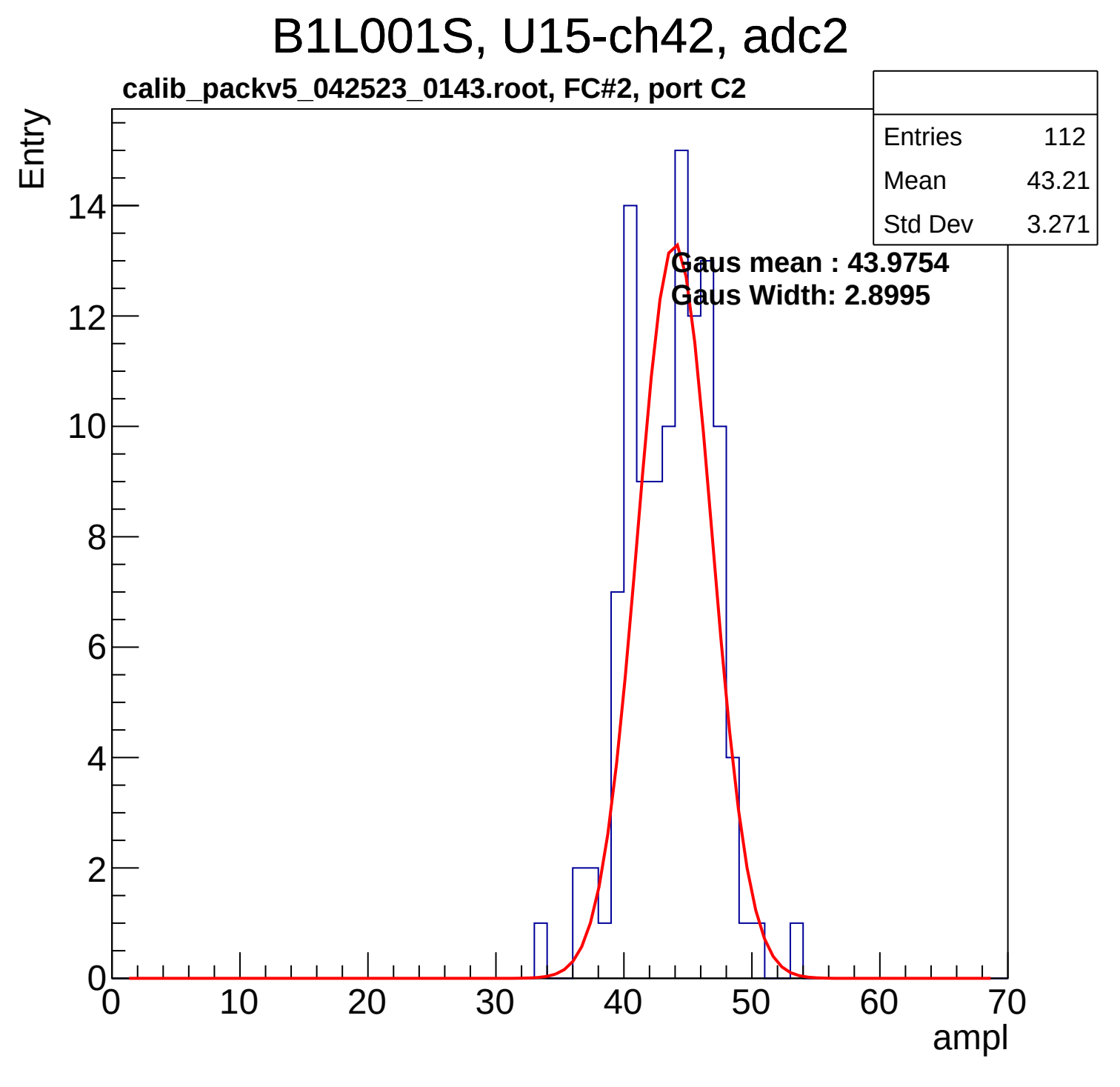
B1L001S, U15-ch42, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43.21
Std Dev 3.271
Gaus mean : 43.9754
Gaus Width: 2.8995
Entry
ampl
0
2
4
6
8
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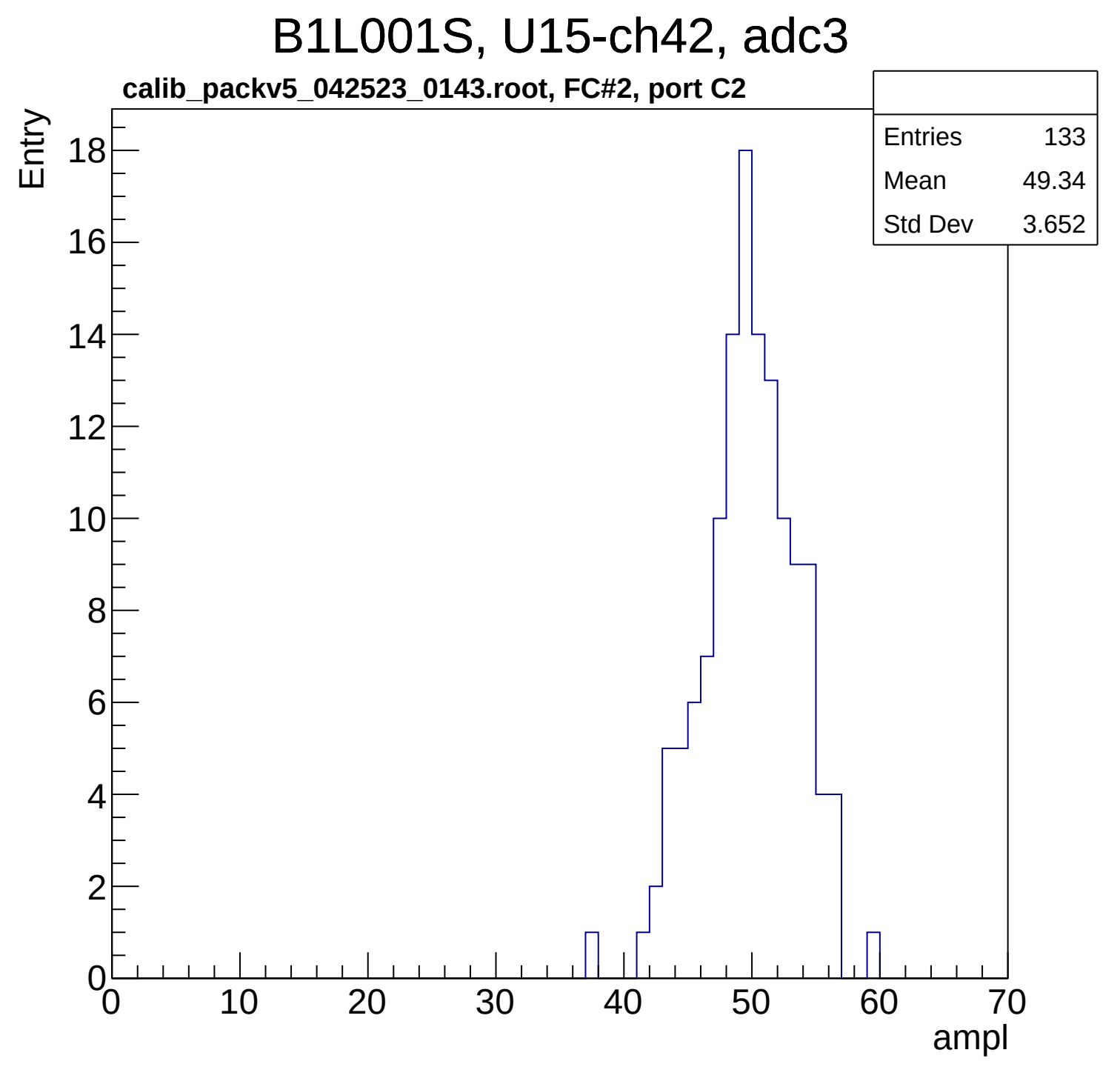
B1L001S, U15-ch42, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 49.34
Std Dev 3.652
Entry
ampl
0
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4
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8
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0
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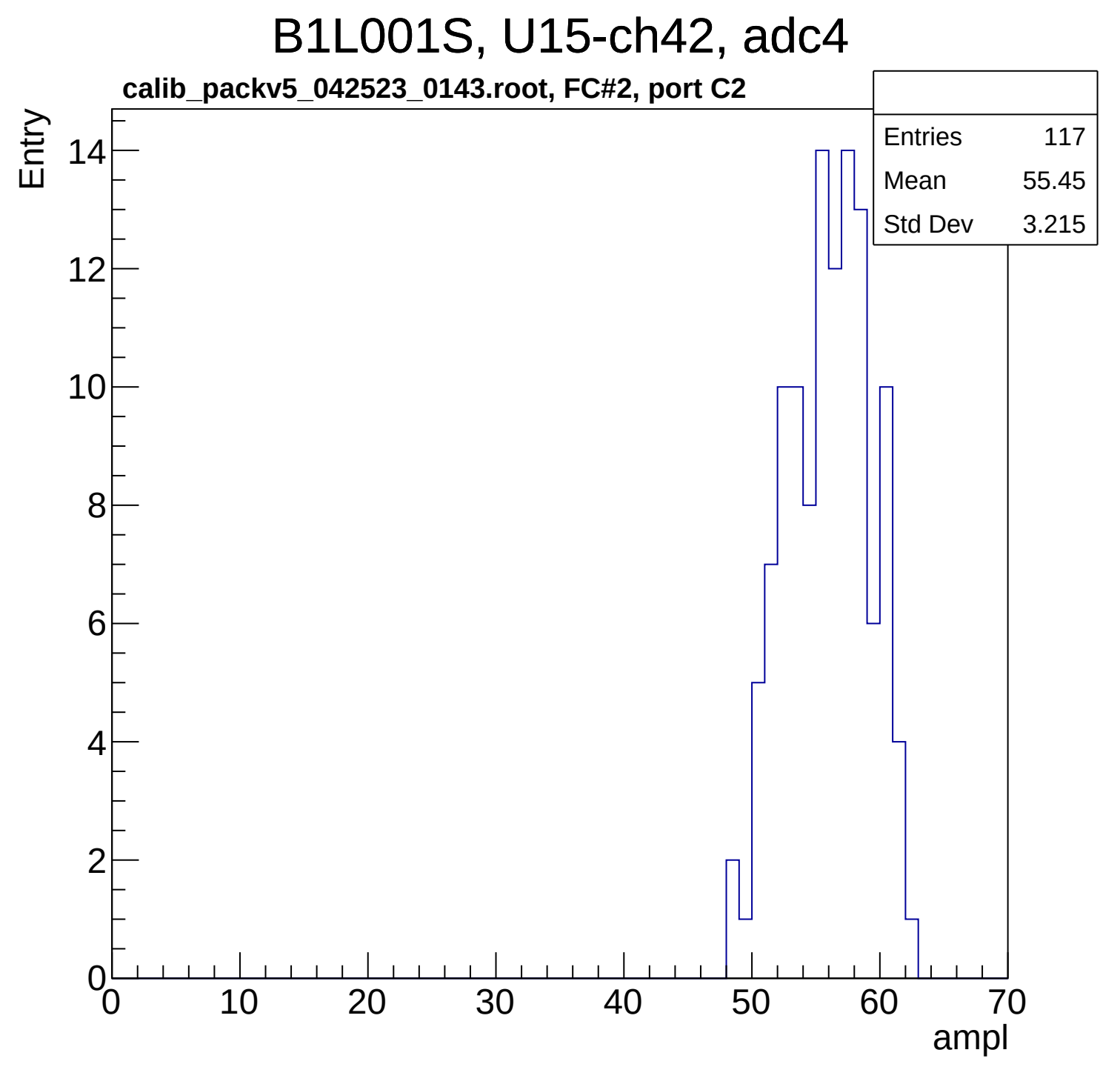
B1L001S, U15-ch42, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 55.45
Std Dev 3.215
Entry
ampl
0
2
4
6
8
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0
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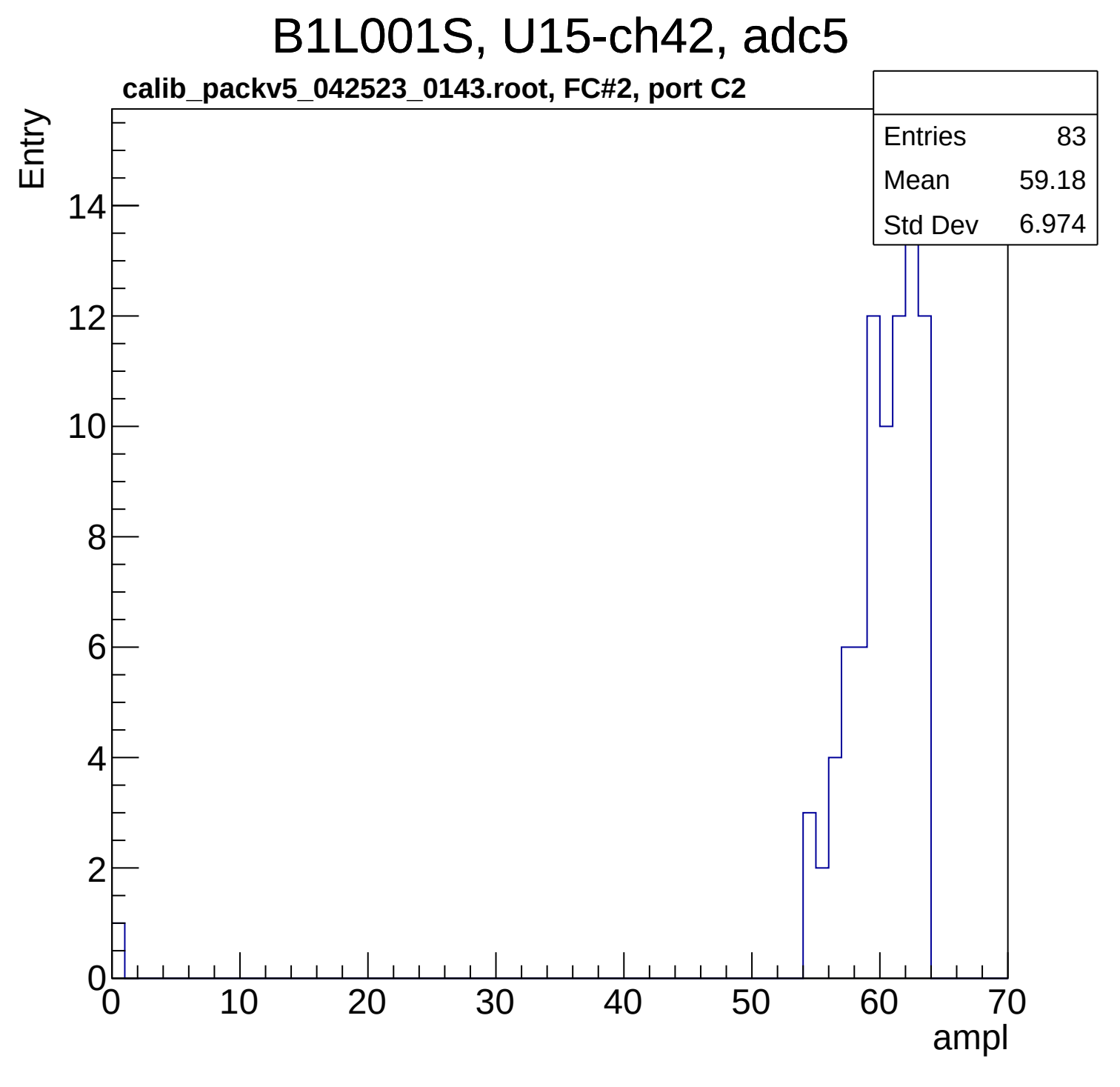
B1L001S, U15-ch42, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 59.18
Std Dev 6.974
Entry
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2
4
6
8
10
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14
0
10
20
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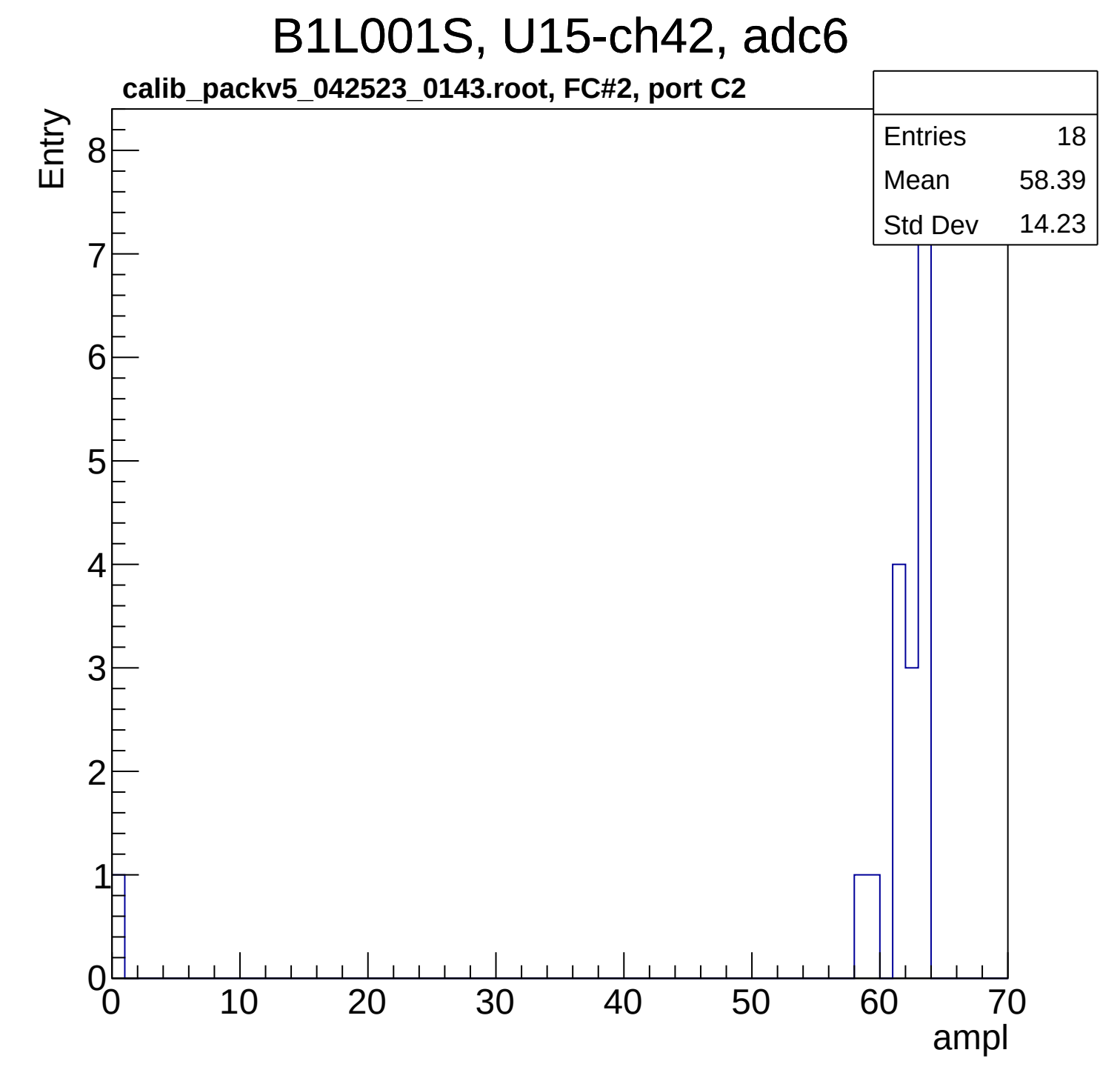
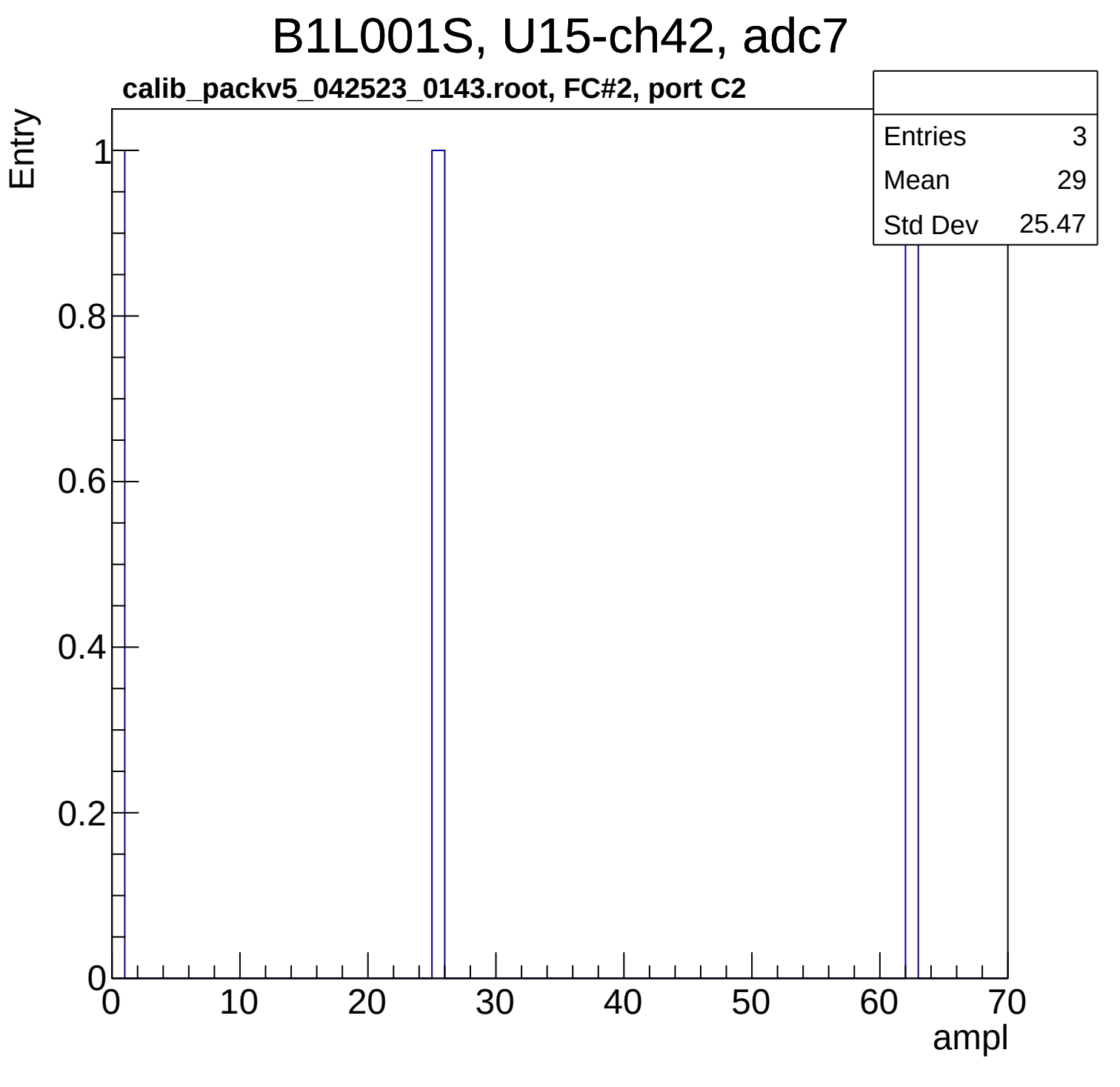
60
70
ampl


B1L001S, U15-ch42, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 58.39
Std Dev 14.23
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch42, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 29
Std Dev 25.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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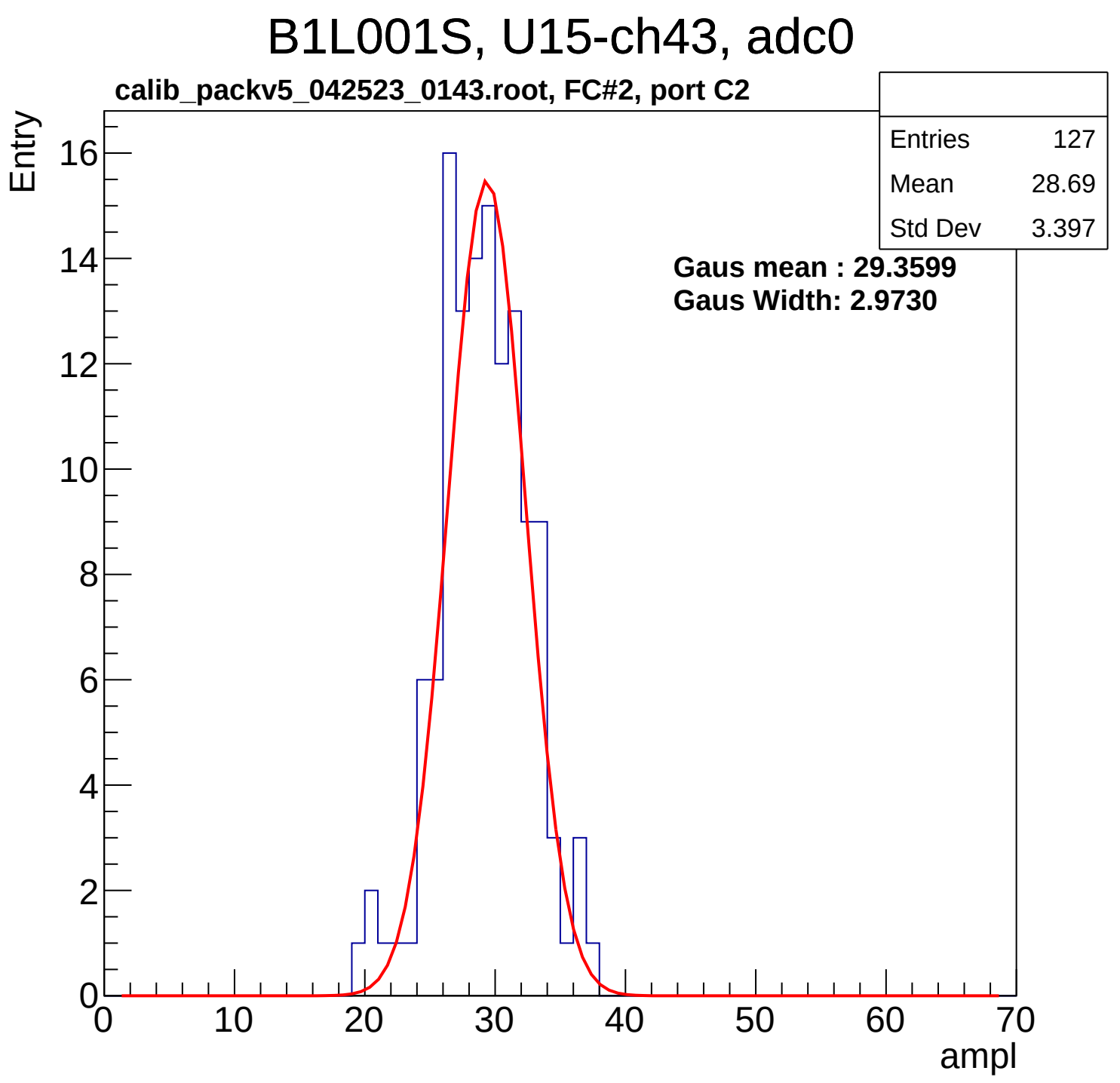
B1L001S, U15-ch43, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 28.69
Std Dev 3.397
Gaus mean : 29.3599
Gaus Width: 2.9730
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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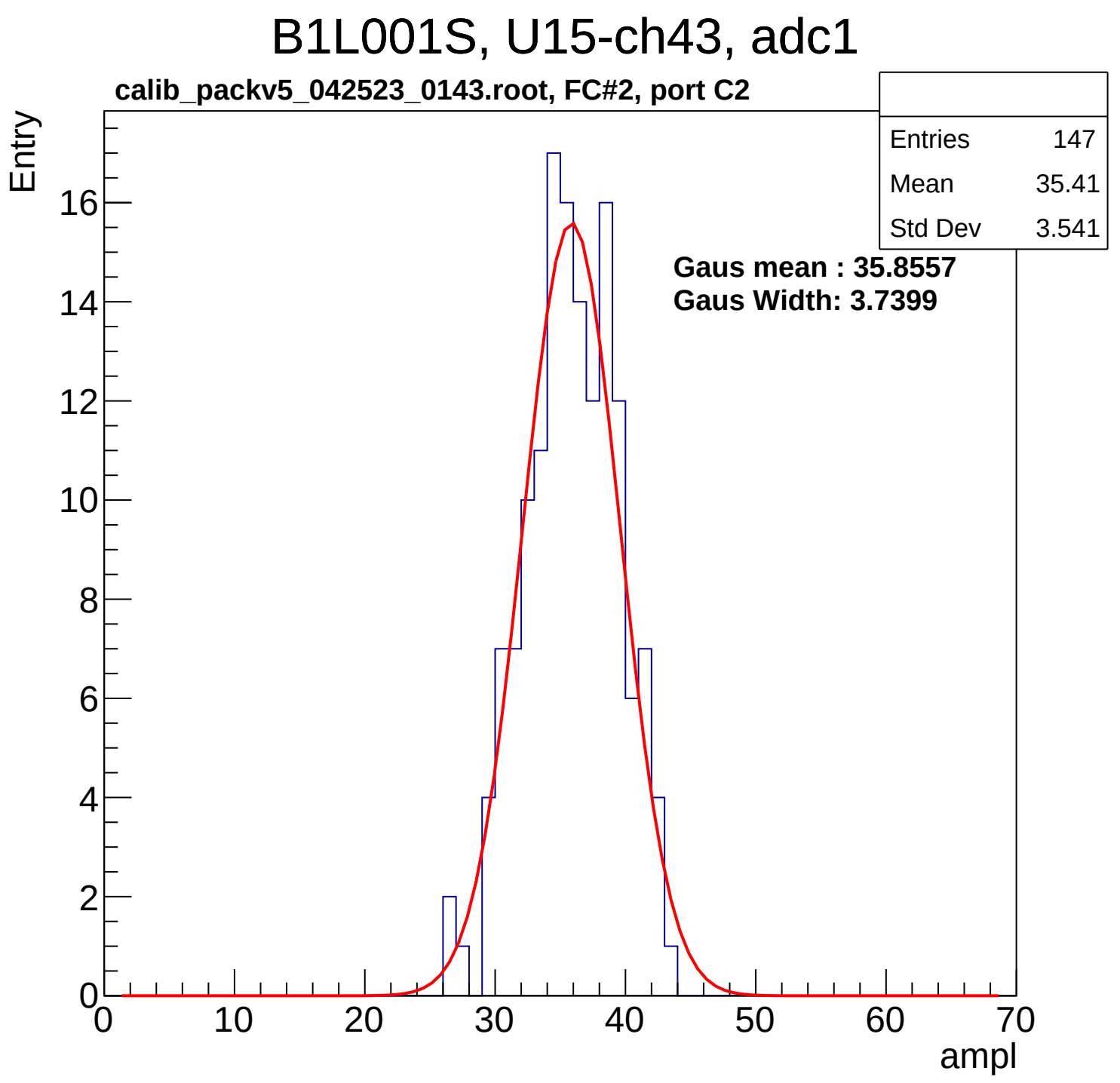
B1L001S, U15-ch43, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 35.41
Std Dev 3.541
Gaus mean : 35.8557
Gaus Width: 3.7399
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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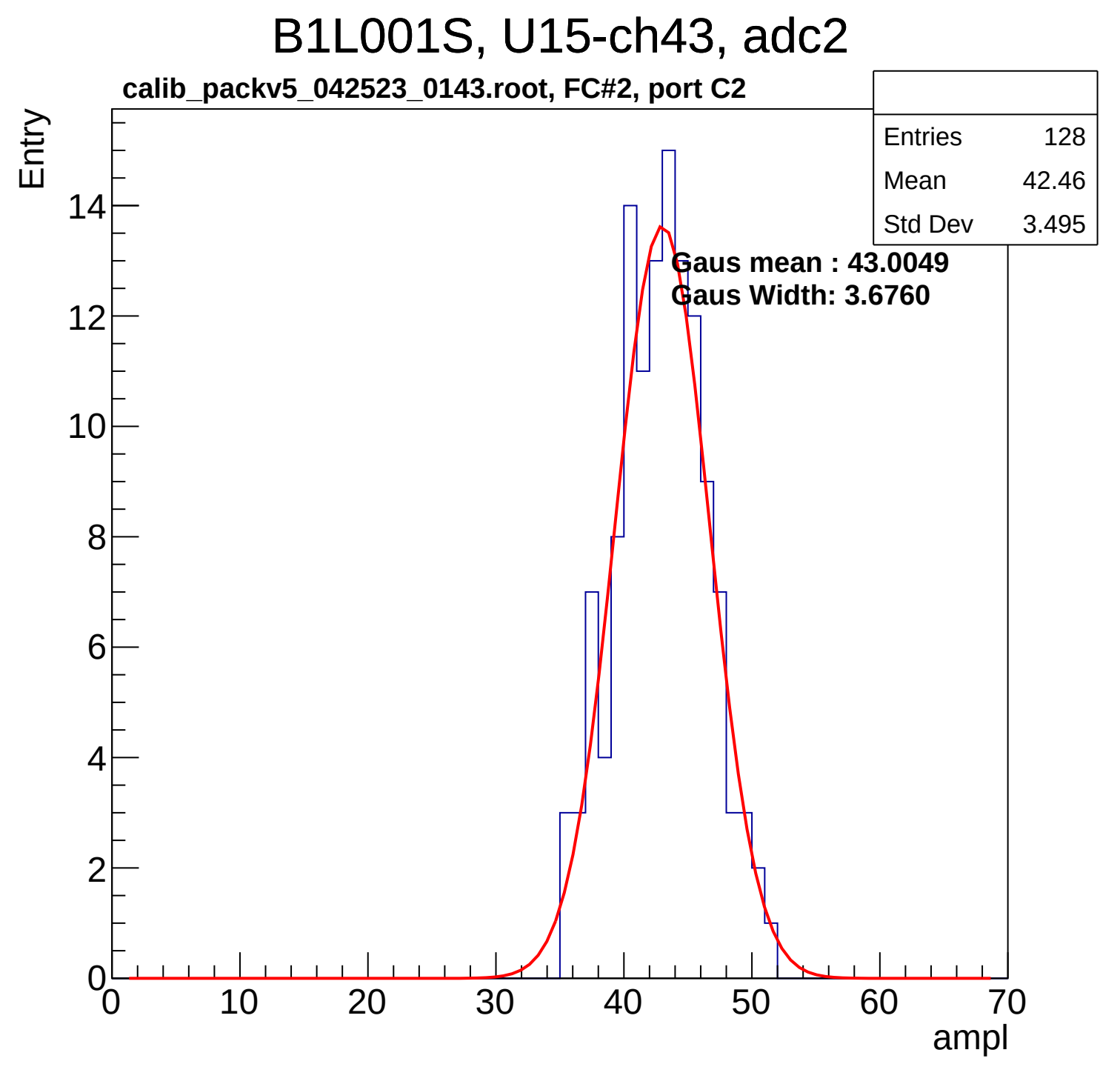
B1L001S, U15-ch43, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 42.46
Std Dev 3.495
Gaus mean : 43.0049
Gaus Width: 3.6760
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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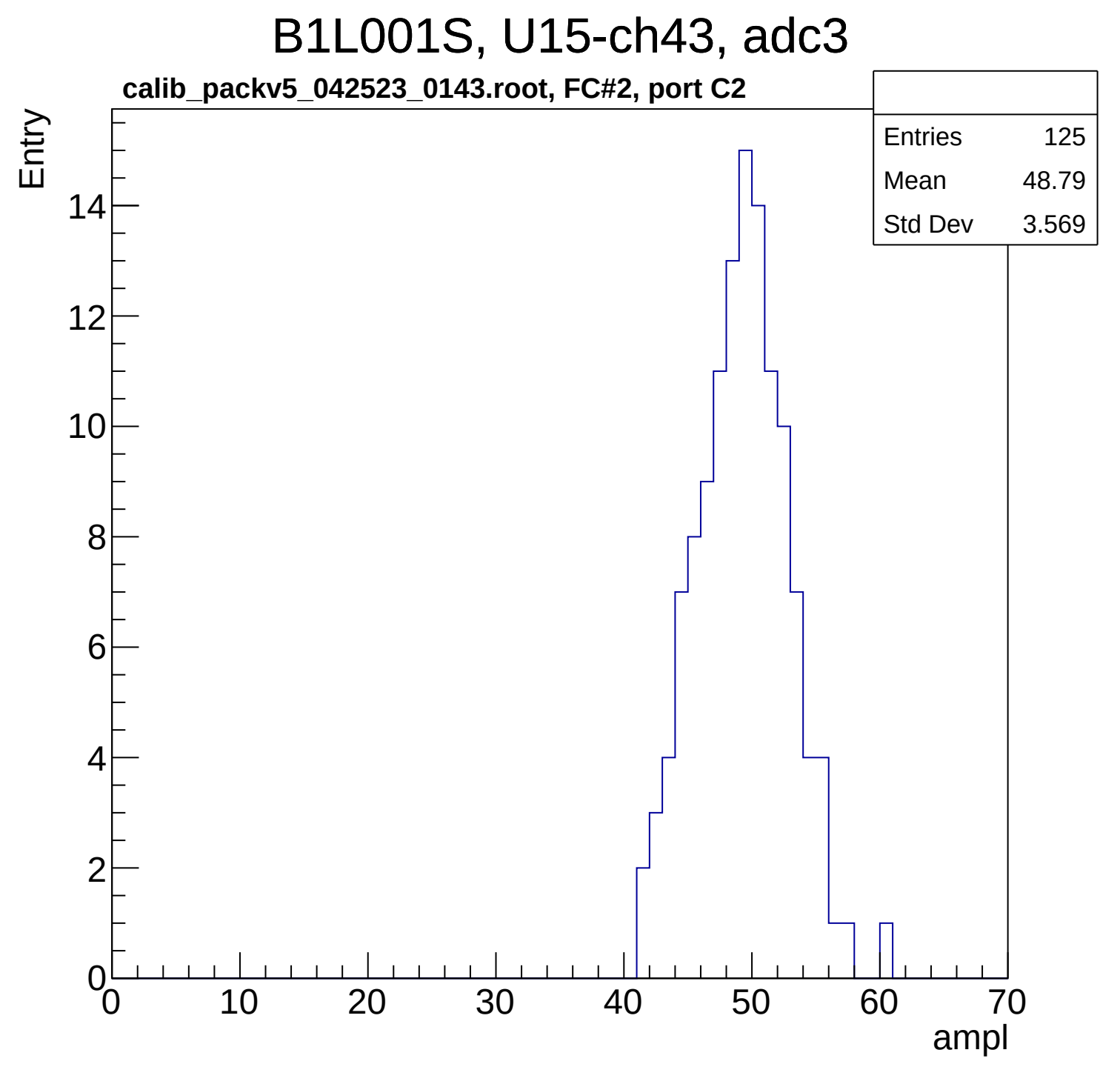
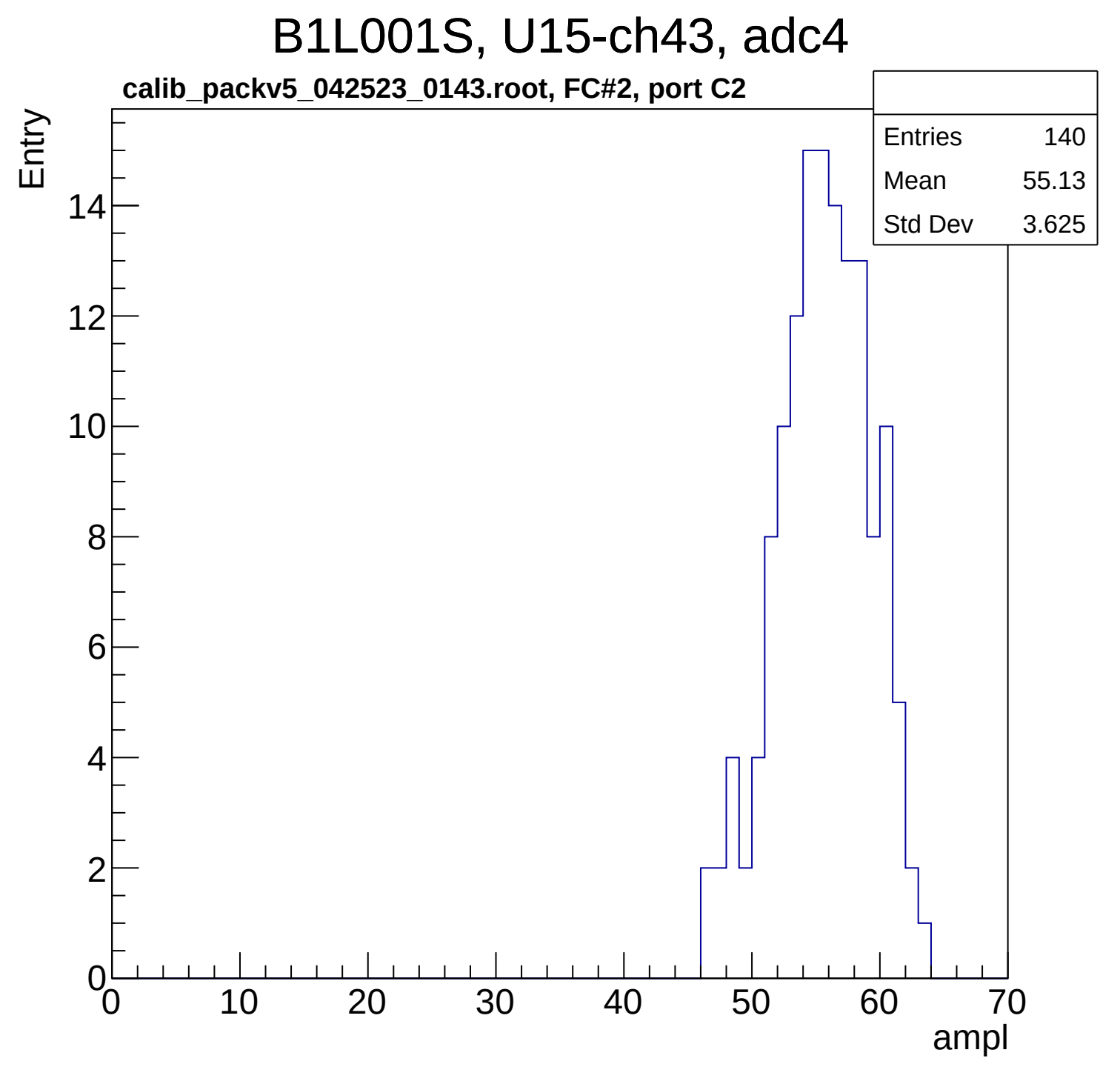

B1L001S, U15-ch43, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 48.79
Std Dev 3.569
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch43, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 55.13
Std Dev 3.625
Entry
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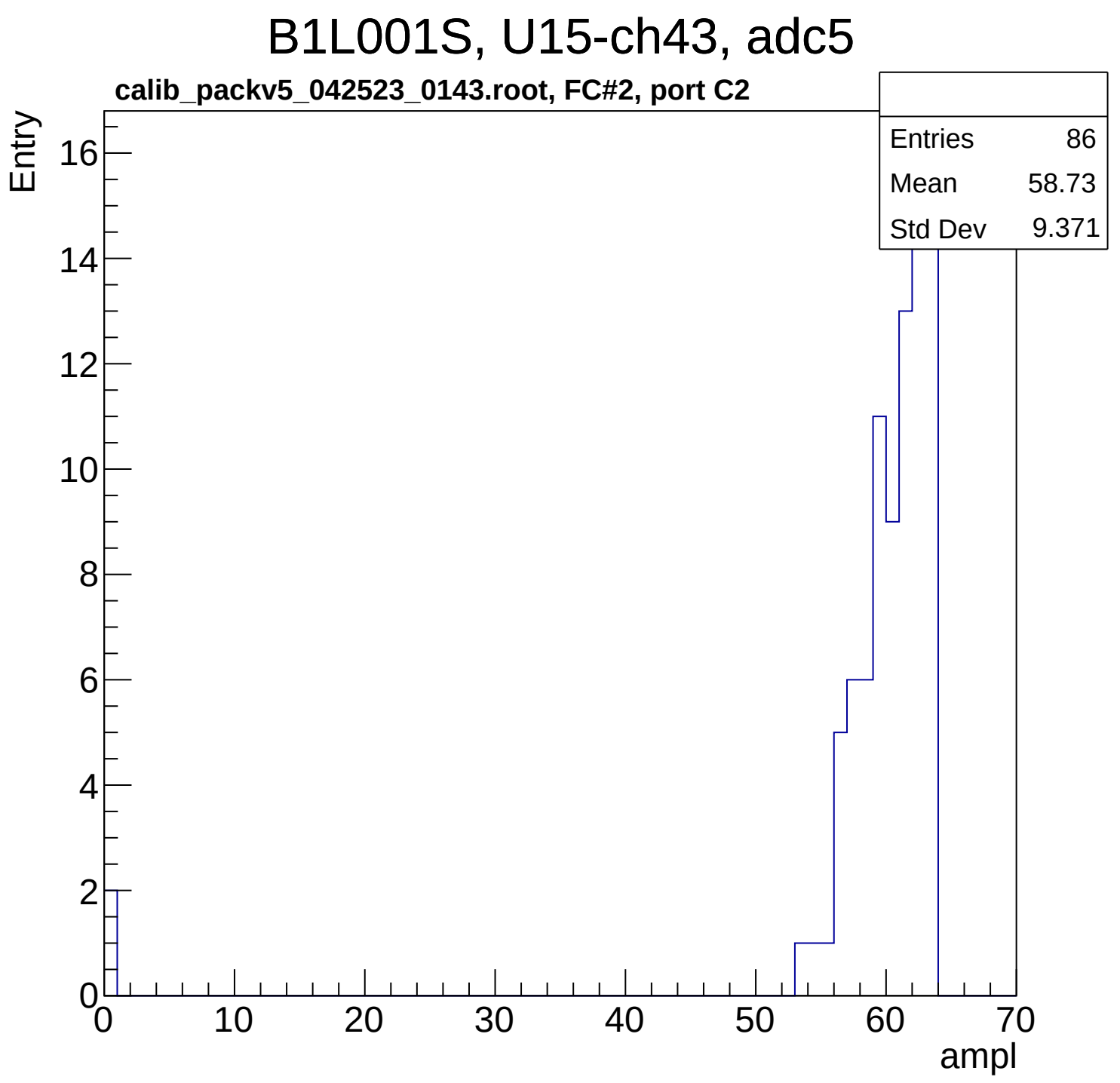
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch43, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 86
Mean 58.73
Std Dev 9.371
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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60
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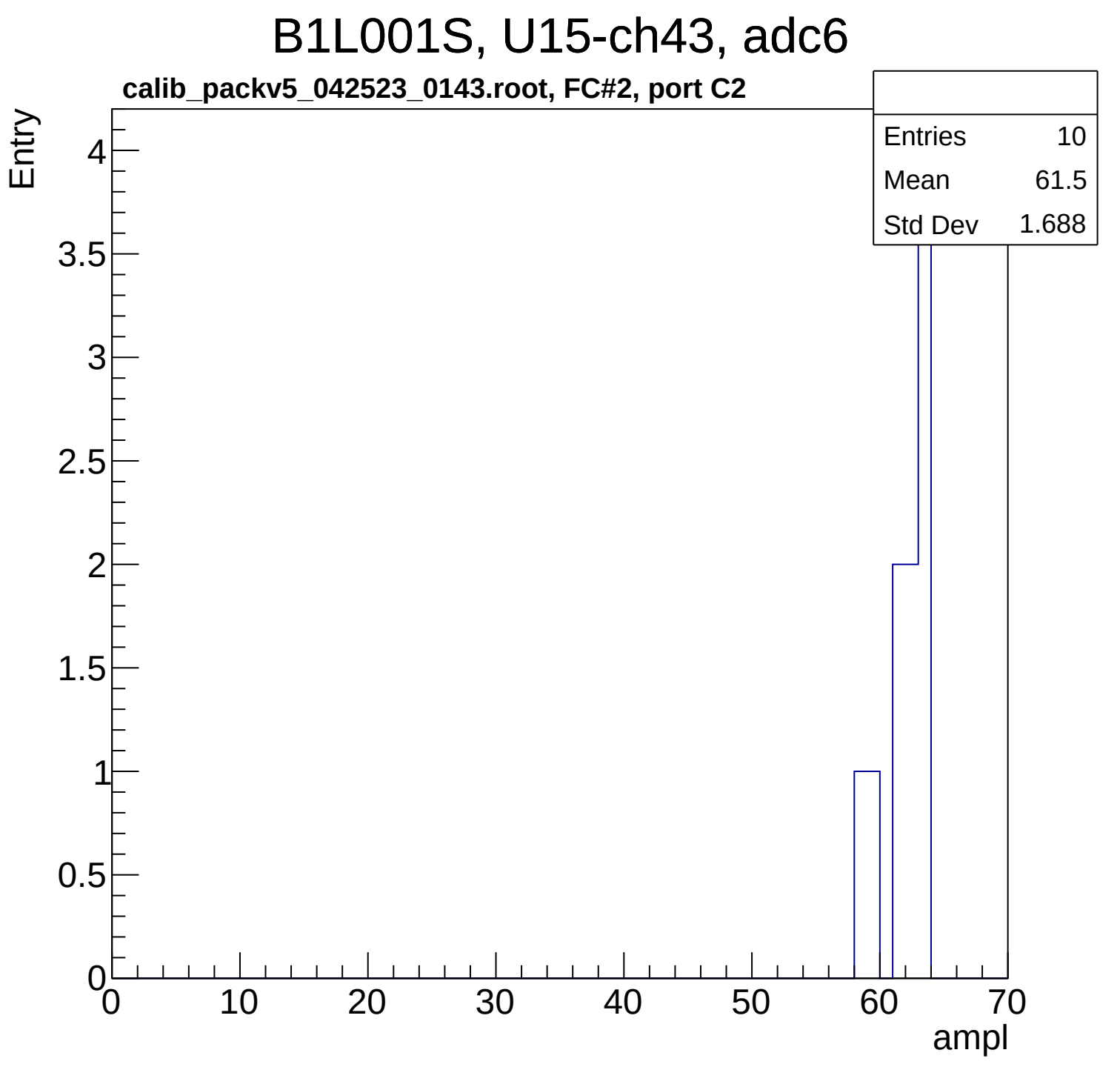
B1L001S, U15-ch43, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 61.5
Std Dev 1.688
Entry
ampl
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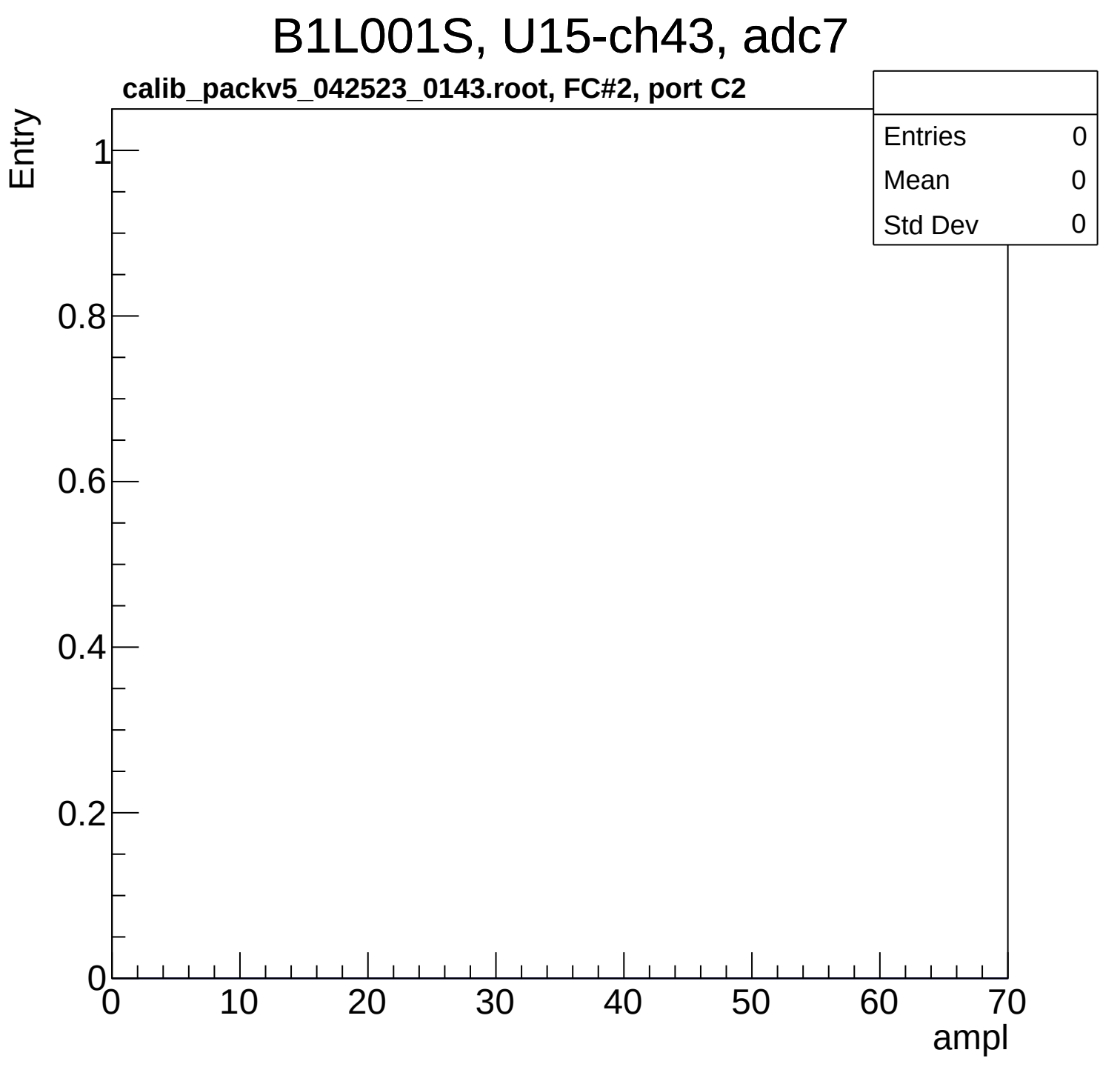
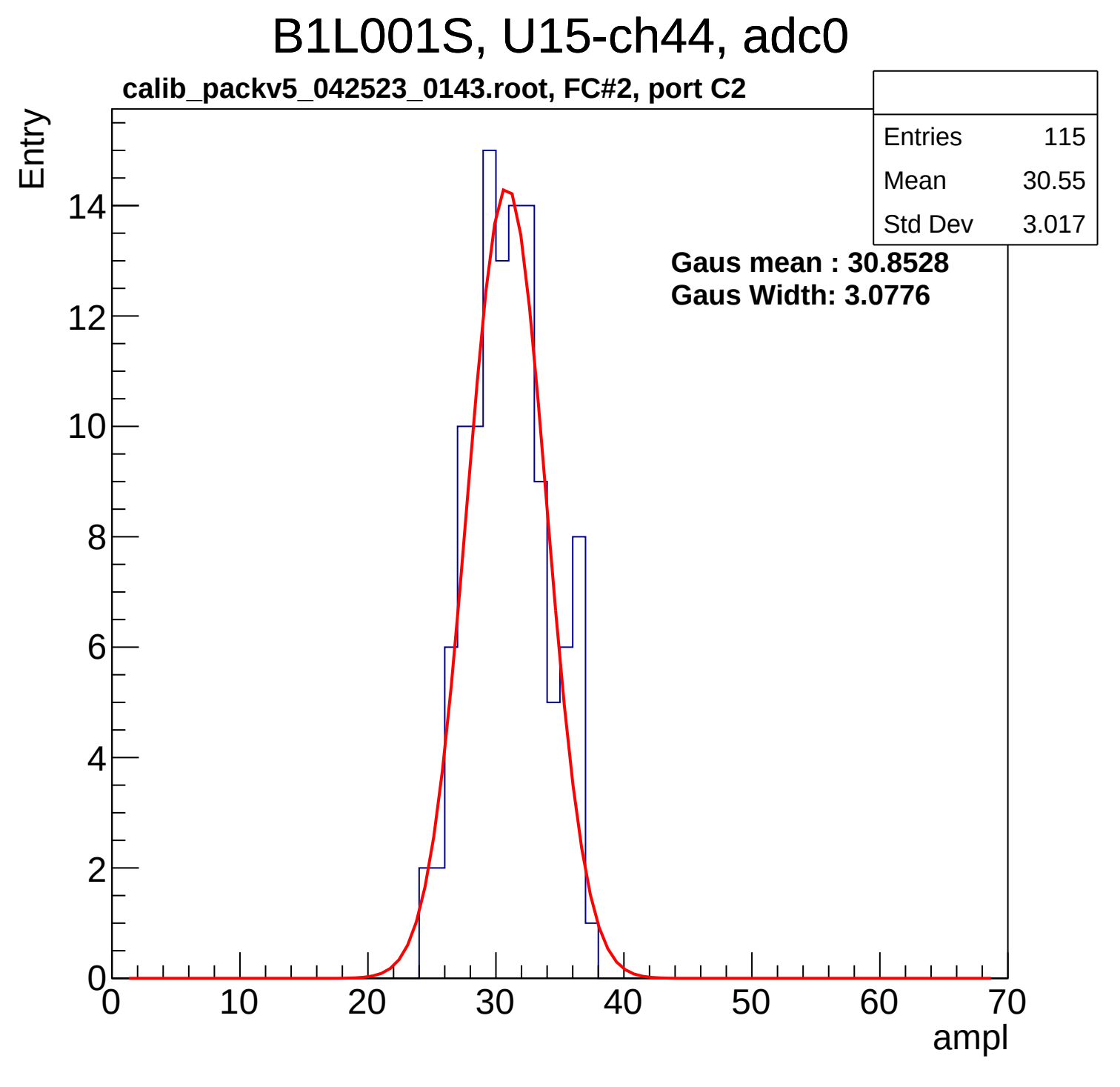
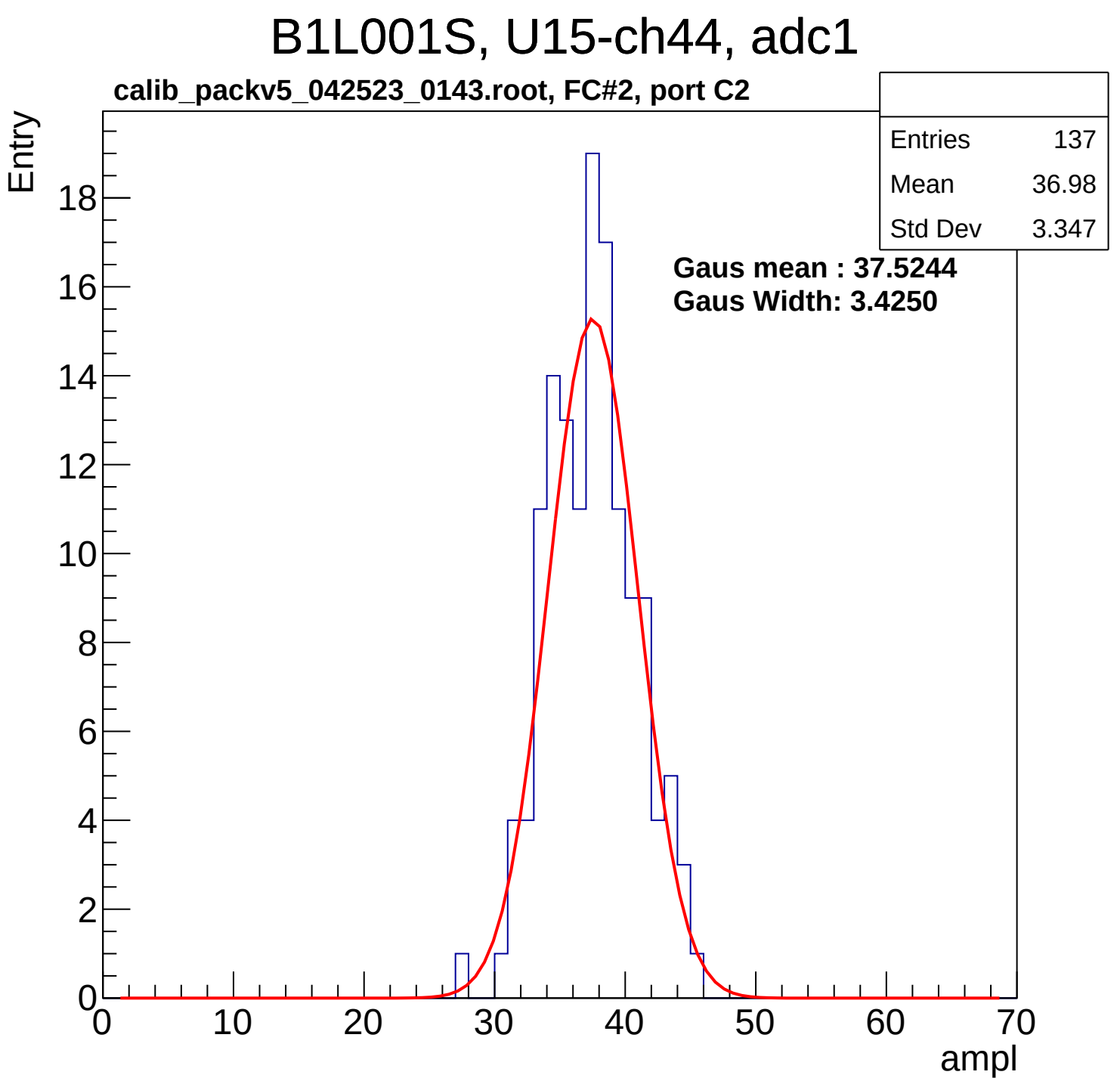
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch43, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch44, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 30.55
Std Dev 3.017
Gaus mean : 30.8528
Gaus Width: 3.0776
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch44, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 36.98
Std Dev 3.347
Gaus mean : 37.5244
Gaus Width: 3.4250
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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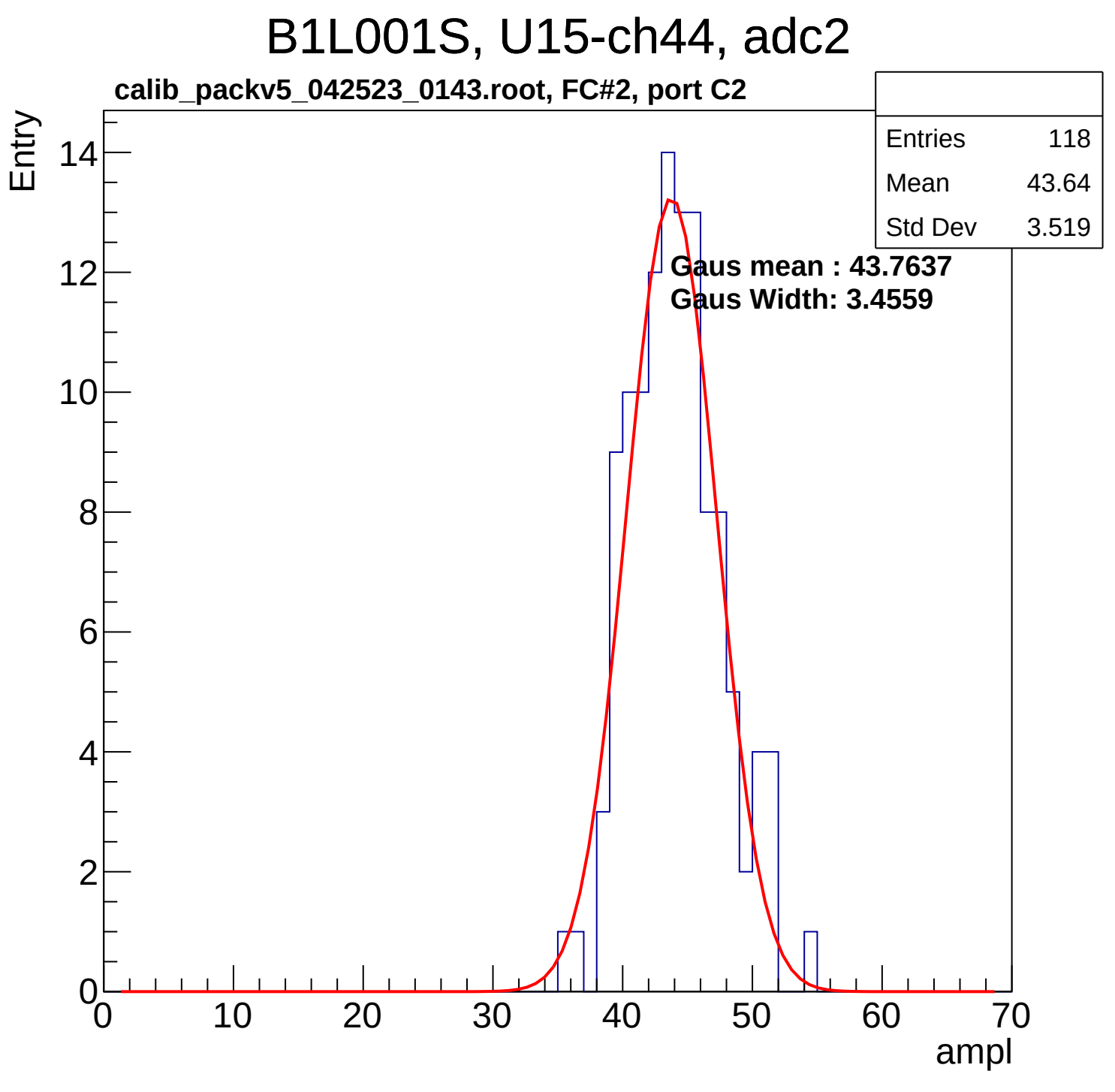
B1L001S, U15-ch44, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 43.64
Std Dev 3.519
Gaus mean : 43.7637
Gaus Width: 3.4559
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
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60
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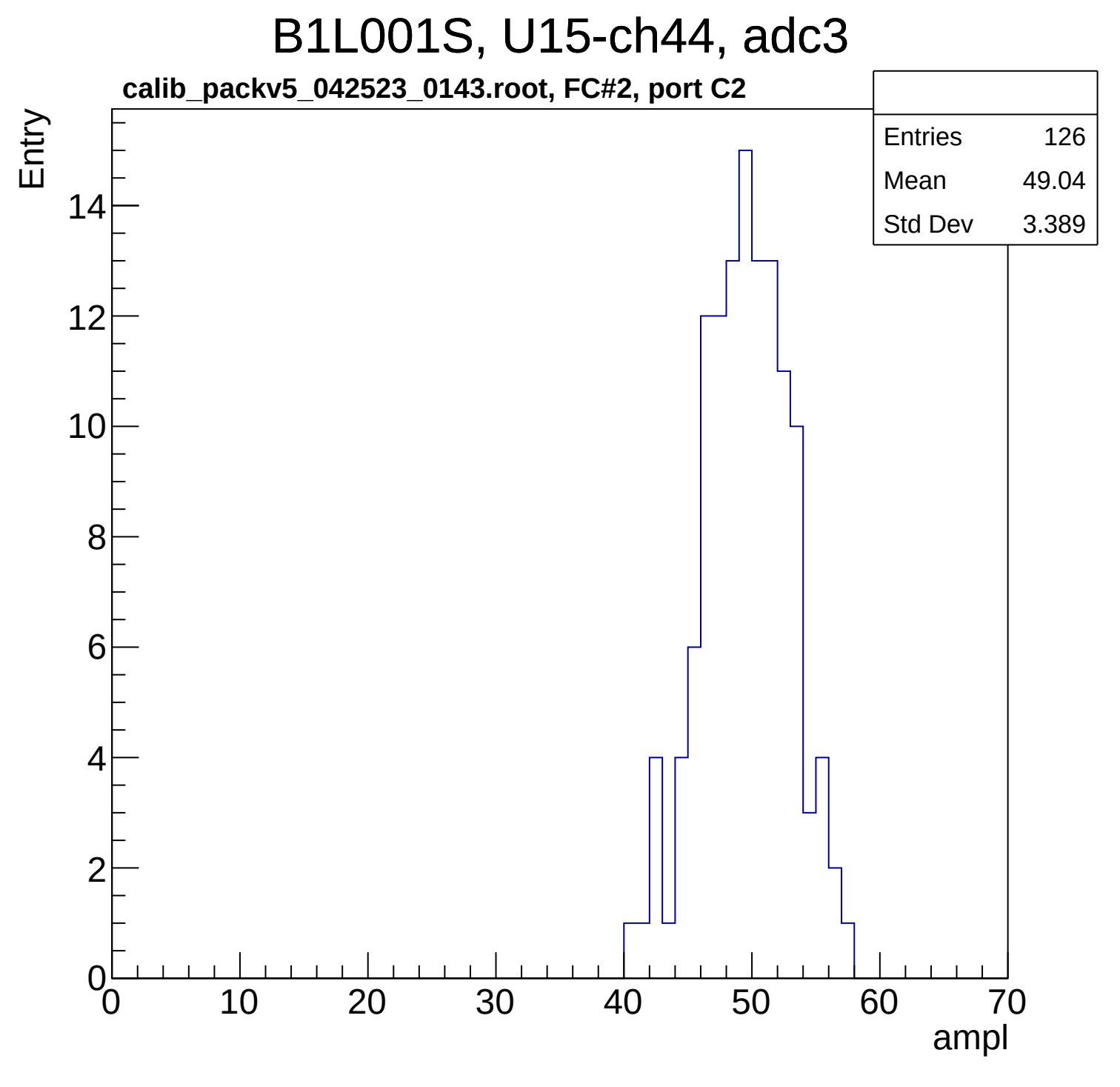
B1L001S, U15-ch44, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 49.04
Std Dev 3.389
Entry
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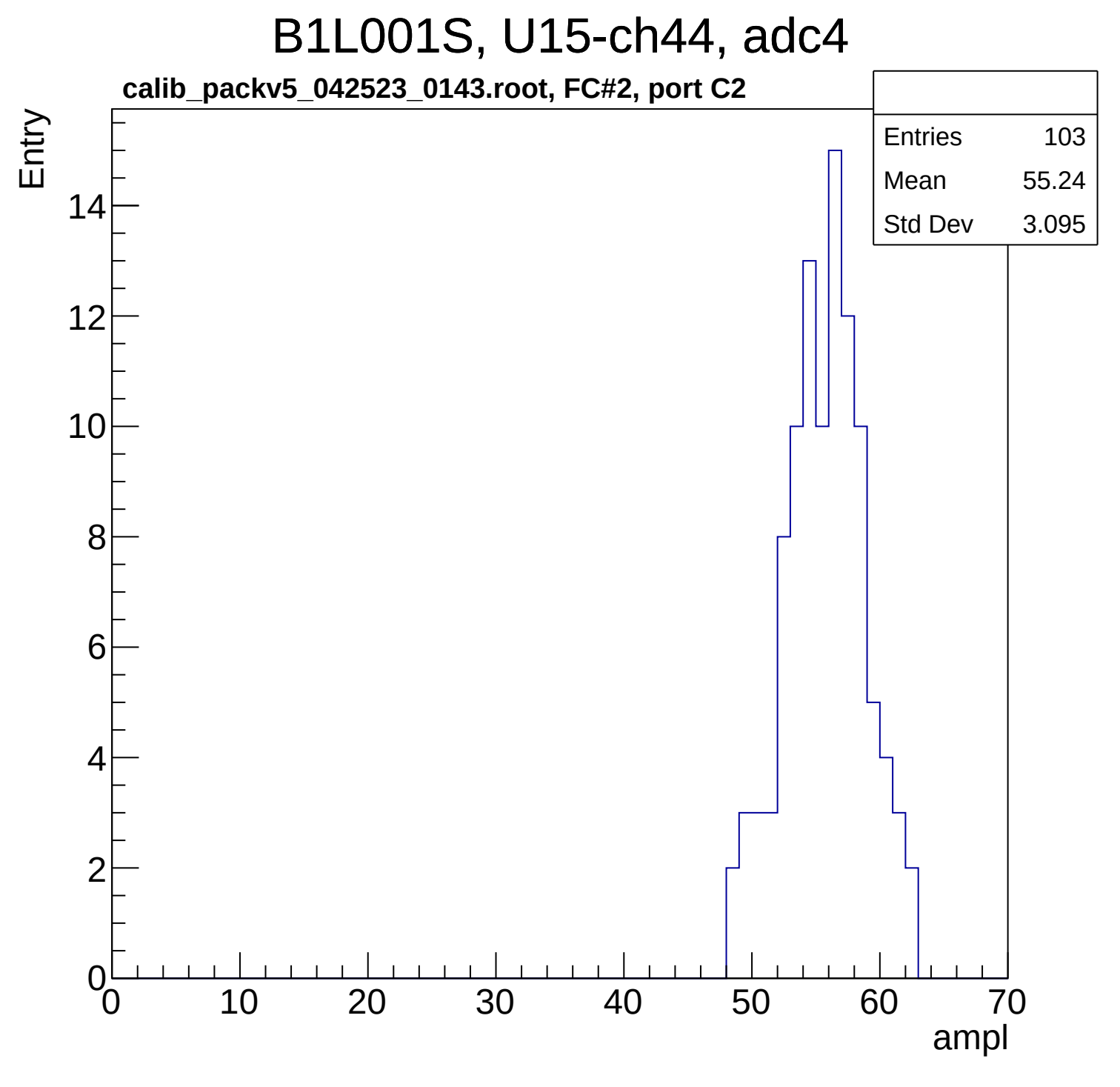
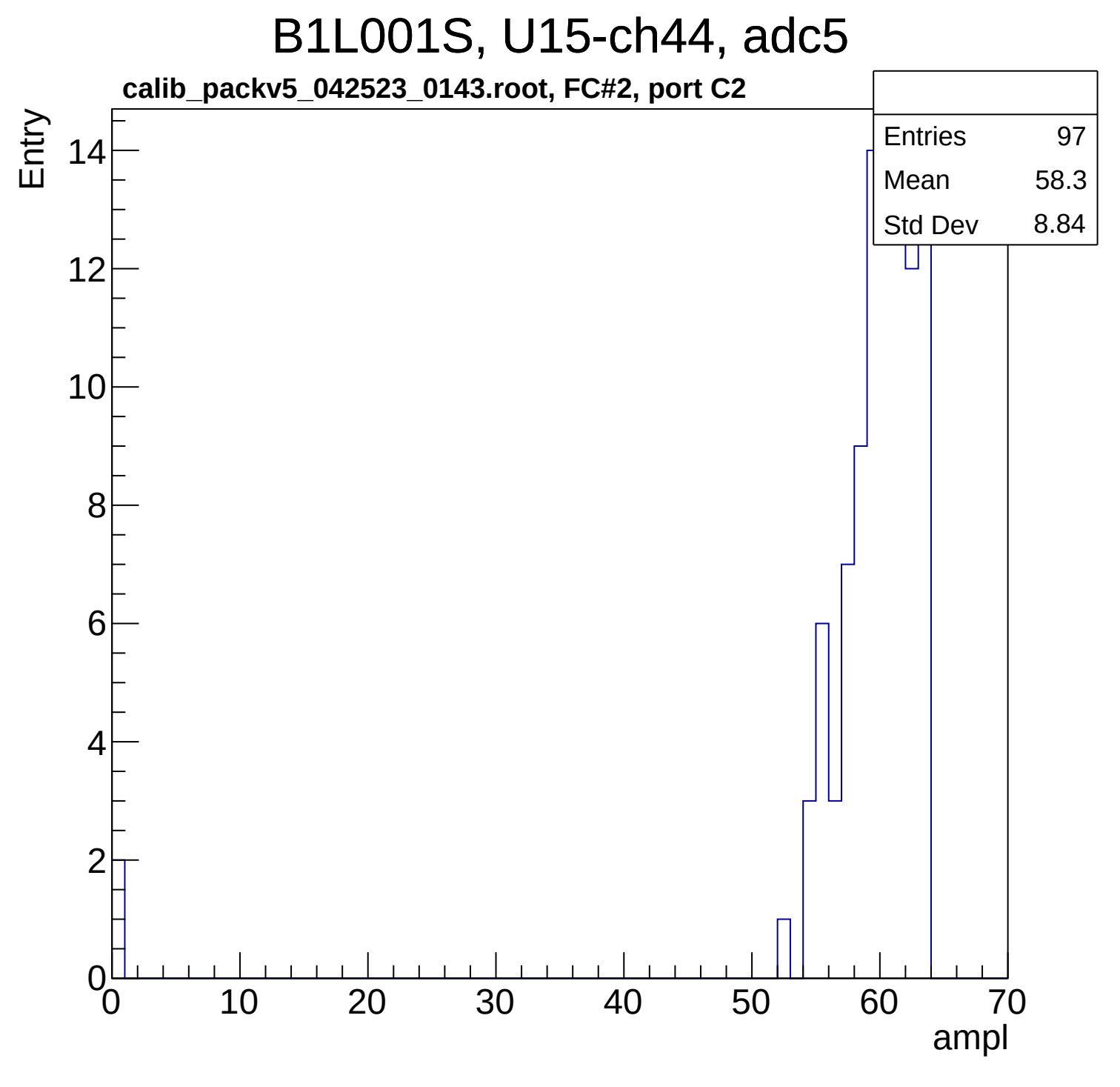
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch44, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 55.24
Std Dev 3.095
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch44, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 58.3
Std Dev 8.84
Entry
ampl
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2
4
6
8
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12
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0
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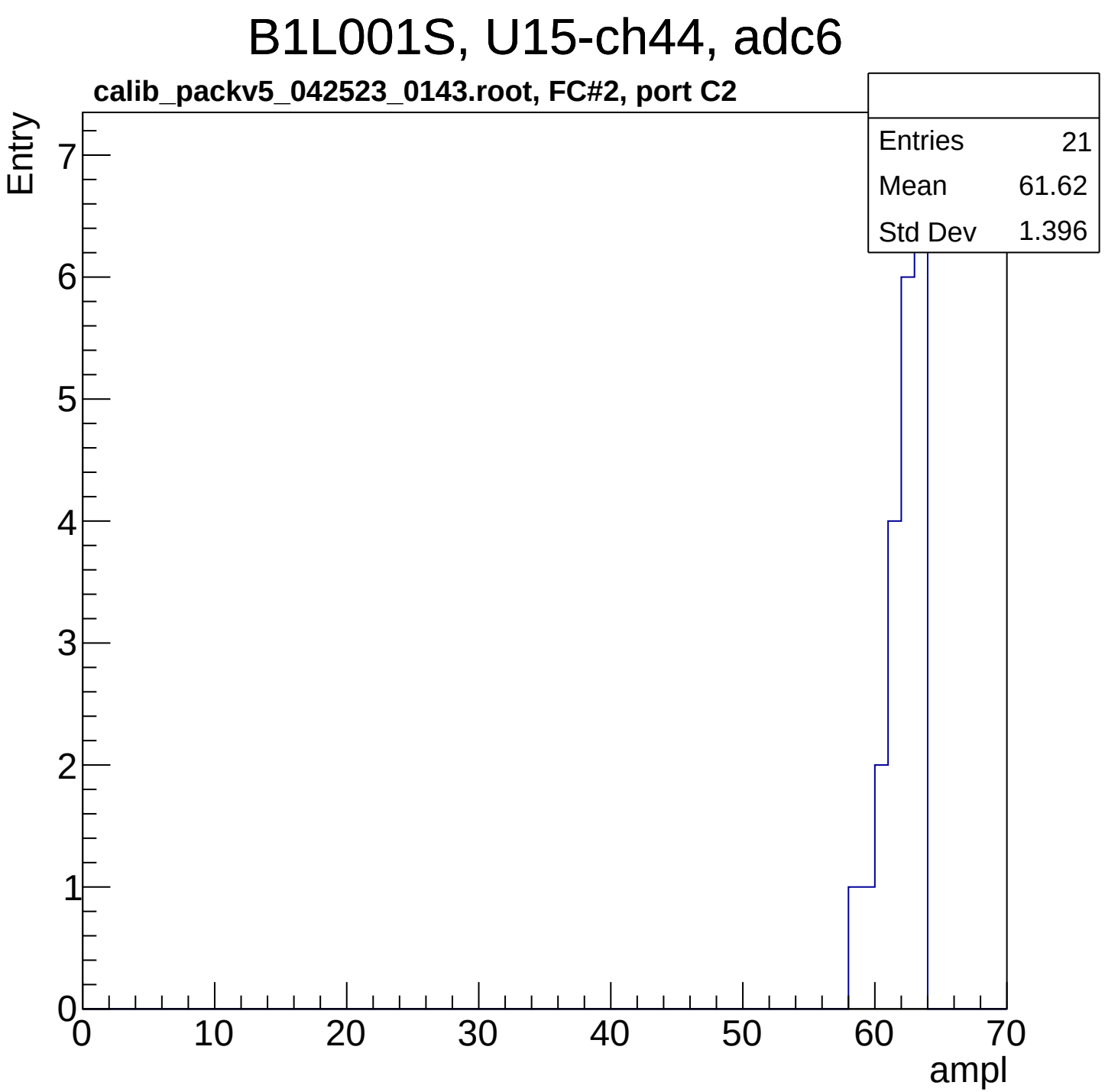
B1L001S, U15-ch44, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 21
Mean 61.62
Std Dev 1.396
Entry
ampl
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1
2
3
4
5
6
7
0
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40
50
60
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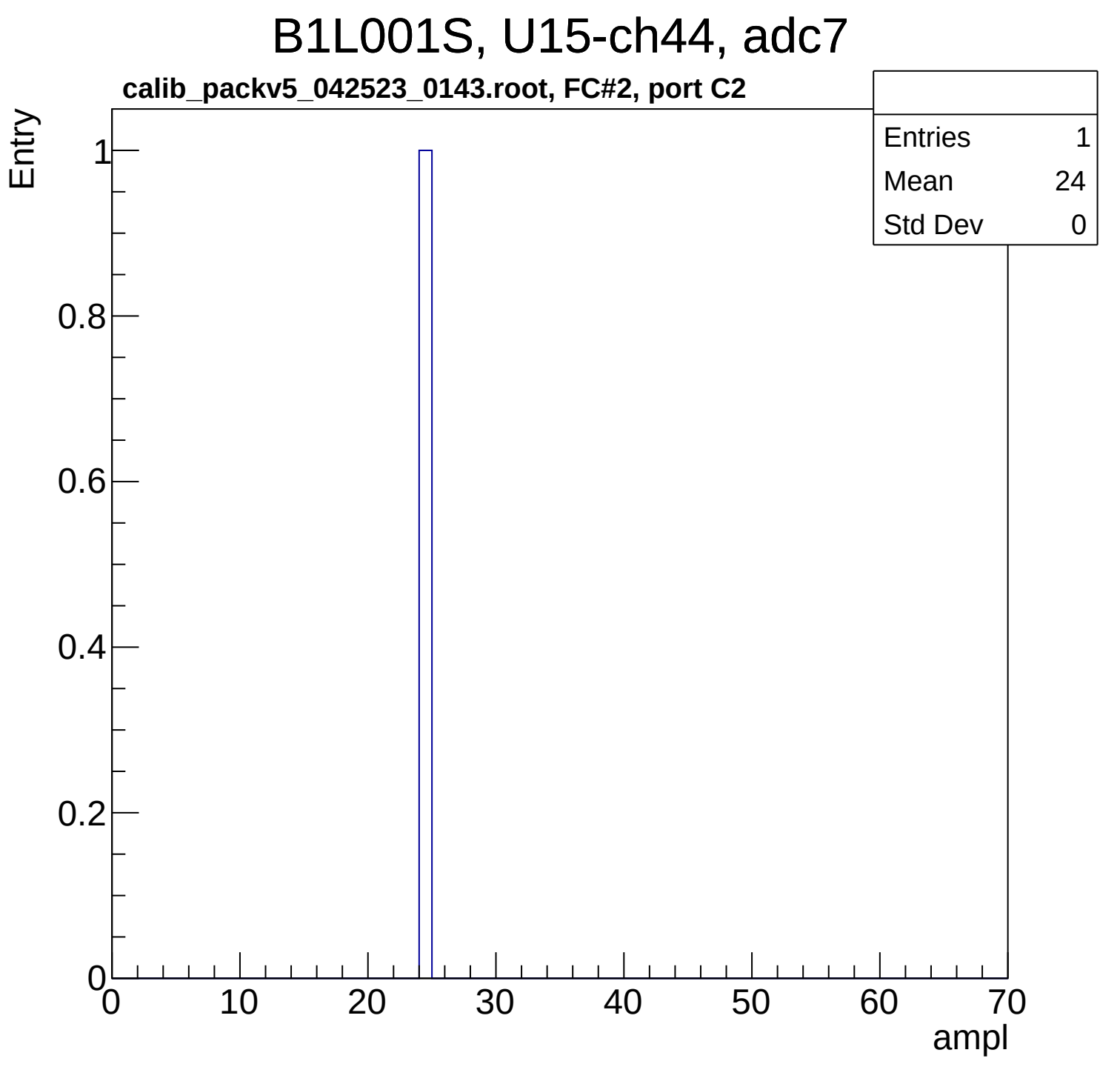
B1L001S, U15-ch44, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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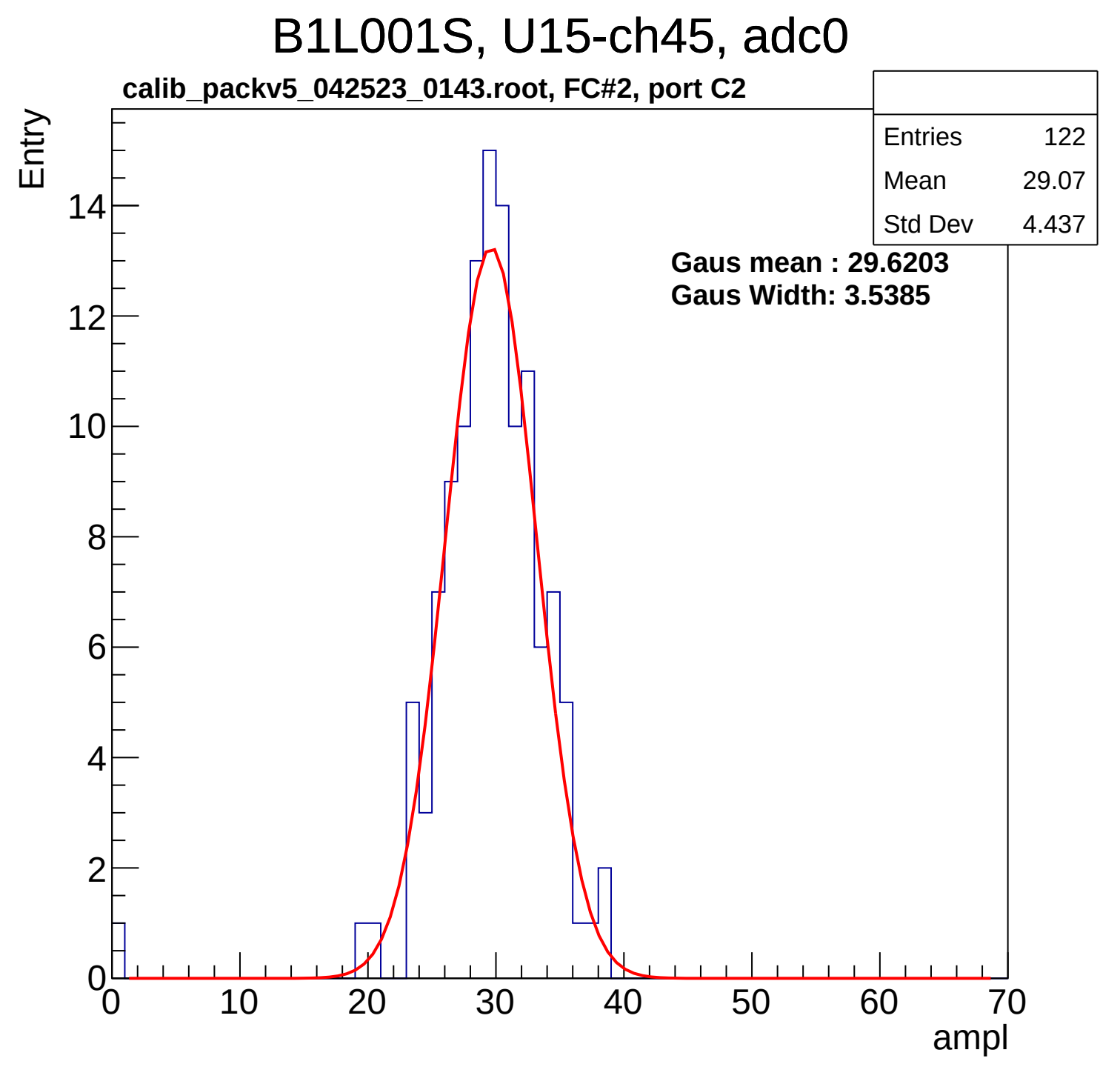
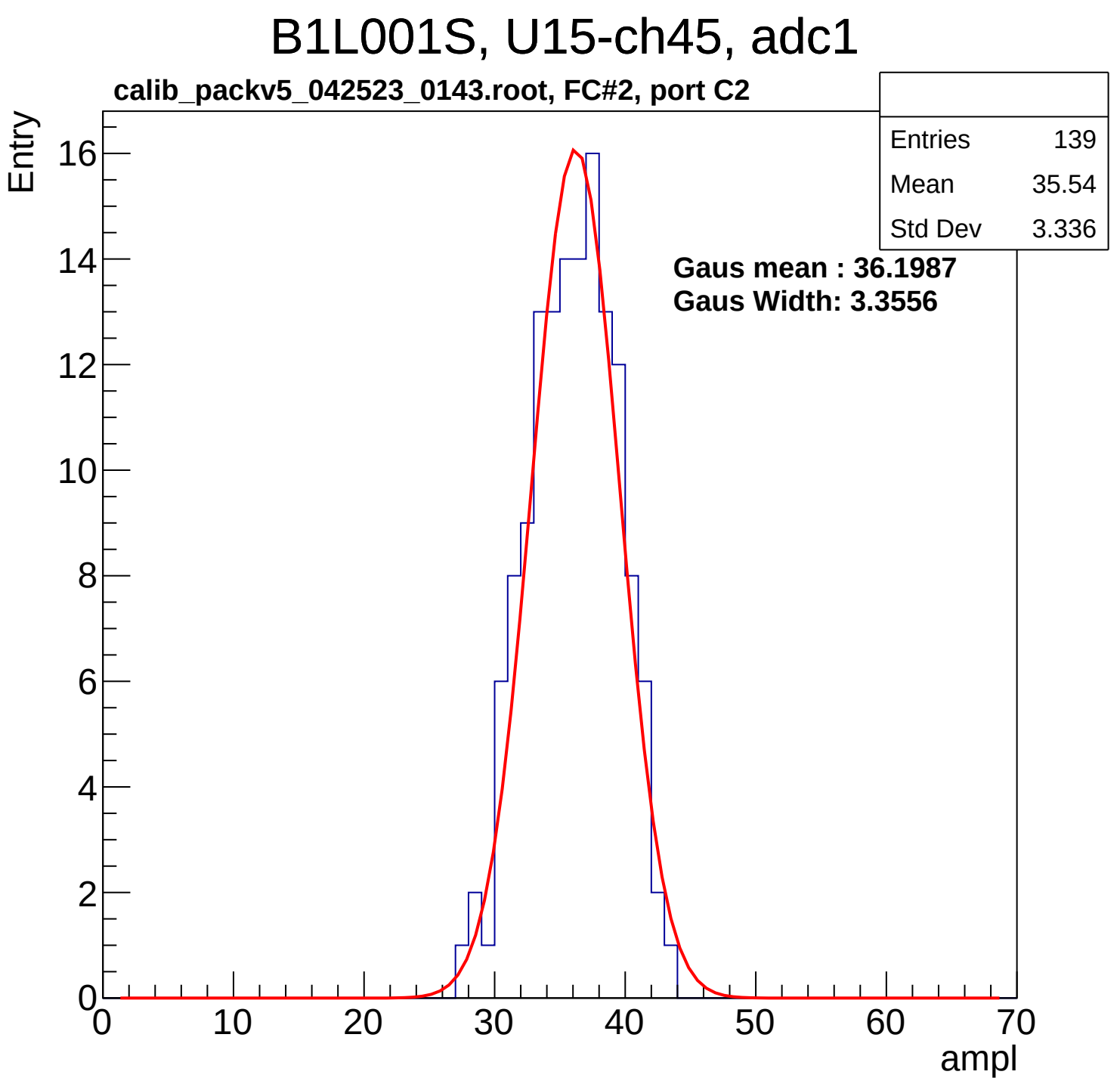
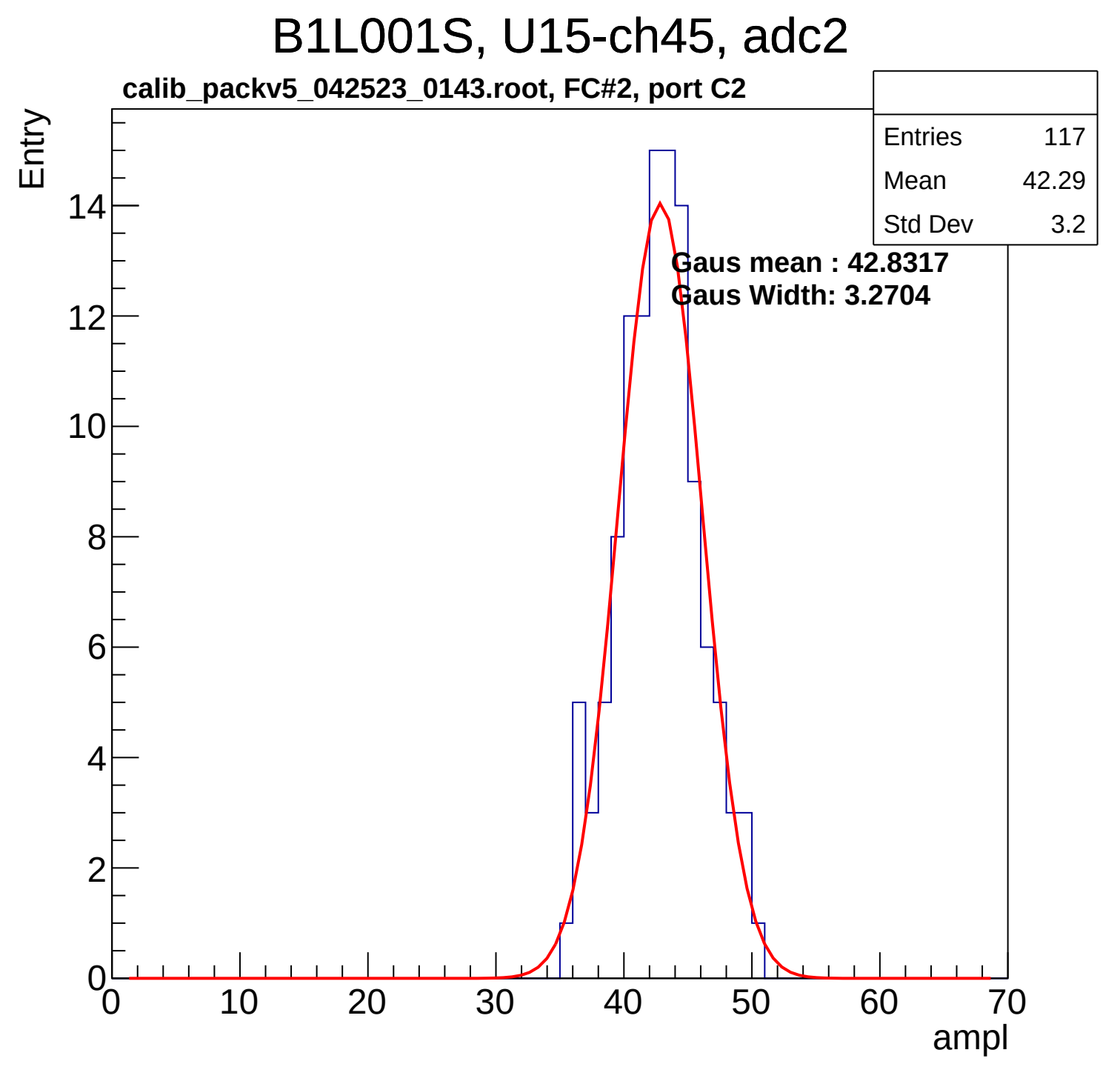
50
60
70
ampl


B1L001S, U15-ch45, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 29.07
Std Dev 4.437
Gaus mean : 29.6203
Gaus Width: 3.5385
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch45, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 35.54
Std Dev 3.336
Gaus mean : 36.1987
Gaus Width: 3.3556
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch45, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 42.29
Std Dev 3.2
Gaus mean : 42.8317
Gaus Width: 3.2704
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
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60
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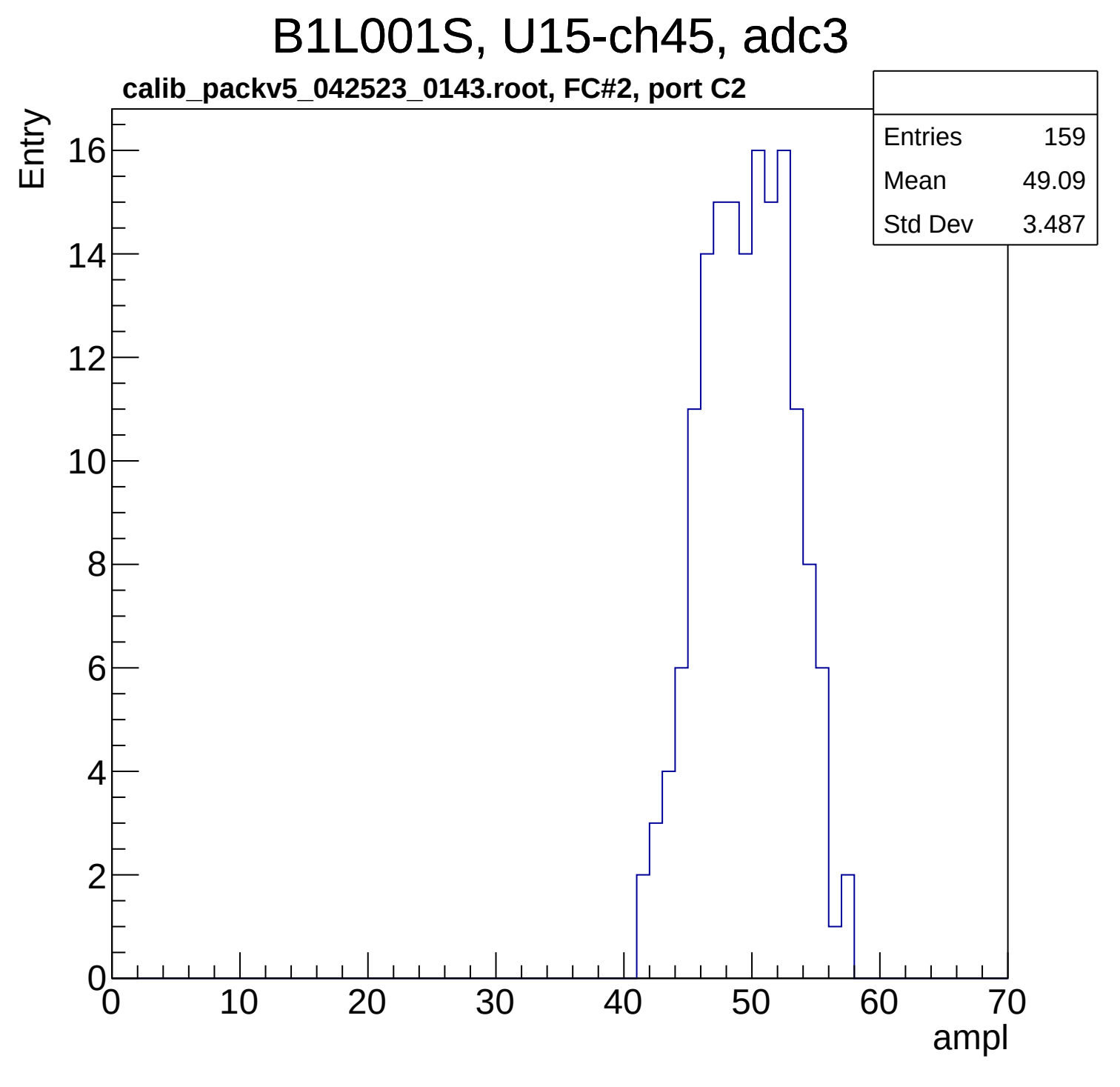
B1L001S, U15-ch45, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 49.09
Std Dev 3.487
Entry
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4
6
8
10
12
14
16
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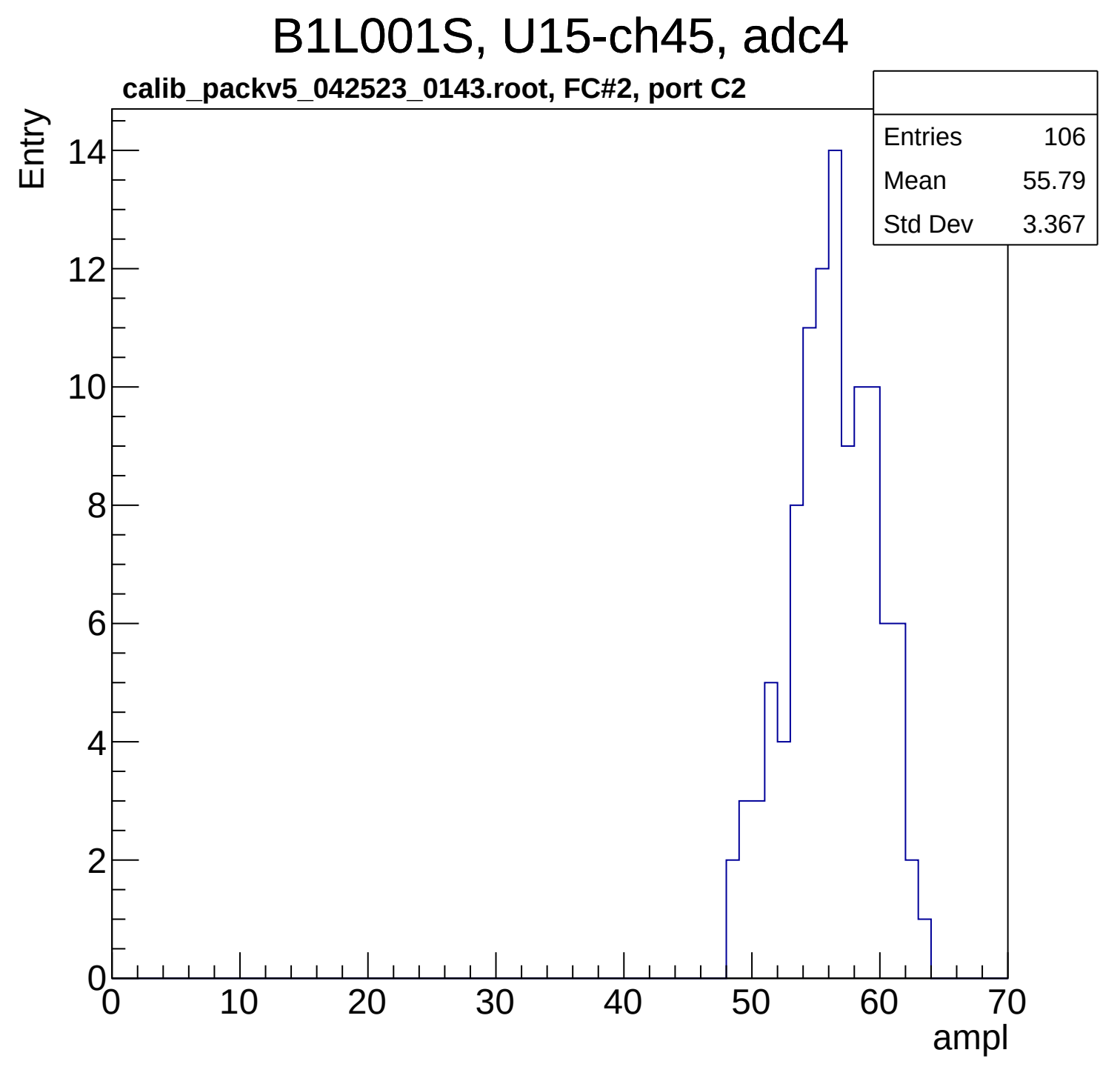
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch45, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 55.79
Std Dev 3.367
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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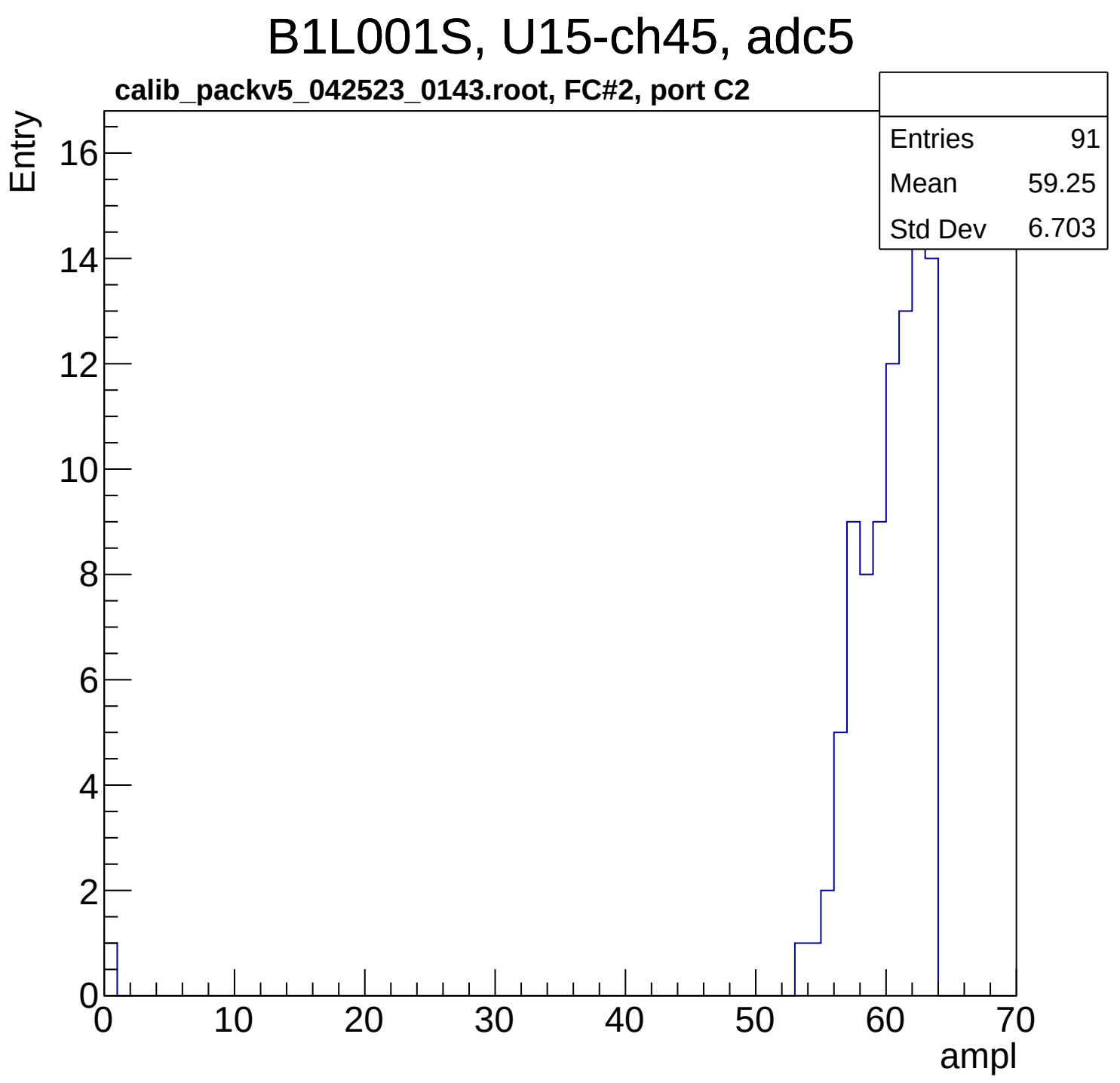
B1L001S, U15-ch45, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.25
Std Dev 6.703
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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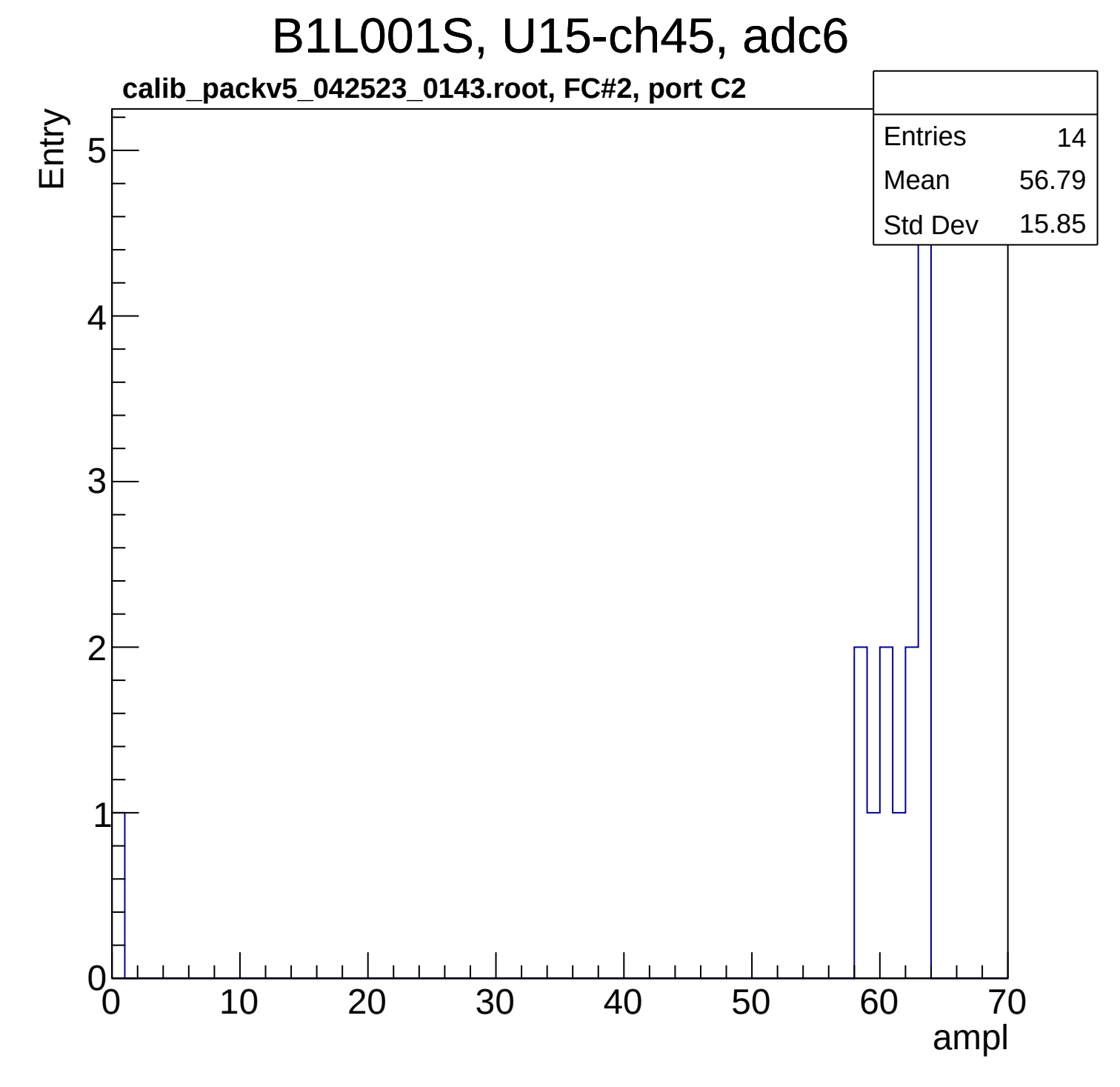
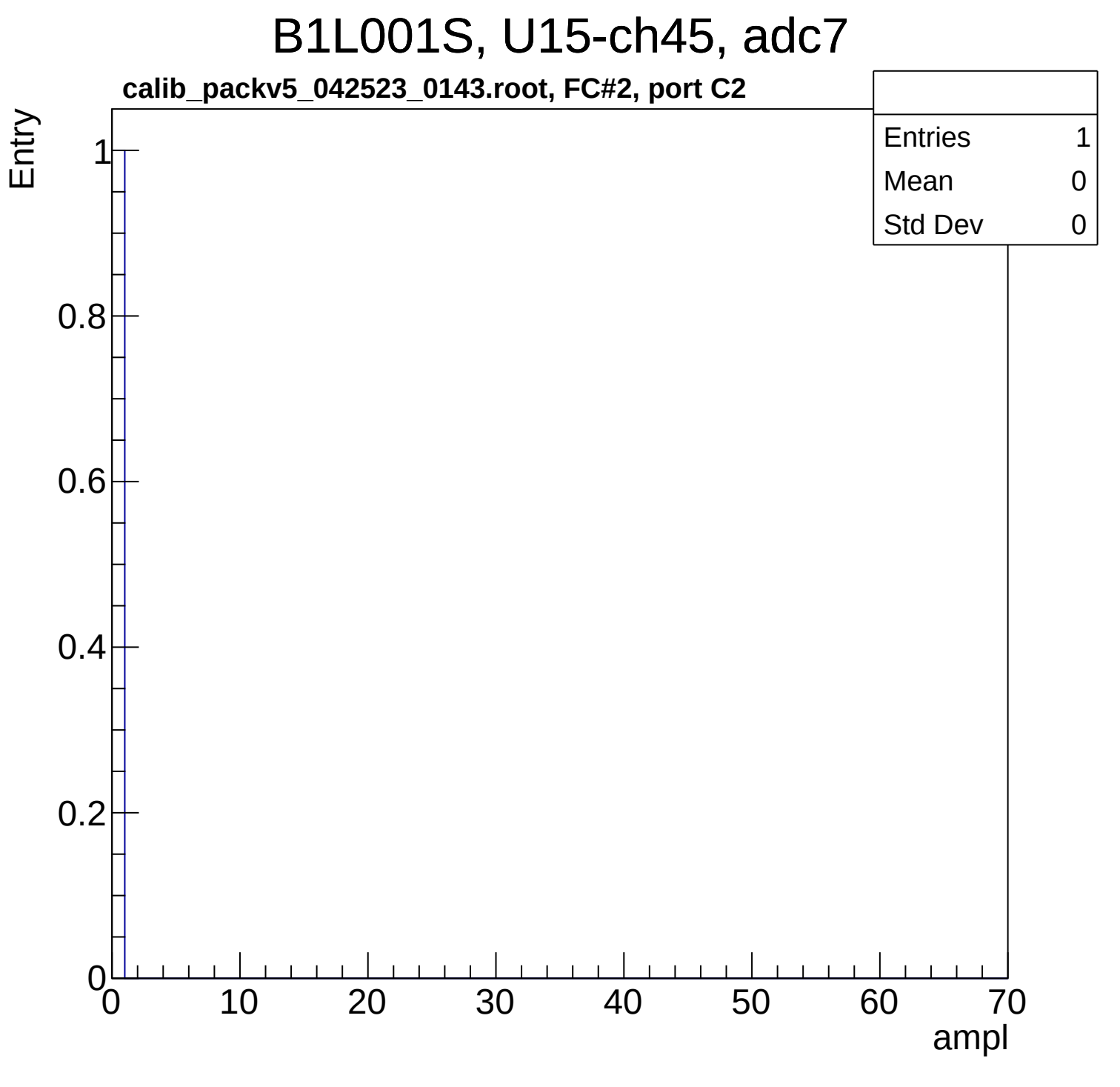

B1L001S, U15-ch45, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 56.79
Std Dev 15.85
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L001S, U15-ch45, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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50
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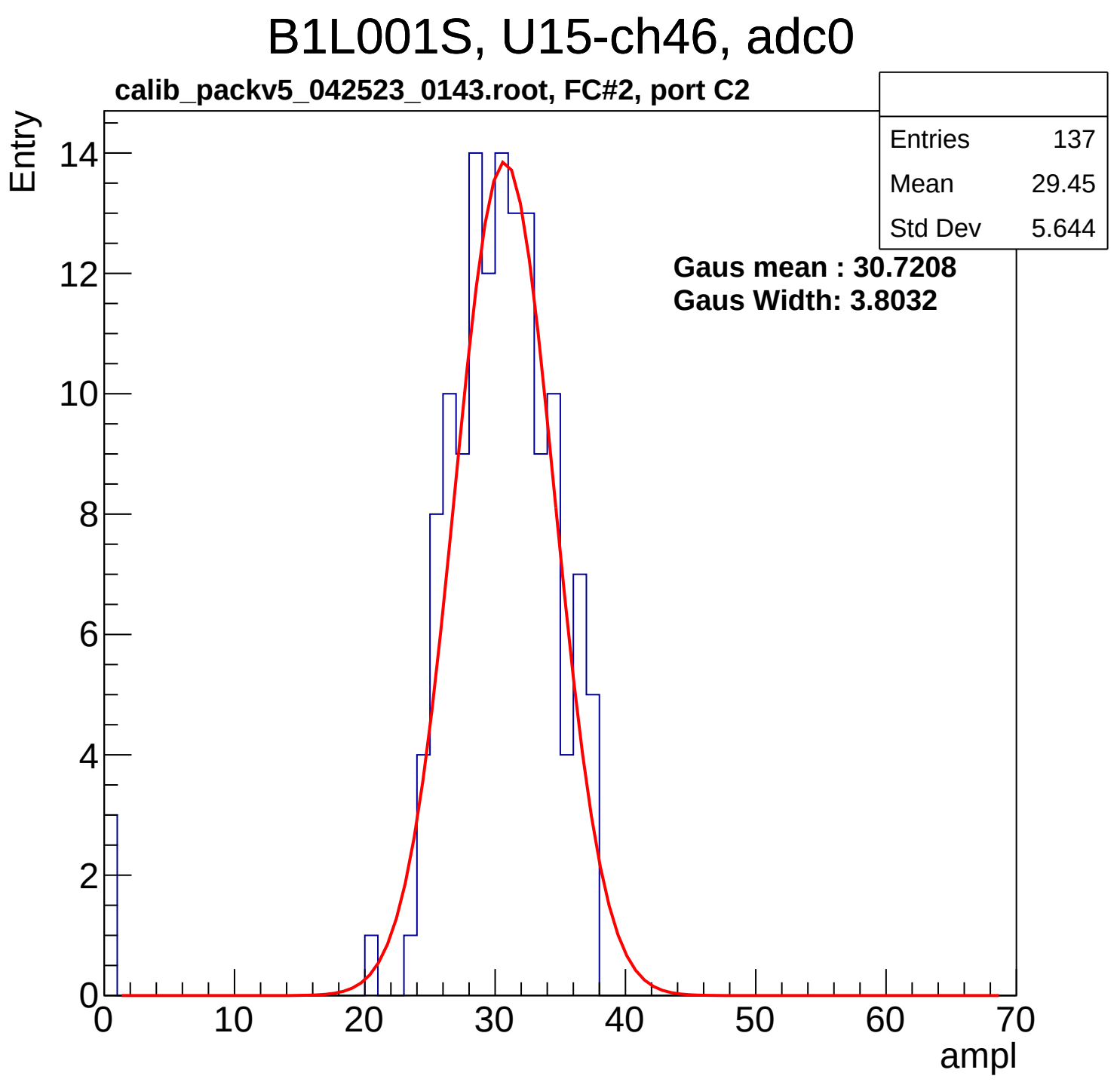
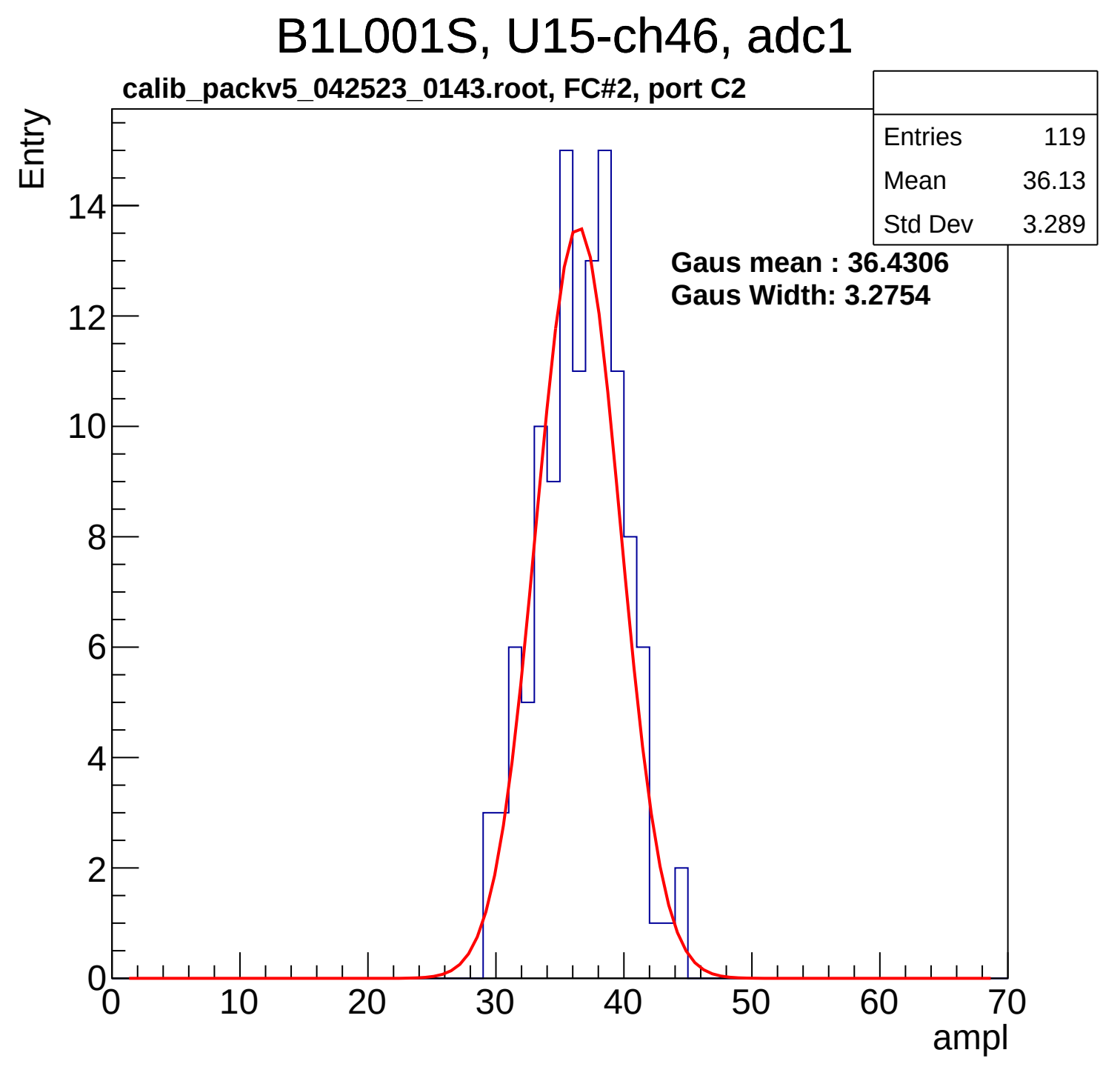
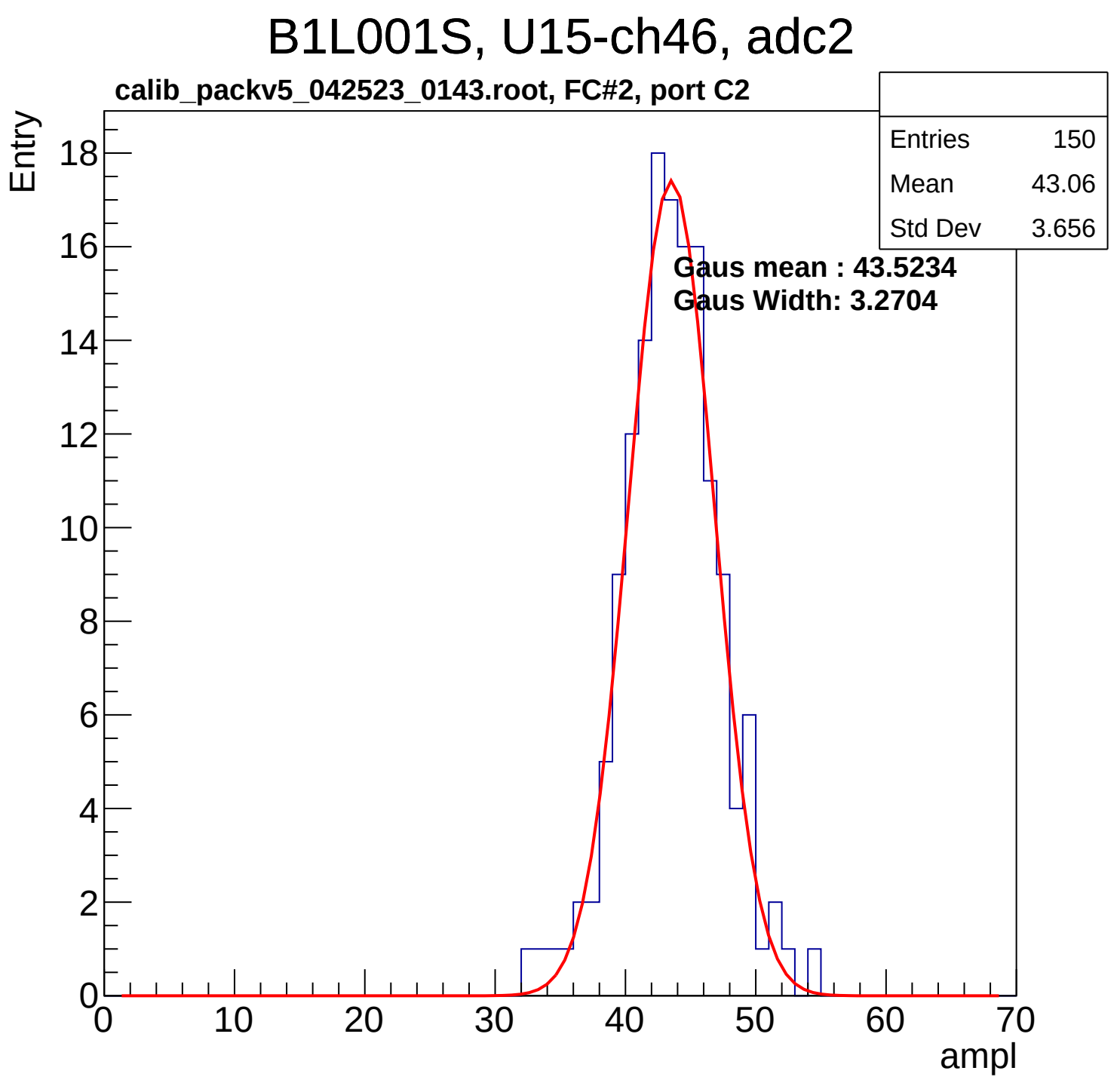
70
ampl


B1L001S, U15-ch46, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 29.45
Std Dev 5.644
Gaus mean : 30.7208
Gaus Width: 3.8032
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch46, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 36.13
Std Dev 3.289
Gaus mean : 36.4306
Gaus Width: 3.2754
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch46, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 43.06
Std Dev 3.656
Gaus mean : 43.5234
Gaus Width: 3.2704
Entry
ampl
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4
6
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10
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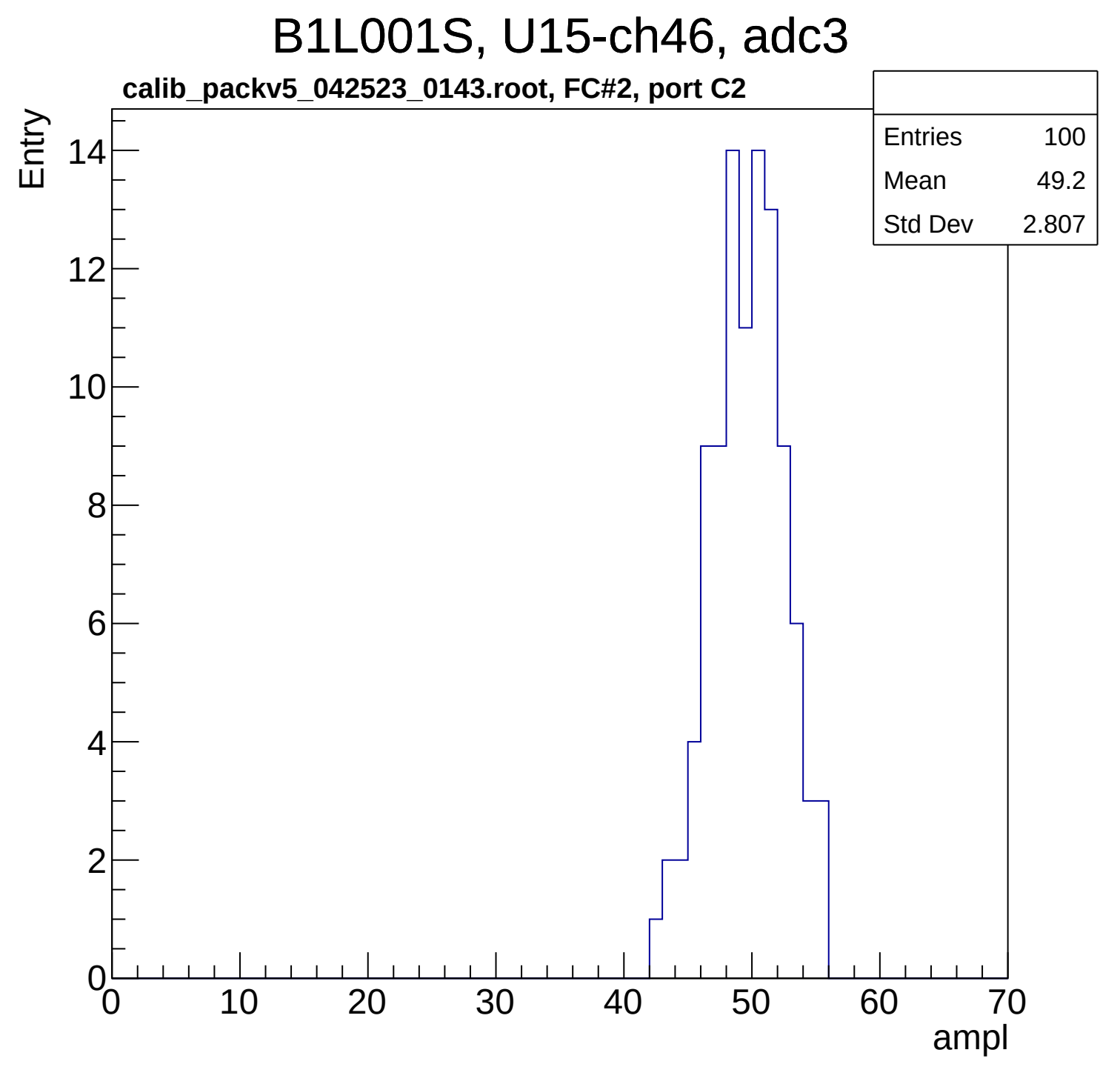
B1L001S, U15-ch46, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 49.2
Std Dev 2.807
Entry
0
2
4
6
8
10
12
14
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10
20
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50
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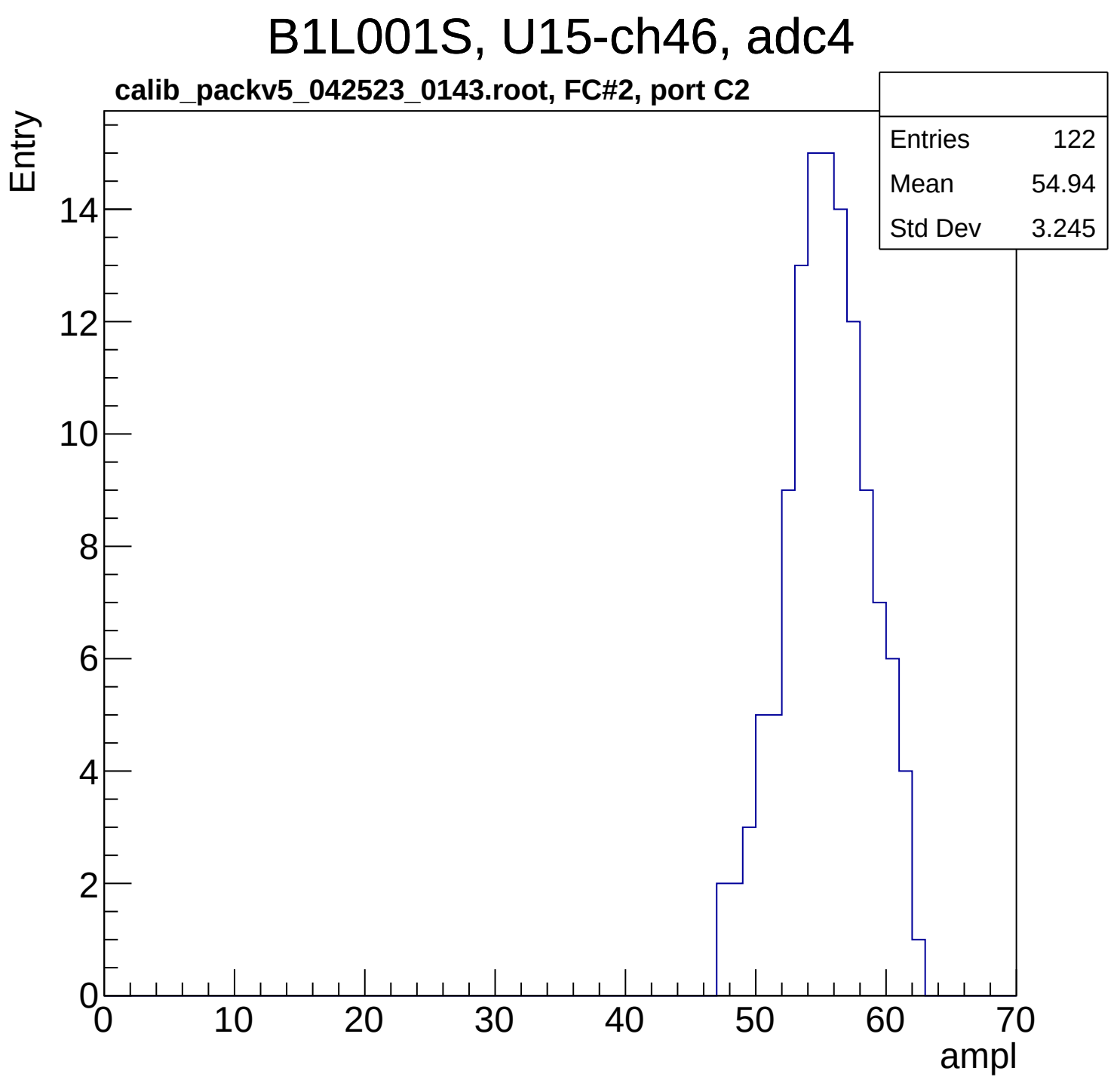
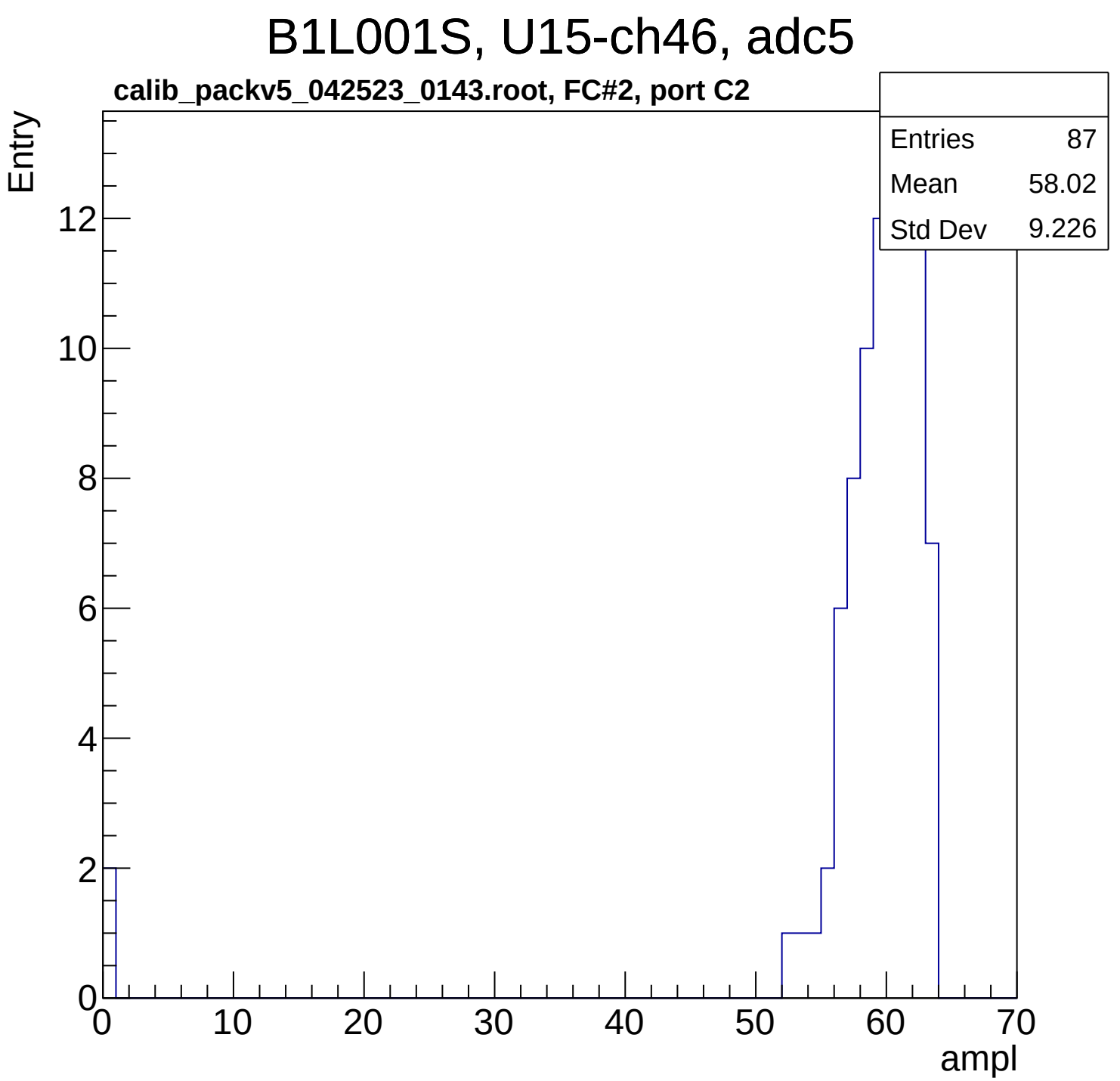
70
ampl


B1L001S, U15-ch46, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 54.94
Std Dev 3.245
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch46, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 58.02
Std Dev 9.226
Entry
0
2
4
6
8
10
12
0
10
20
30
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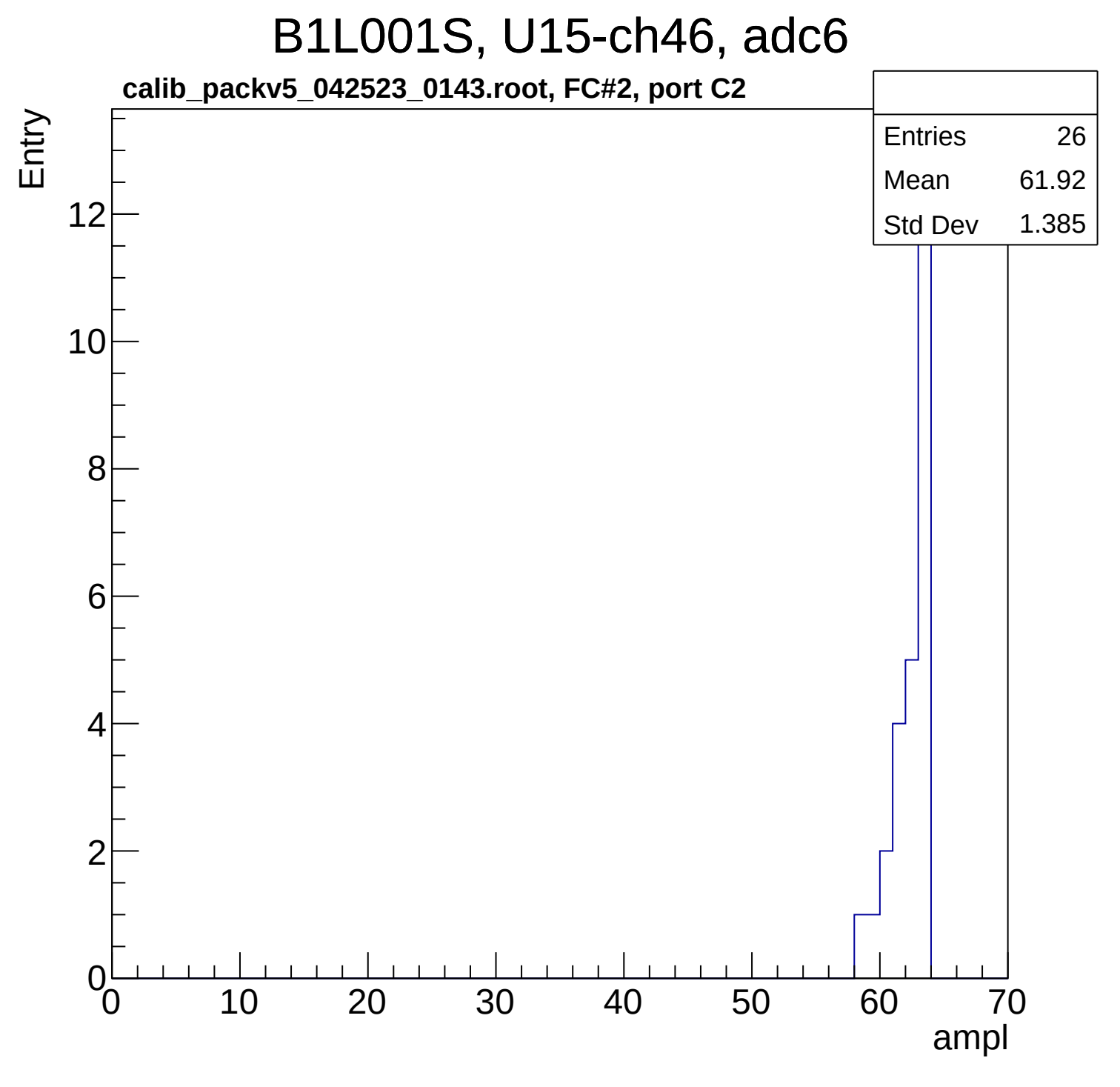
50
60
70
ampl


B1L001S, U15-ch46, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 61.92
Std Dev 1.385
Entry
ampl
0
2
4
6
8
10
12
0
10
20
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40
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60
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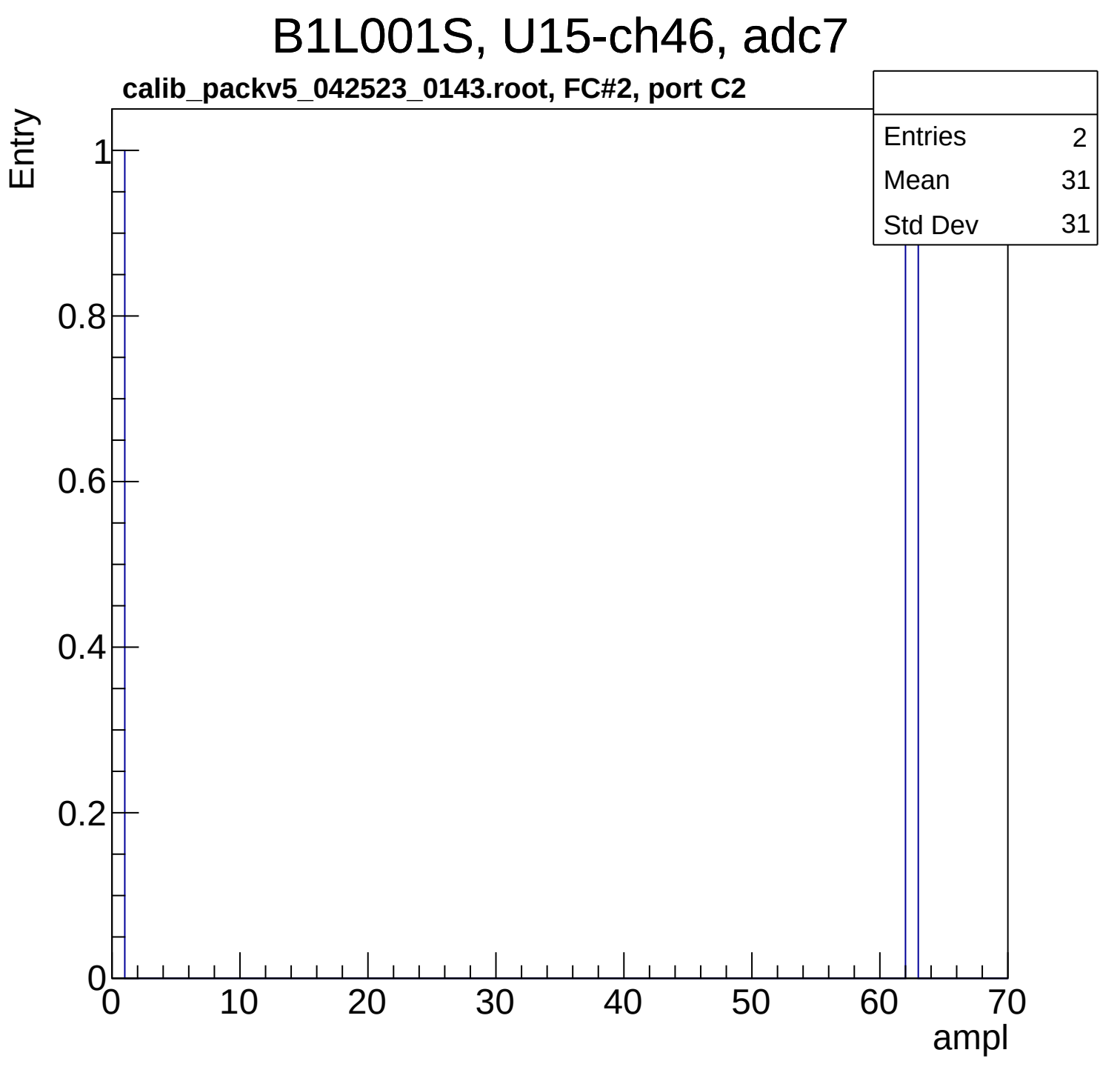
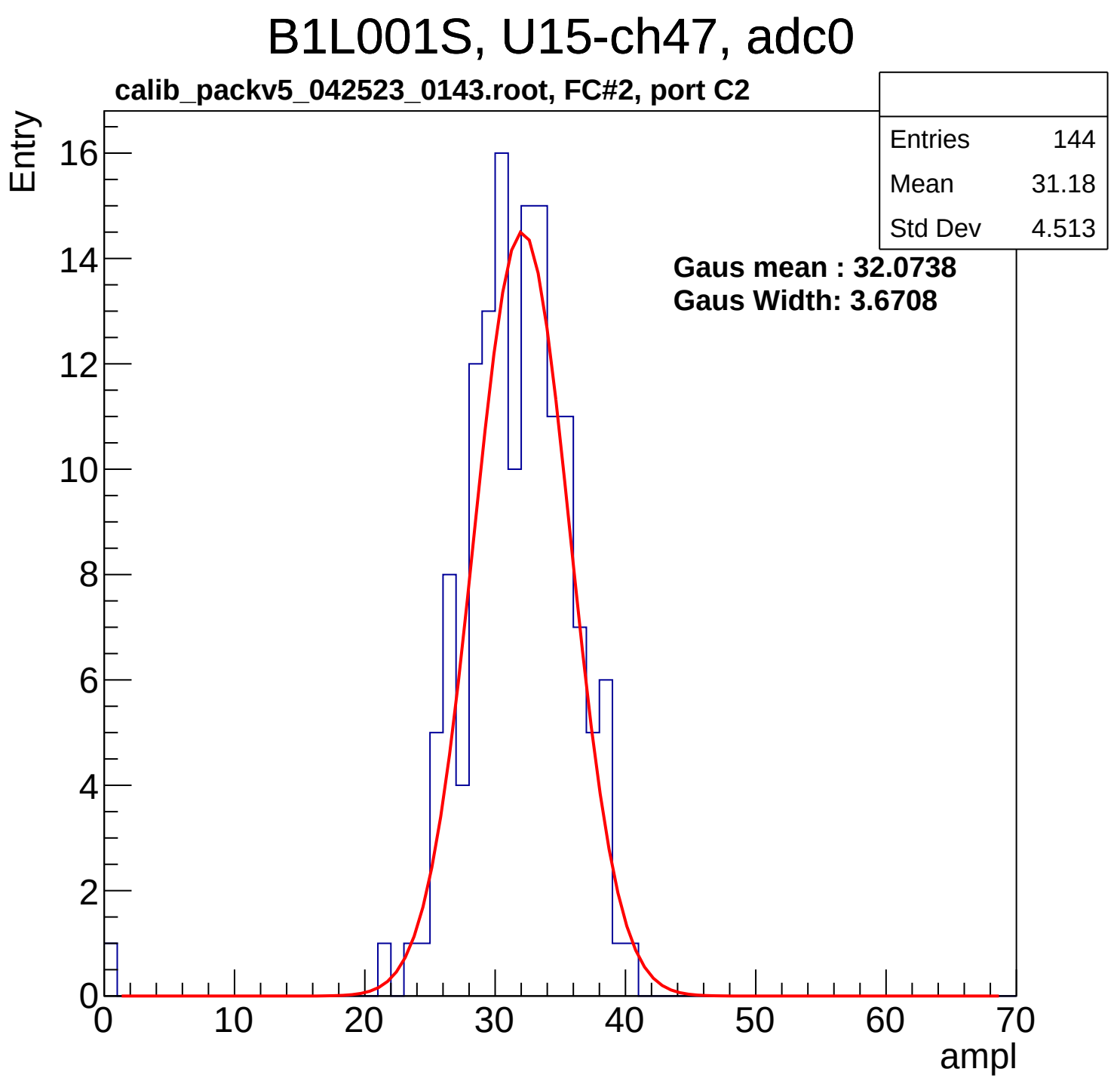
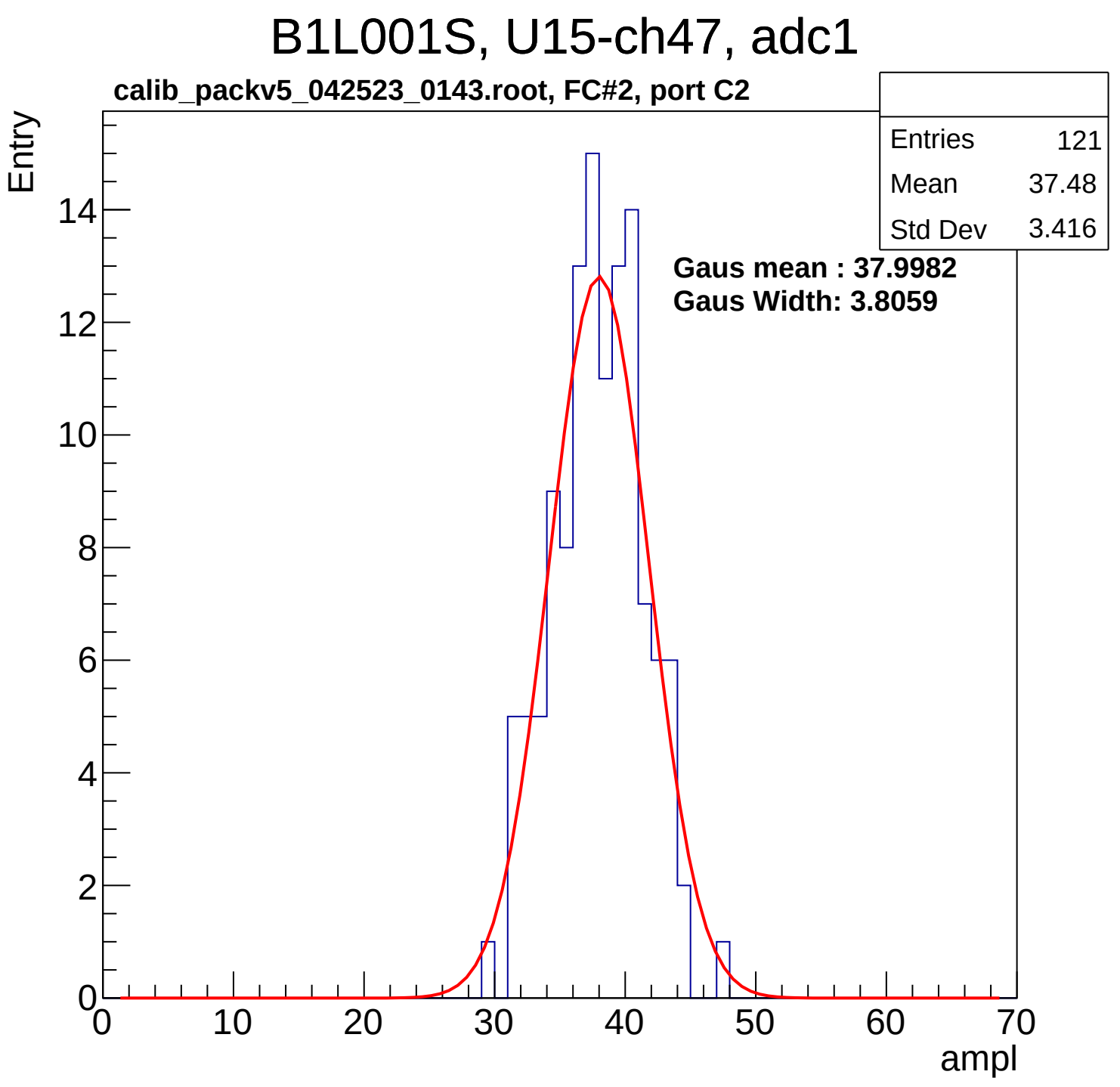

B1L001S, U15-ch46, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch47, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 31.18
Std Dev 4.513
Gaus mean : 32.0738
Gaus Width: 3.6708
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch47, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 37.48
Std Dev 3.416
Gaus mean : 37.9982
Gaus Width: 3.8059
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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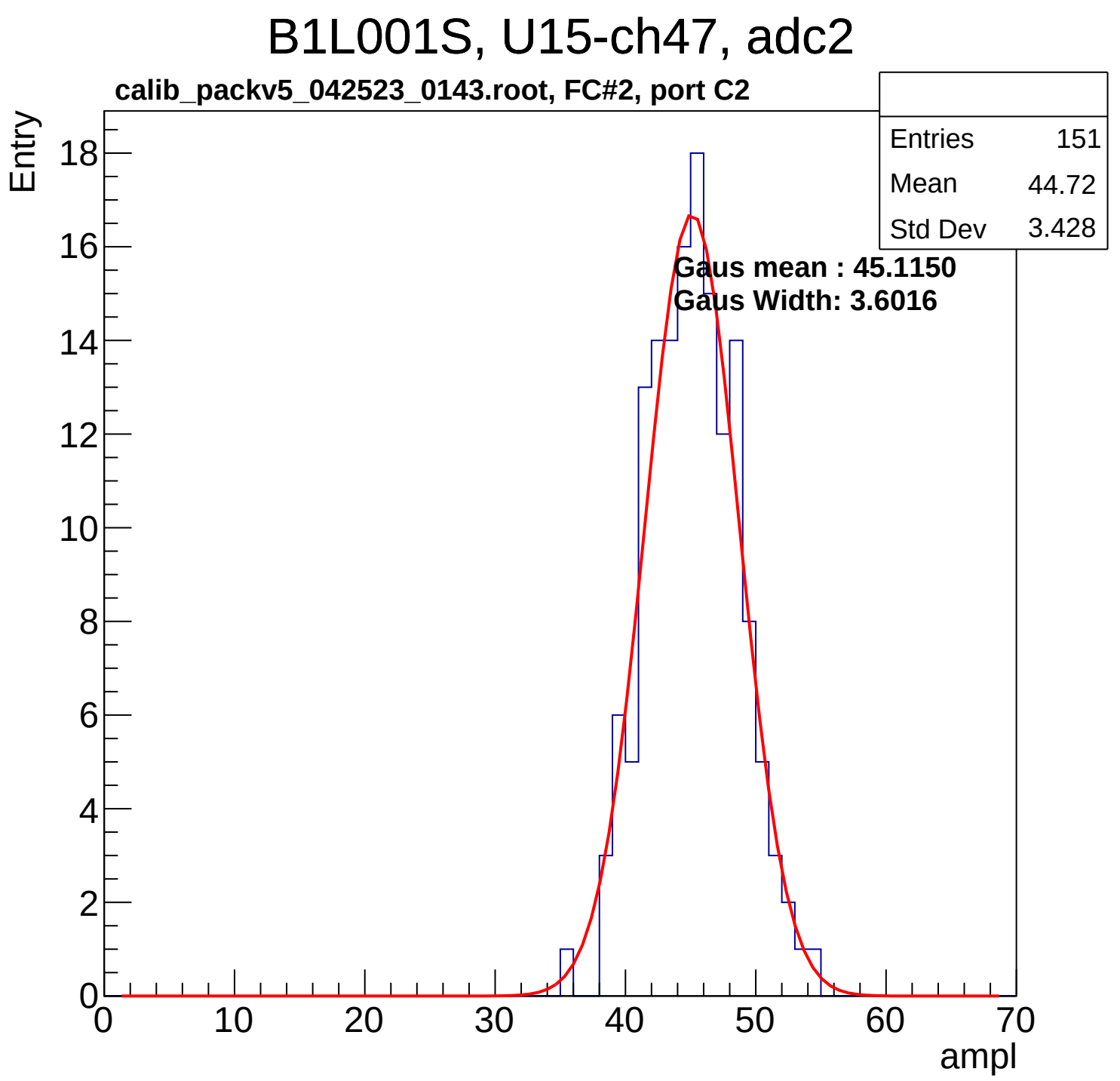
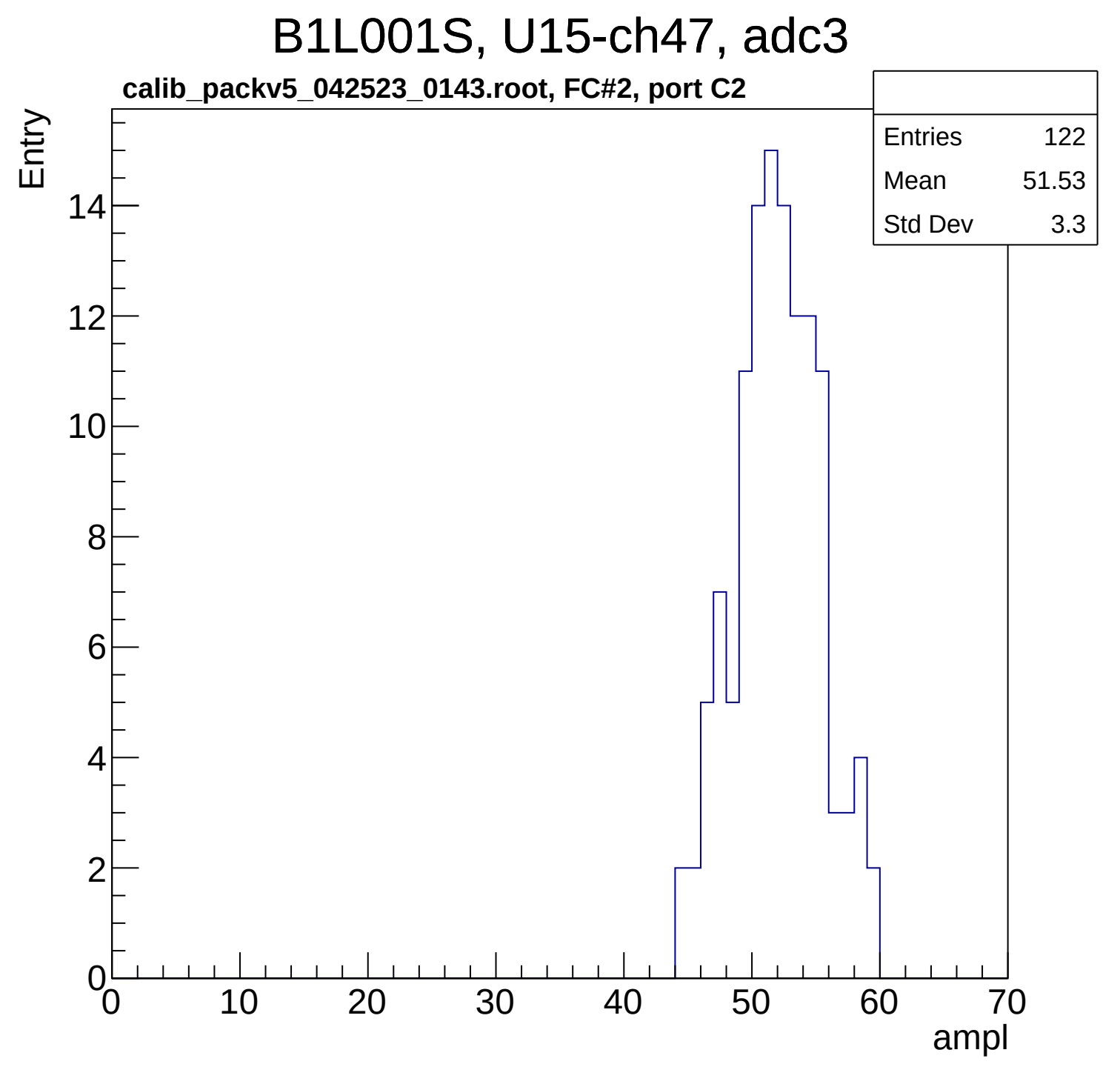
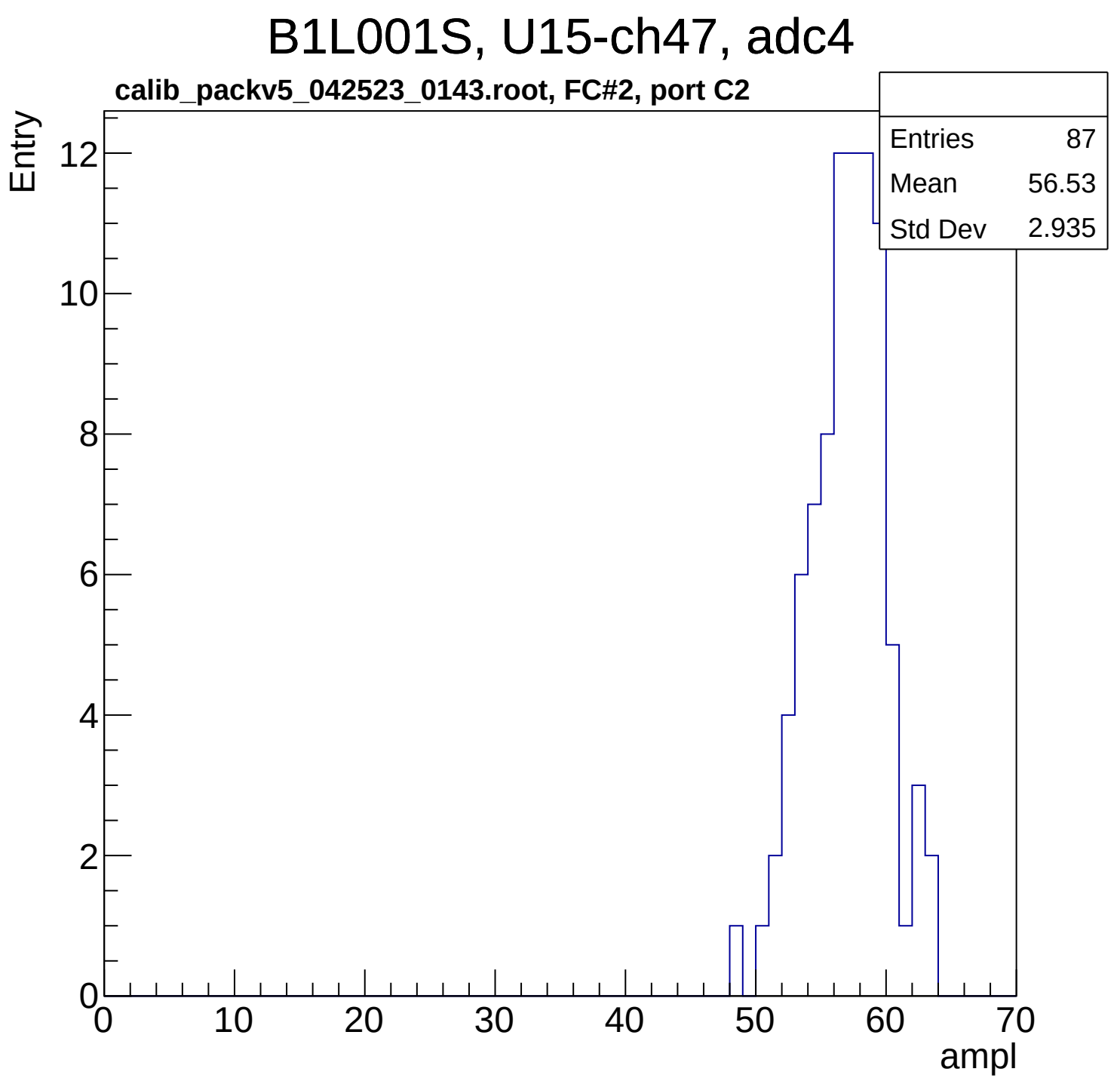
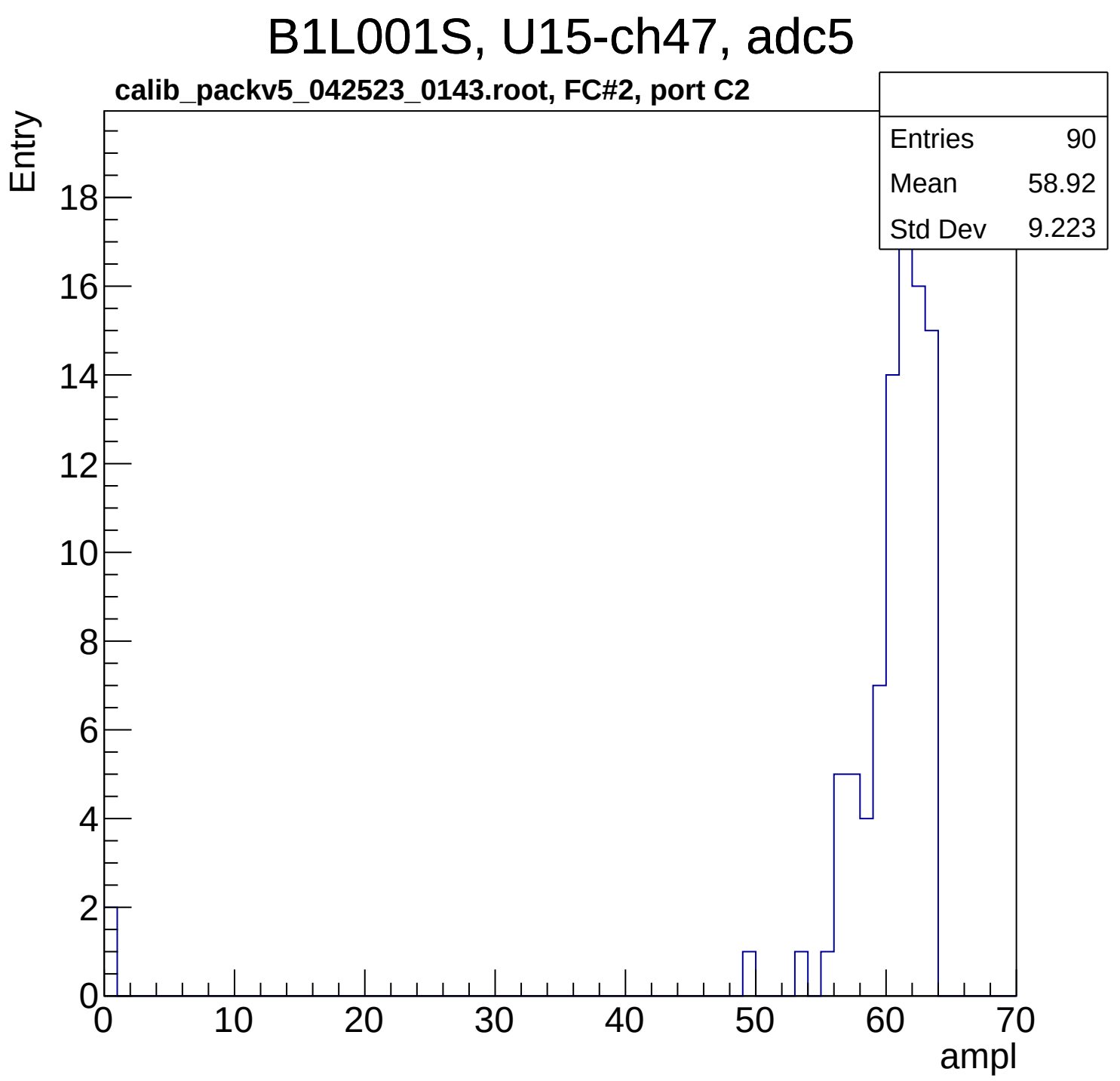

B1L001S, U15-ch47, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 44.72
Std Dev 3.428
Gaus mean : 45.1150
Gaus Width: 3.6016
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch47, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 51.53
Std Dev 3.3
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch47, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 56.53
Std Dev 2.935
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch47, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 58.92
Std Dev 9.223
Entry
ampl
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4
6
8
10
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16
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0
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60
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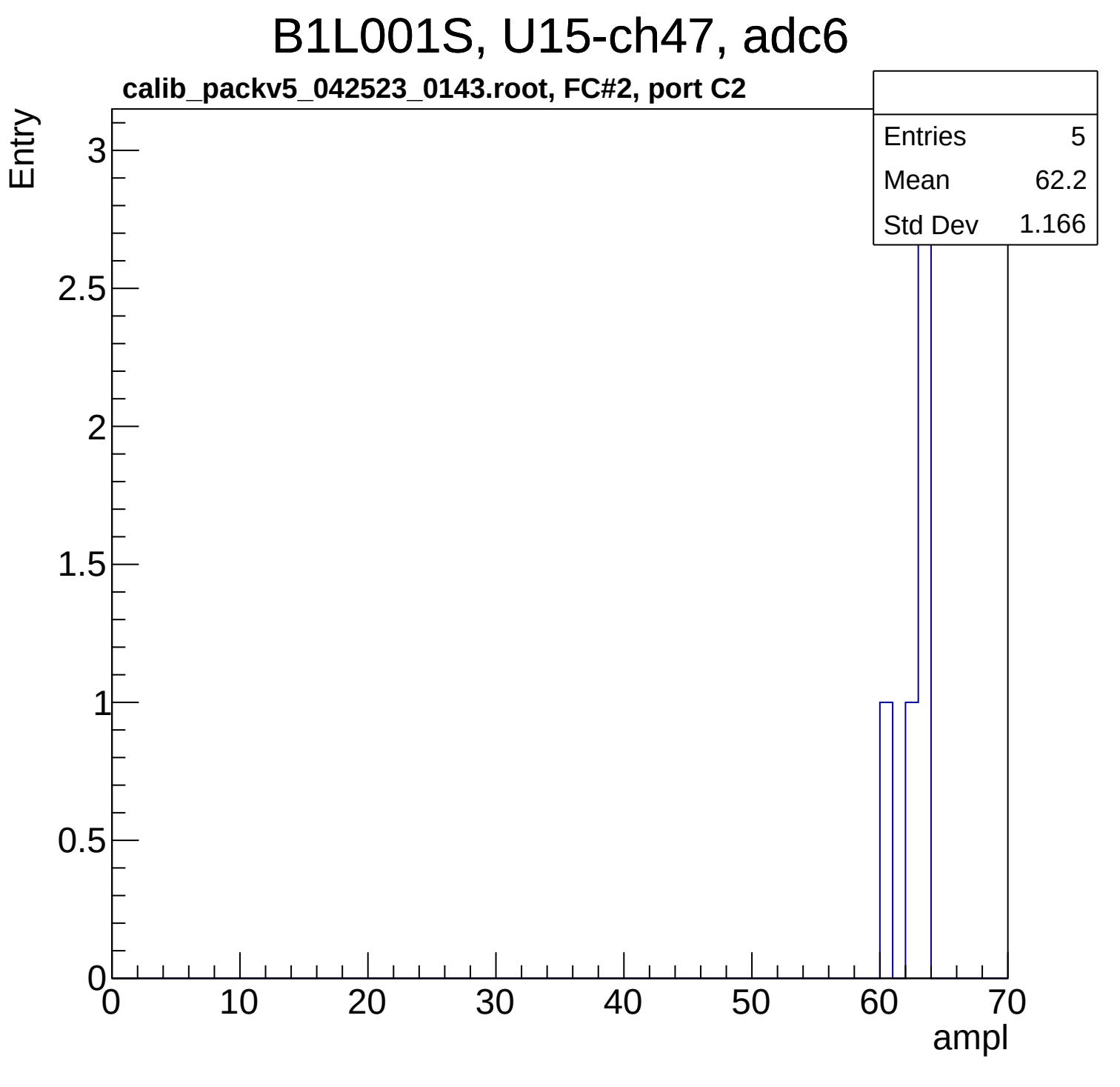
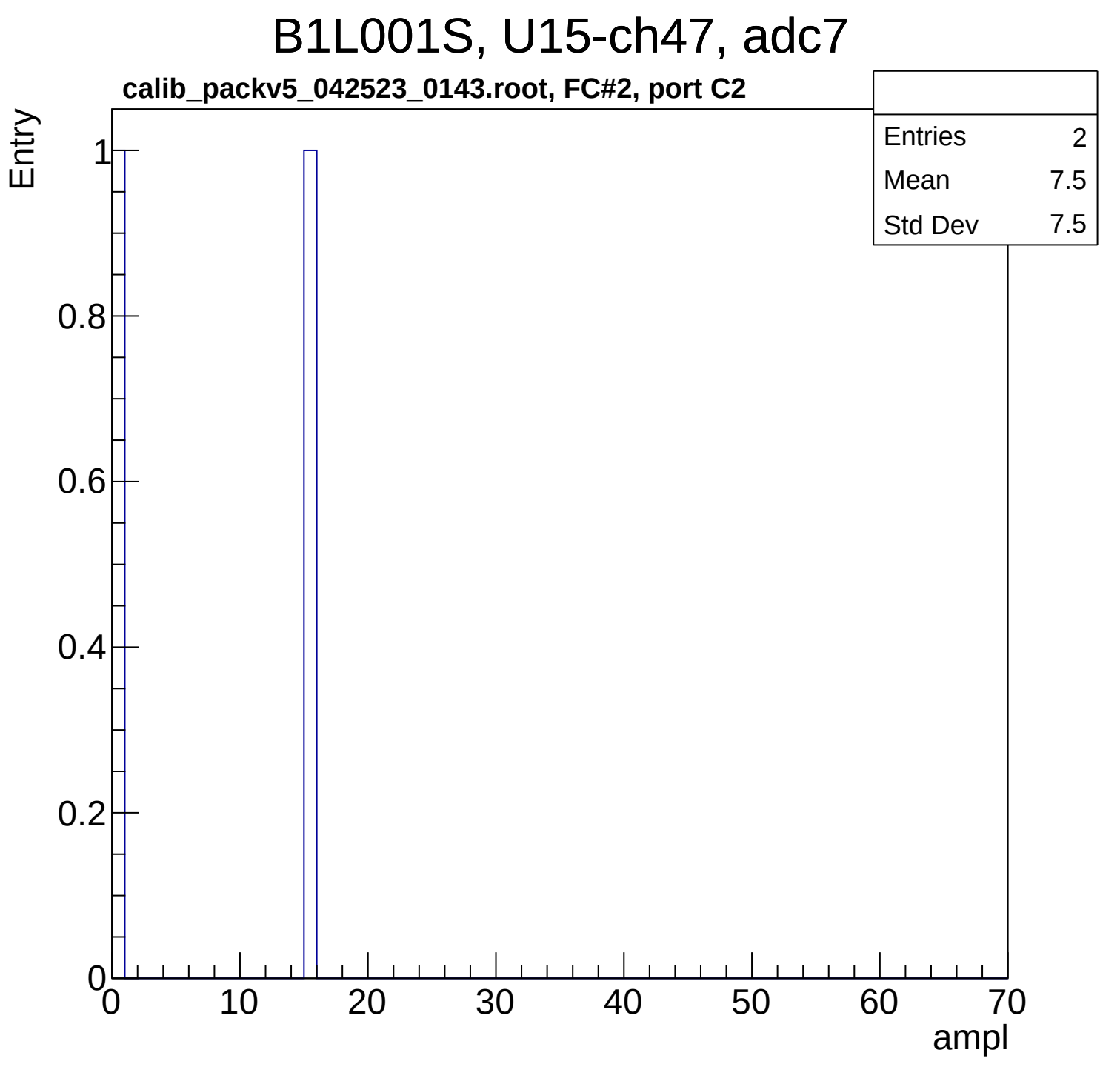
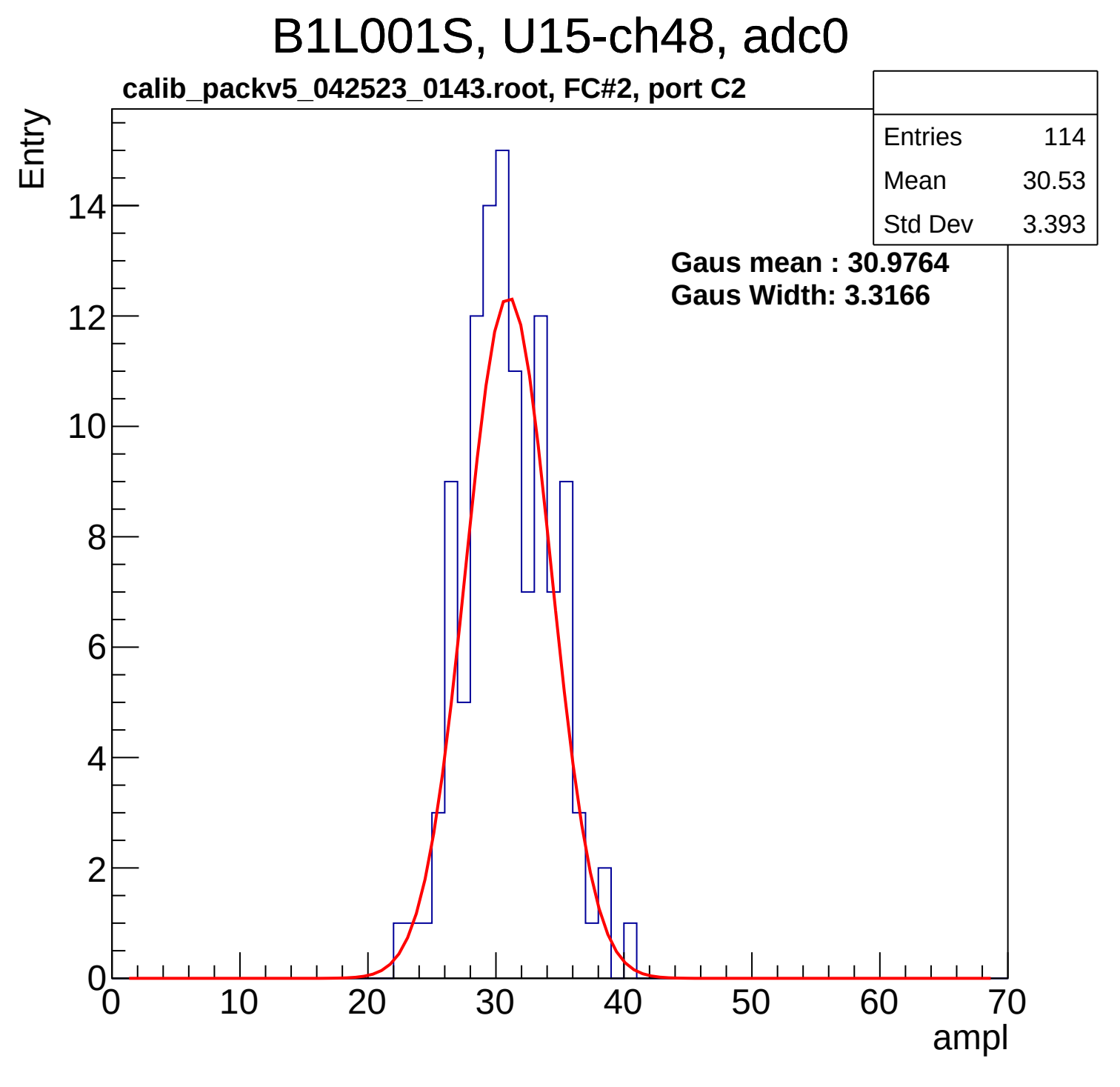
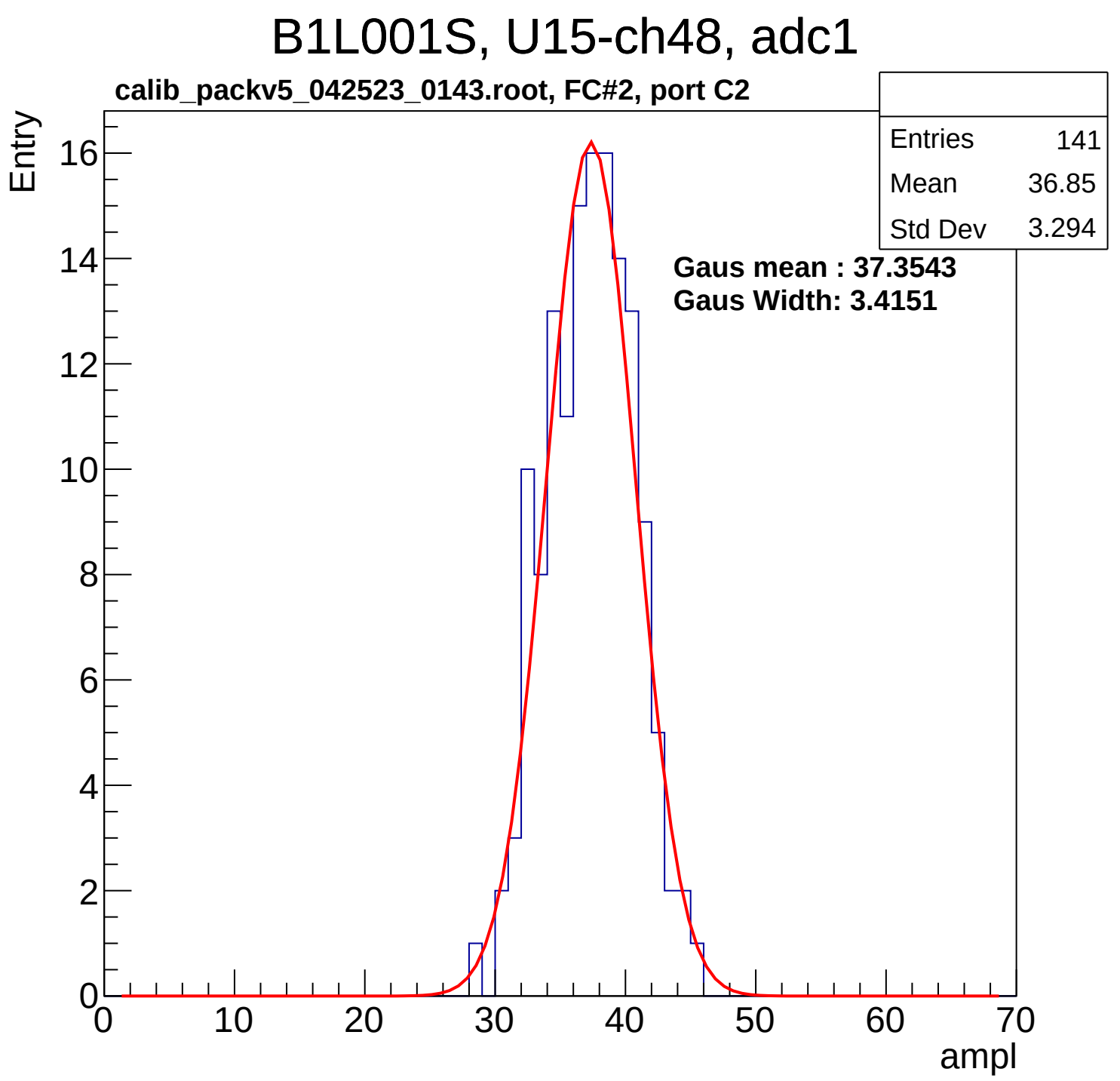
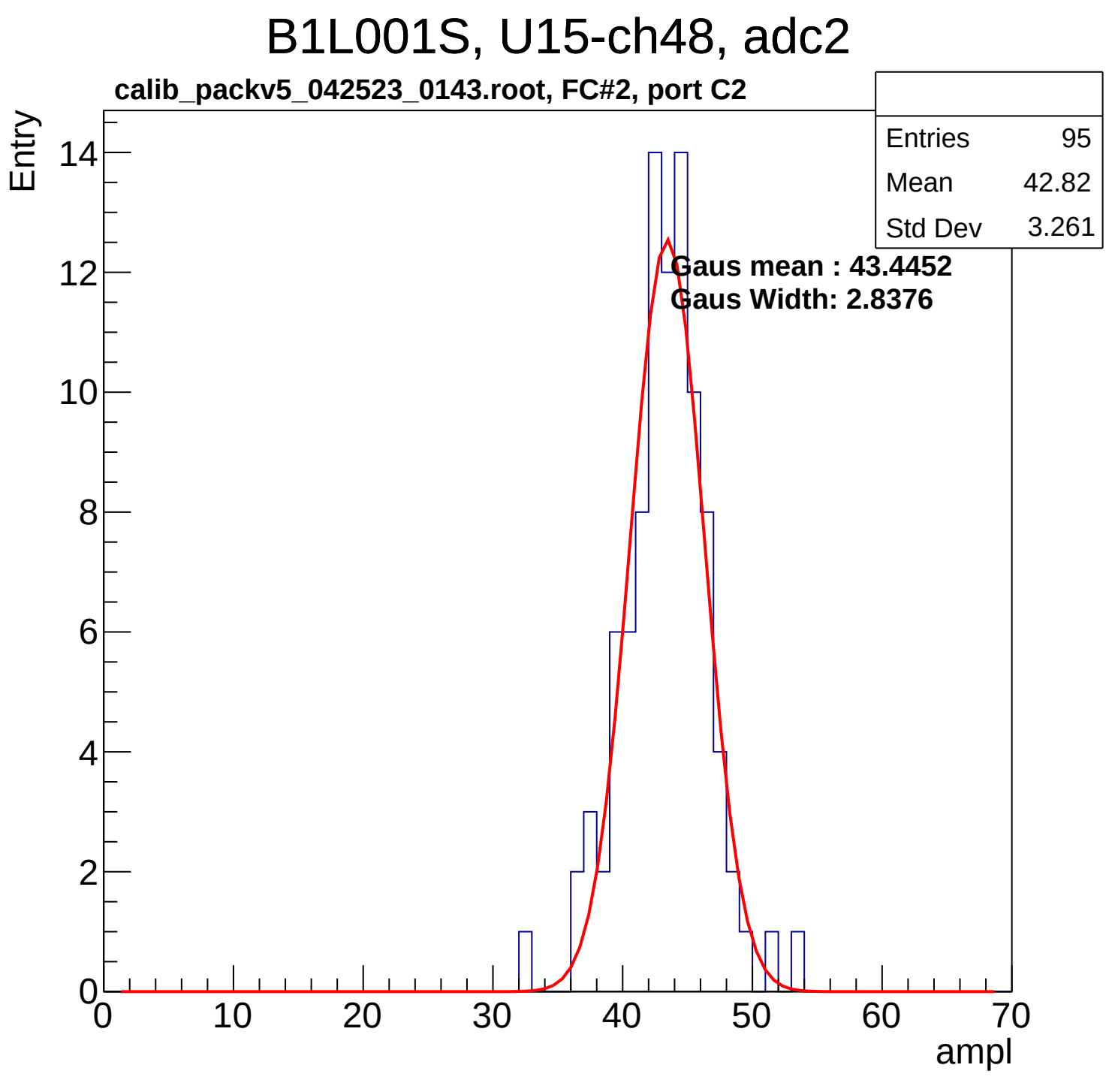
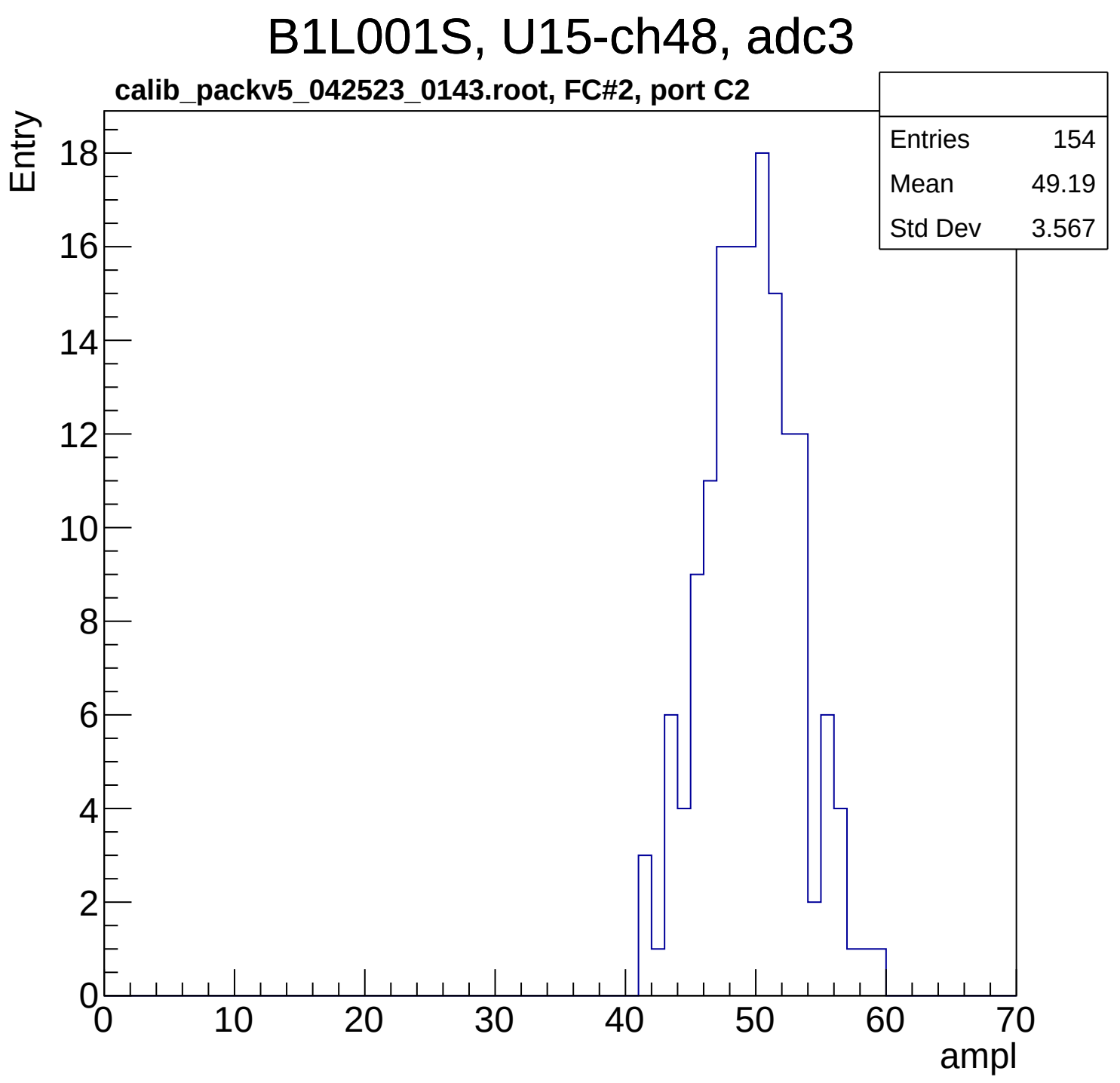

B1L001S, U15-ch47, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch47, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 7.5
Std Dev 7.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U15-ch48, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 30.53
Std Dev 3.393
Gaus mean : 30.9764
Gaus Width: 3.3166
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch48, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 36.85
Std Dev 3.294
Gaus mean : 37.3543
Gaus Width: 3.4151
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch48, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 42.82
Std Dev 3.261
Gaus mean : 43.4452
Gaus Width: 2.8376
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch48, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 49.19
Std Dev 3.567
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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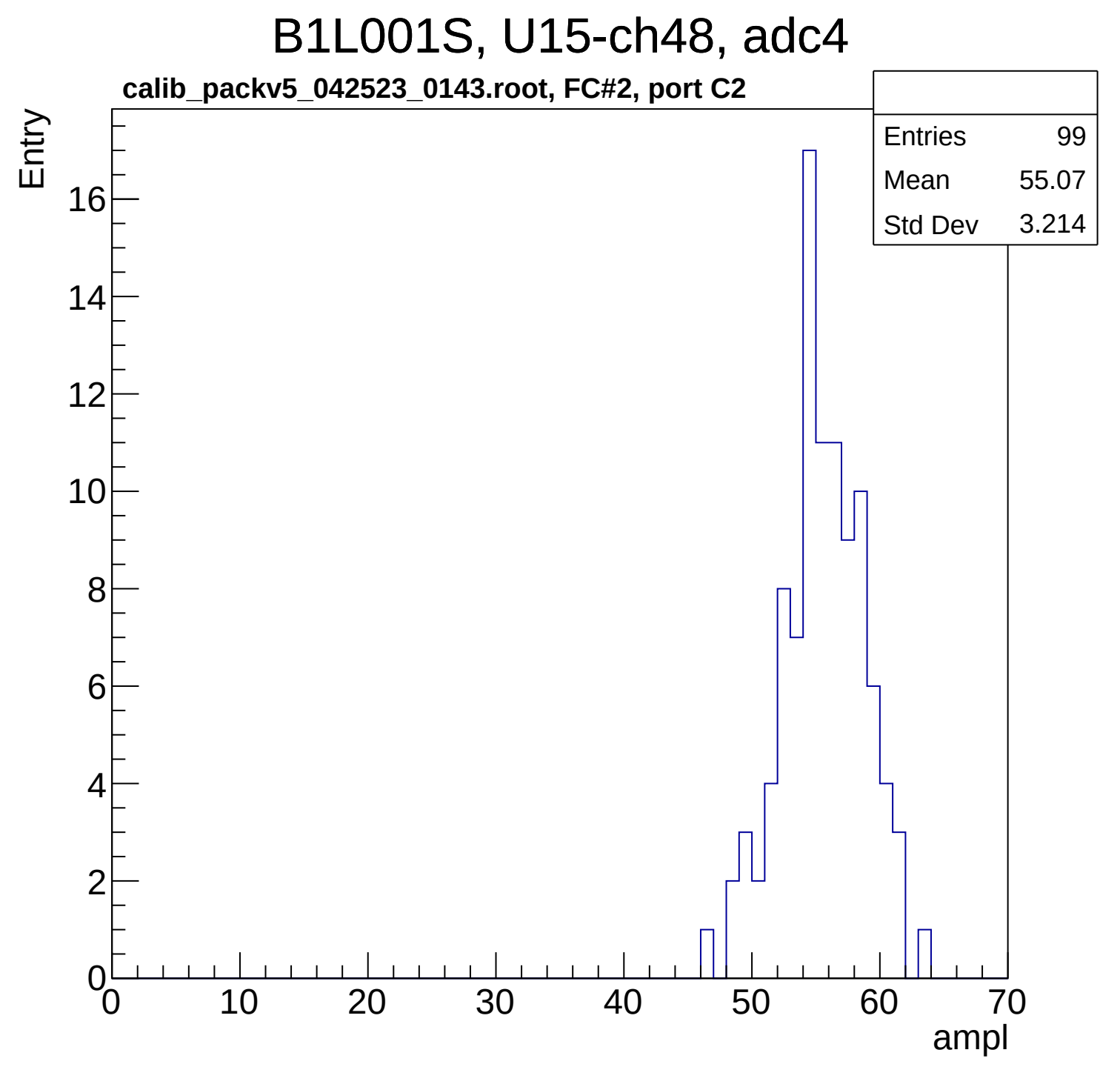
60
70


B1L001S, U15-ch48, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 55.07
Std Dev 3.214
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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40
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60
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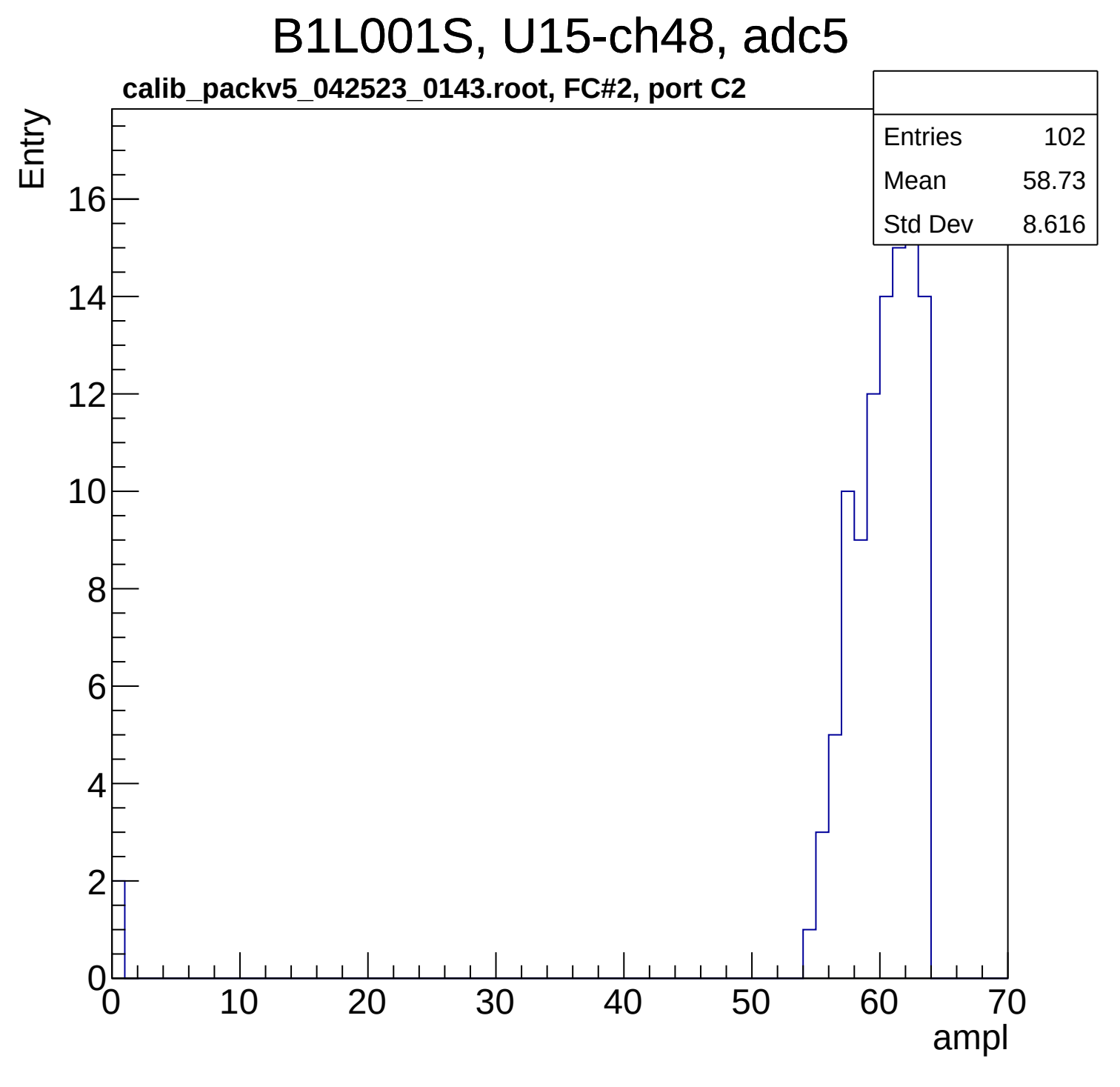
B1L001S, U15-ch48, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 58.73
Std Dev 8.616
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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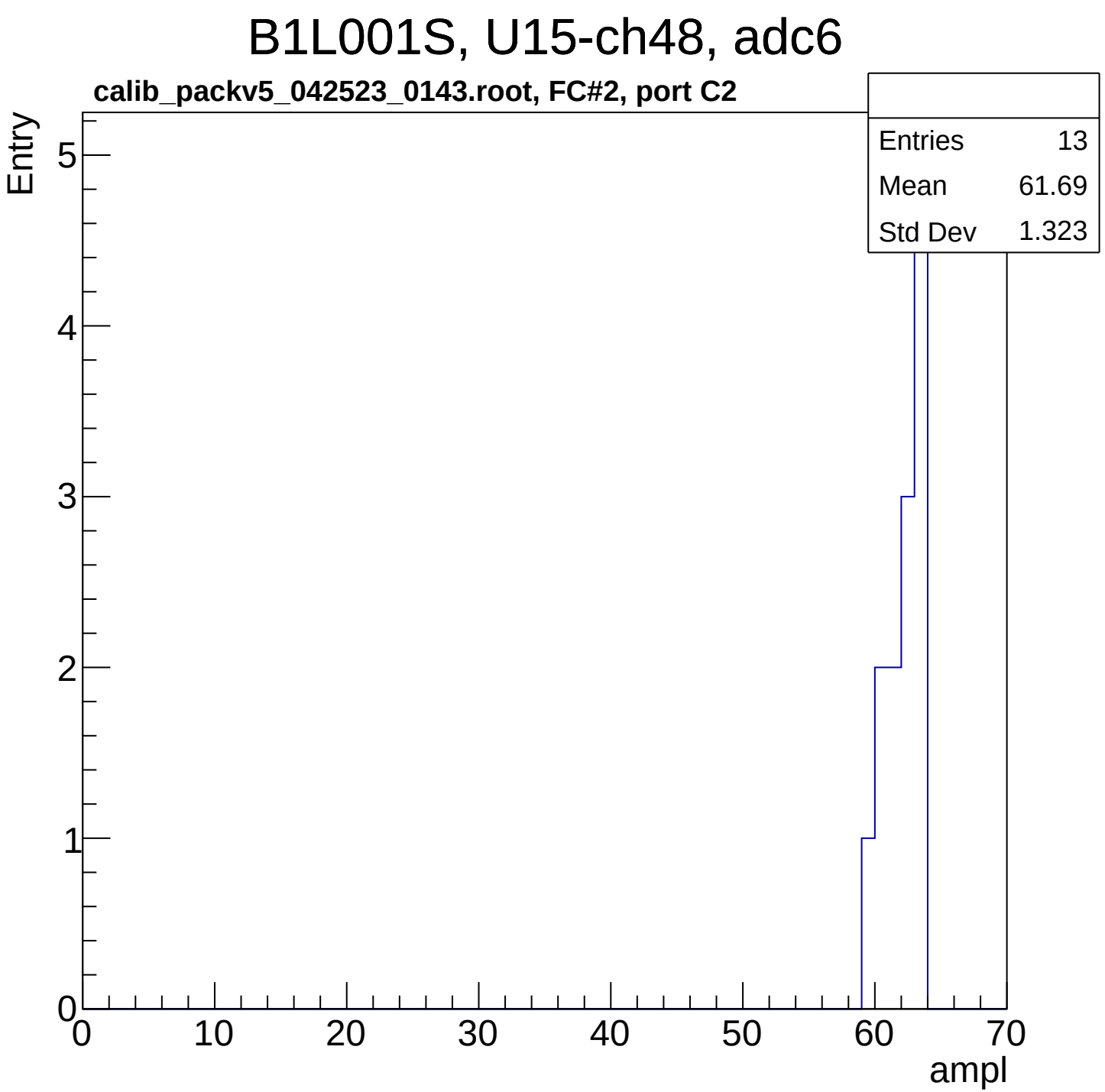
B1L001S, U15-ch48, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 61.69
Std Dev 1.323
Entry
0
1
2
3
4
5
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10
20
30
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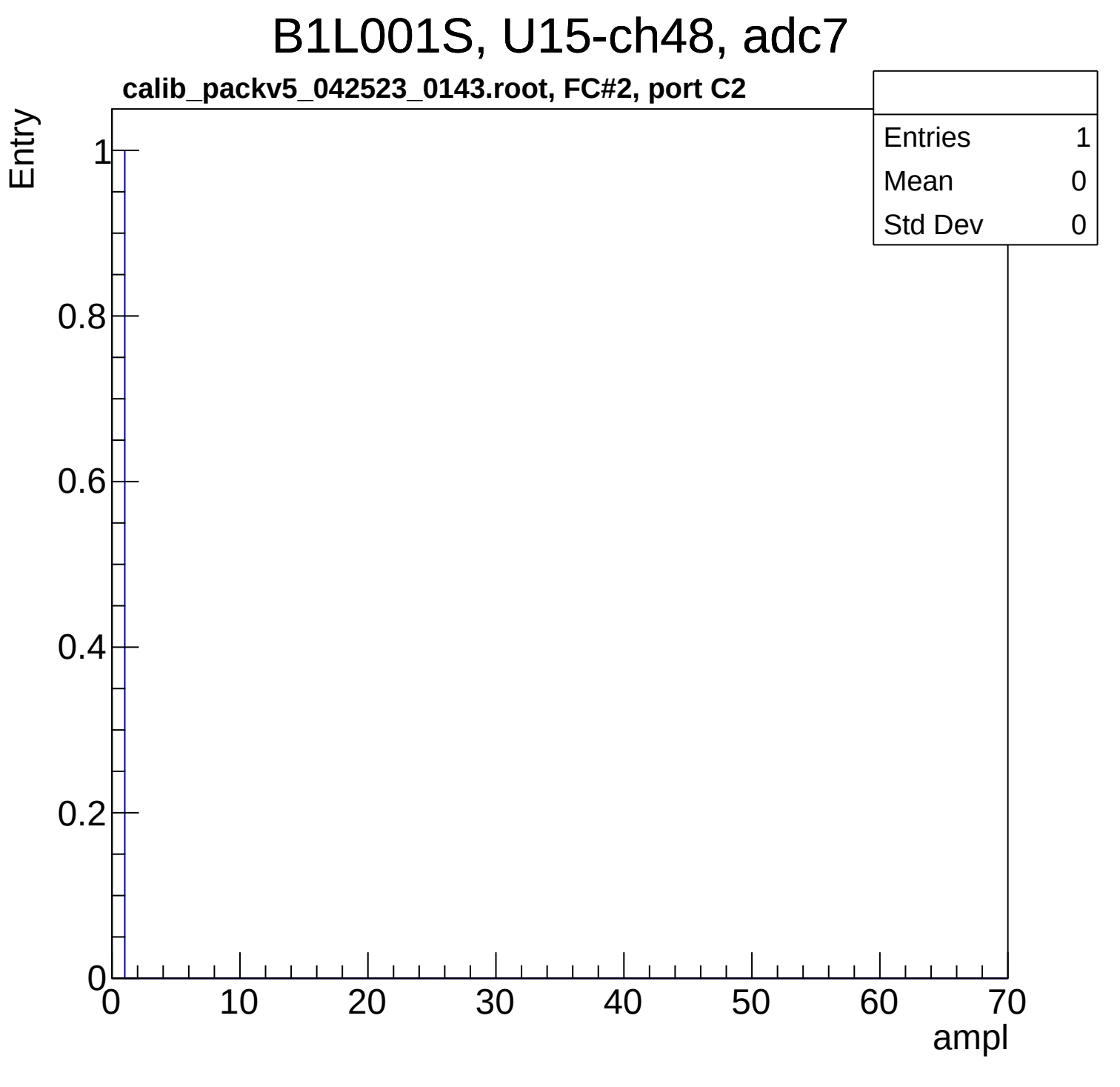
50
60
70
ampl


B1L001S, U15-ch48, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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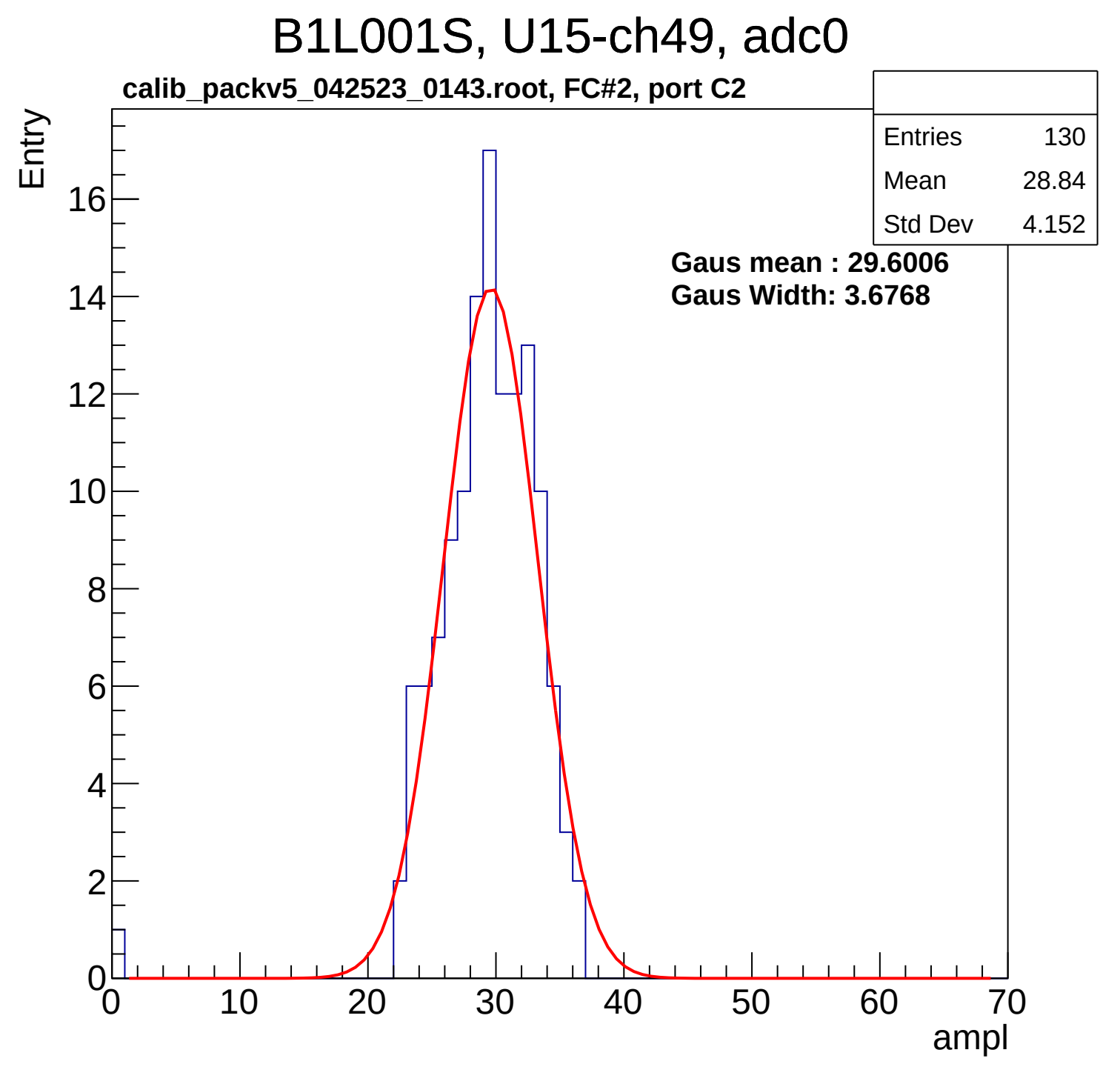
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch49, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 28.84
Std Dev 4.152
Gaus mean : 29.6006
Gaus Width: 3.6768
Entry
ampl
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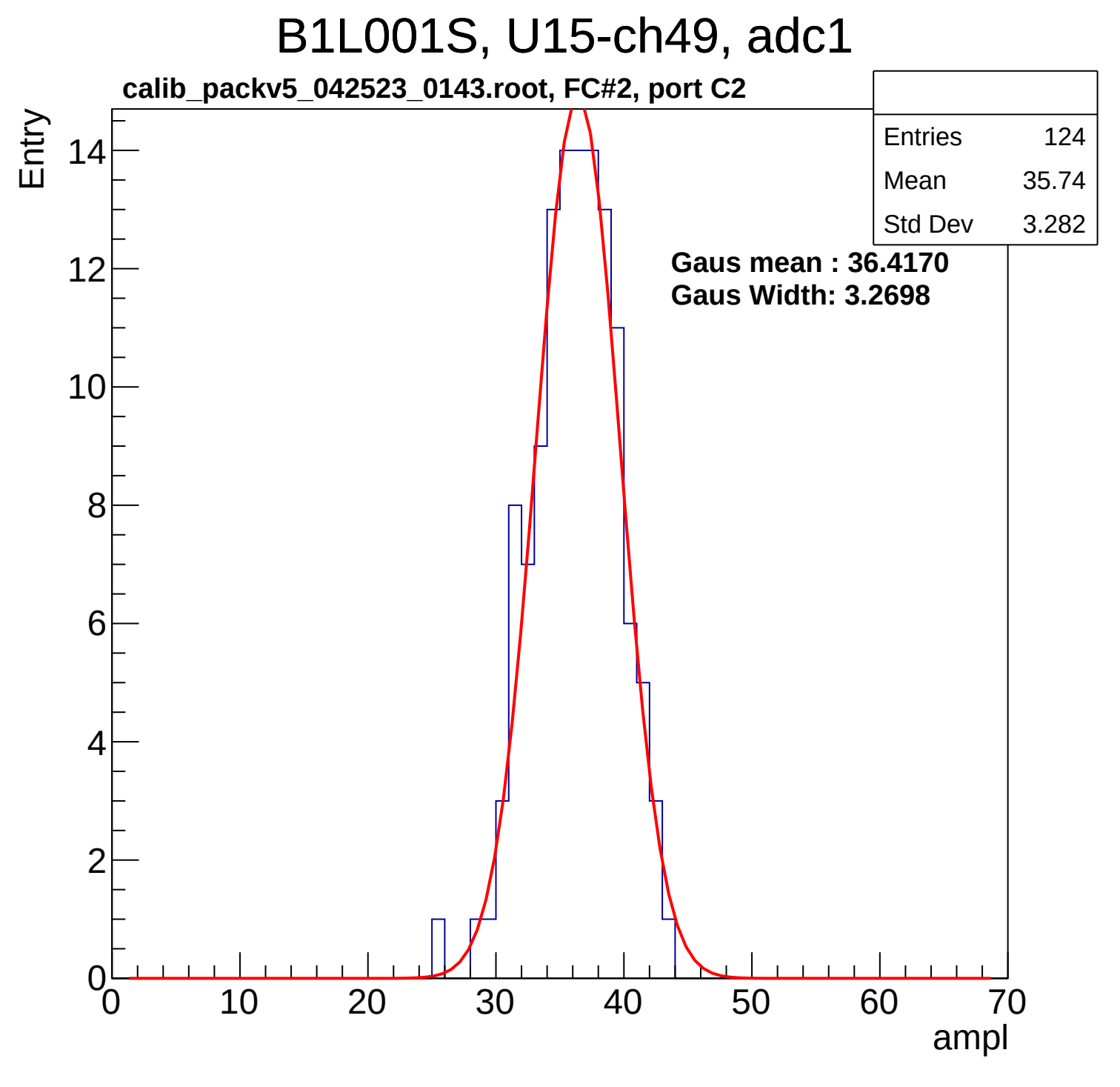
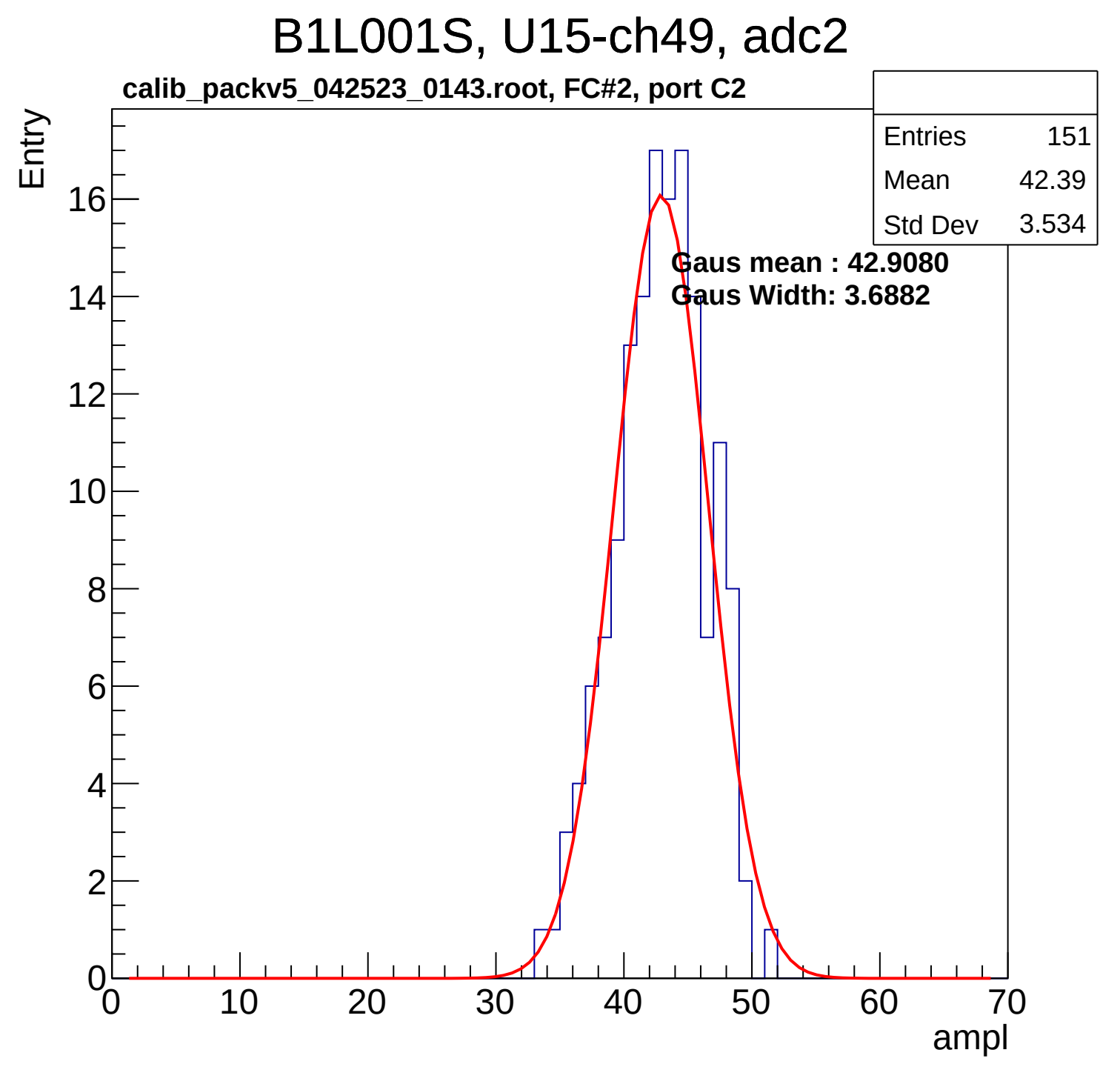
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch49, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 35.74
Std Dev 3.282
Gaus mean : 36.4170
Gaus Width: 3.2698
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch49, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 42.39
Std Dev 3.534
Gaus mean : 42.9080
Gaus Width: 3.6882
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
30
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60
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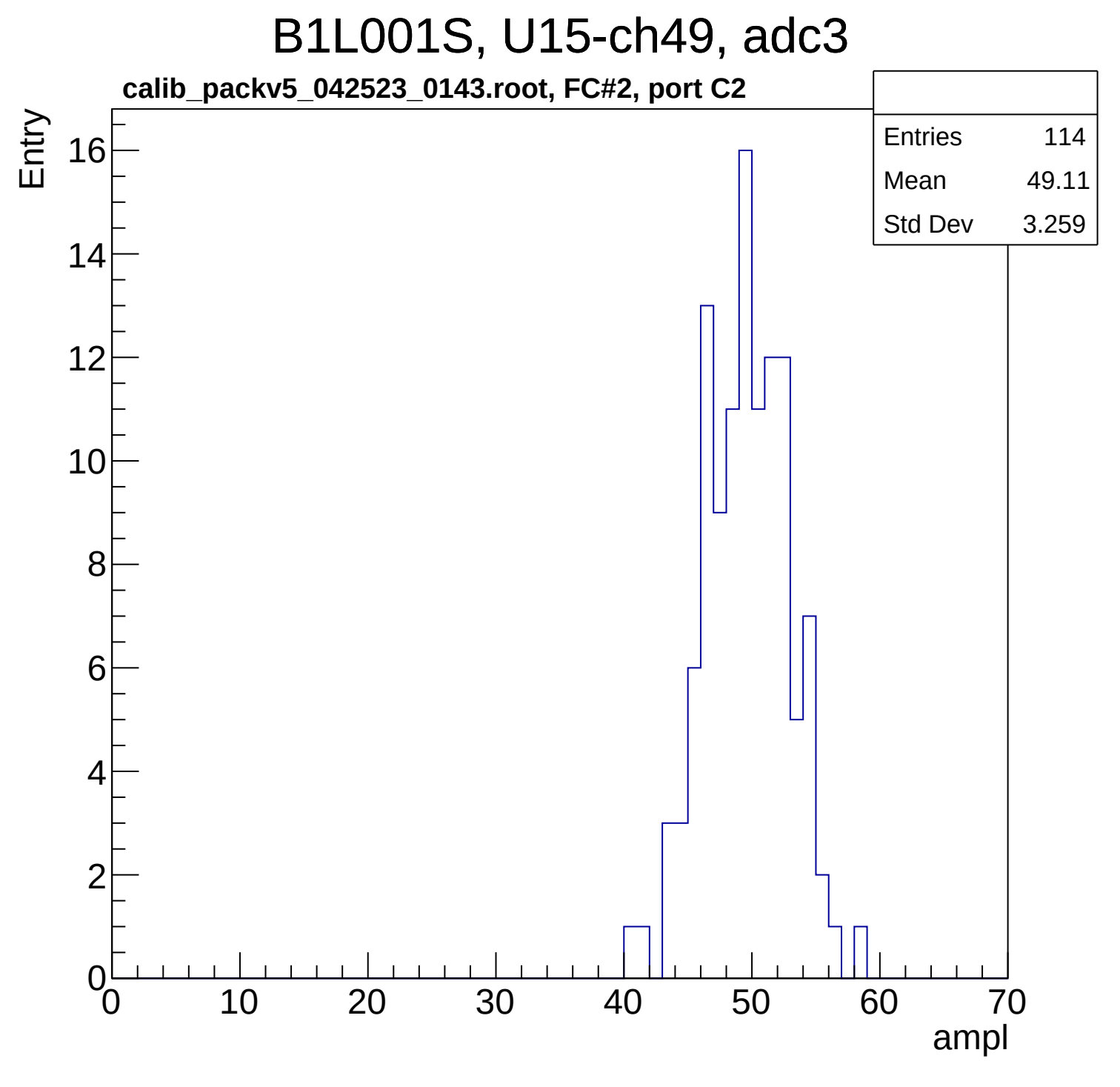
B1L001S, U15-ch49, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 49.11
Std Dev 3.259
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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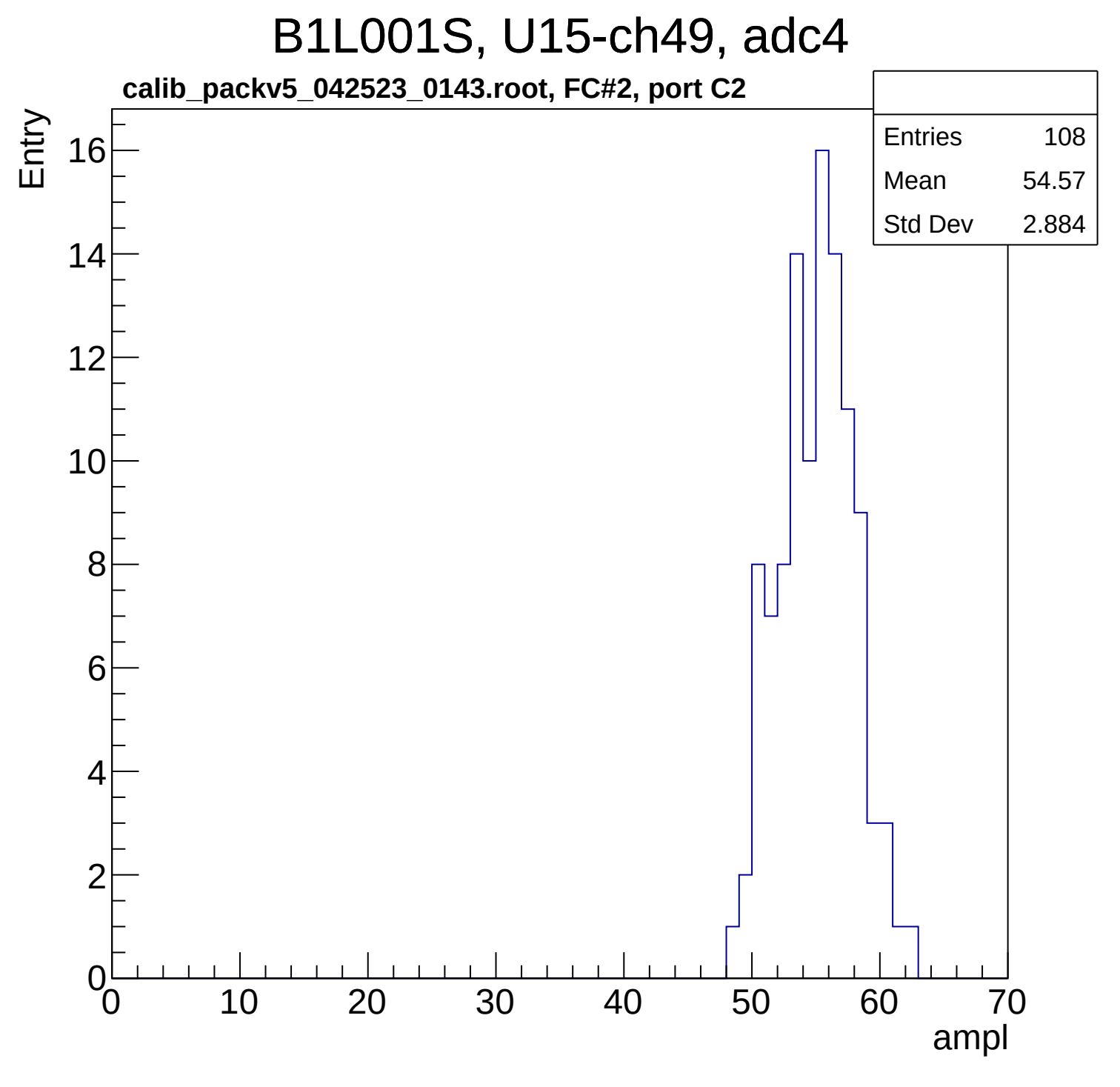
B1L001S, U15-ch49, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 54.57
Std Dev 2.884
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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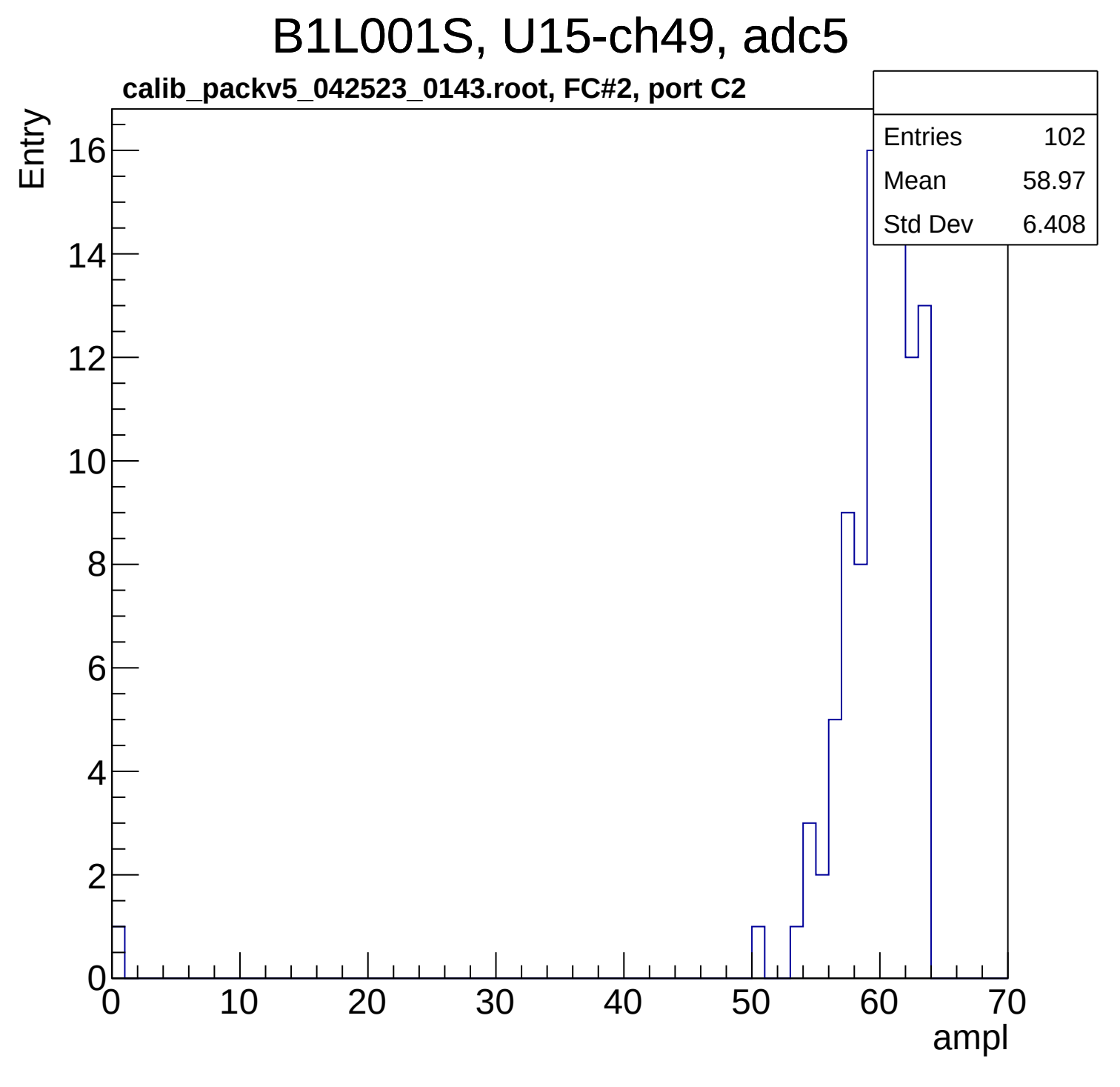
B1L001S, U15-ch49, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 58.97
Std Dev 6.408
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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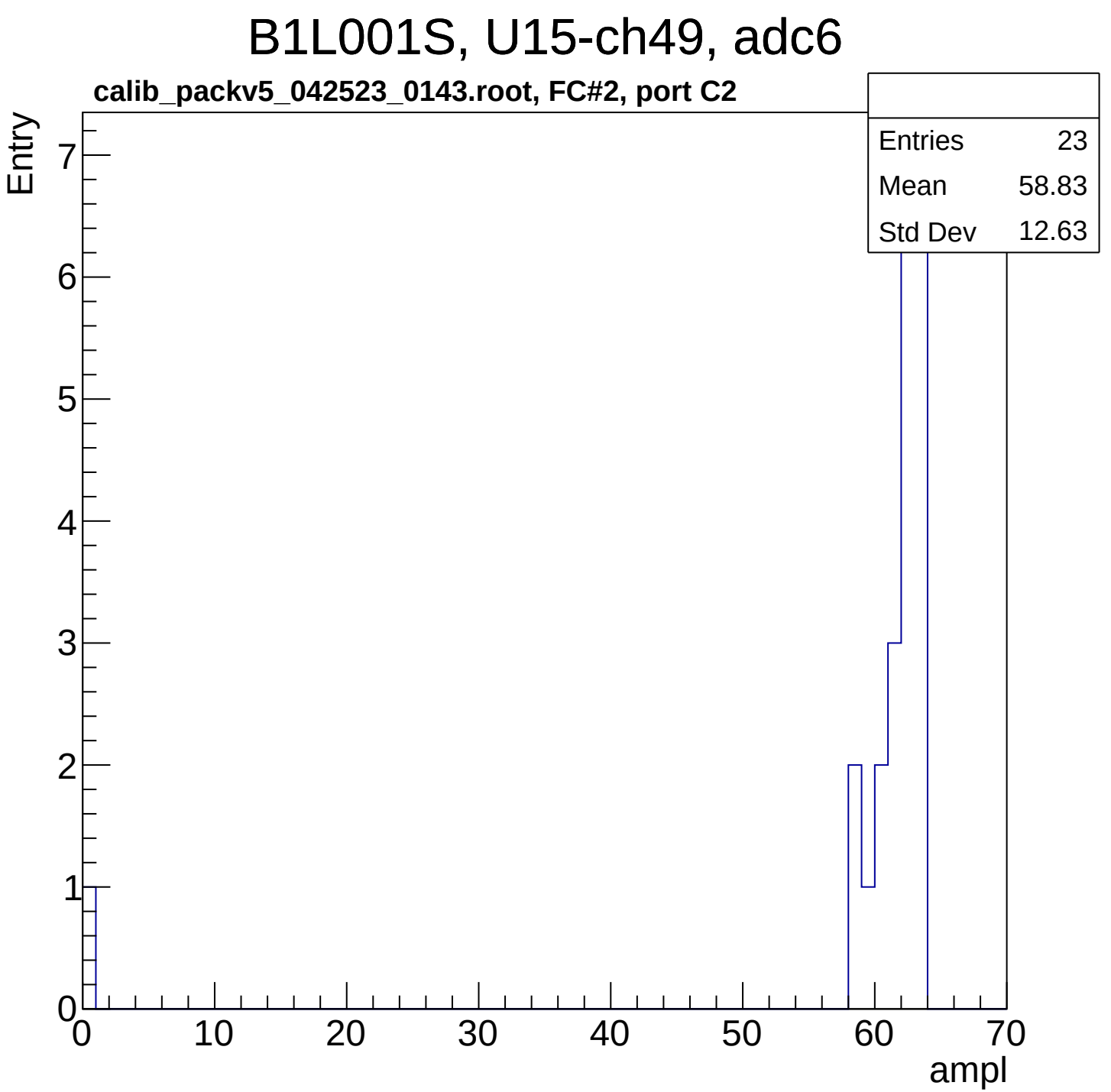
B1L001S, U15-ch49, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 23
Mean 58.83
Std Dev 12.63
Entry
ampl
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4
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6
7
0
10
20
30
40
50
60
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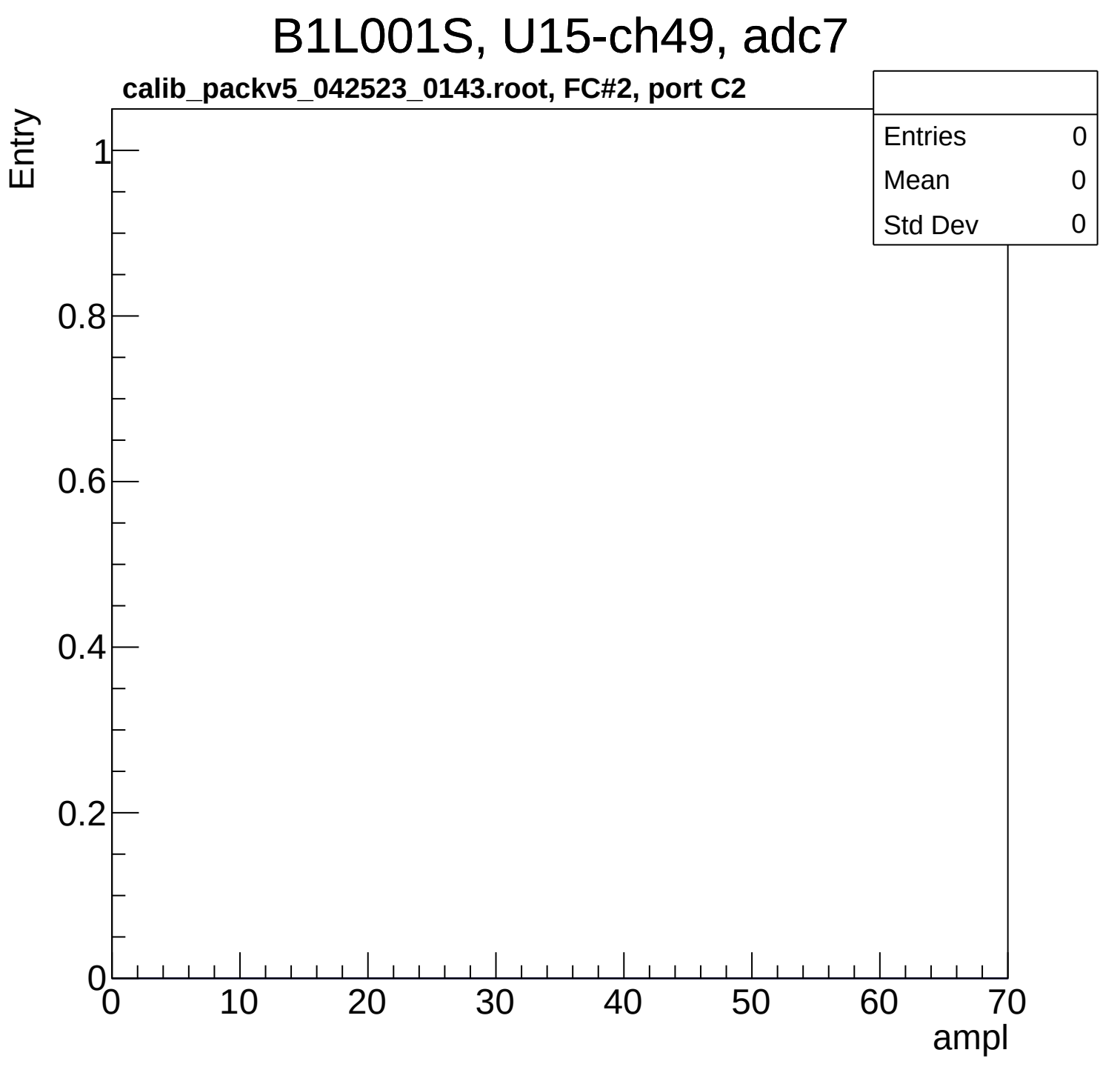
B1L001S, U15-ch49, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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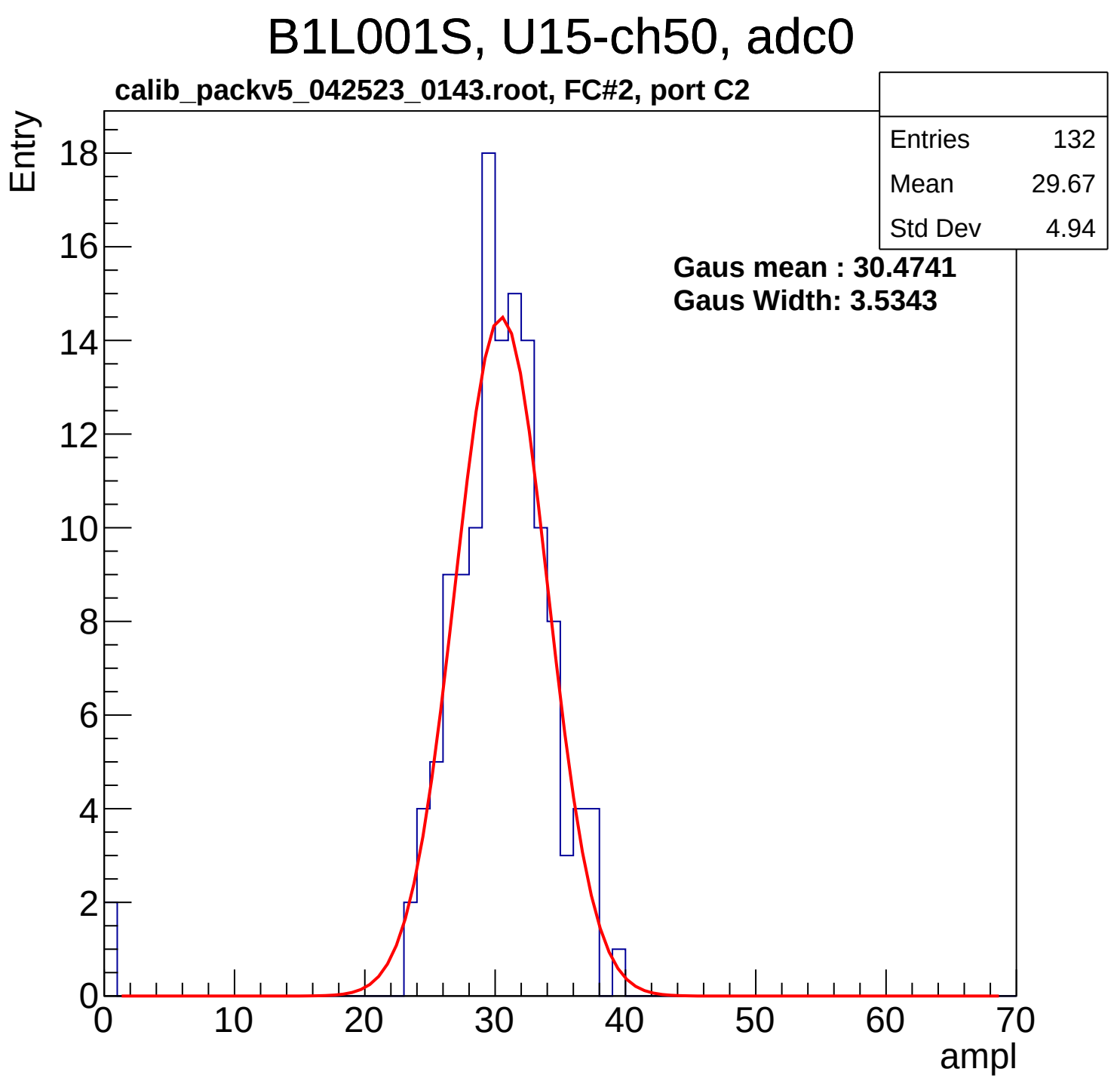
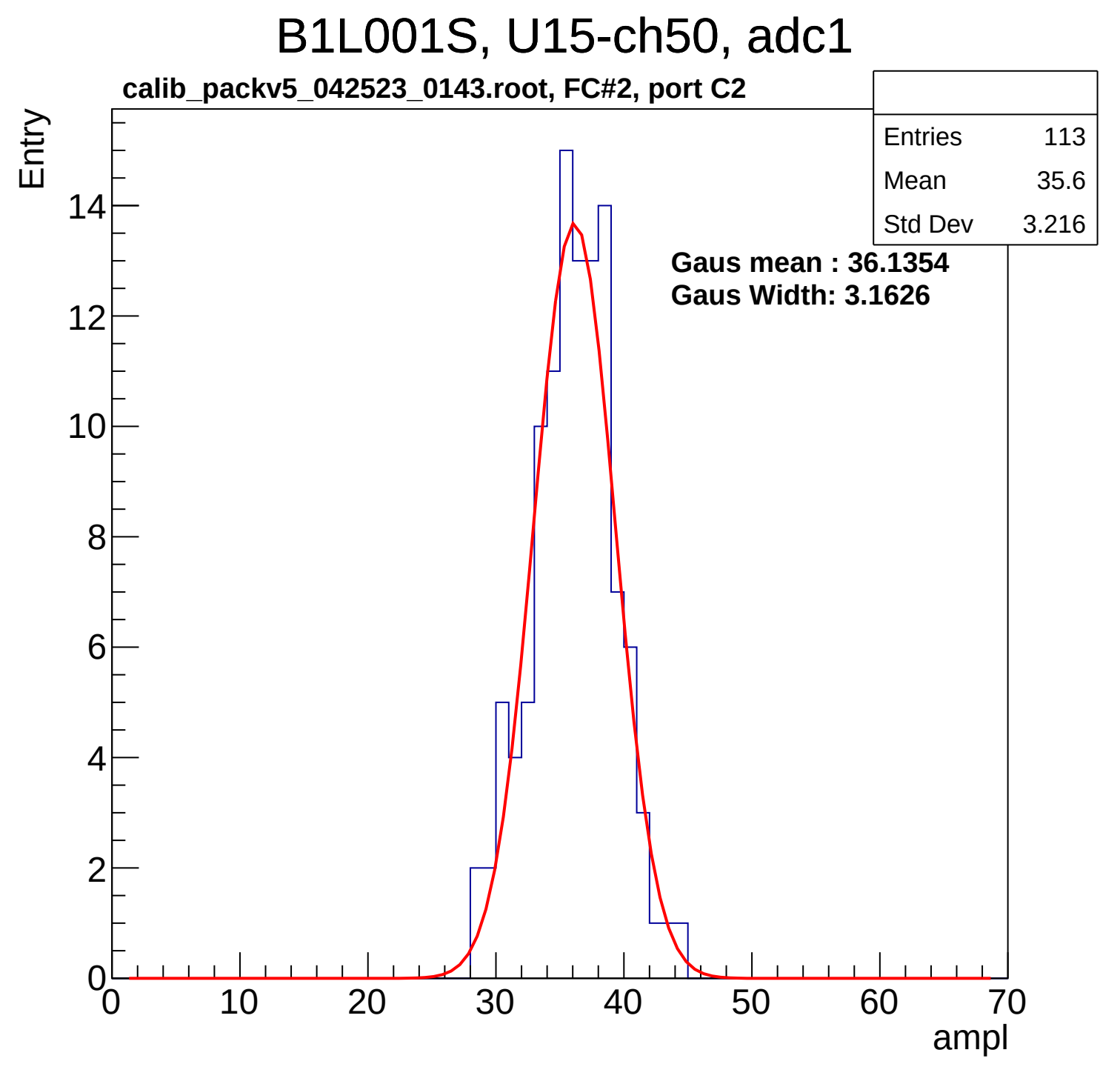
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch50, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 29.67
Std Dev 4.94
Gaus mean : 30.4741
Gaus Width: 3.5343
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch50, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 35.6
Std Dev 3.216
Gaus mean : 36.1354
Gaus Width: 3.1626
Entry
ampl
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2
4
6
8
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0
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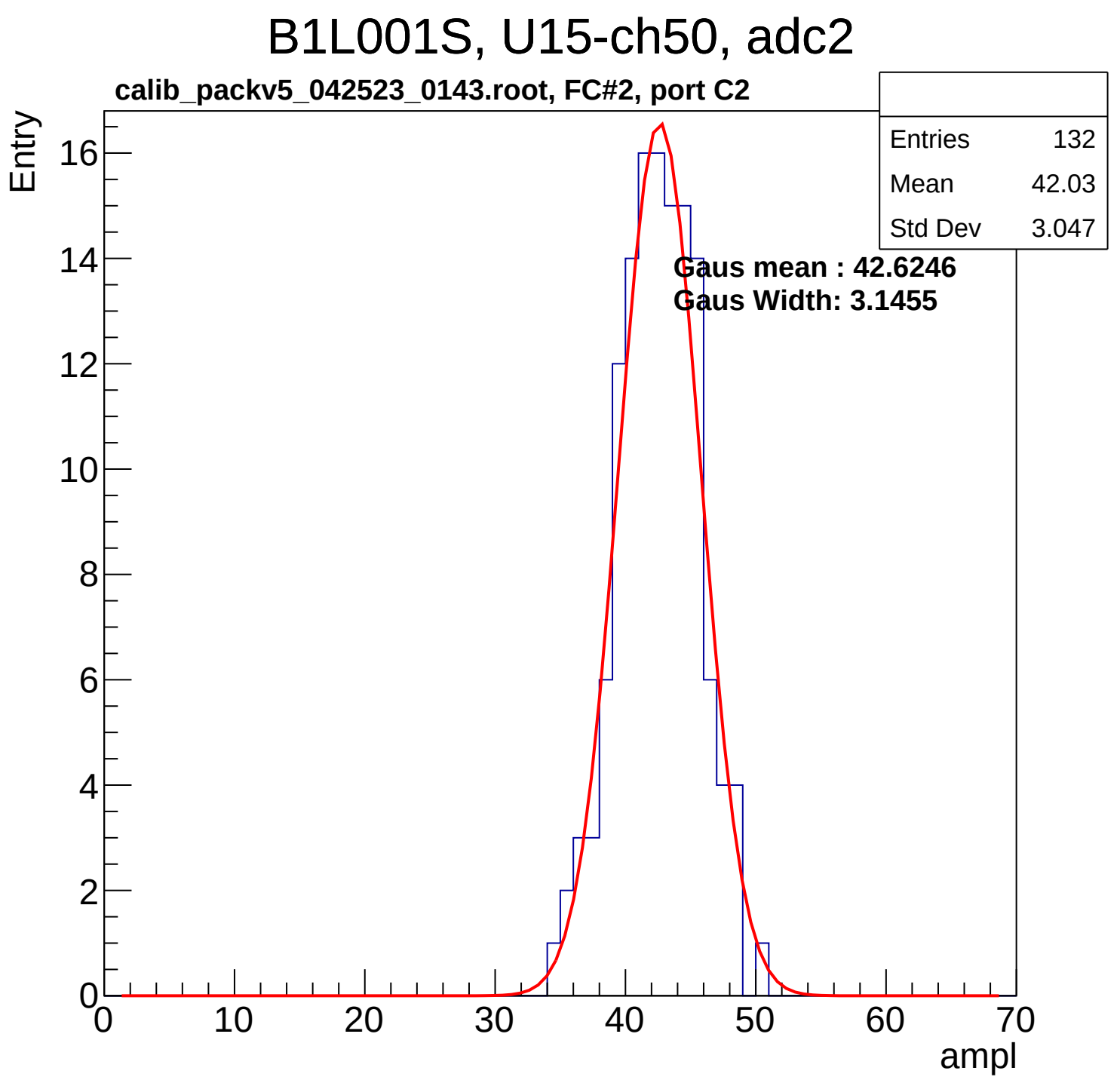
B1L001S, U15-ch50, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 42.03
Std Dev 3.047
Gaus mean : 42.6246
Gaus Width: 3.1455
Entry
ampl
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8
10
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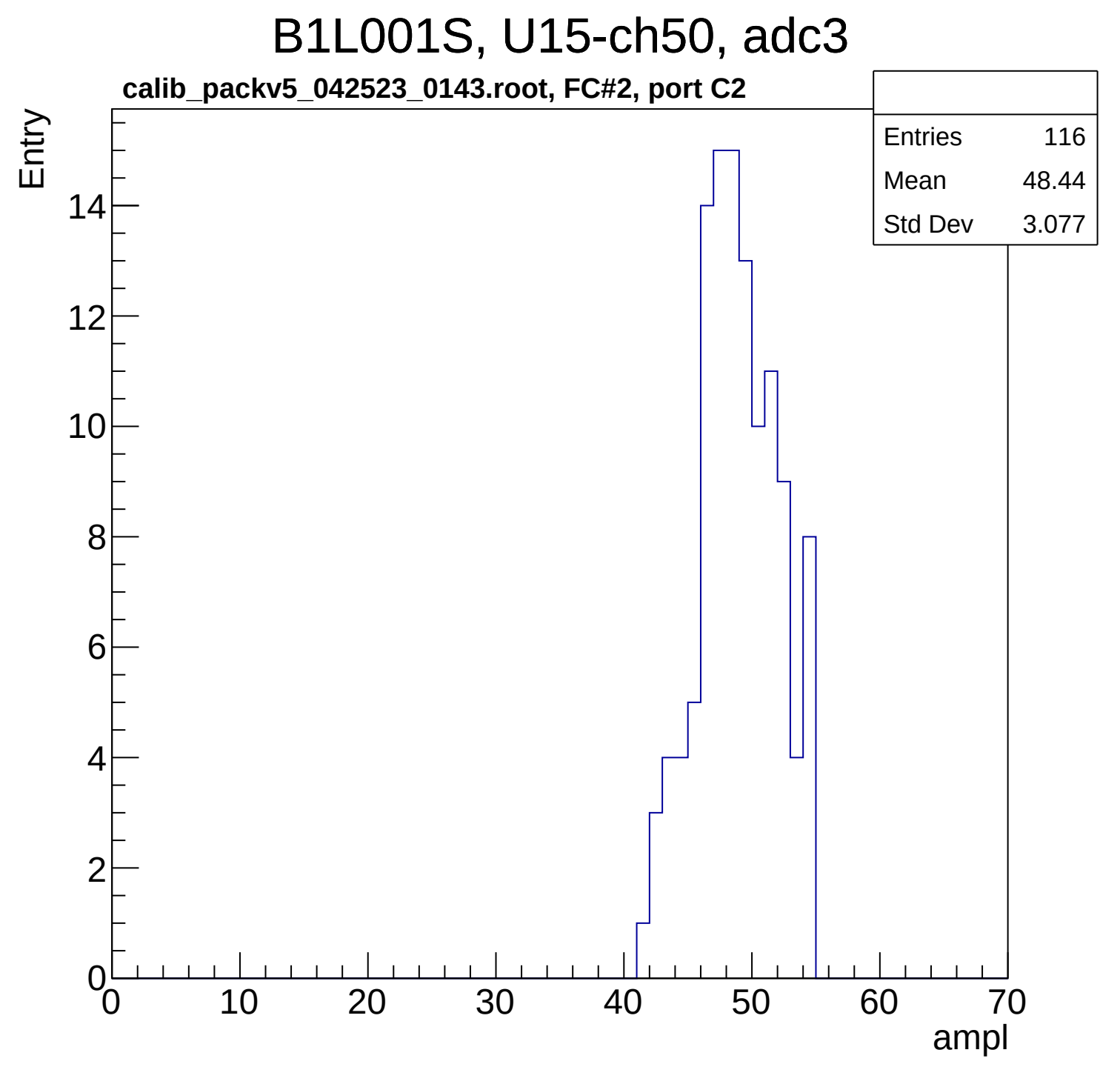
B1L001S, U15-ch50, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 48.44
Std Dev 3.077
Entry
0
2
4
6
8
10
12
14
0
10
20
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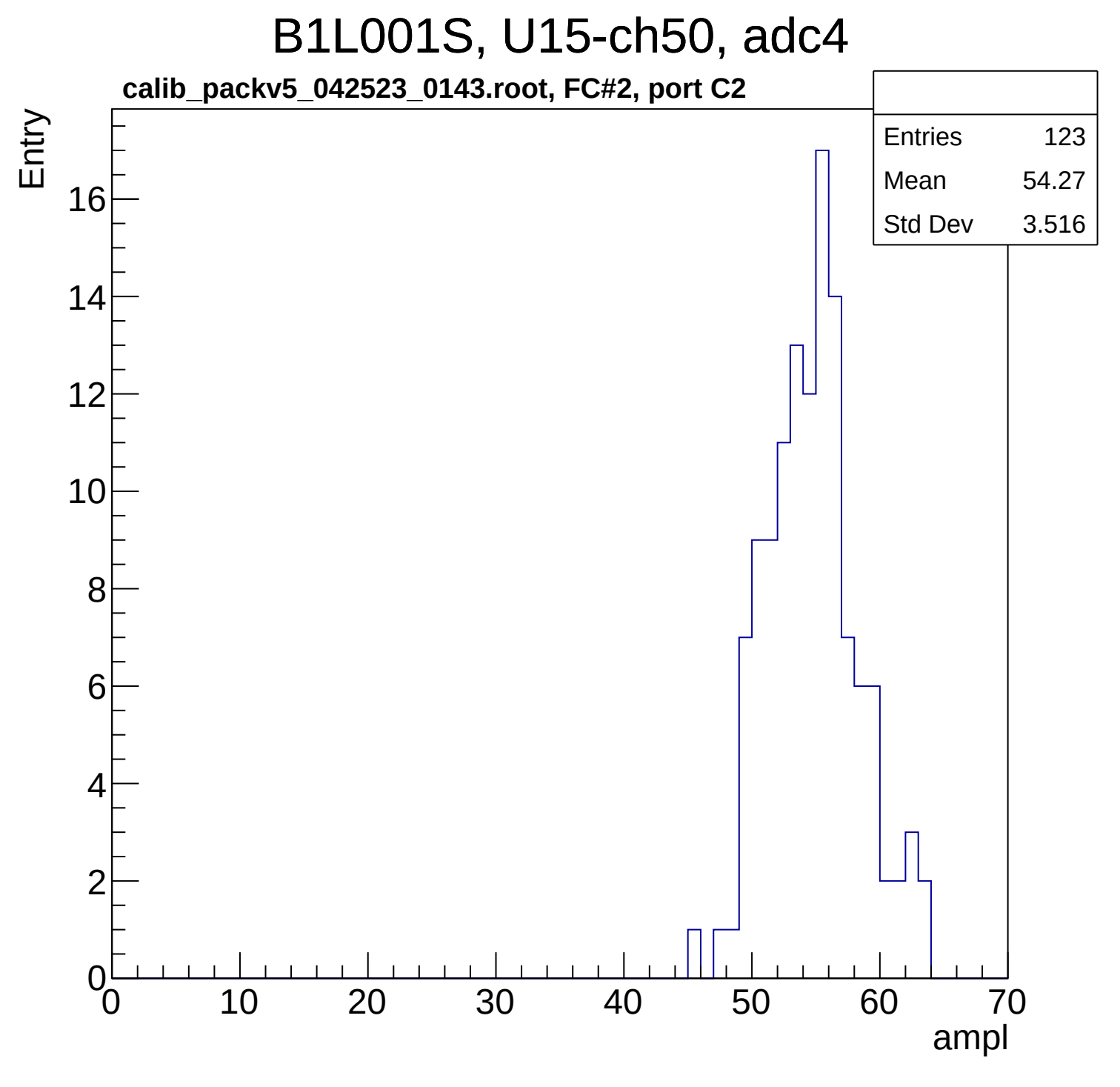
40
50
60
70
ampl


B1L001S, U15-ch50, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 54.27
Std Dev 3.516
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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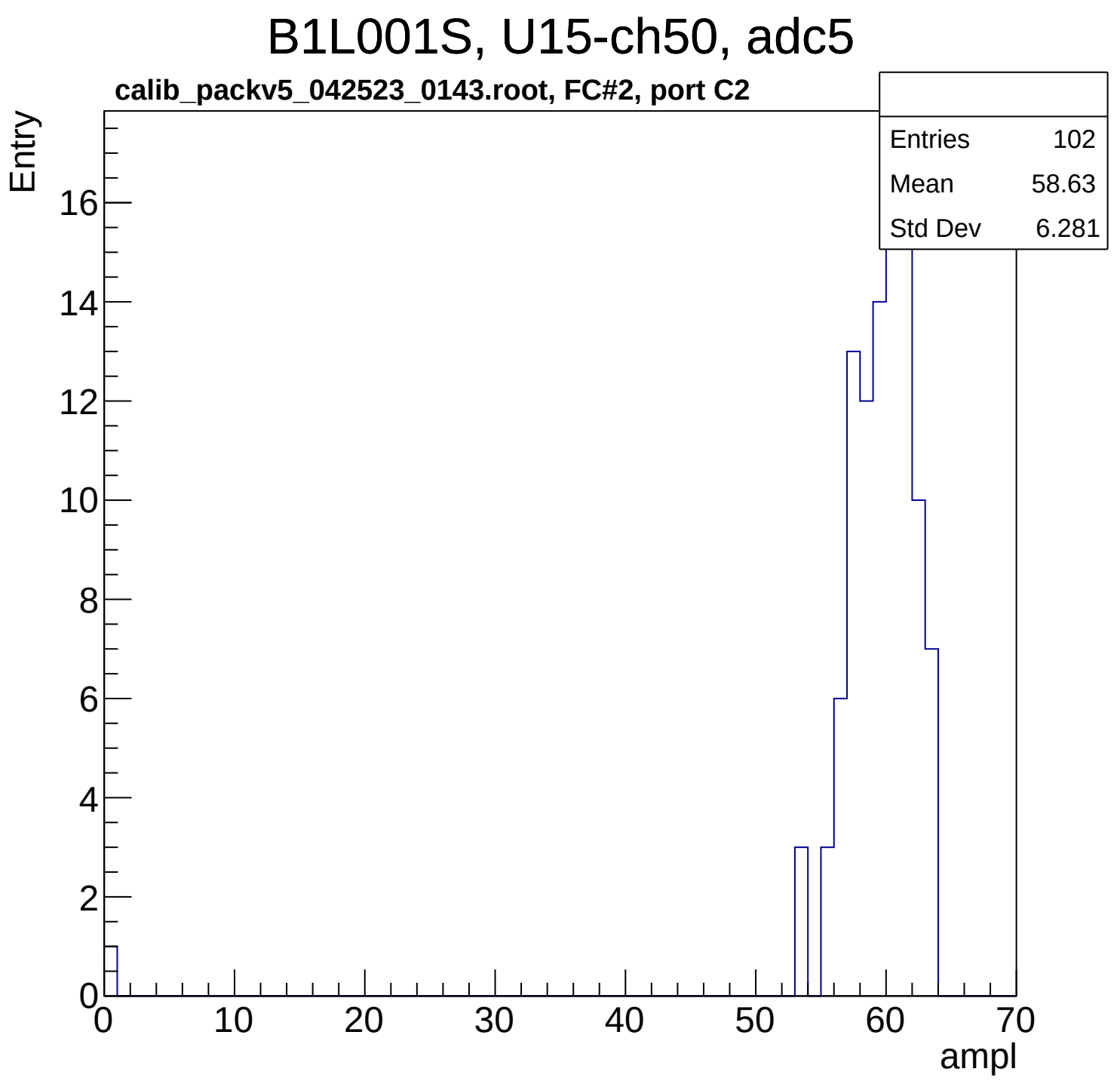
B1L001S, U15-ch50, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 58.63
Std Dev 6.281
Entry
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4
6
8
10
12
14
16
0
10
20
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50
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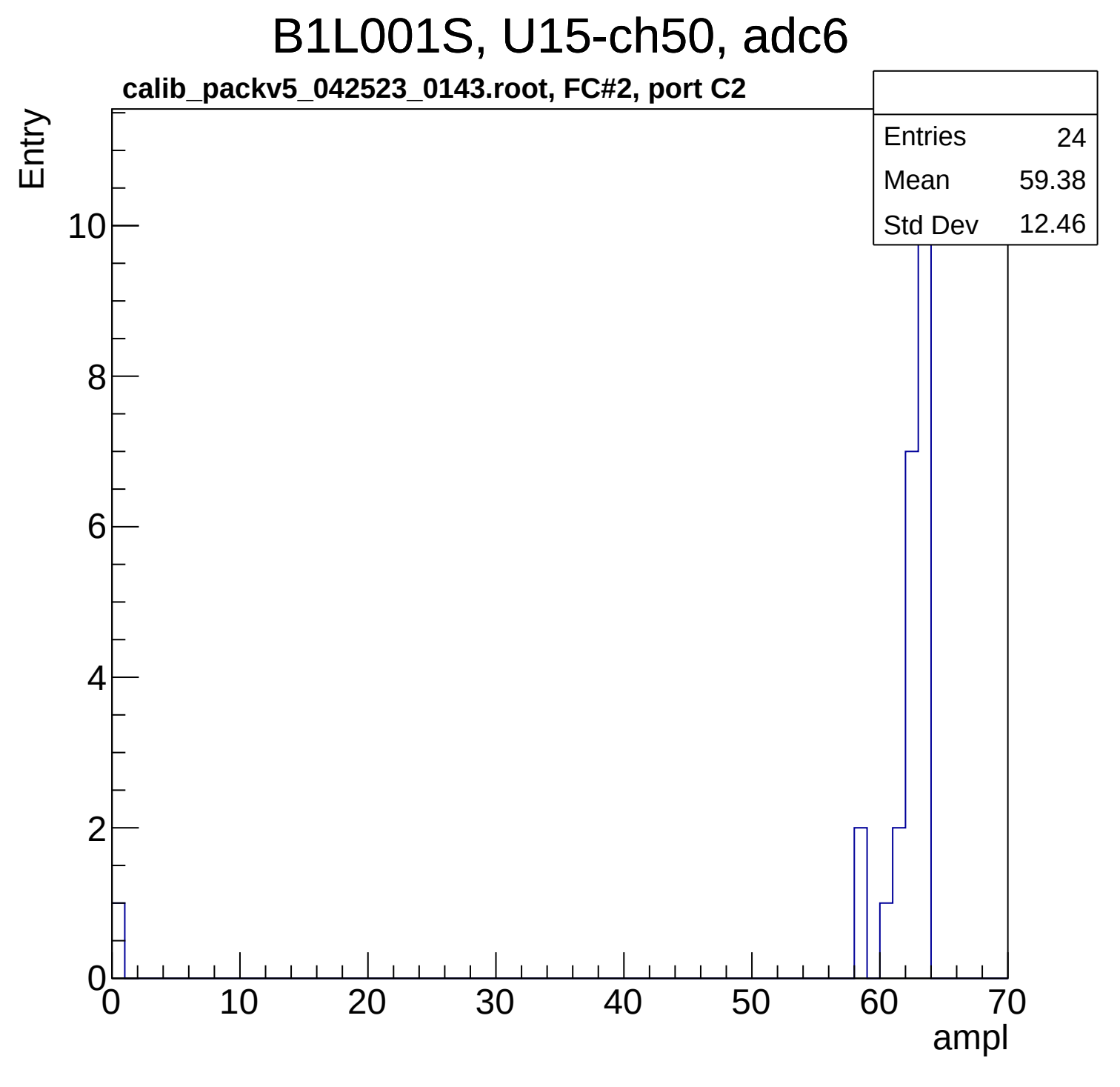
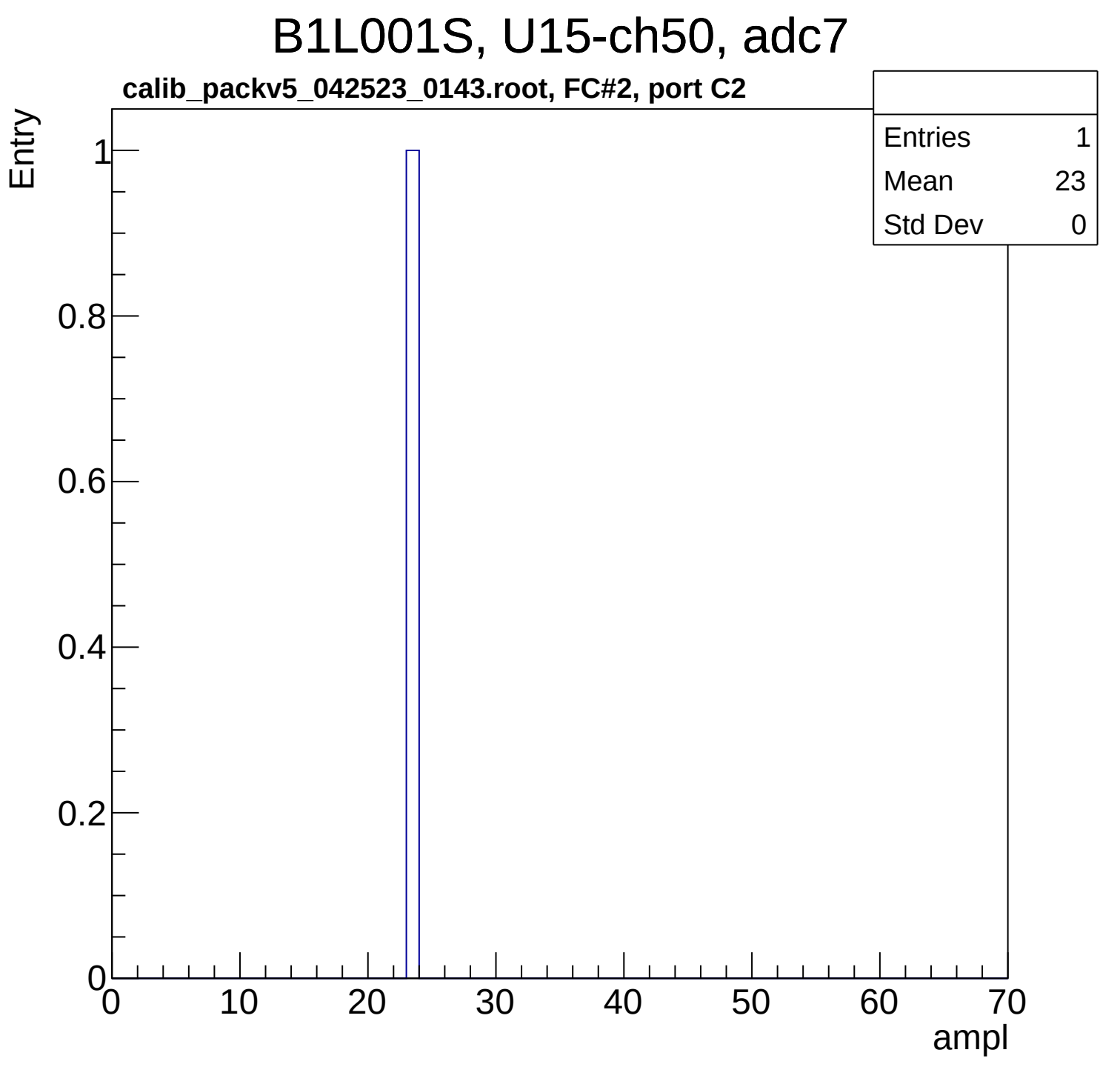
70
ampl


B1L001S, U15-ch50, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 24
Mean 59.38
Std Dev 12.46
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch50, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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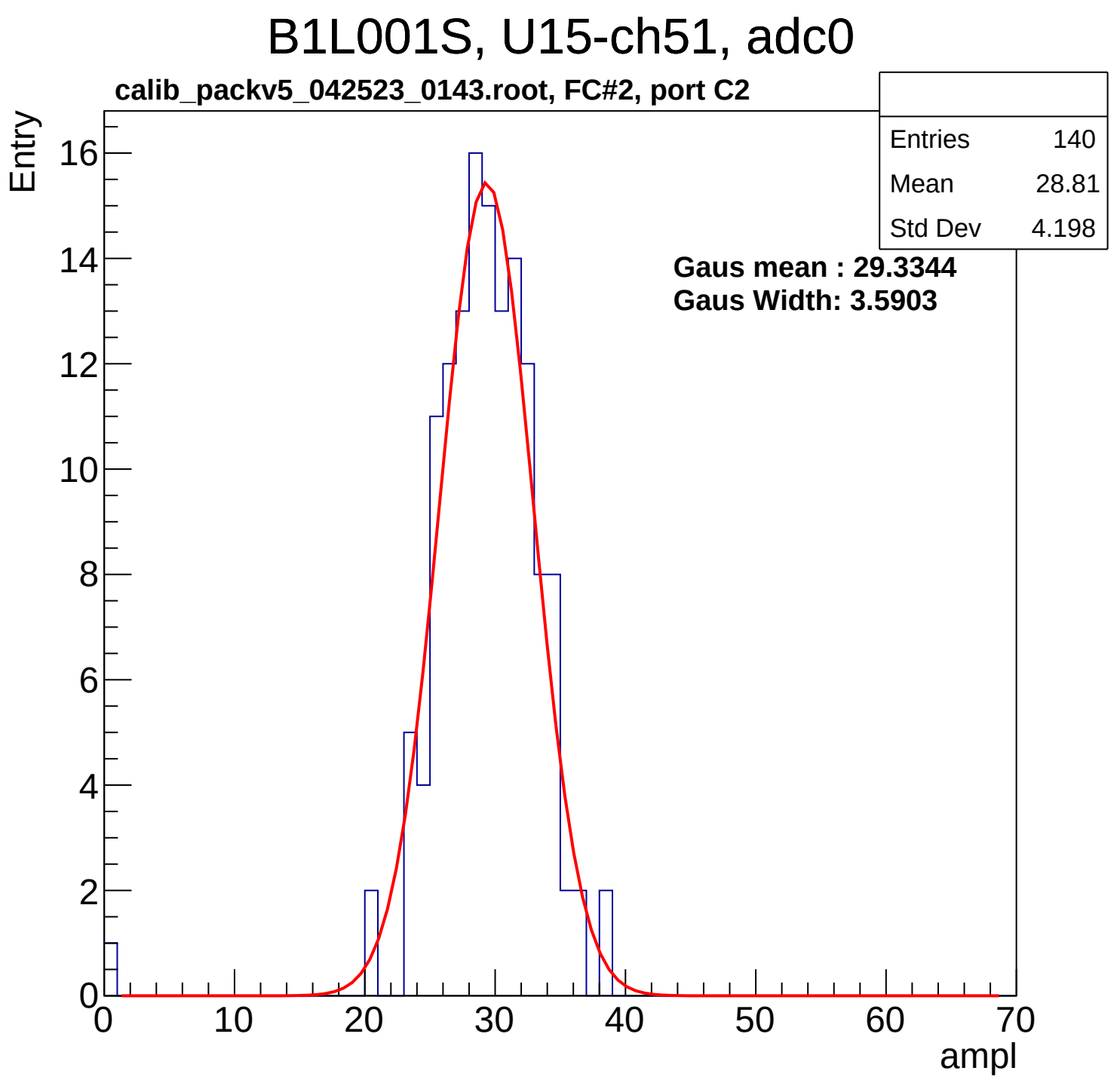
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch51, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 28.81
Std Dev 4.198
Gaus mean : 29.3344
Gaus Width: 3.5903
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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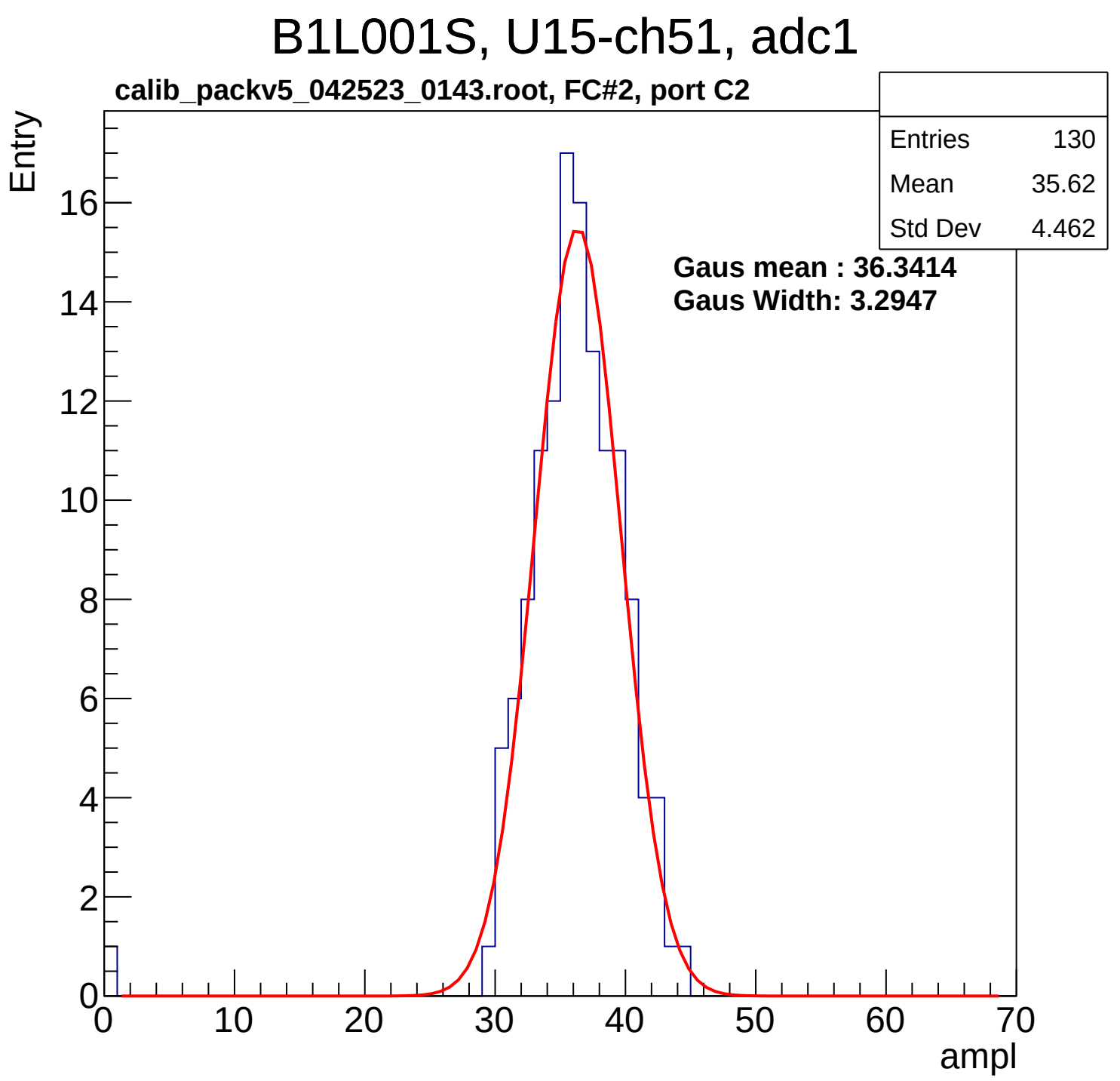
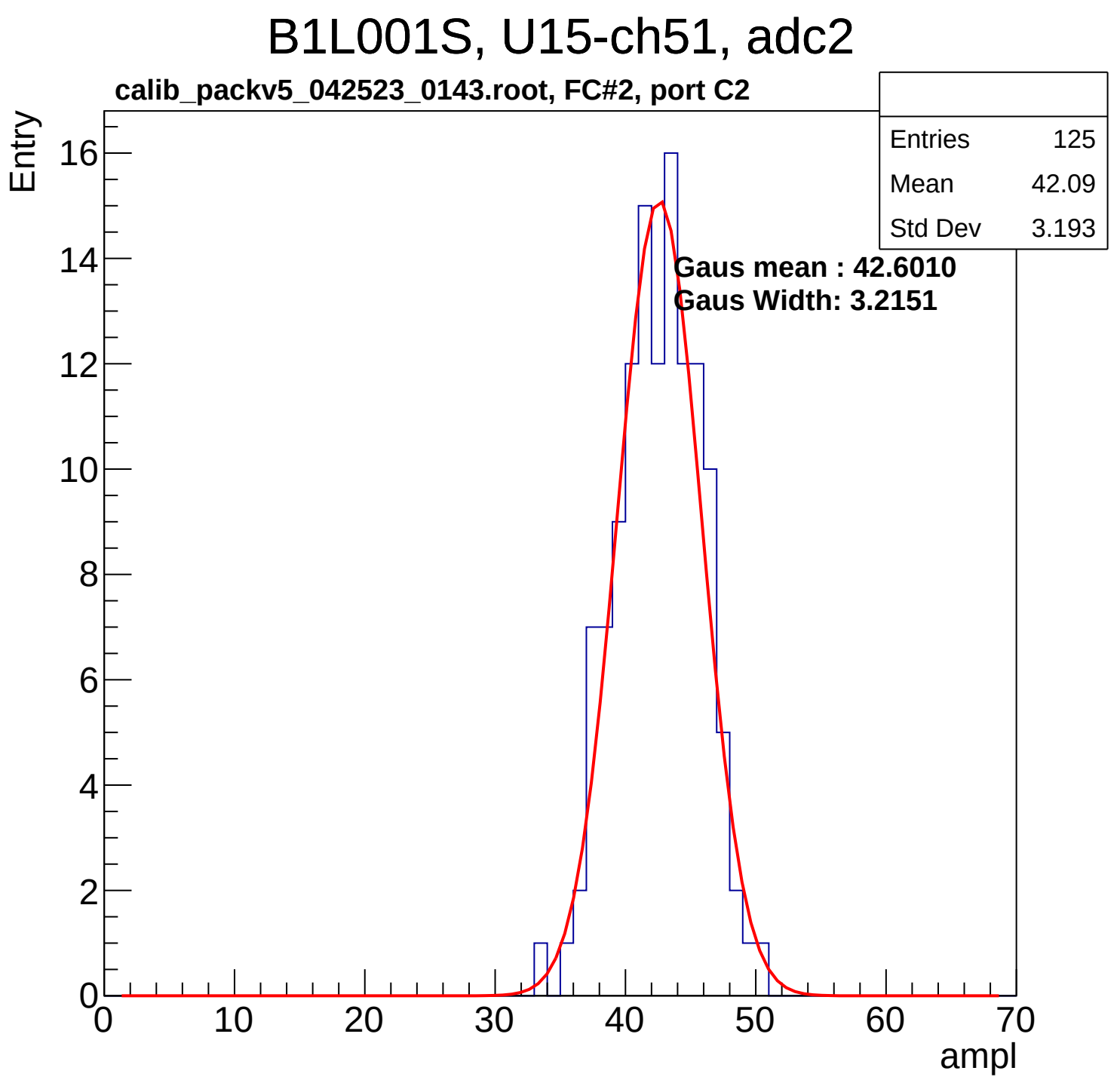

B1L001S, U15-ch51, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 35.62
Std Dev 4.462
Gaus mean : 36.3414
Gaus Width: 3.2947
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch51, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 42.09
Std Dev 3.193
Gaus mean : 42.6010
Gaus Width: 3.2151
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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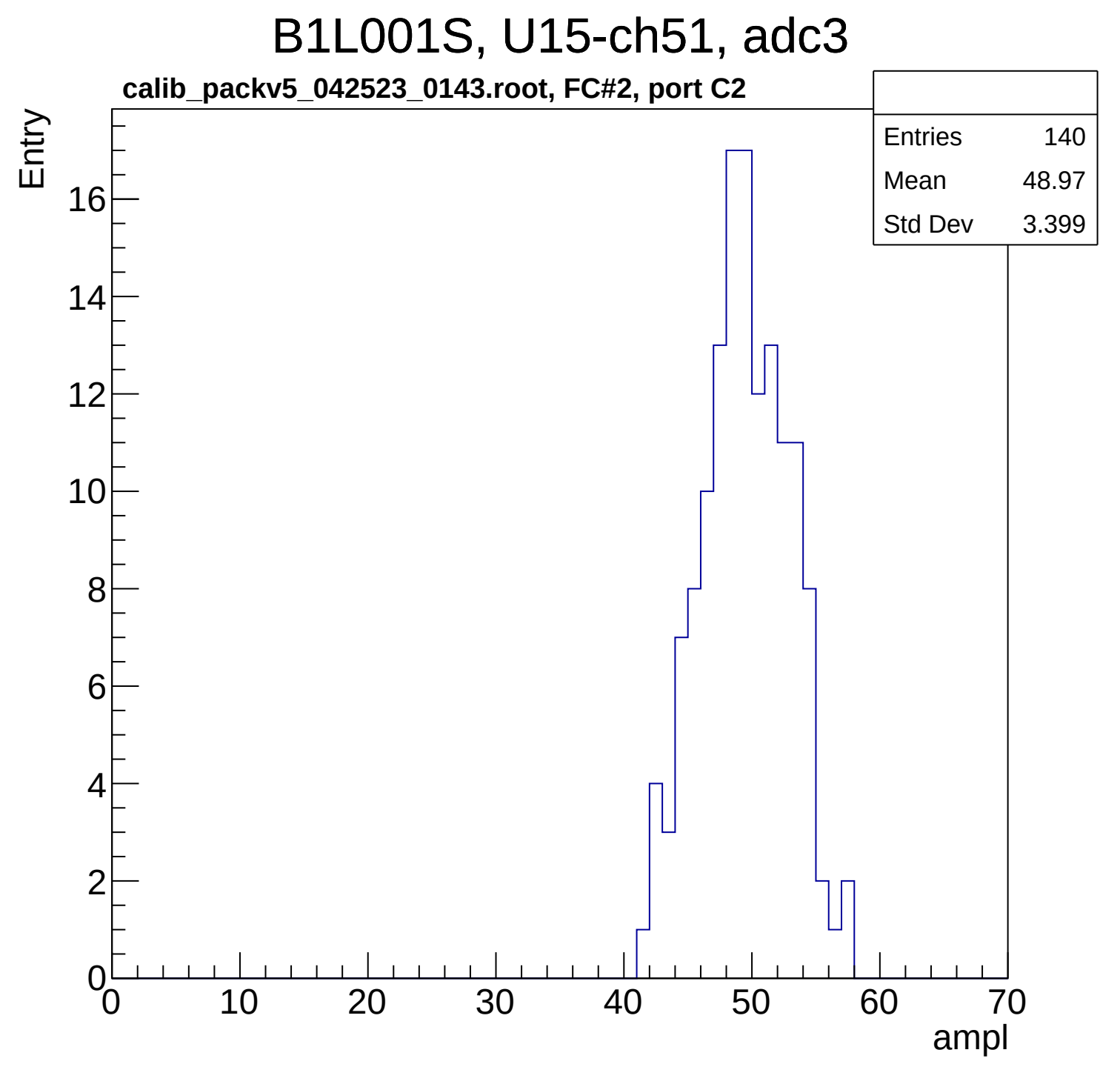
B1L001S, U15-ch51, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 48.97
Std Dev 3.399
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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60
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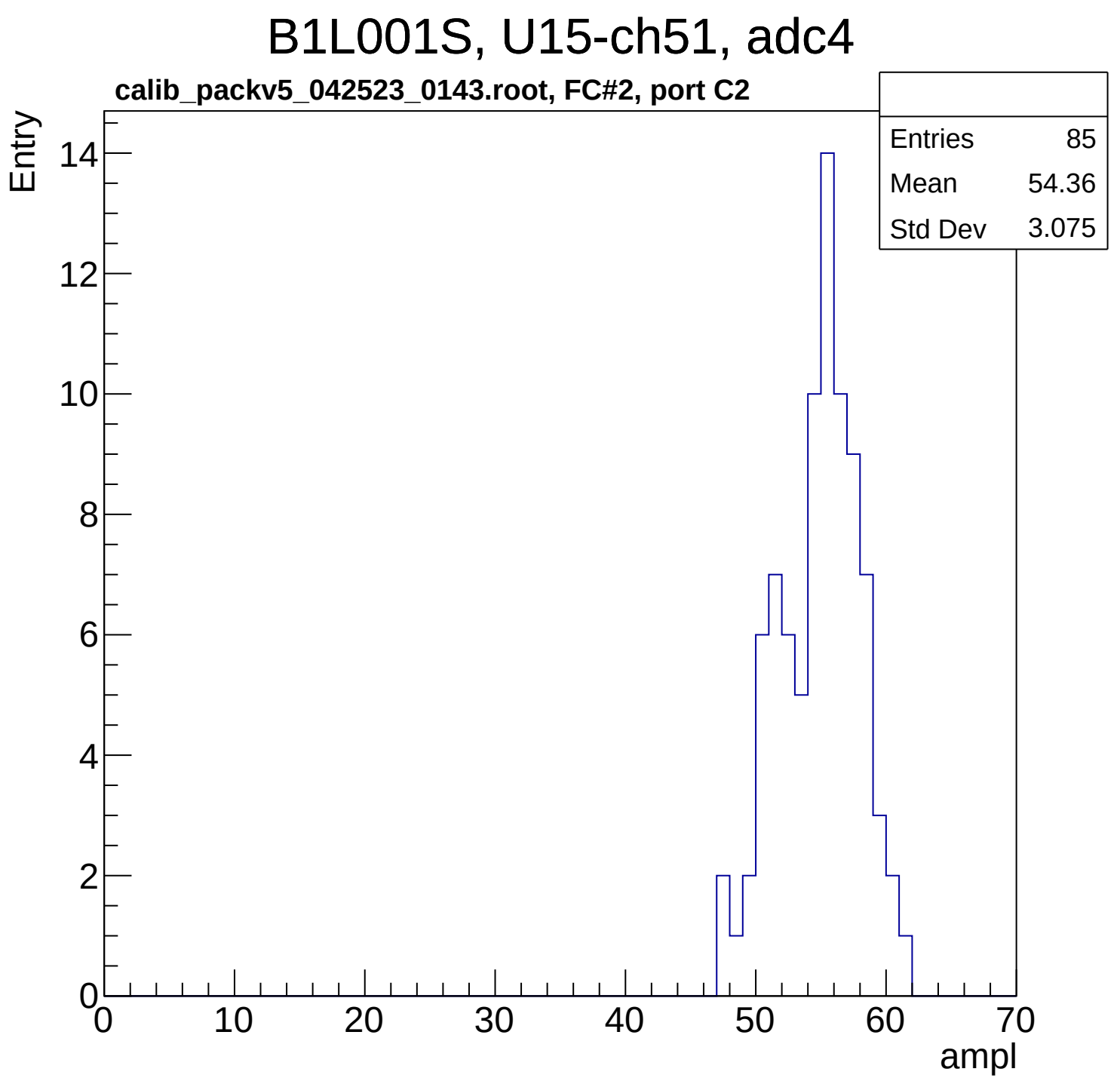
B1L001S, U15-ch51, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 54.36
Std Dev 3.075
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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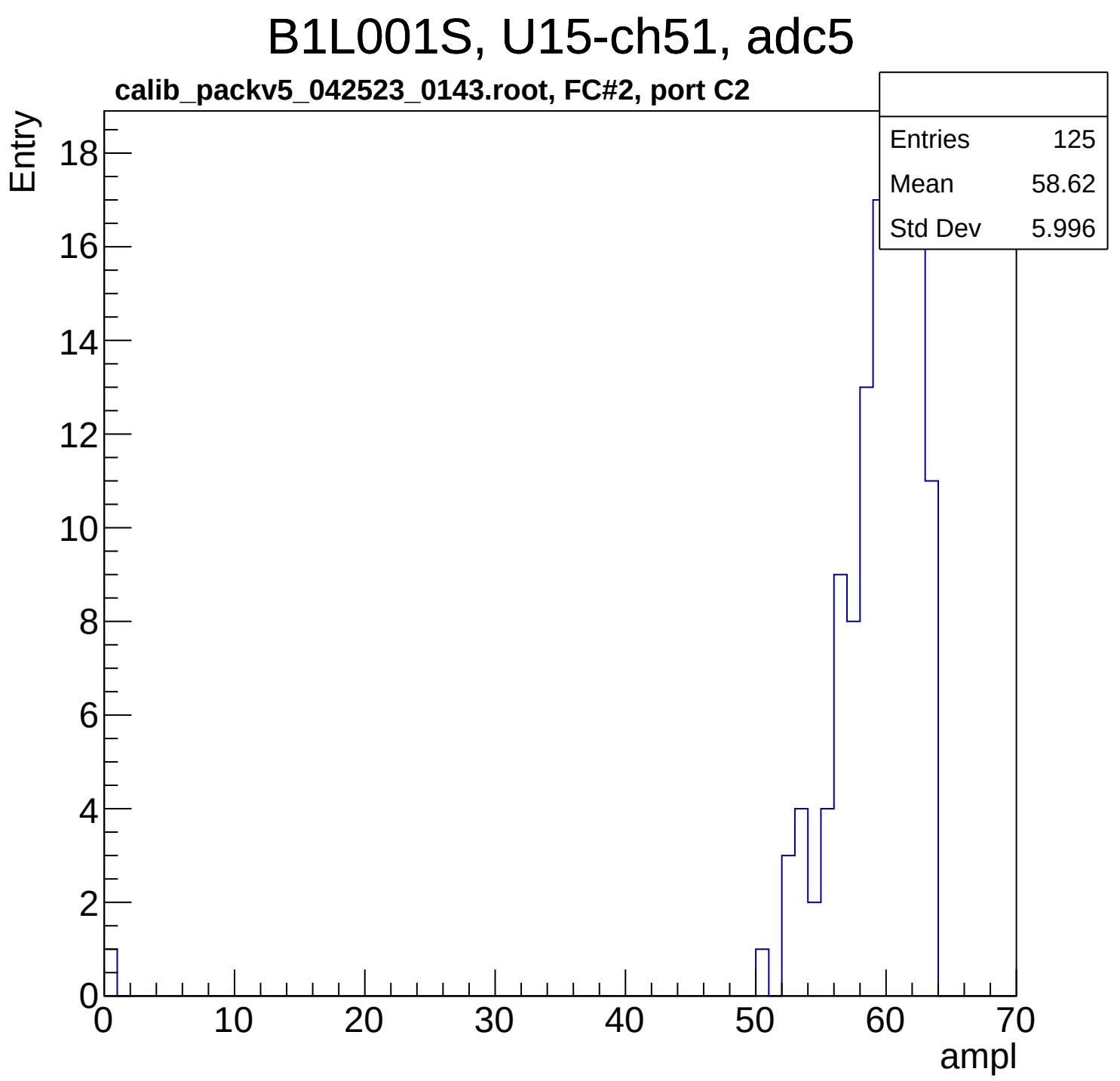
B1L001S, U15-ch51, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 58.62
Std Dev 5.996
Entry
ampl
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4
6
8
10
12
14
16
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0
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40
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60
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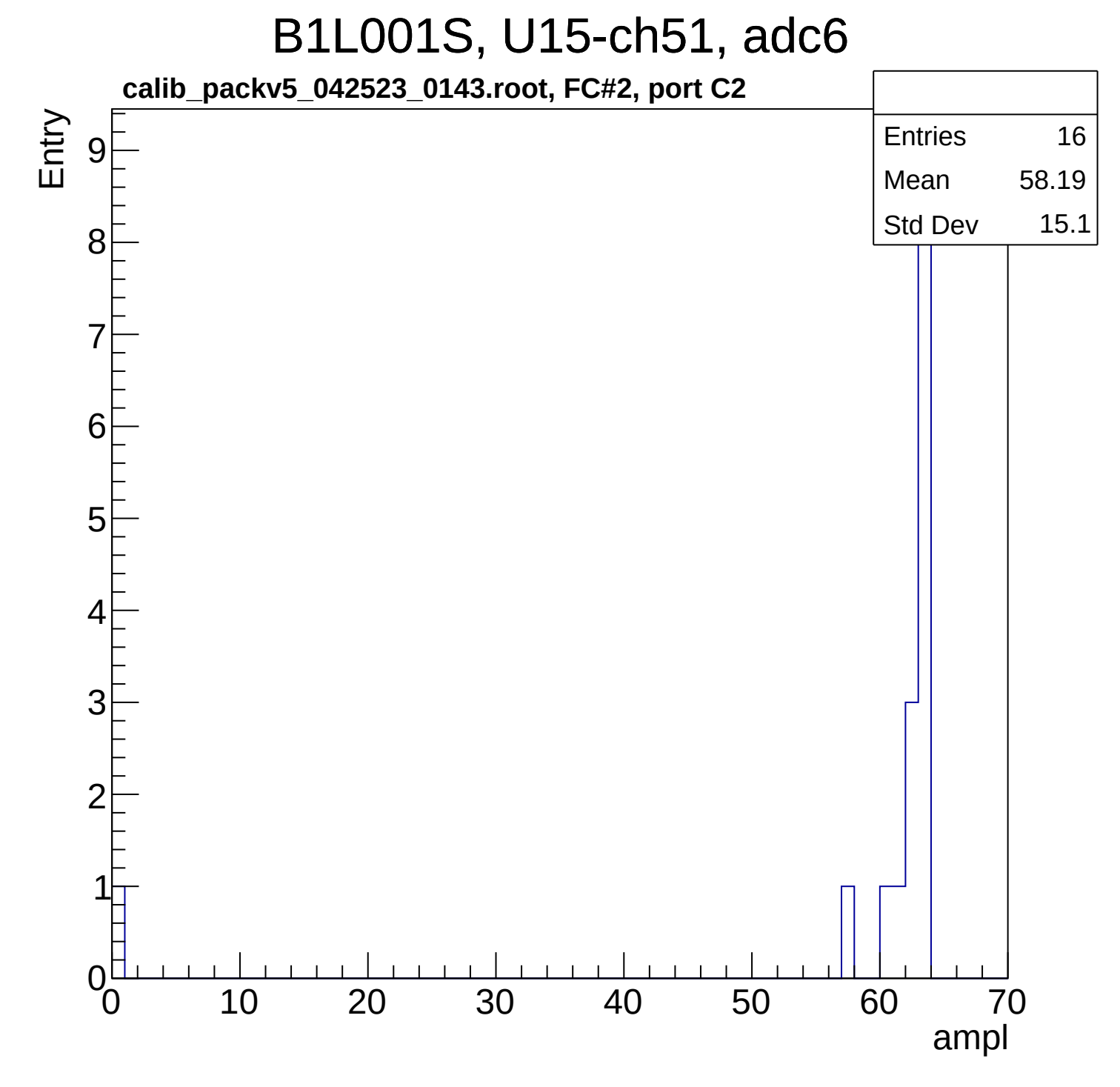
B1L001S, U15-ch51, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 58.19
Std Dev 15.1
Entry
ampl
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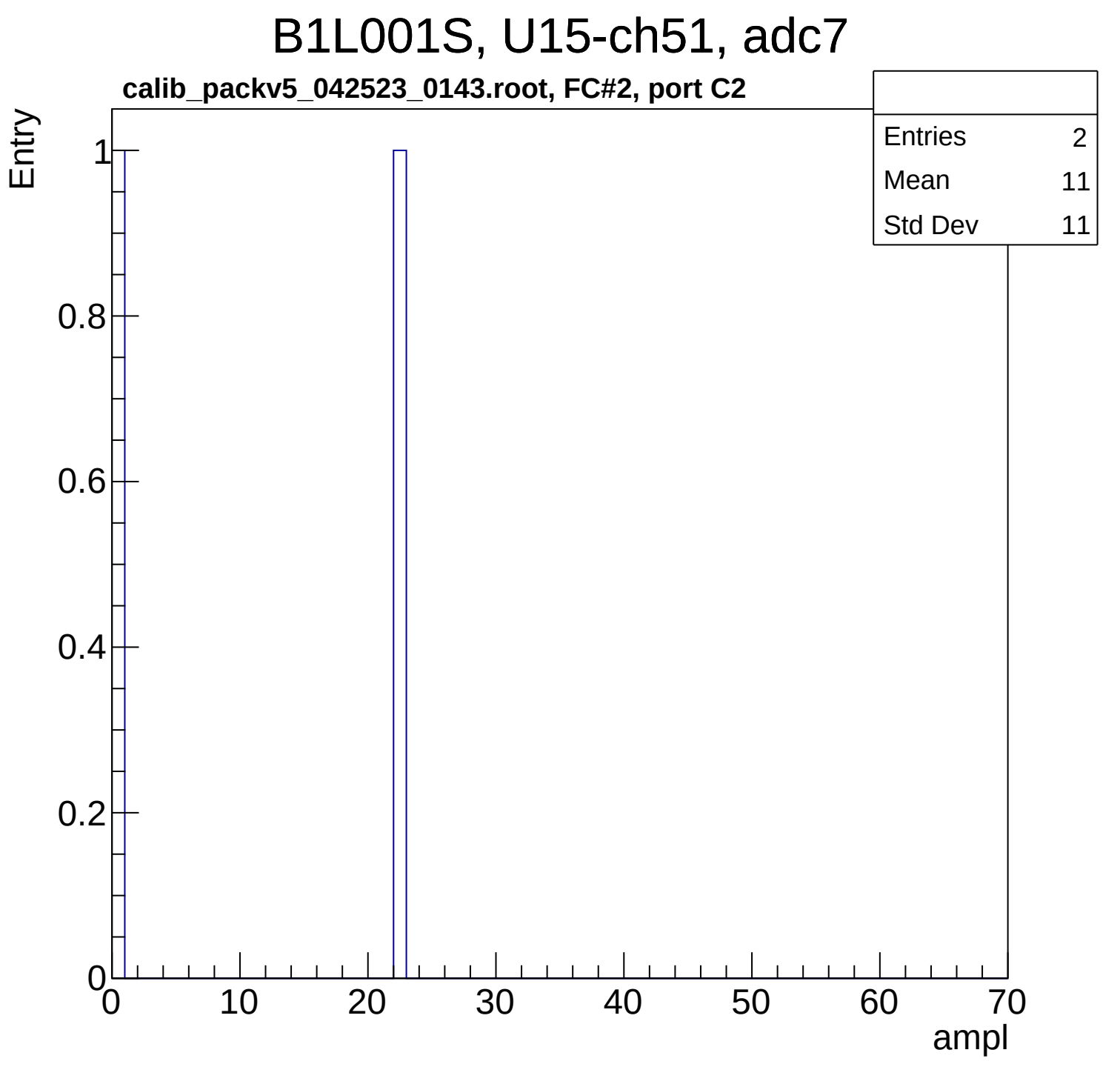
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L001S, U15-ch51, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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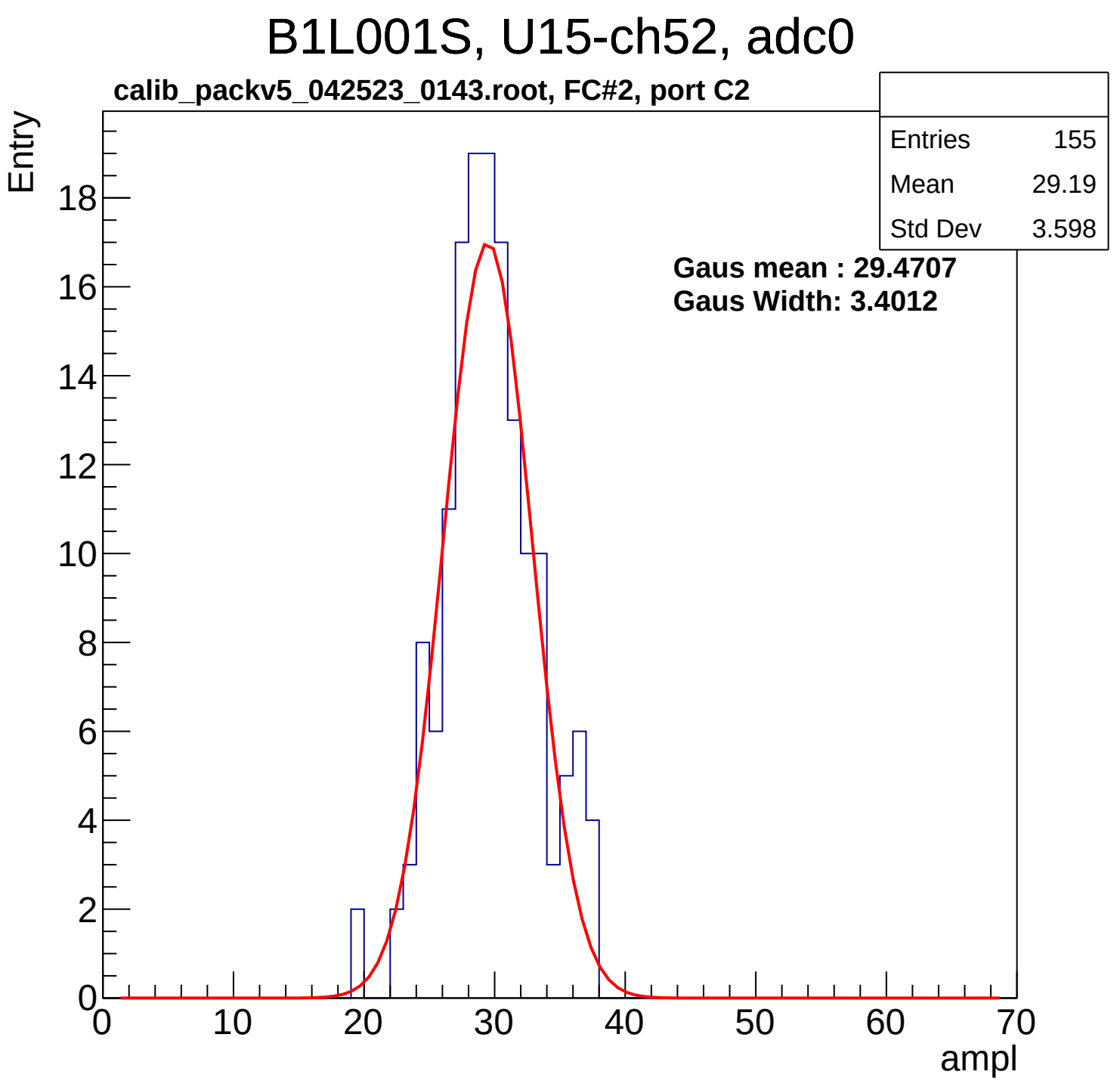
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch52, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 29.19
Std Dev 3.598
Gaus mean : 29.4707
Gaus Width: 3.4012
Entry
ampl
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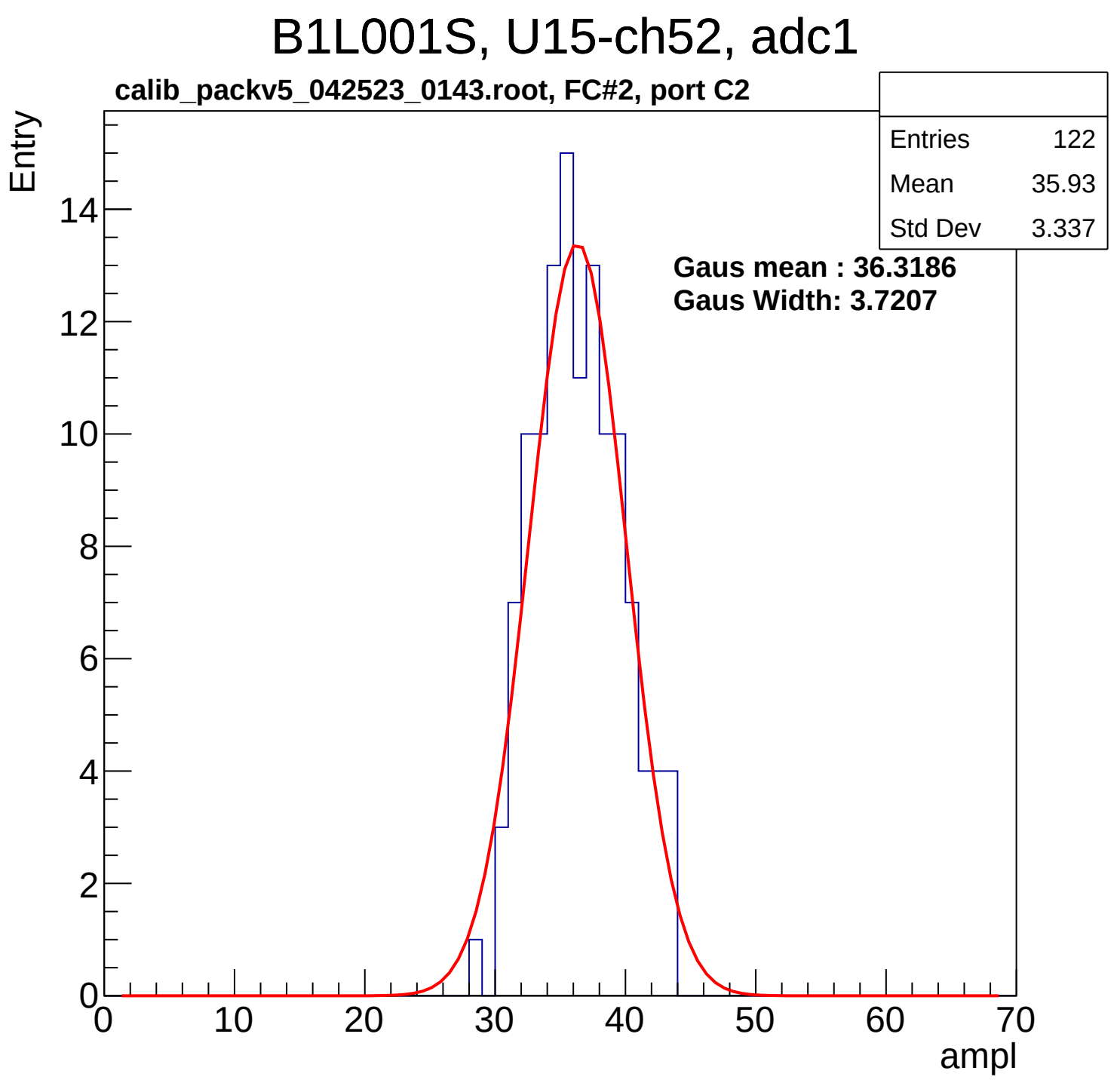
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch52, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 35.93
Std Dev 3.337
Gaus mean : 36.3186
Gaus Width: 3.7207
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
50
60
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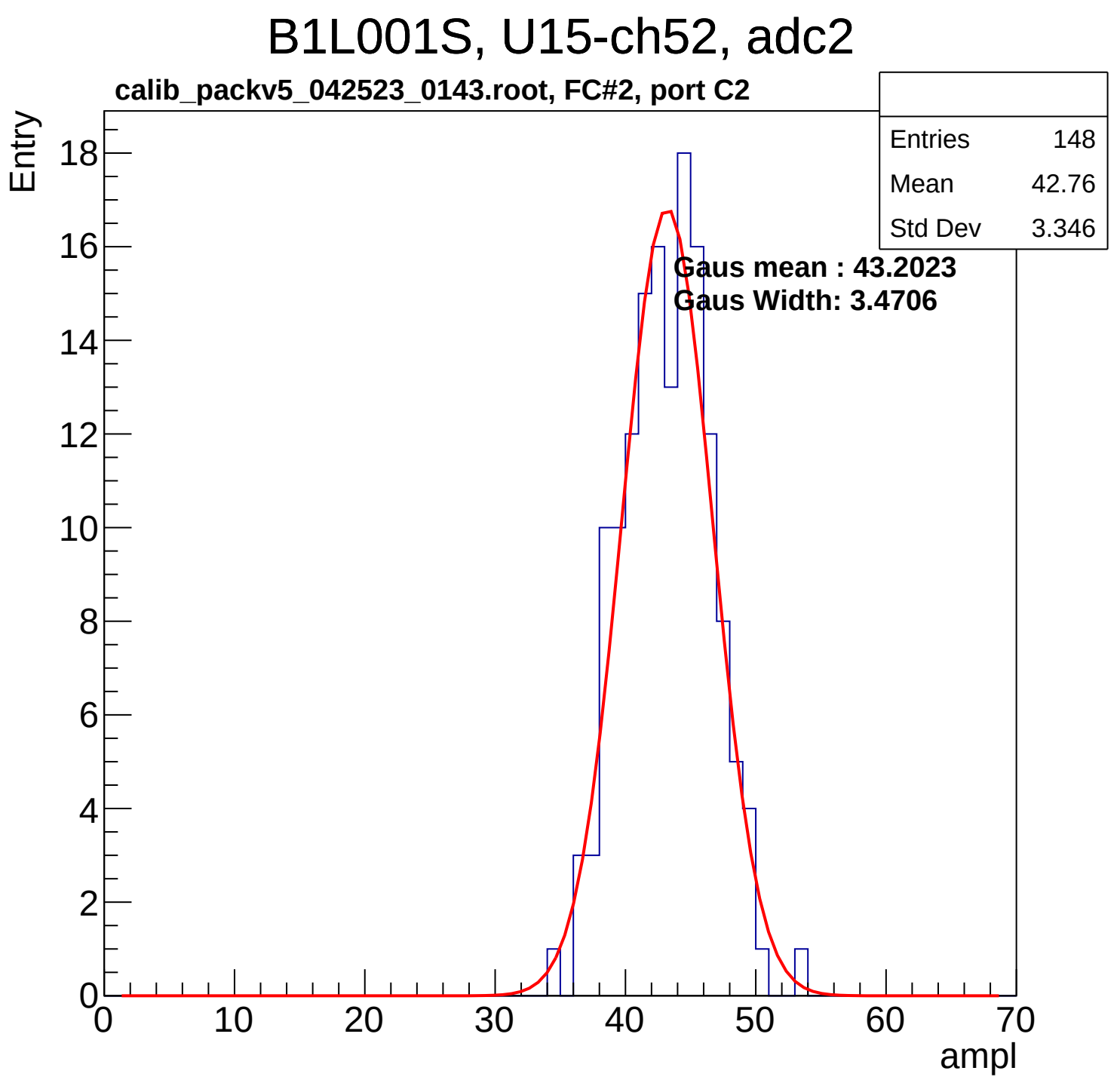
B1L001S, U15-ch52, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 42.76
Std Dev 3.346
Gaus mean : 43.2023
Gaus Width: 3.4706
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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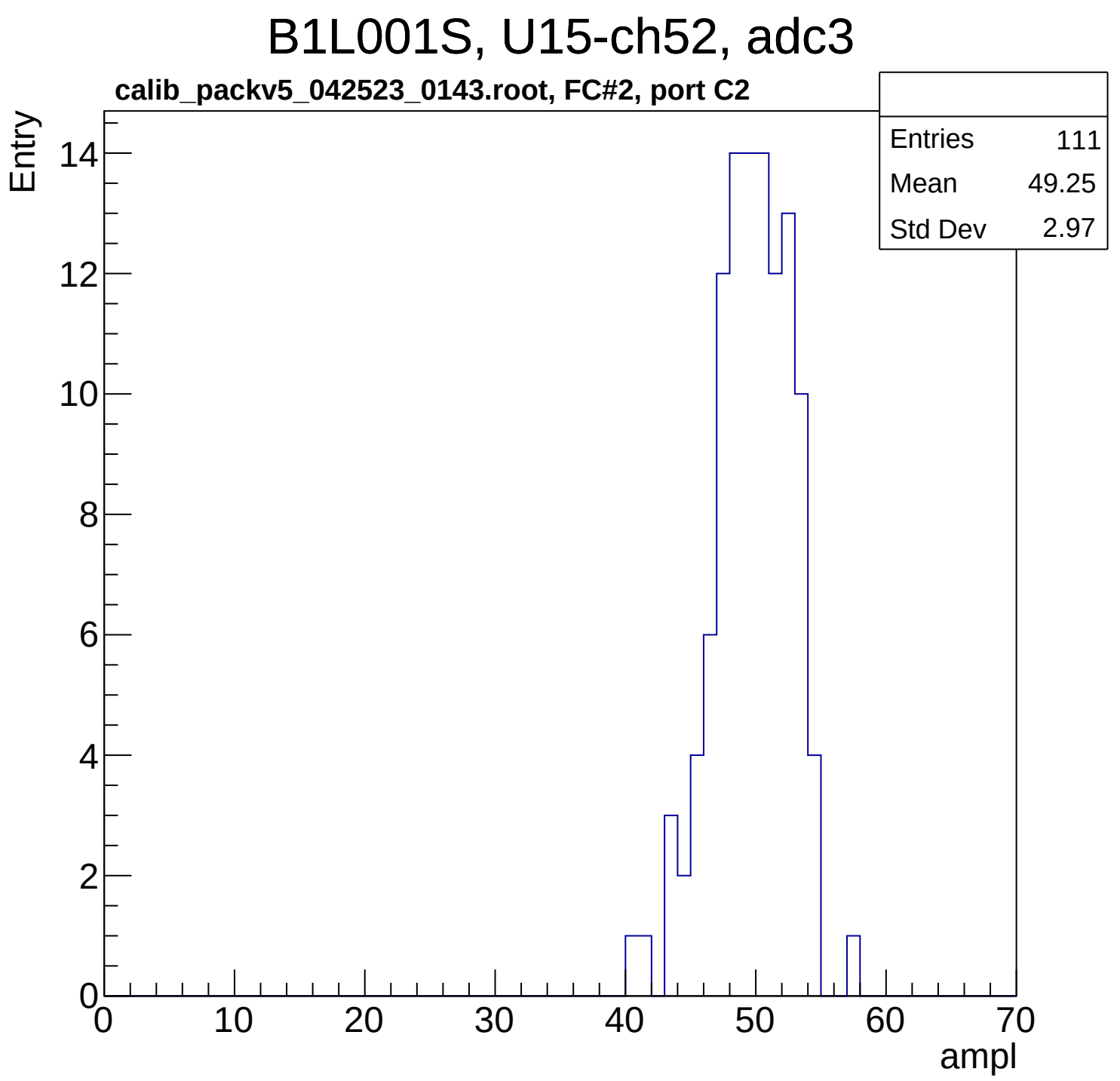
B1L001S, U15-ch52, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 49.25
Std Dev 2.97
Entry
ampl
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2
4
6
8
10
12
14
0
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20
30
40
50
60
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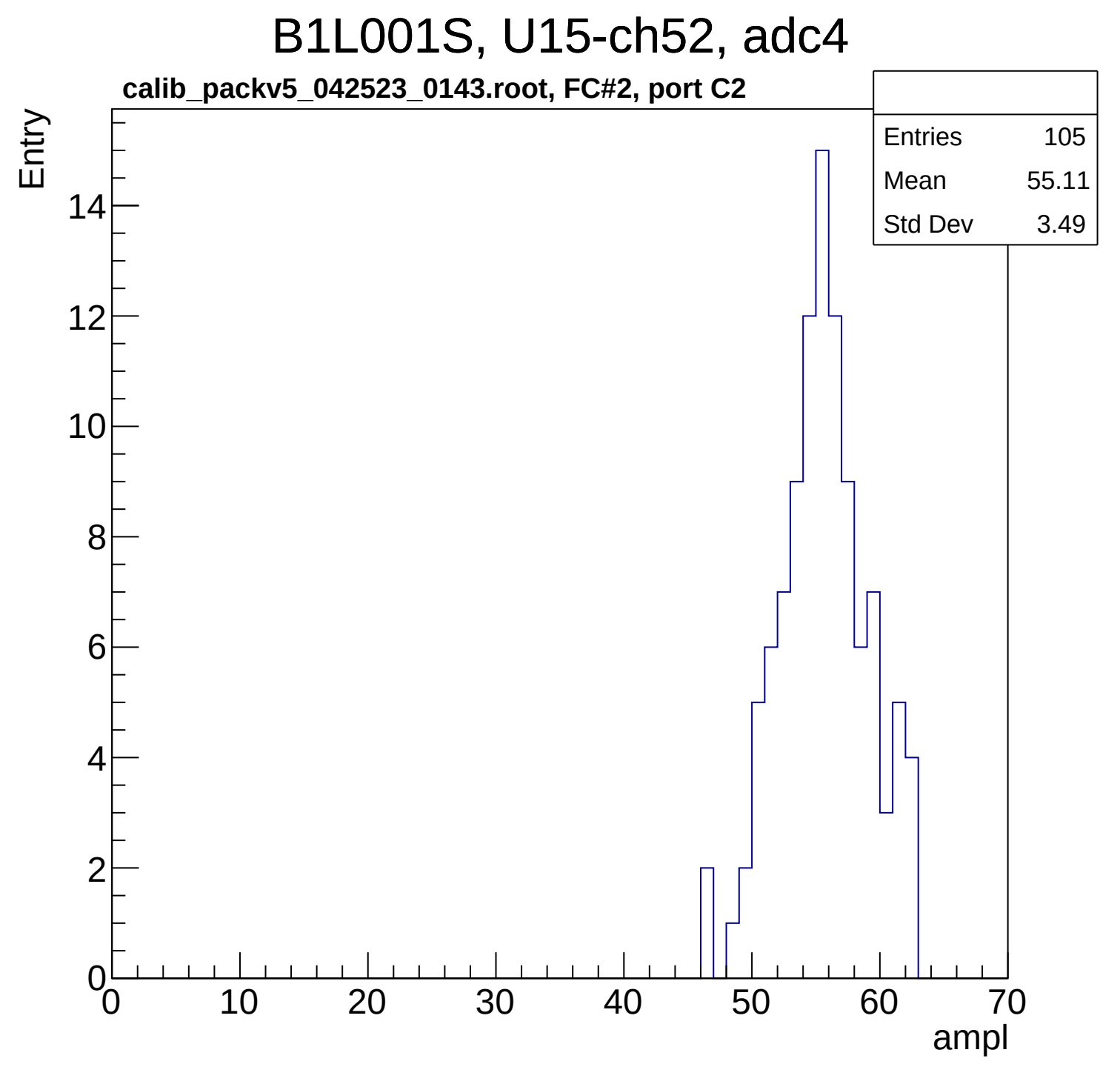
B1L001S, U15-ch52, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 55.11
Std Dev 3.49
Entry
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8
10
12
14
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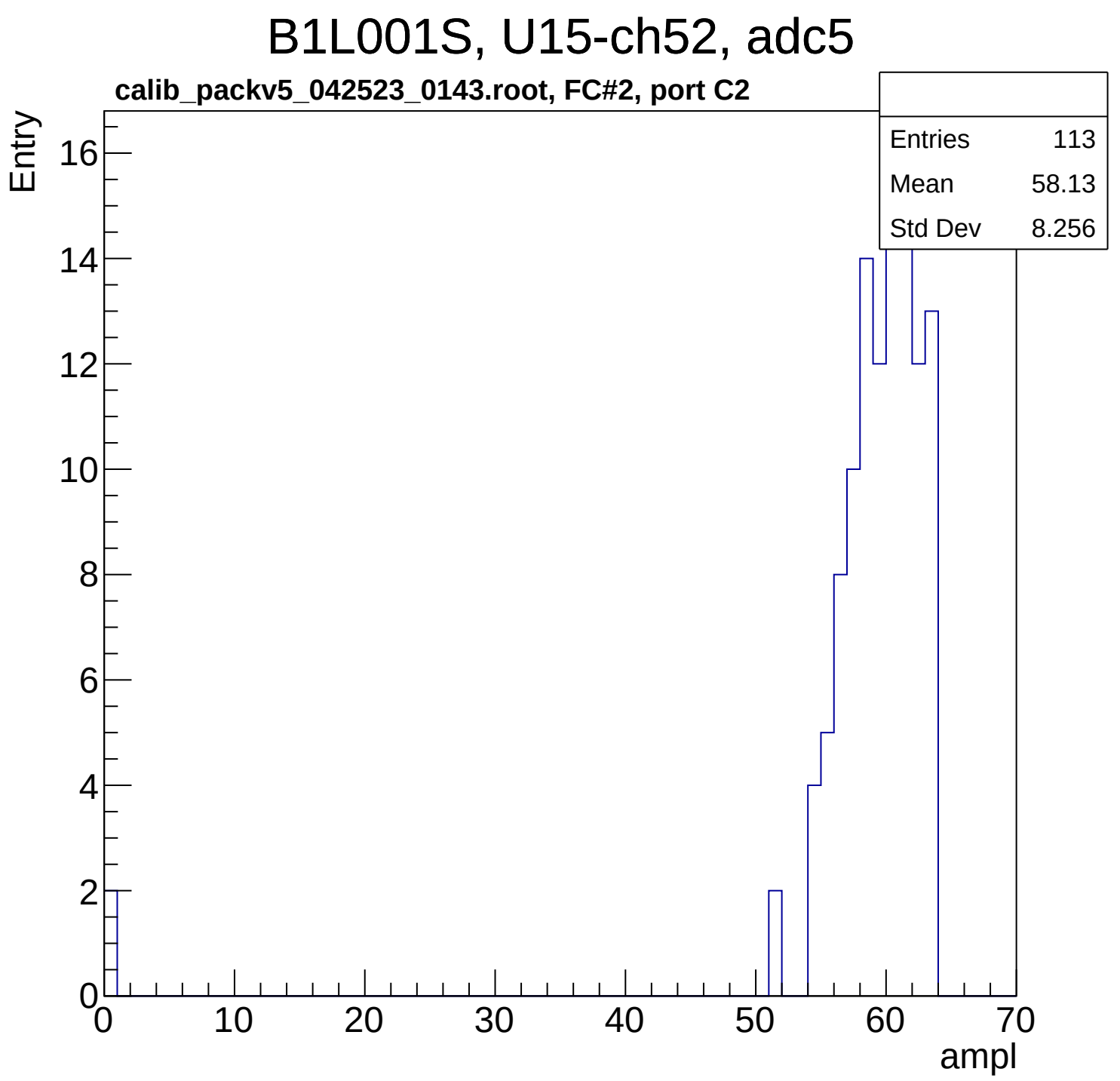
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch52, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 58.13
Std Dev 8.256
Entry
ampl
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2
4
6
8
10
12
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0
10
20
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40
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60
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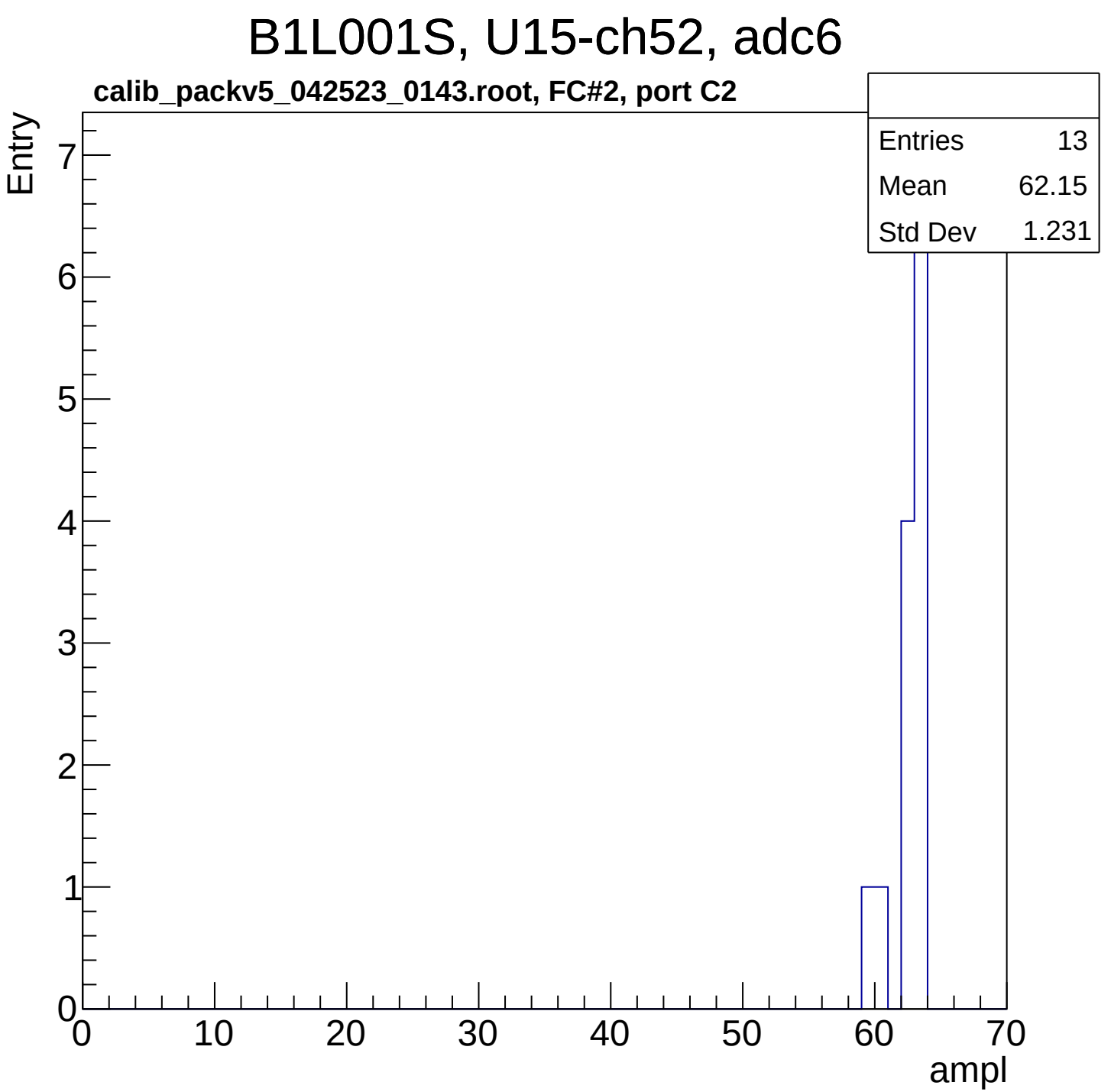
B1L001S, U15-ch52, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62.15
Std Dev 1.231
Entry
ampl
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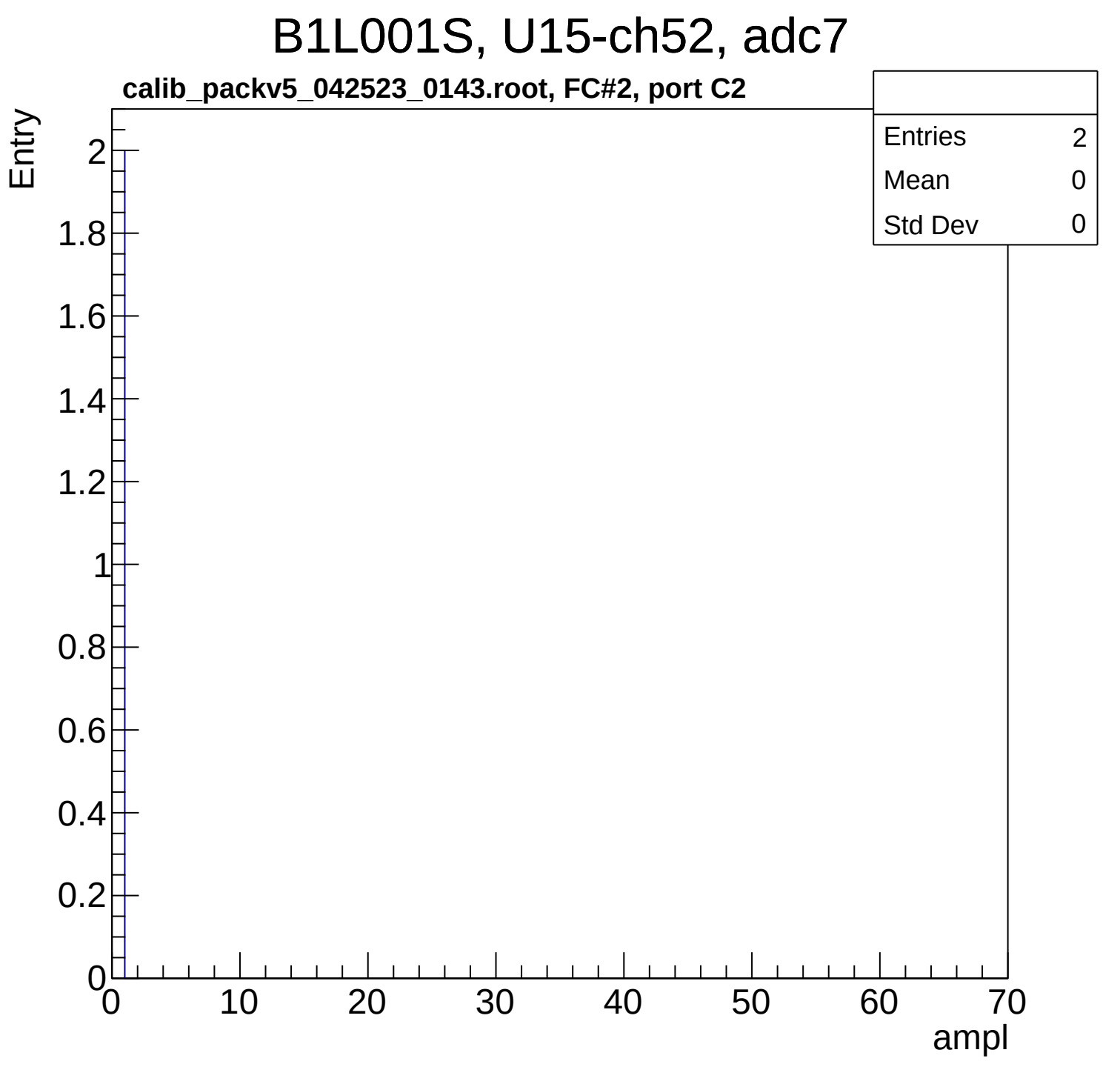
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch52, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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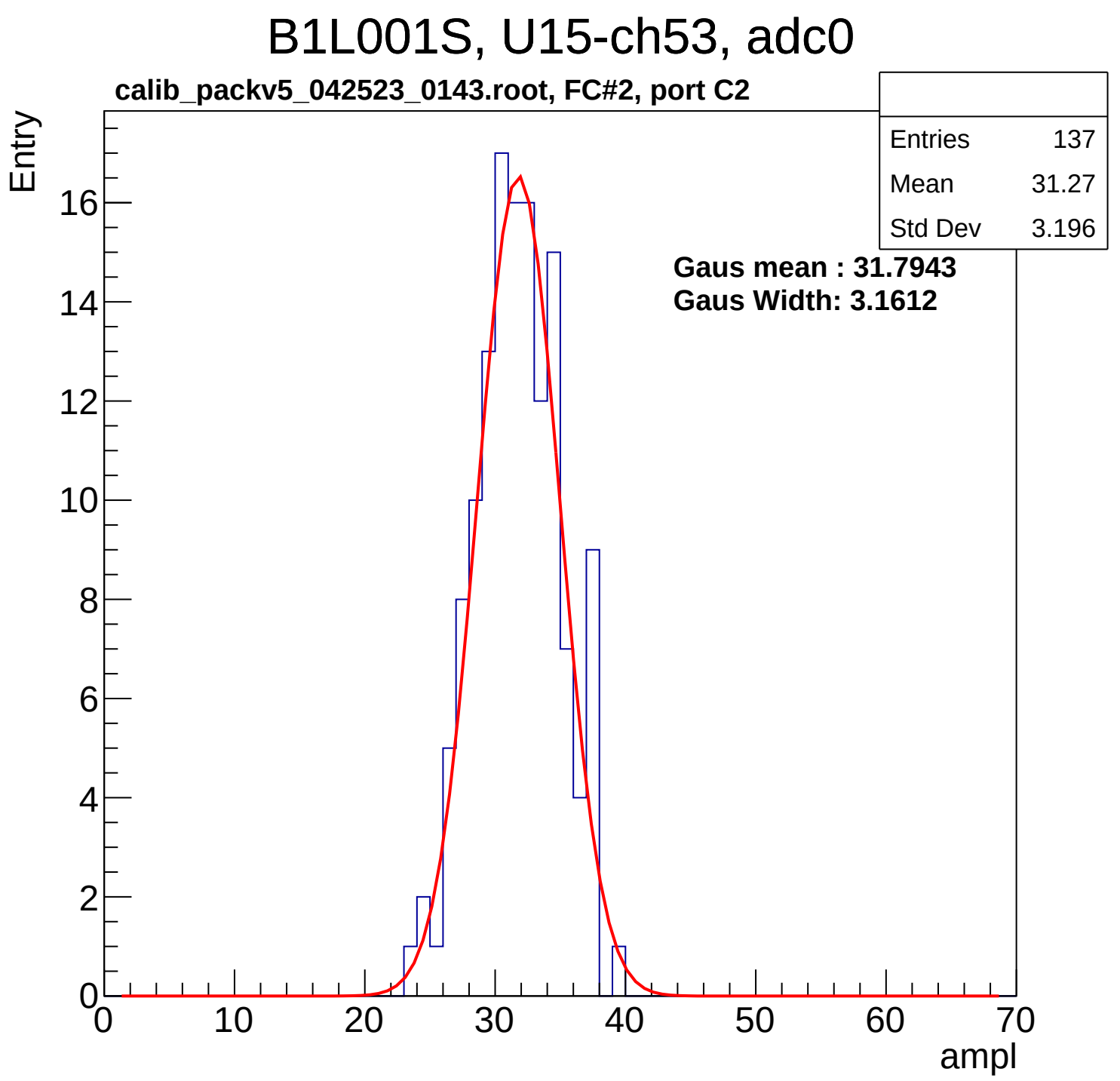
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch53, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 31.27
Std Dev 3.196
Gaus mean : 31.7943
Gaus Width: 3.1612
Entry
ampl
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2
4
6
8
10
12
14
16
0
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30
40
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60
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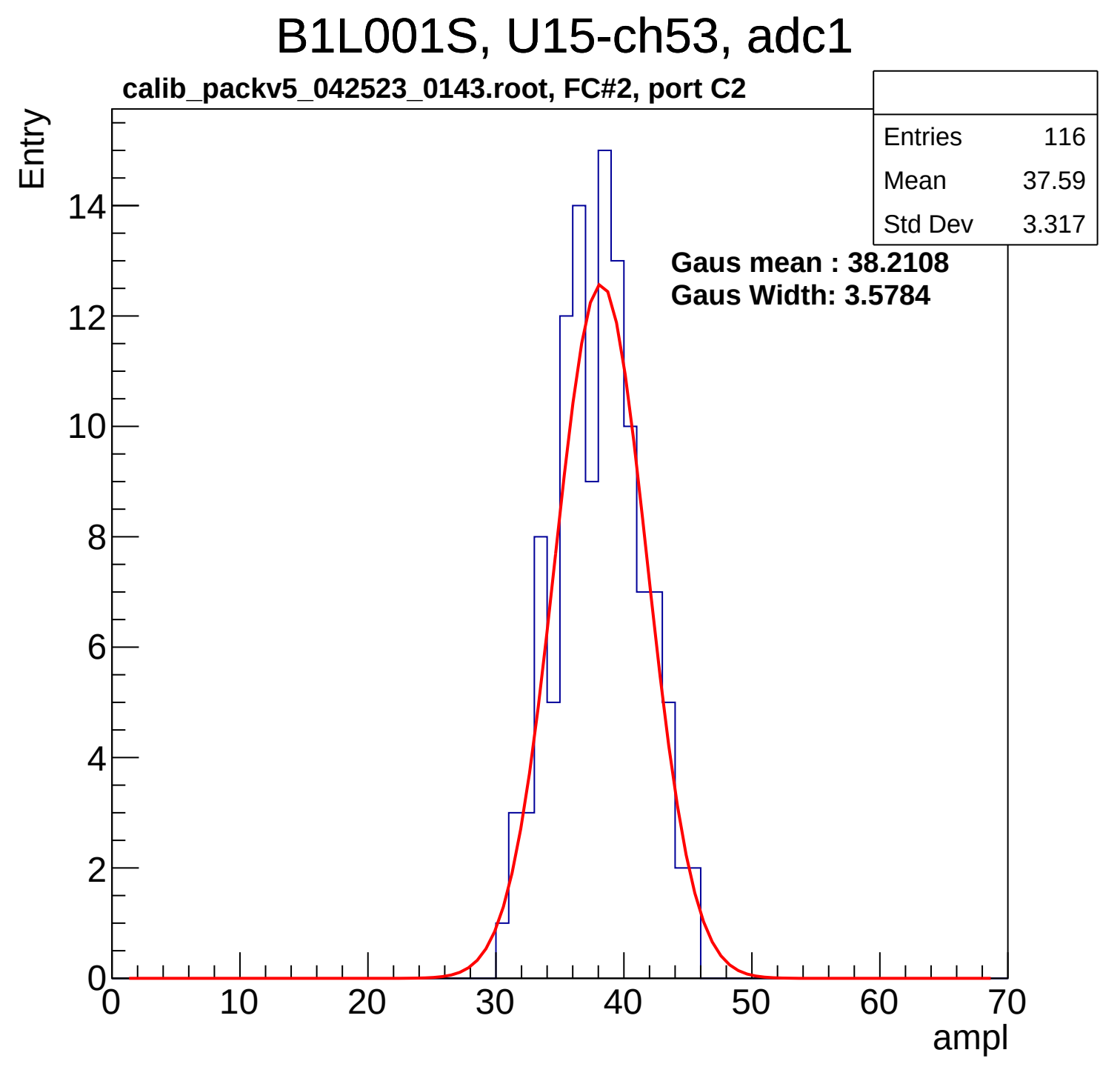
B1L001S, U15-ch53, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 37.59
Std Dev 3.317
Gaus mean : 38.2108
Gaus Width: 3.5784
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
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50
60
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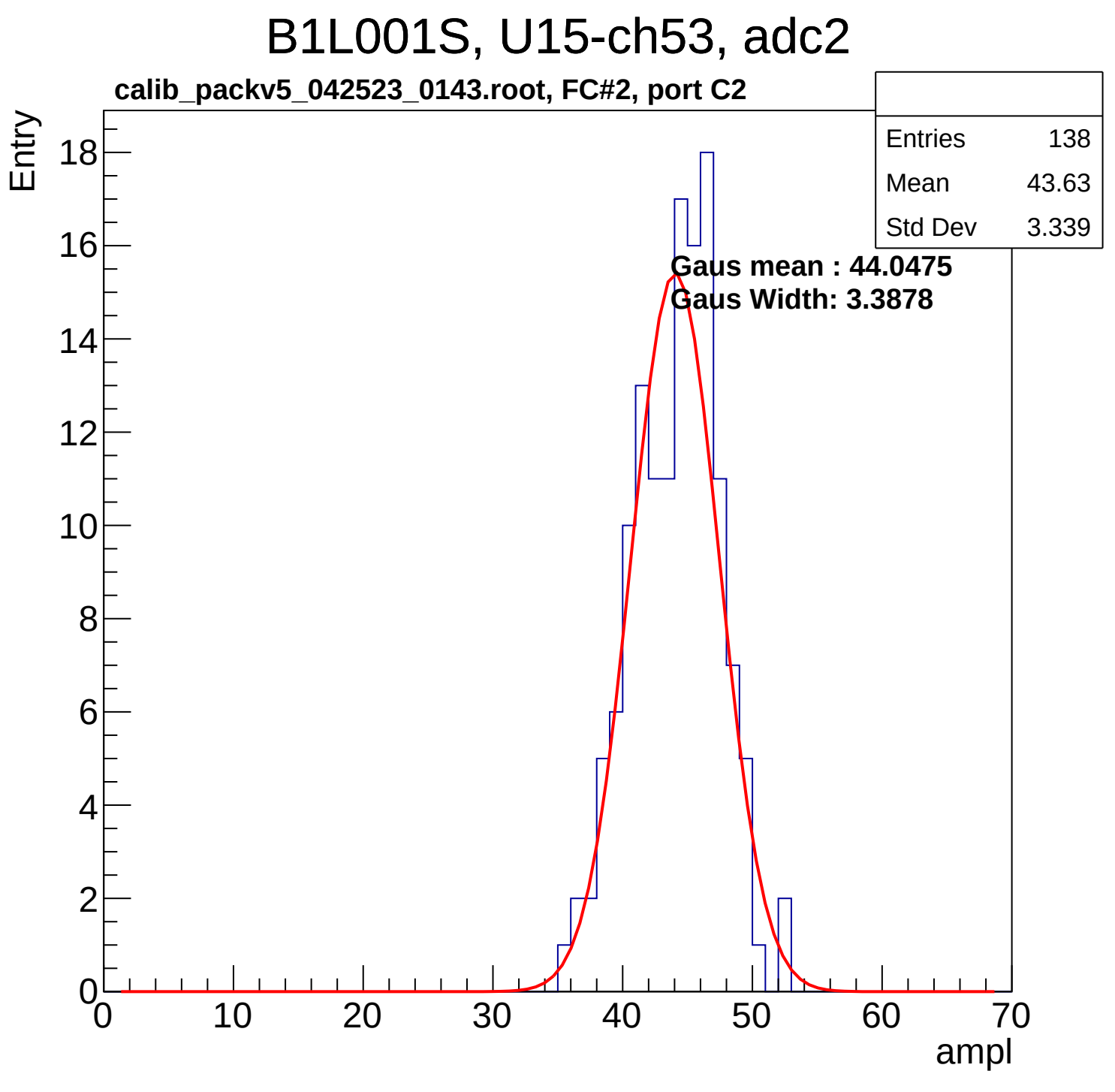
B1L001S, U15-ch53, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.63
Std Dev 3.339
Gaus mean : 44.0475
Gaus Width: 3.3878
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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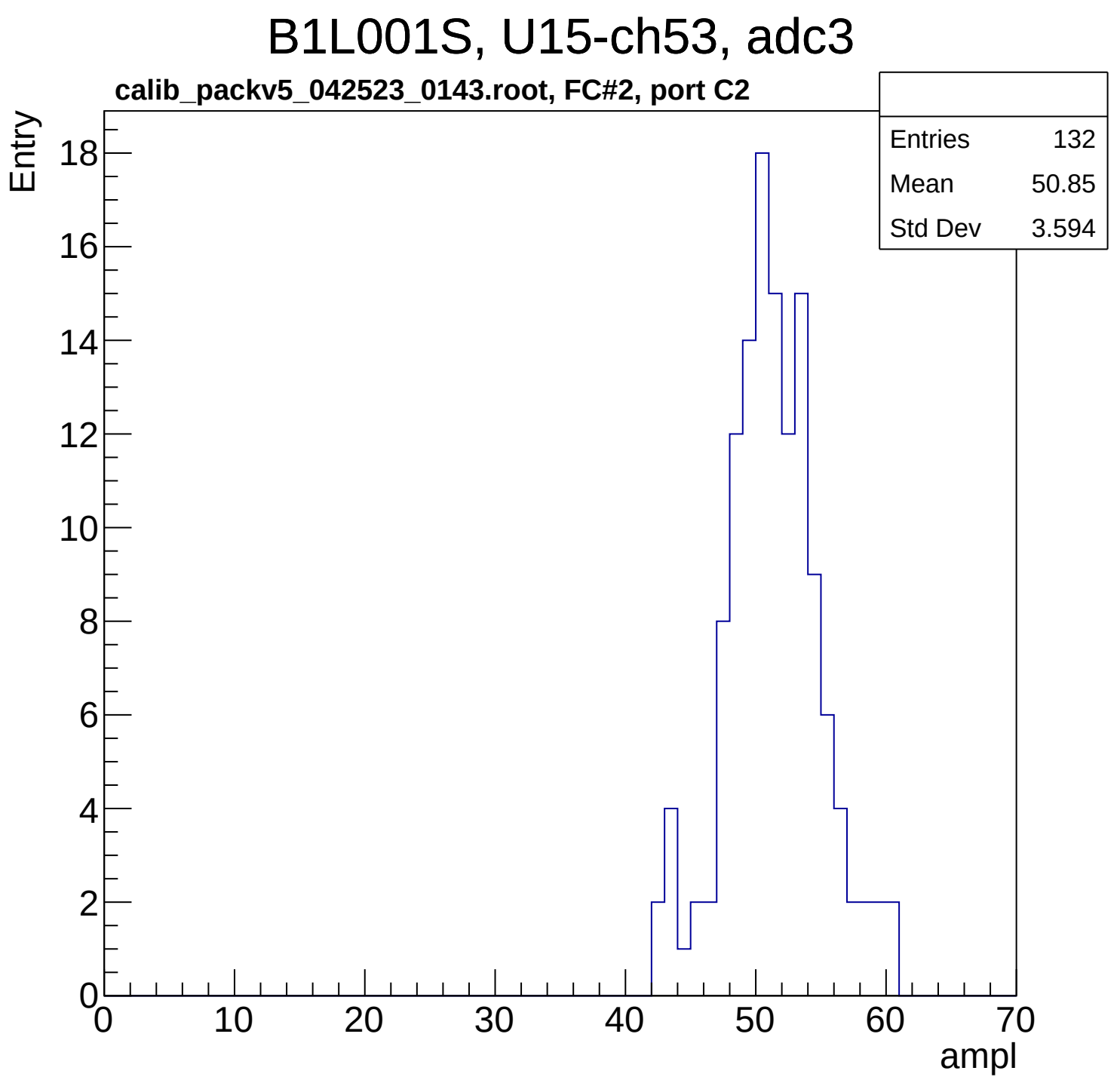
B1L001S, U15-ch53, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 50.85
Std Dev 3.594
Entry
ampl
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6
8
10
12
14
16
18
0
10
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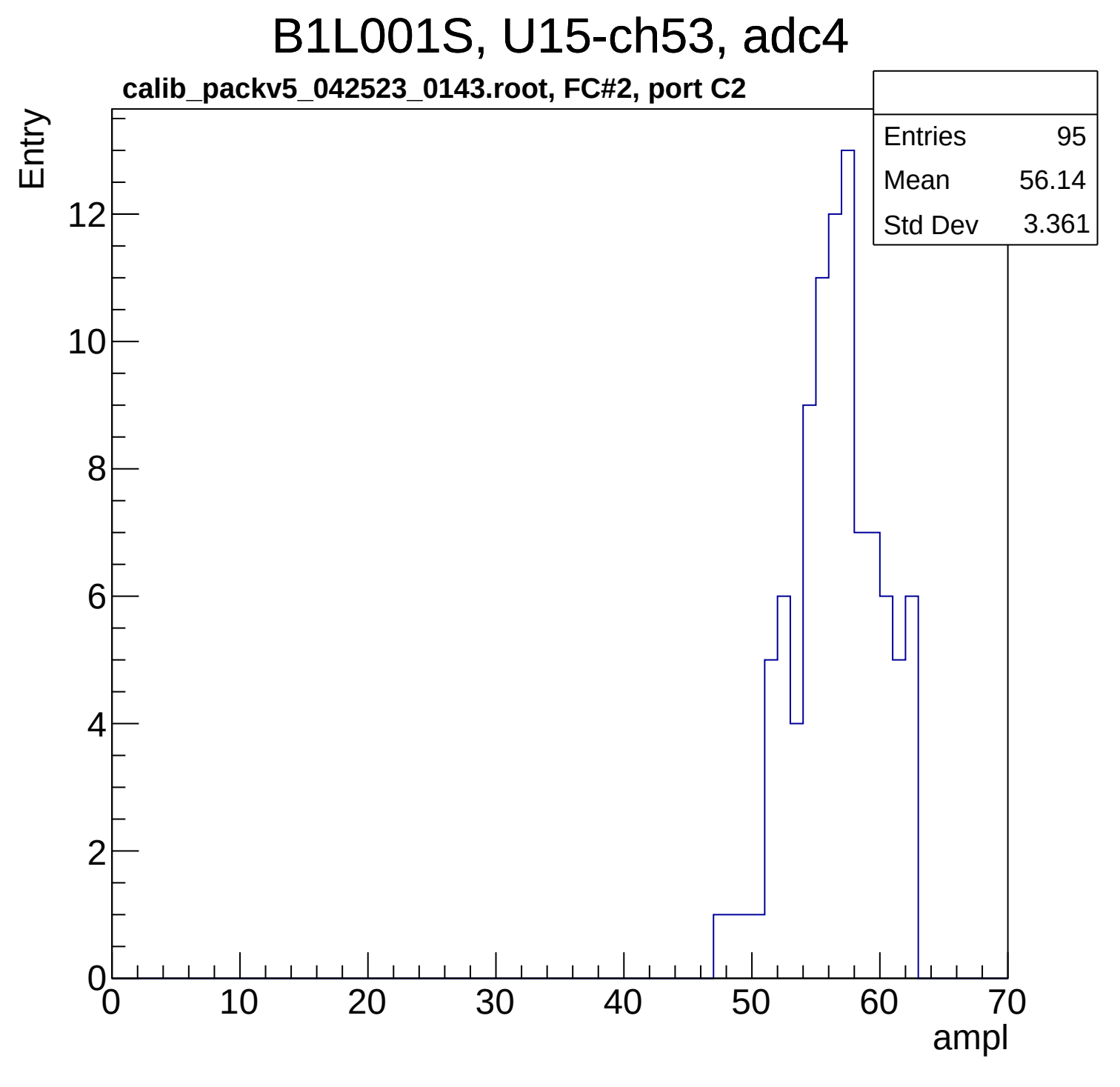
B1L001S, U15-ch53, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 56.14
Std Dev 3.361
Entry
ampl
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0
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60
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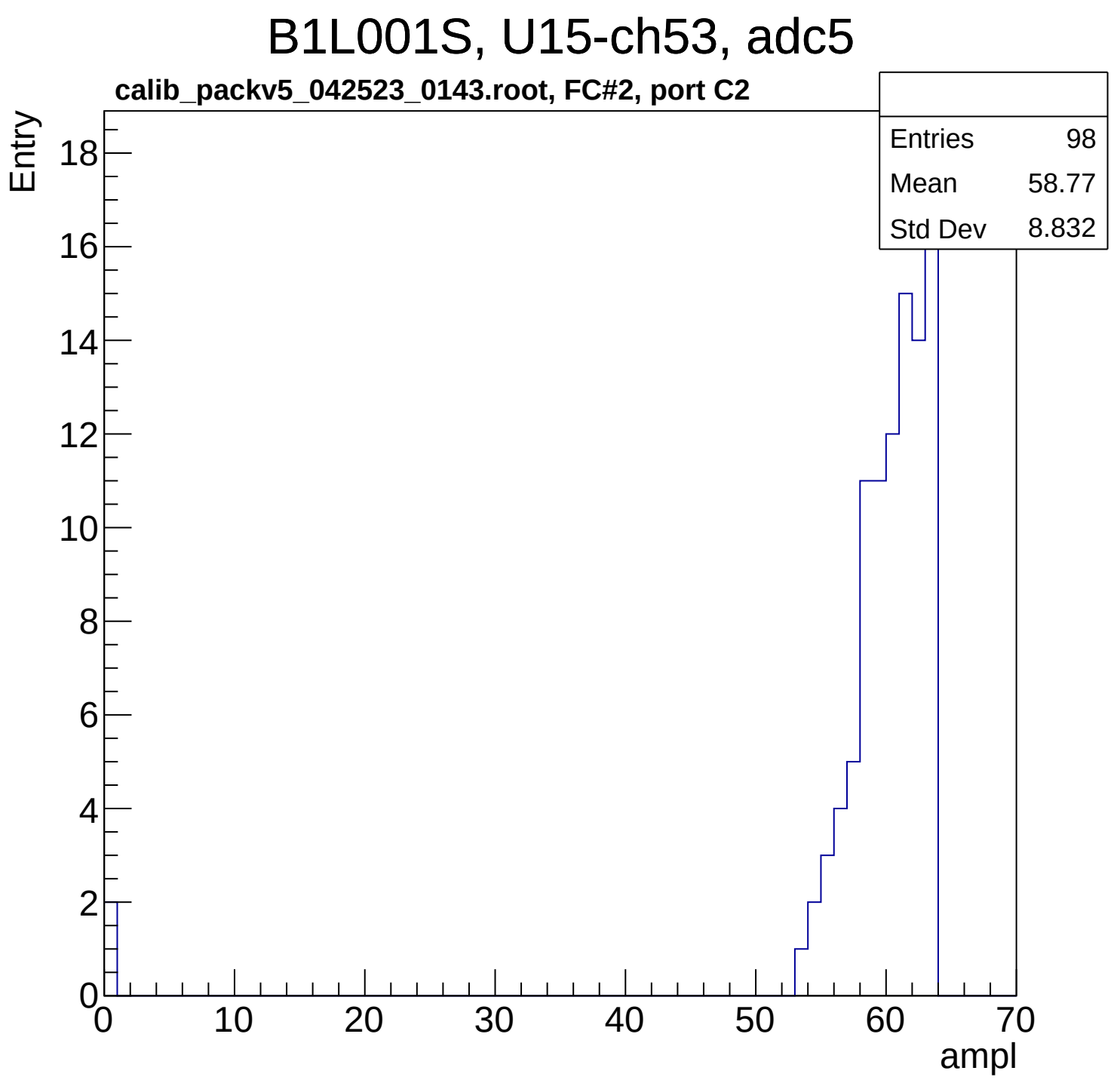
B1L001S, U15-ch53, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 58.77
Std Dev 8.832
Entry
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6
8
10
12
14
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18
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10
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30
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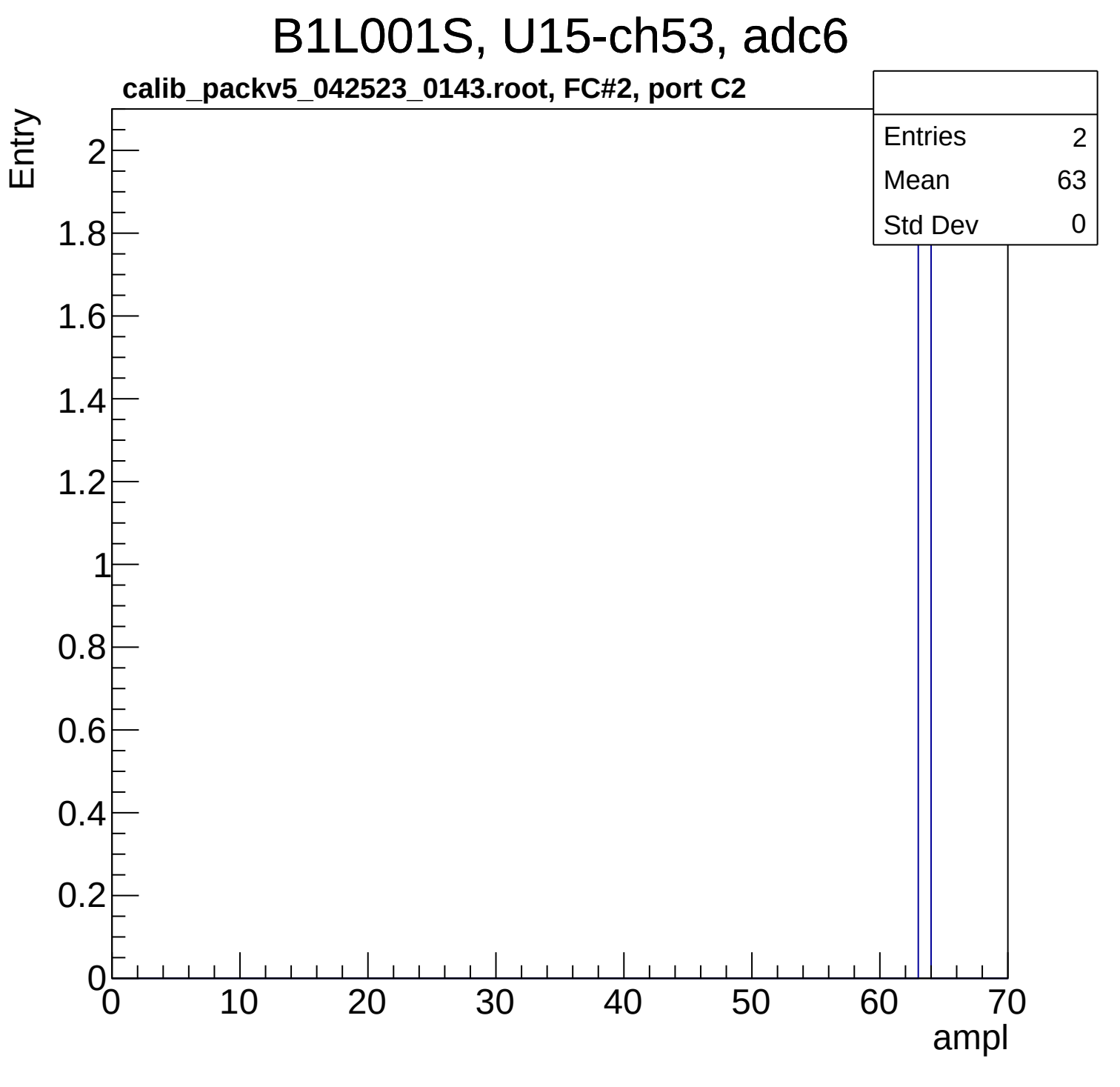
50
60
70
ampl


B1L001S, U15-ch53, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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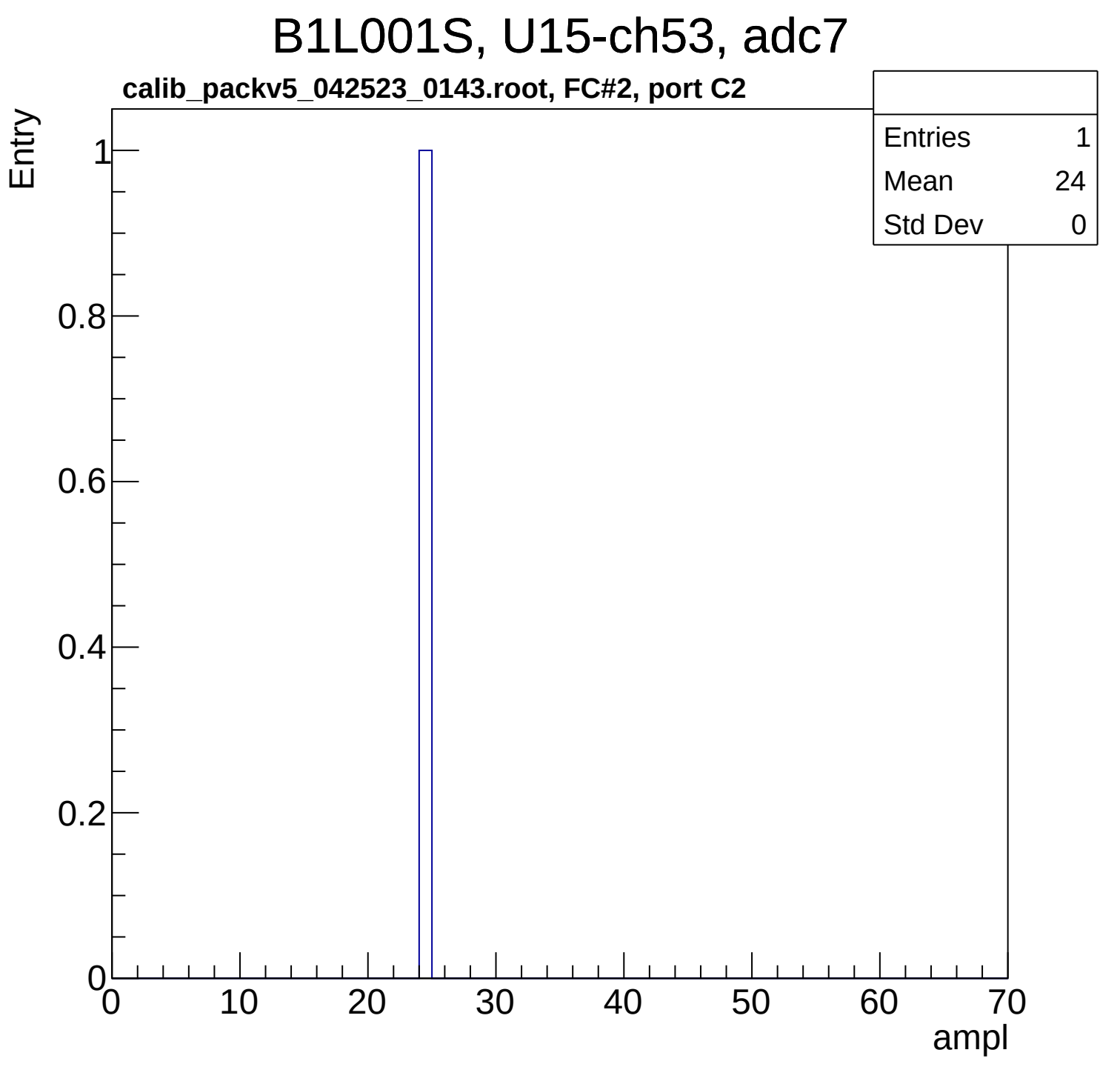
50
60
70


B1L001S, U15-ch53, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
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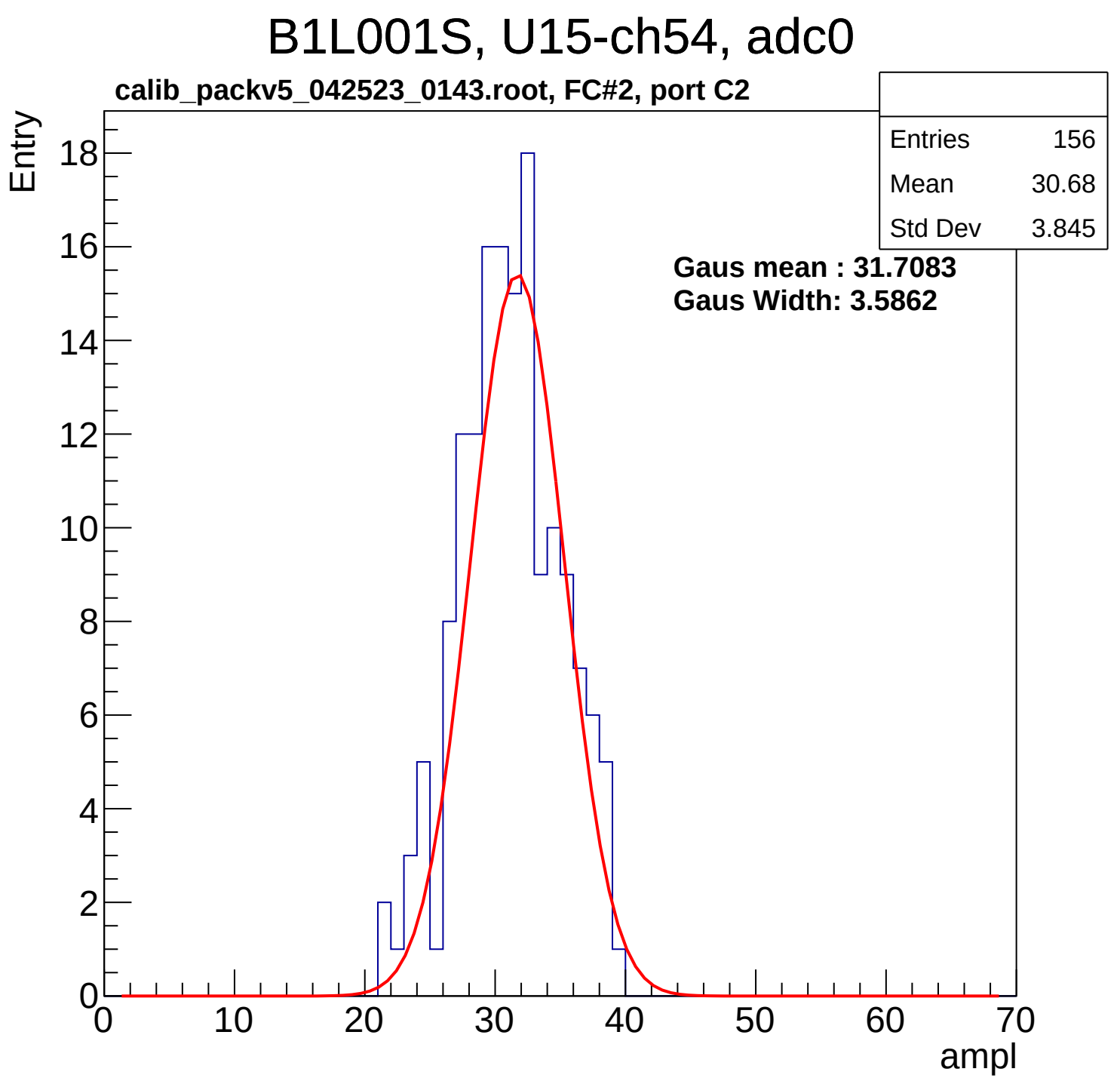
60
70
ampl


B1L001S, U15-ch54, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 30.68
Std Dev 3.845
Gaus mean : 31.7083
Gaus Width: 3.5862
Entry
ampl
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2
4
6
8
10
12
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16
18
0
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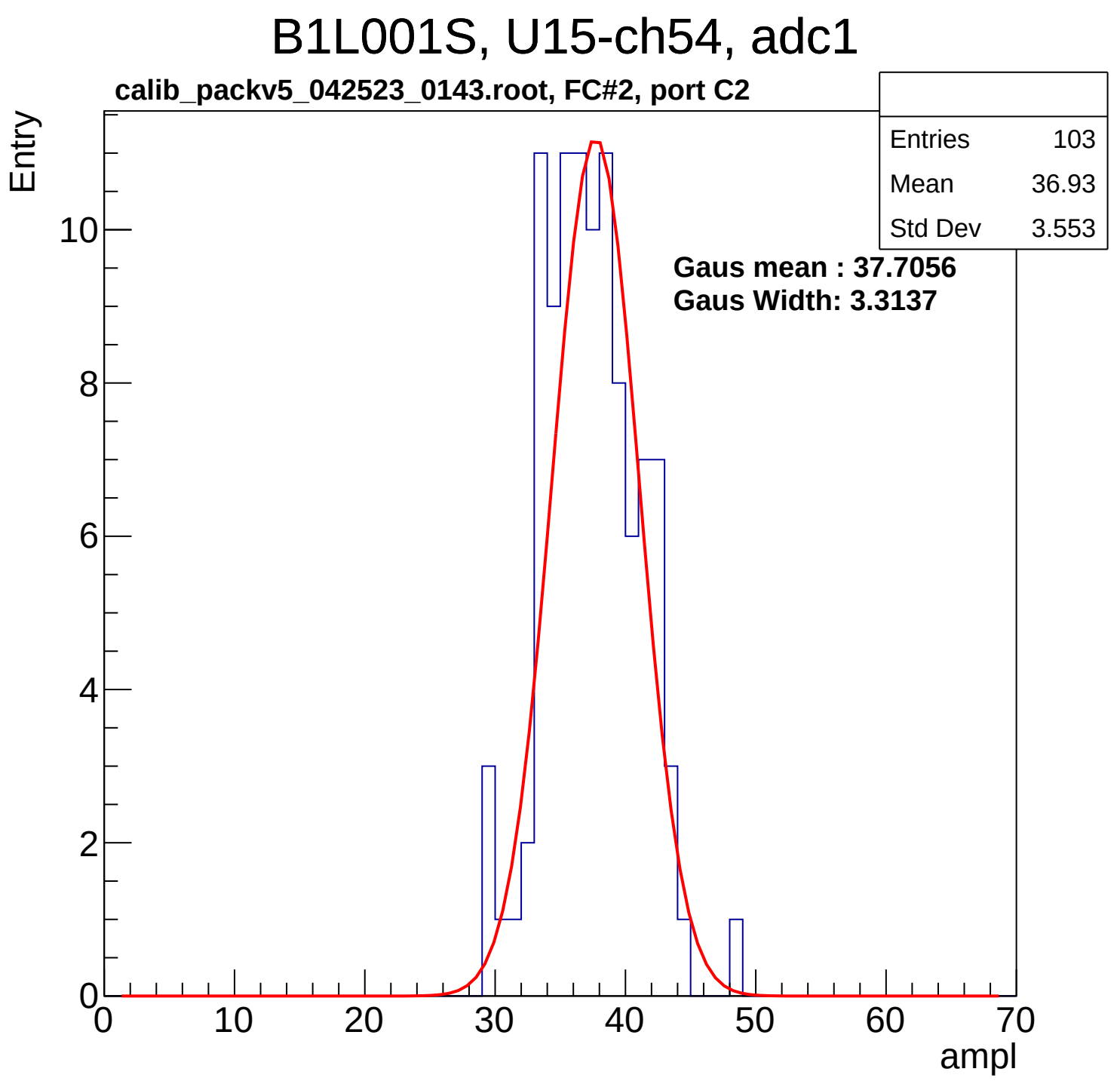
B1L001S, U15-ch54, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 36.93
Std Dev 3.553
Gaus mean : 37.7056
Gaus Width: 3.3137
Entry
ampl
0
2
4
6
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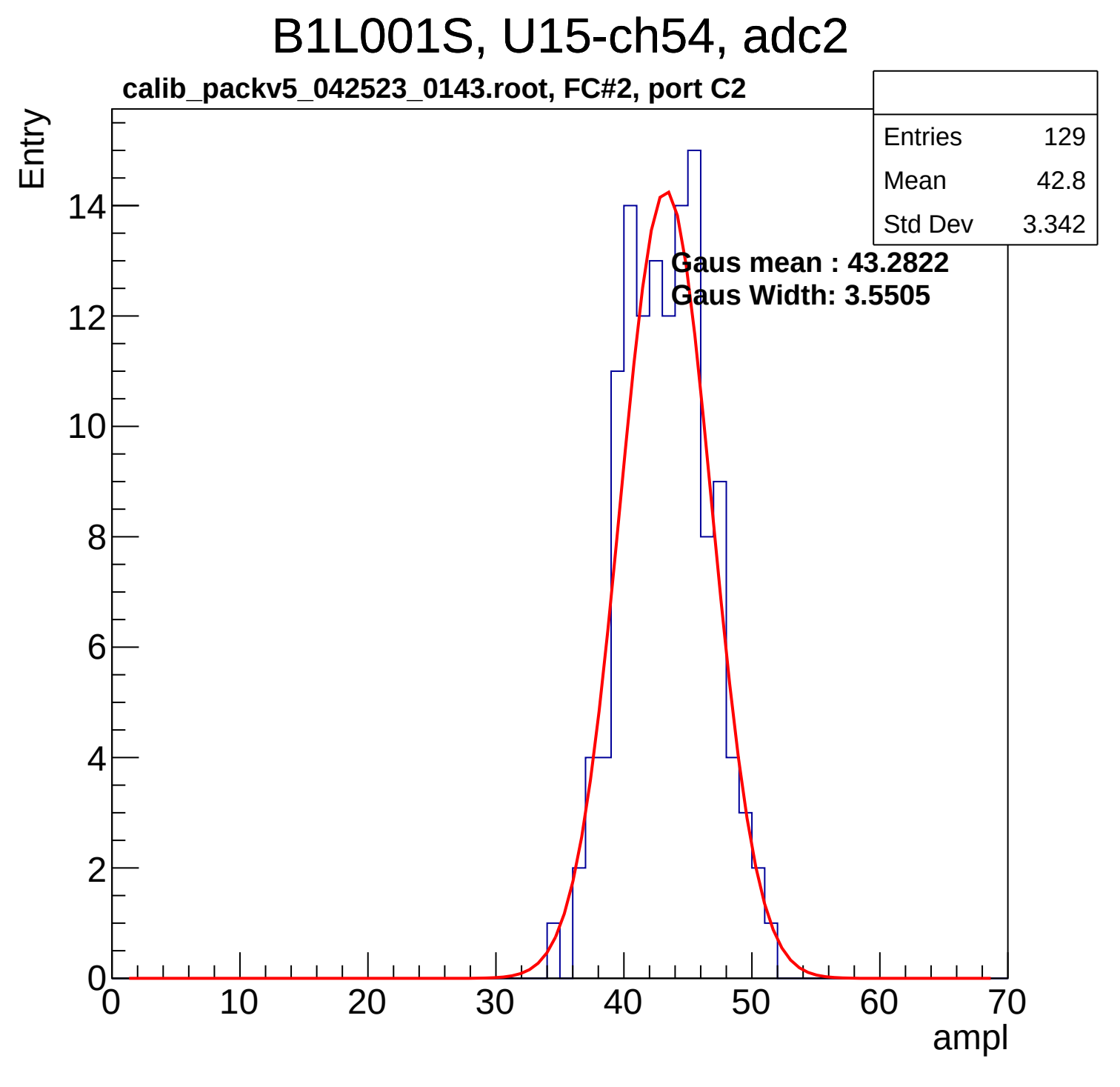
B1L001S, U15-ch54, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 42.8
Std Dev 3.342
Gaus mean : 43.2822
Gaus Width: 3.5505
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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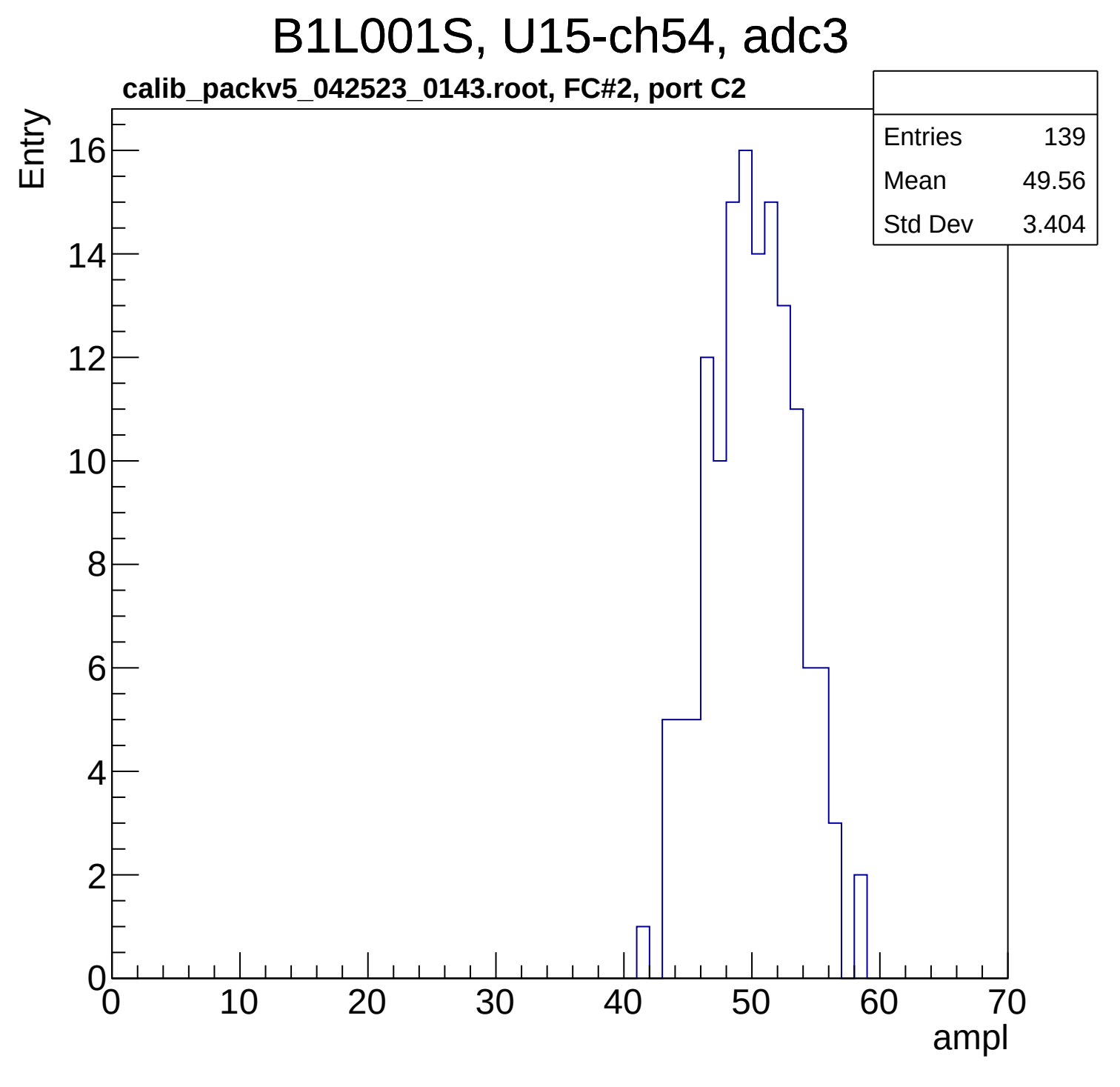
B1L001S, U15-ch54, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.56
Std Dev 3.404
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
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40
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60
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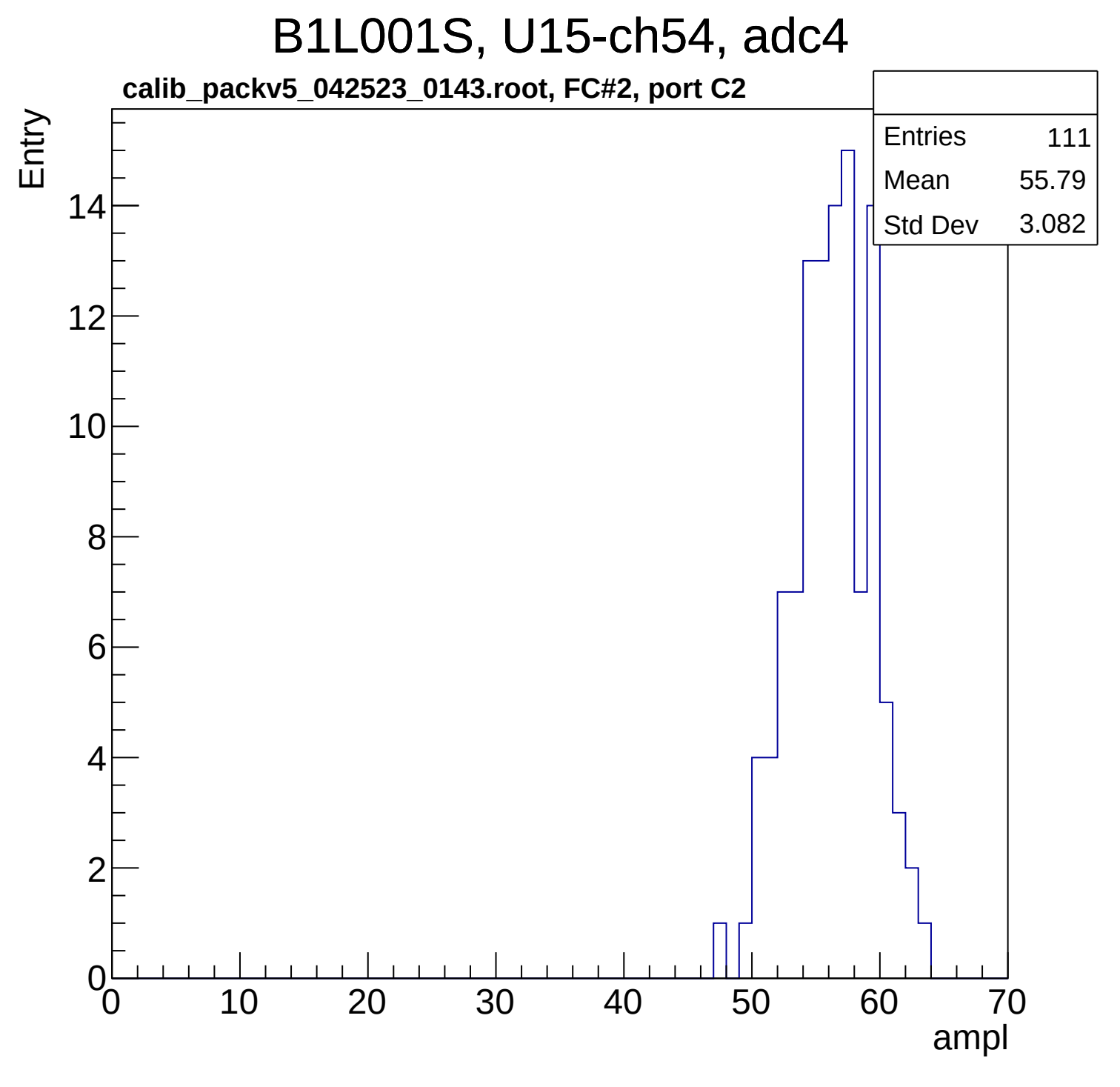
B1L001S, U15-ch54, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 55.79
Std Dev 3.082
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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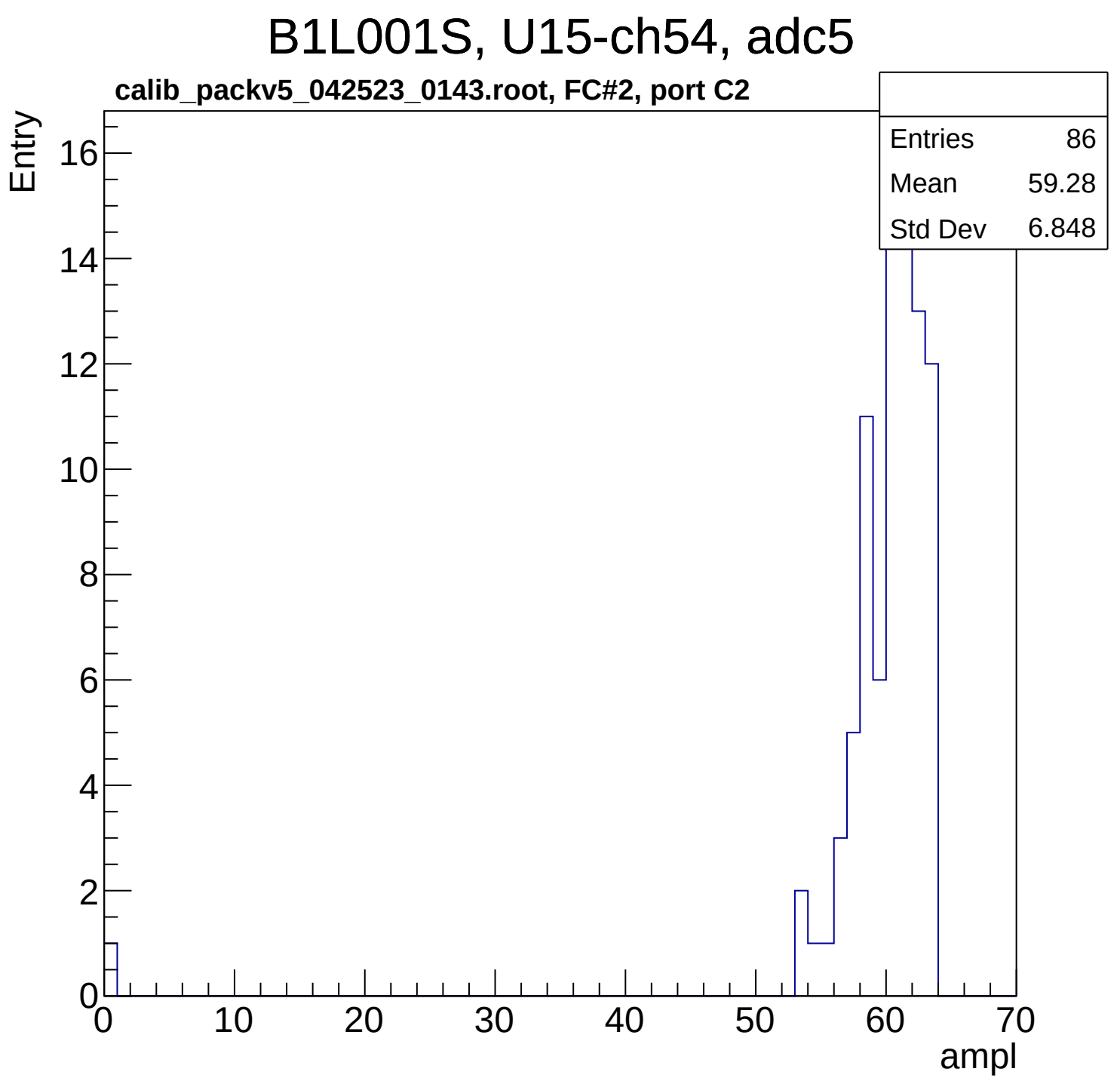
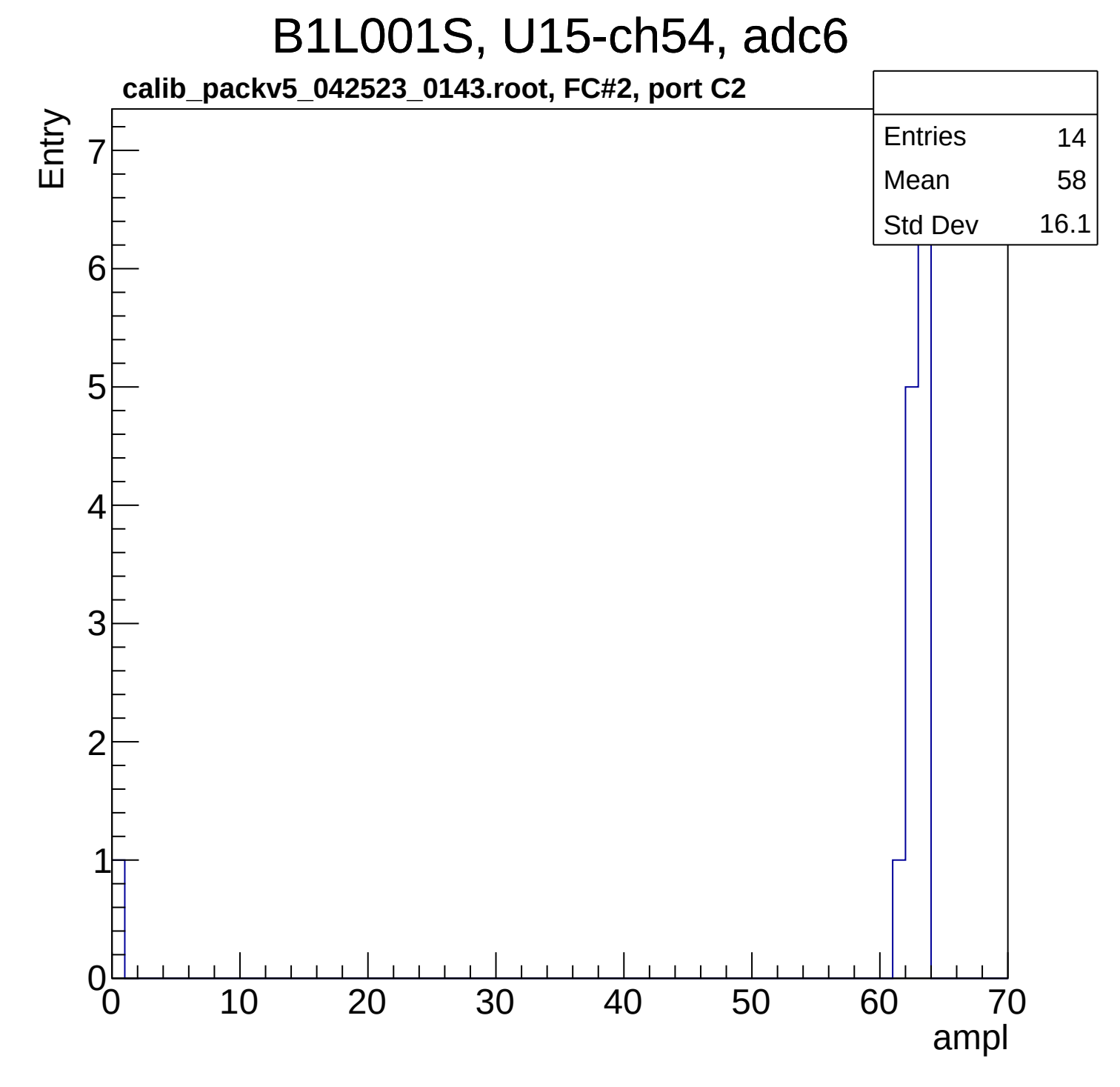
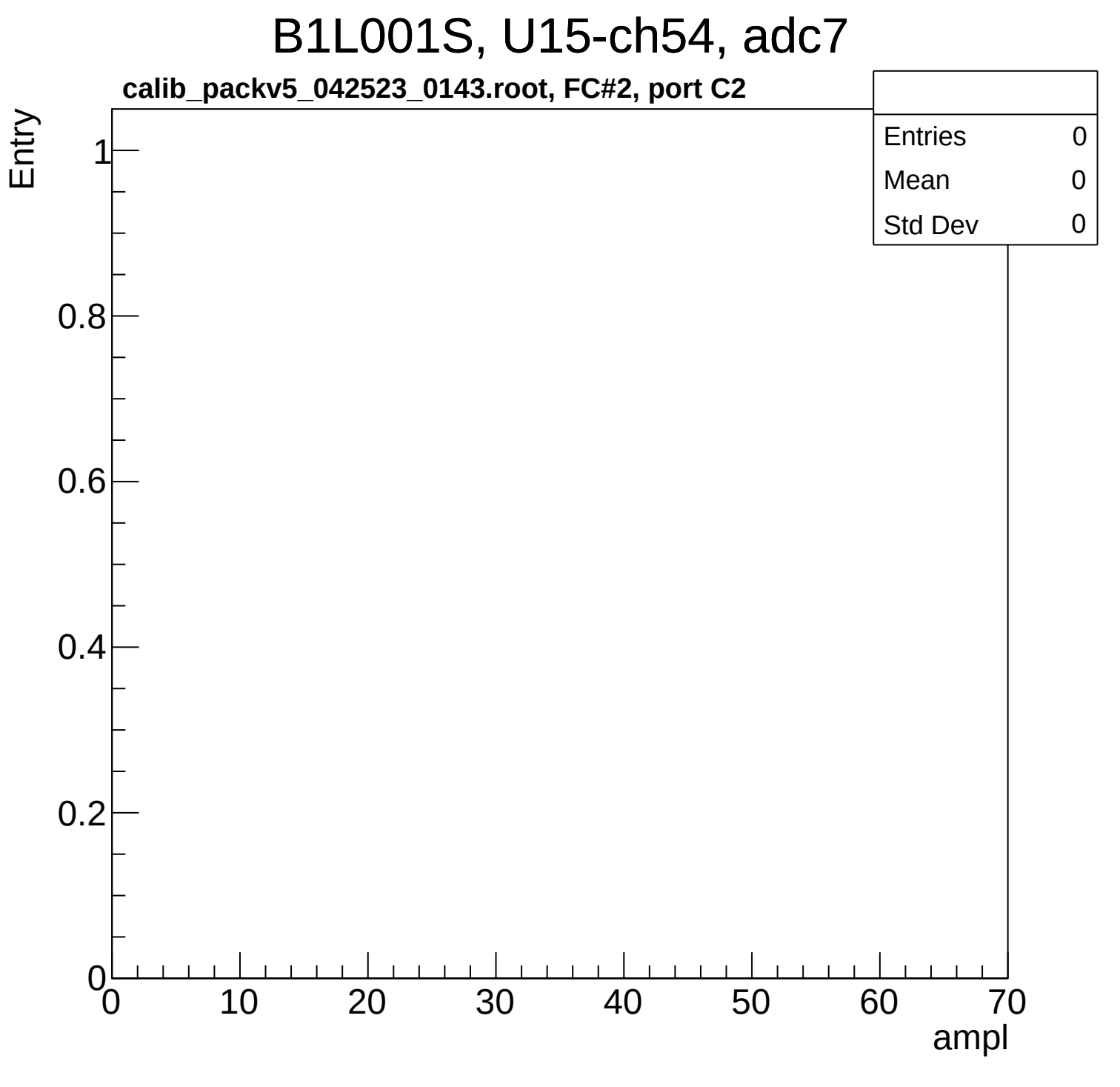

B1L001S, U15-ch54, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 86
Mean 59.28
Std Dev 6.848
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch54, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 58
Std Dev 16.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch54, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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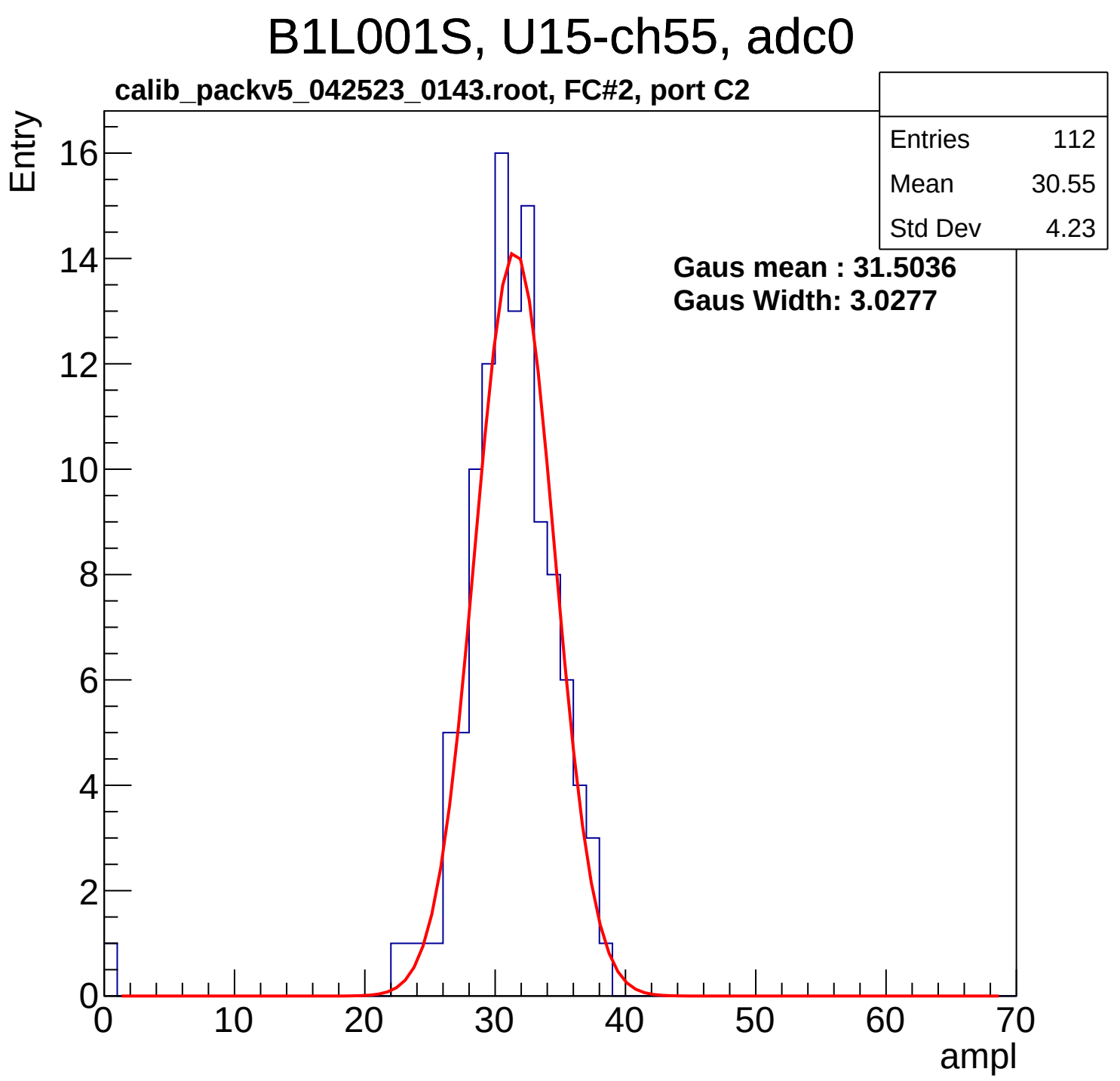
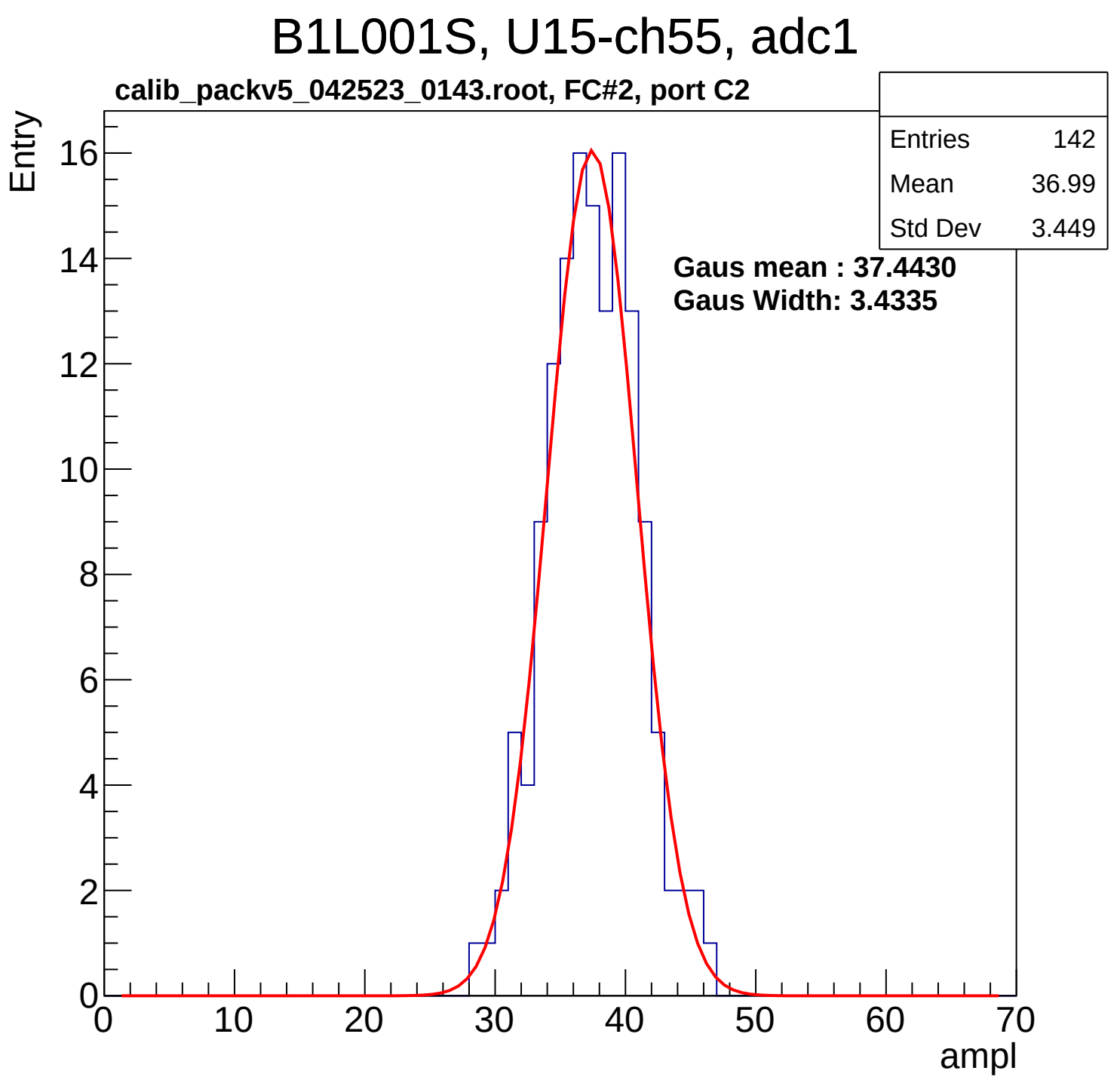
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch55, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 30.55
Std Dev 4.23
Gaus mean : 31.5036
Gaus Width: 3.0277
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch55, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 36.99
Std Dev 3.449
Gaus mean : 37.4430
Gaus Width: 3.4335
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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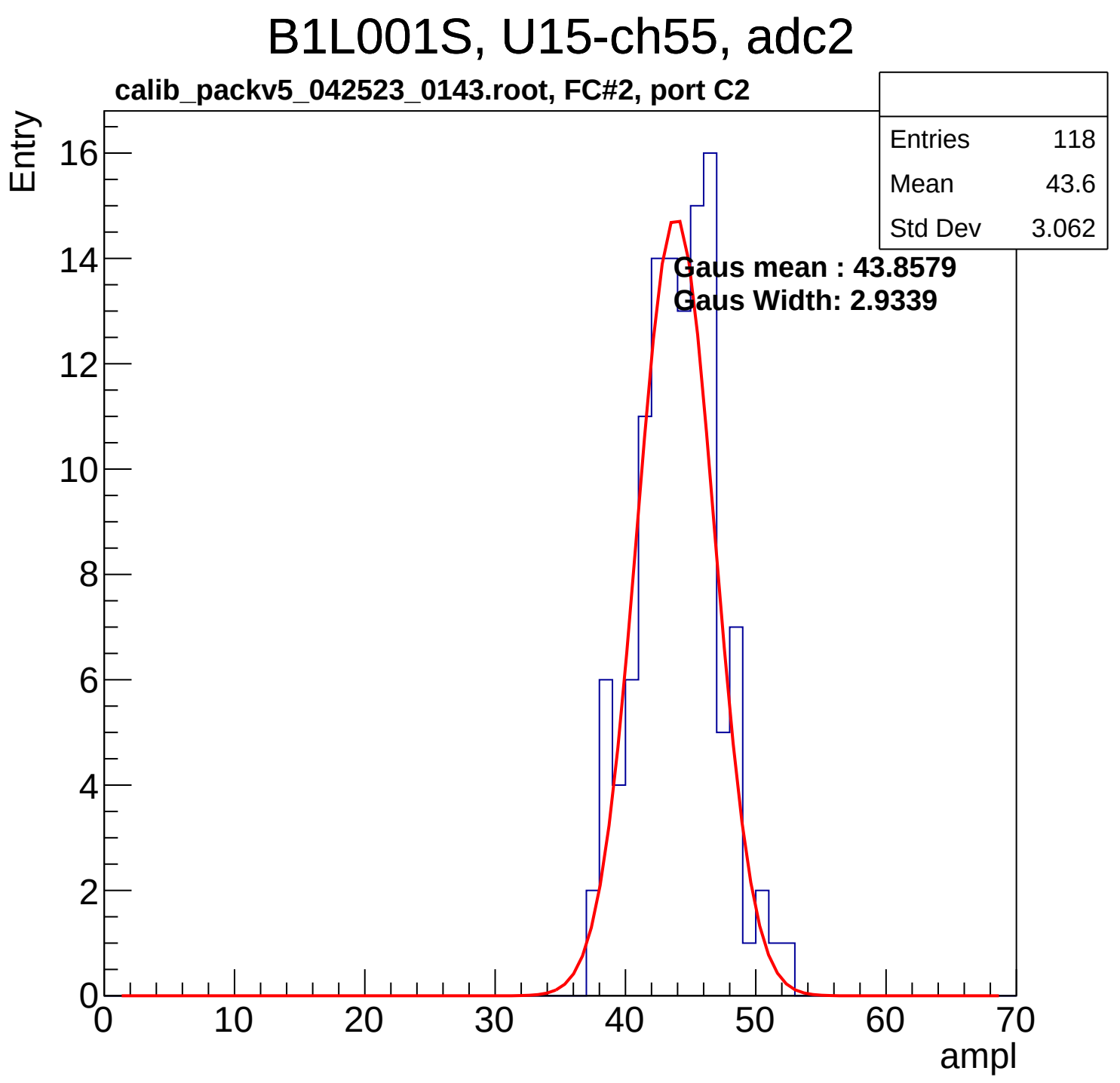
B1L001S, U15-ch55, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 43.6
Std Dev 3.062
Gaus mean : 43.8579
Gaus Width: 2.9339
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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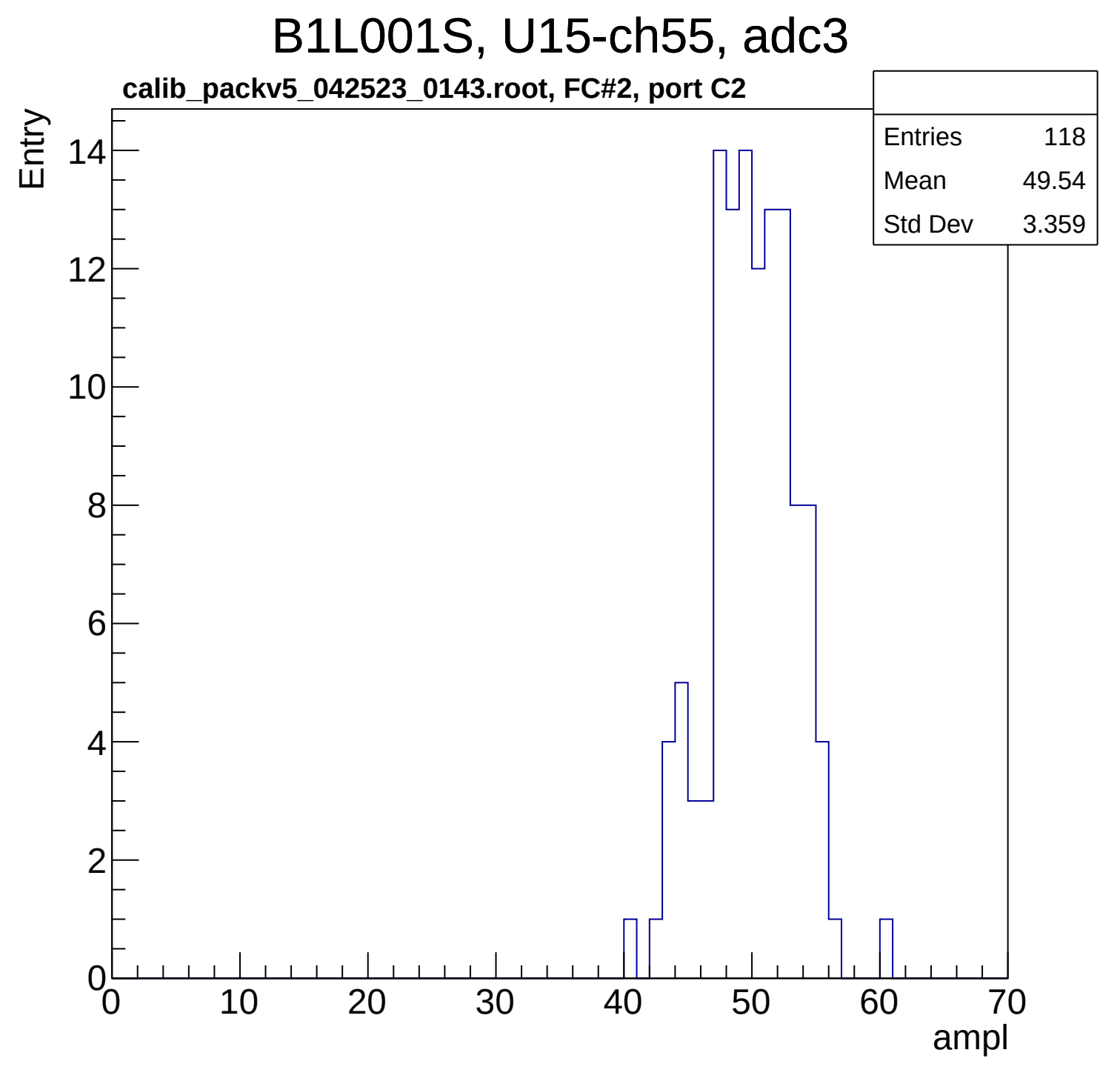
B1L001S, U15-ch55, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 49.54
Std Dev 3.359
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
50
60
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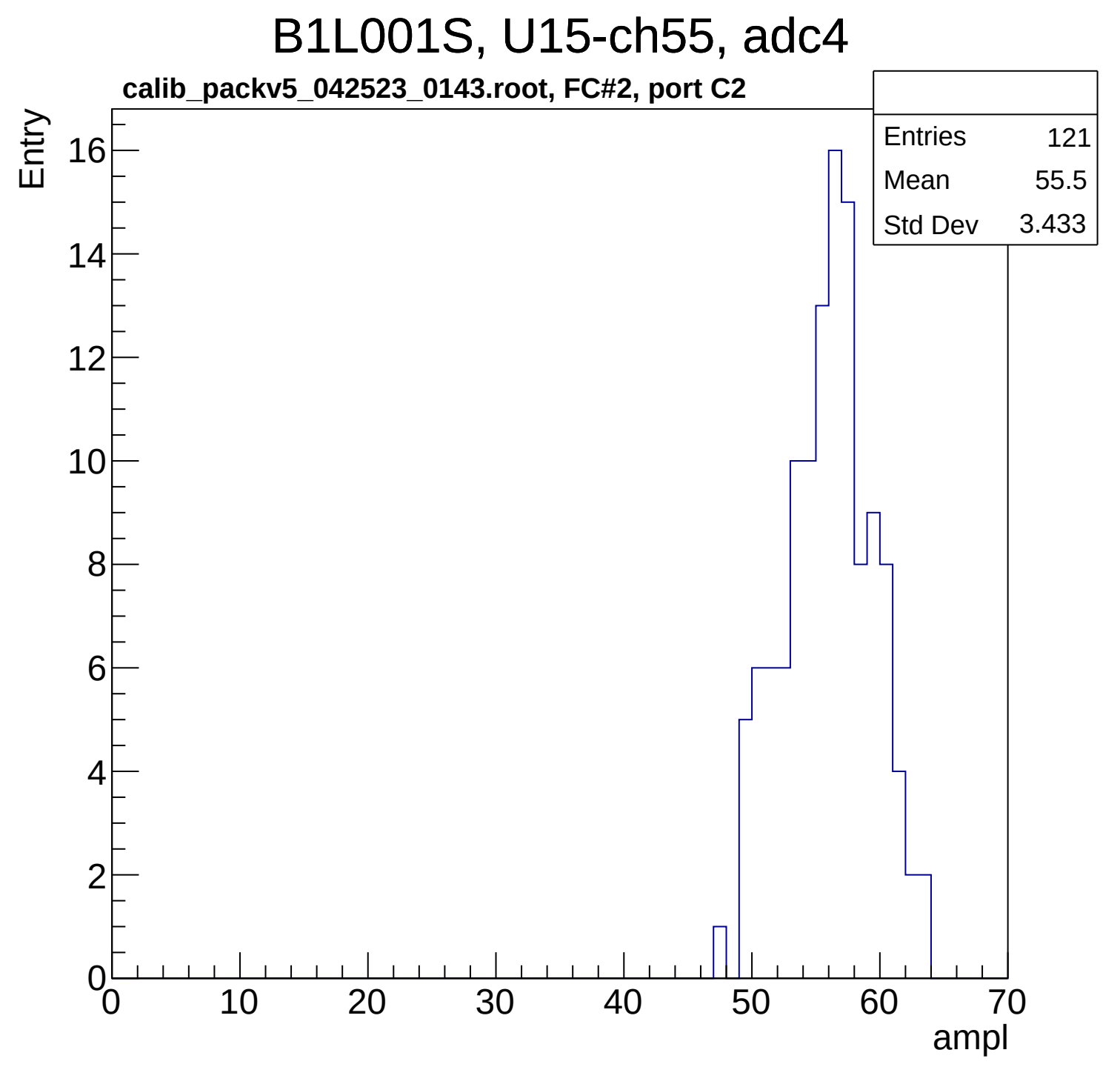
B1L001S, U15-ch55, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 55.5
Std Dev 3.433
Entry
ampl
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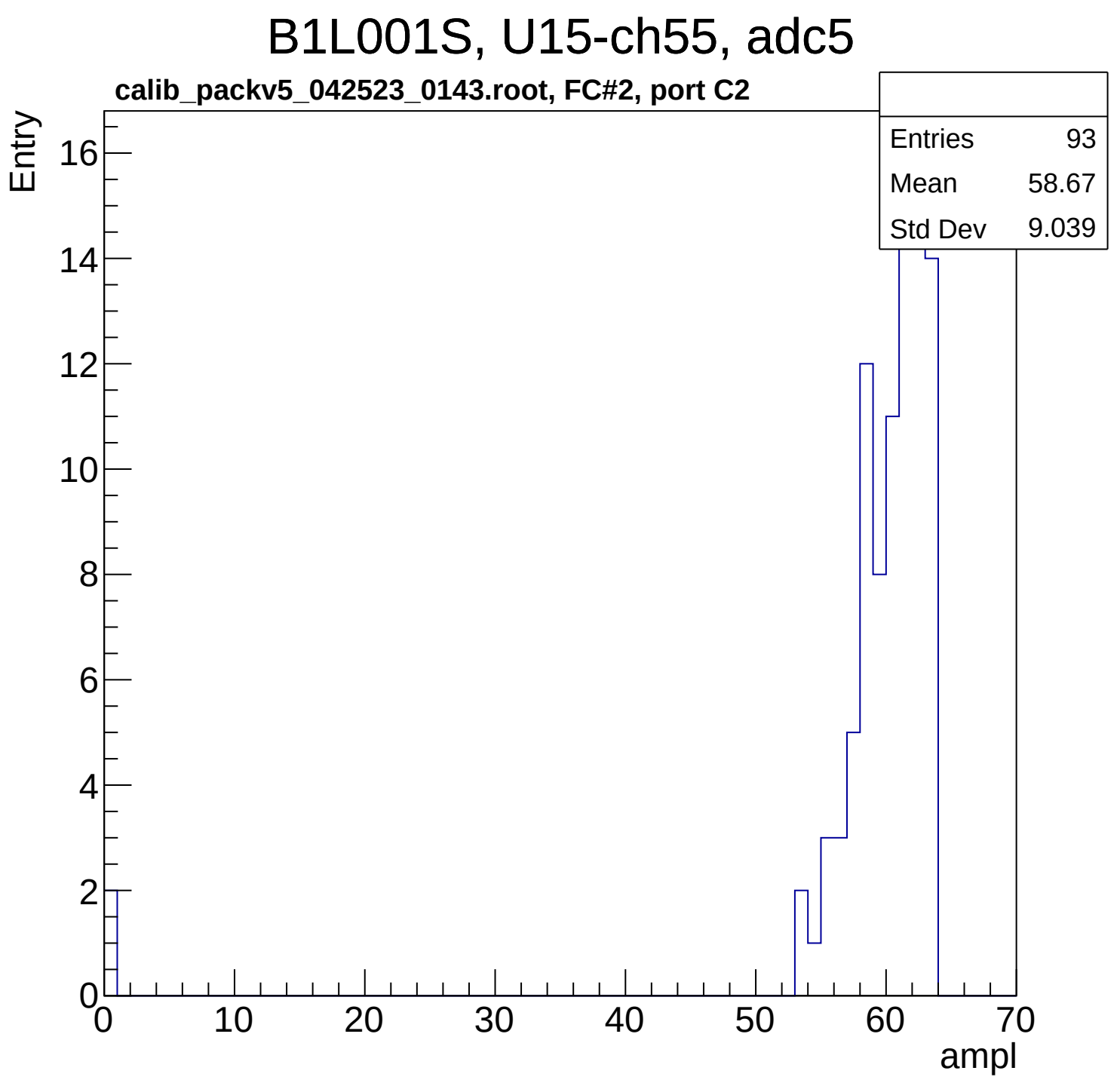
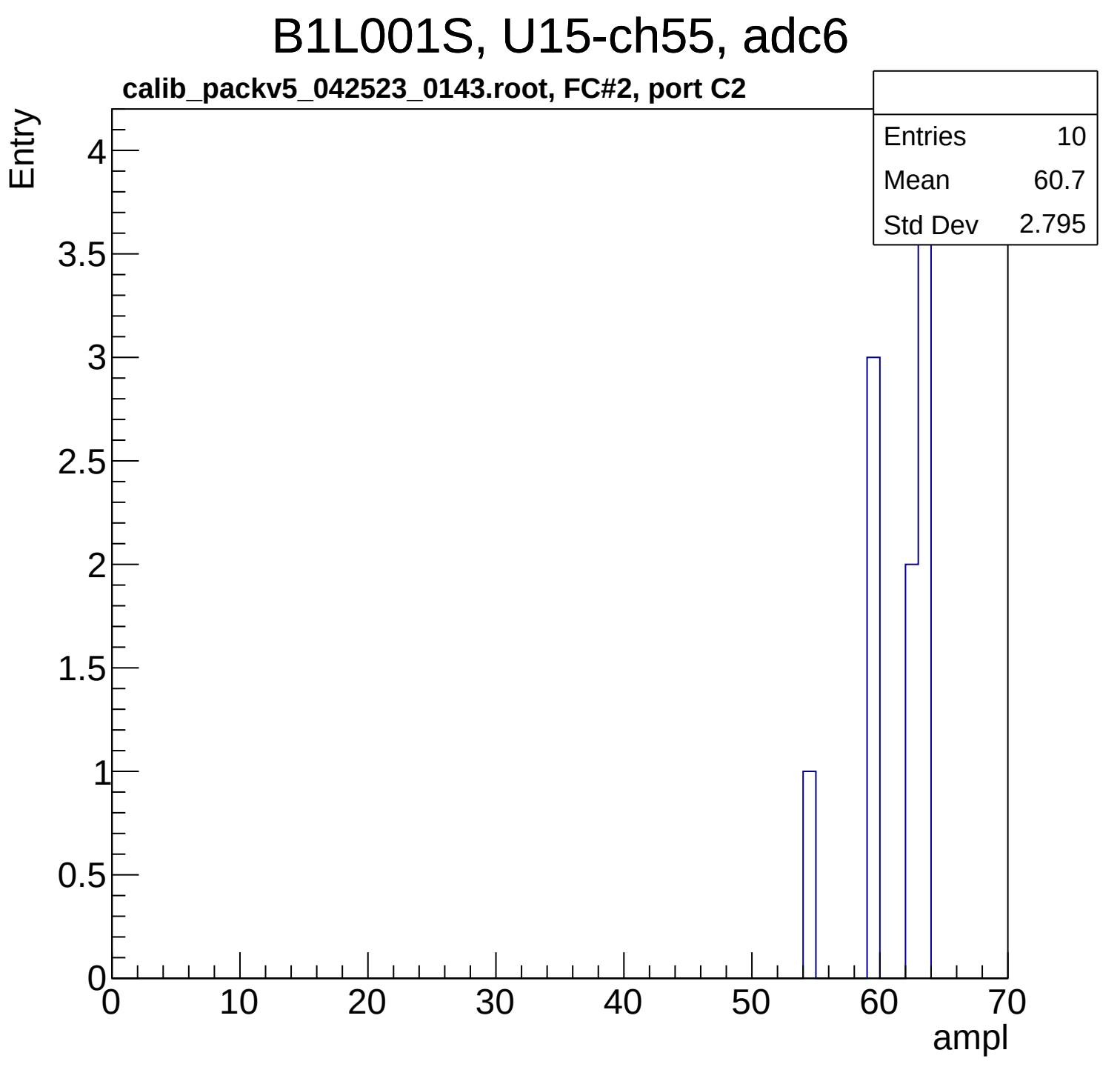
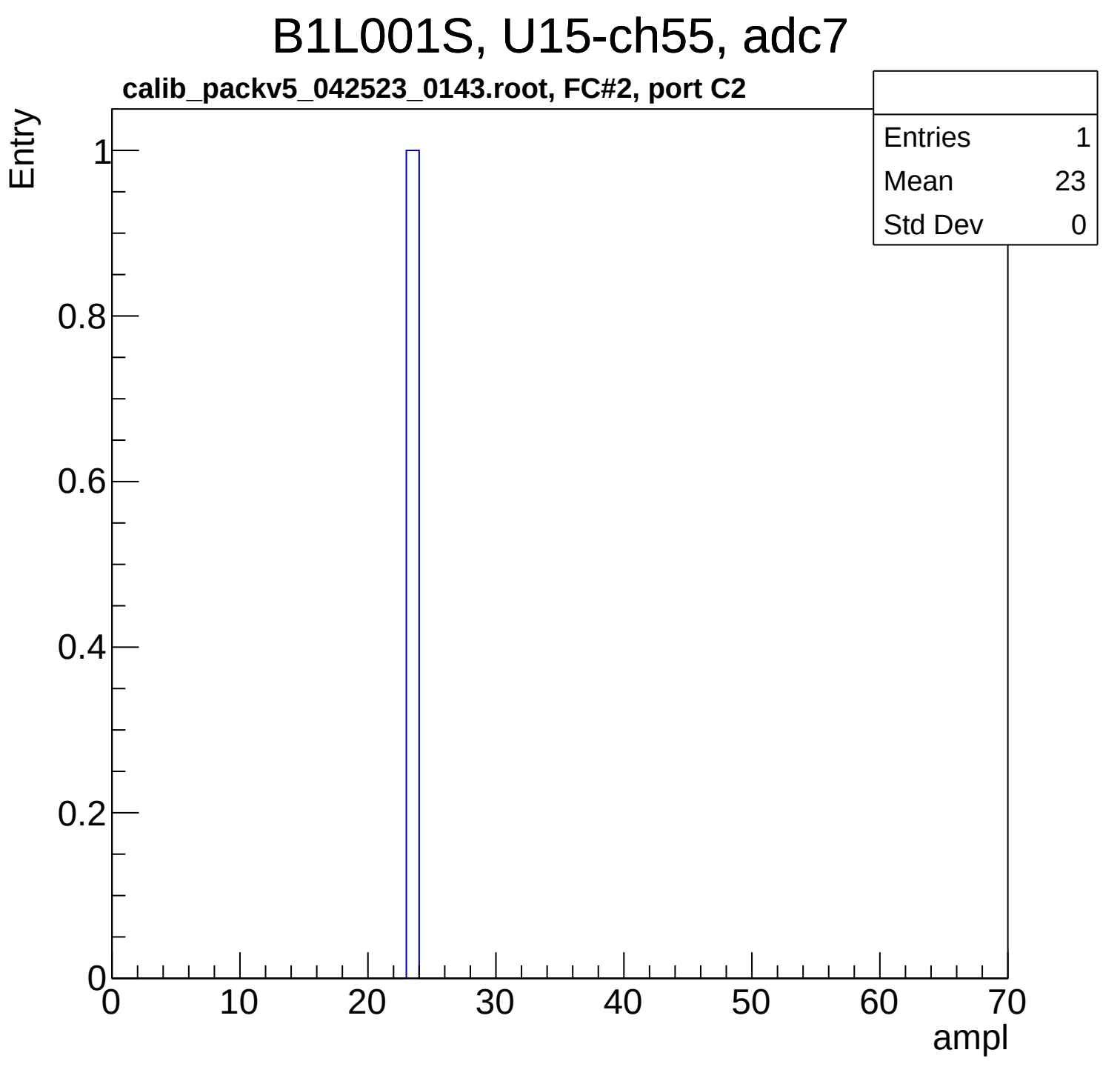
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch55, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 58.67
Std Dev 9.039
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch55, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 60.7
Std Dev 2.795
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch55, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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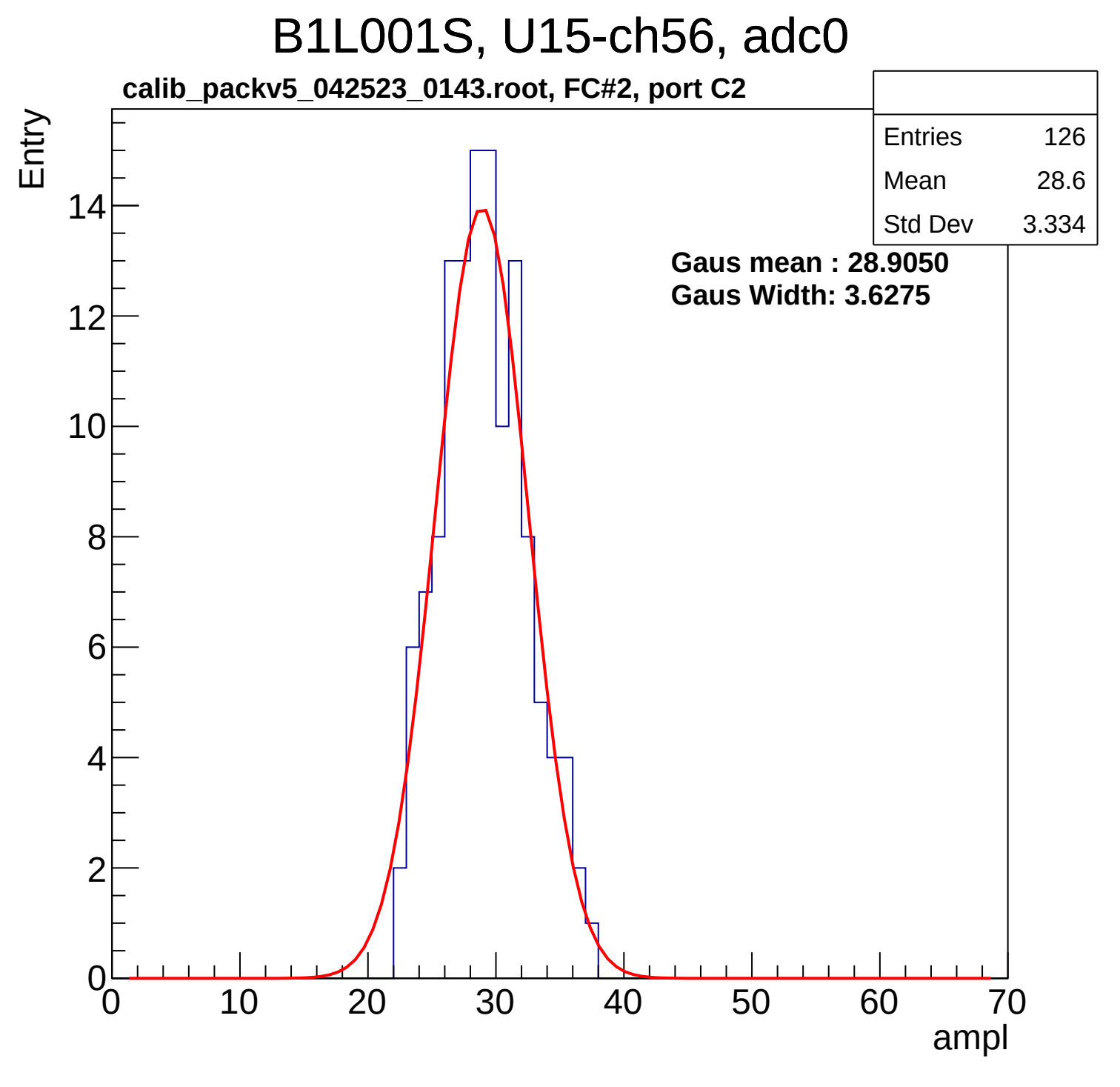
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch56, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 28.6
Std Dev 3.334
Gaus mean : 28.9050
Gaus Width: 3.6275
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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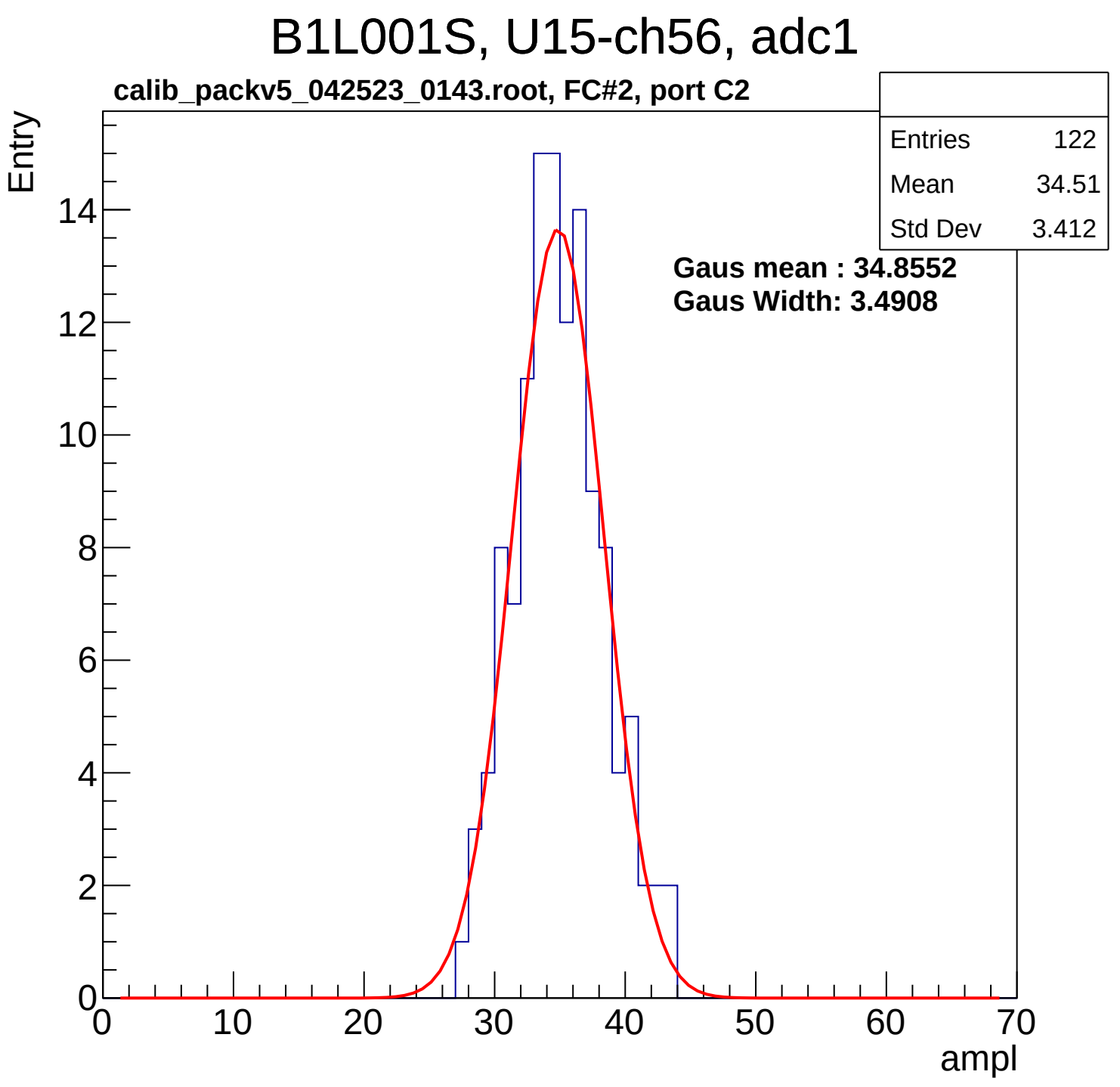
B1L001S, U15-ch56, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 34.51
Std Dev 3.412
Gaus mean : 34.8552
Gaus Width: 3.4908
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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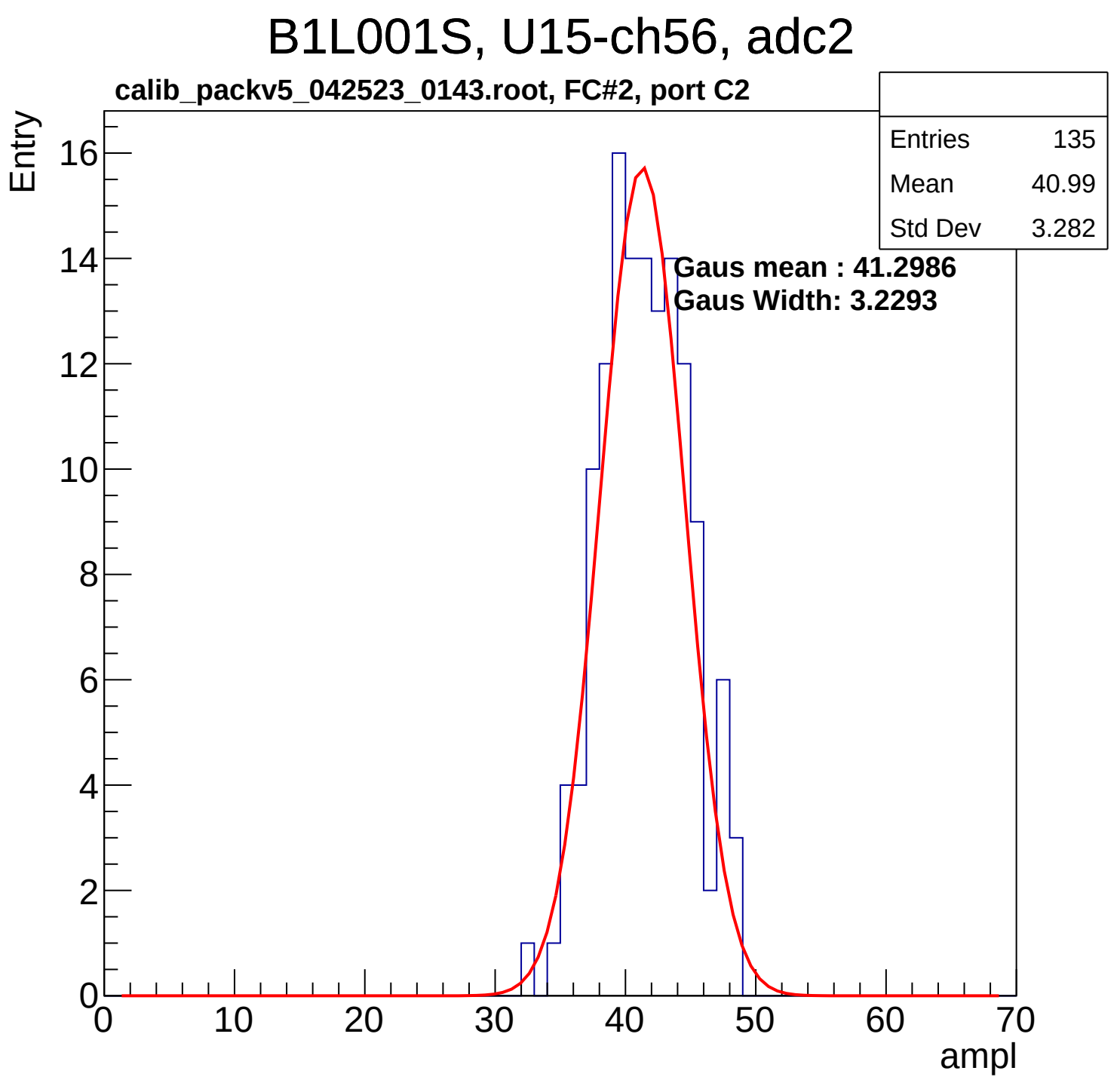
B1L001S, U15-ch56, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 40.99
Std Dev 3.282
Gaus mean : 41.2986
Gaus Width: 3.2293
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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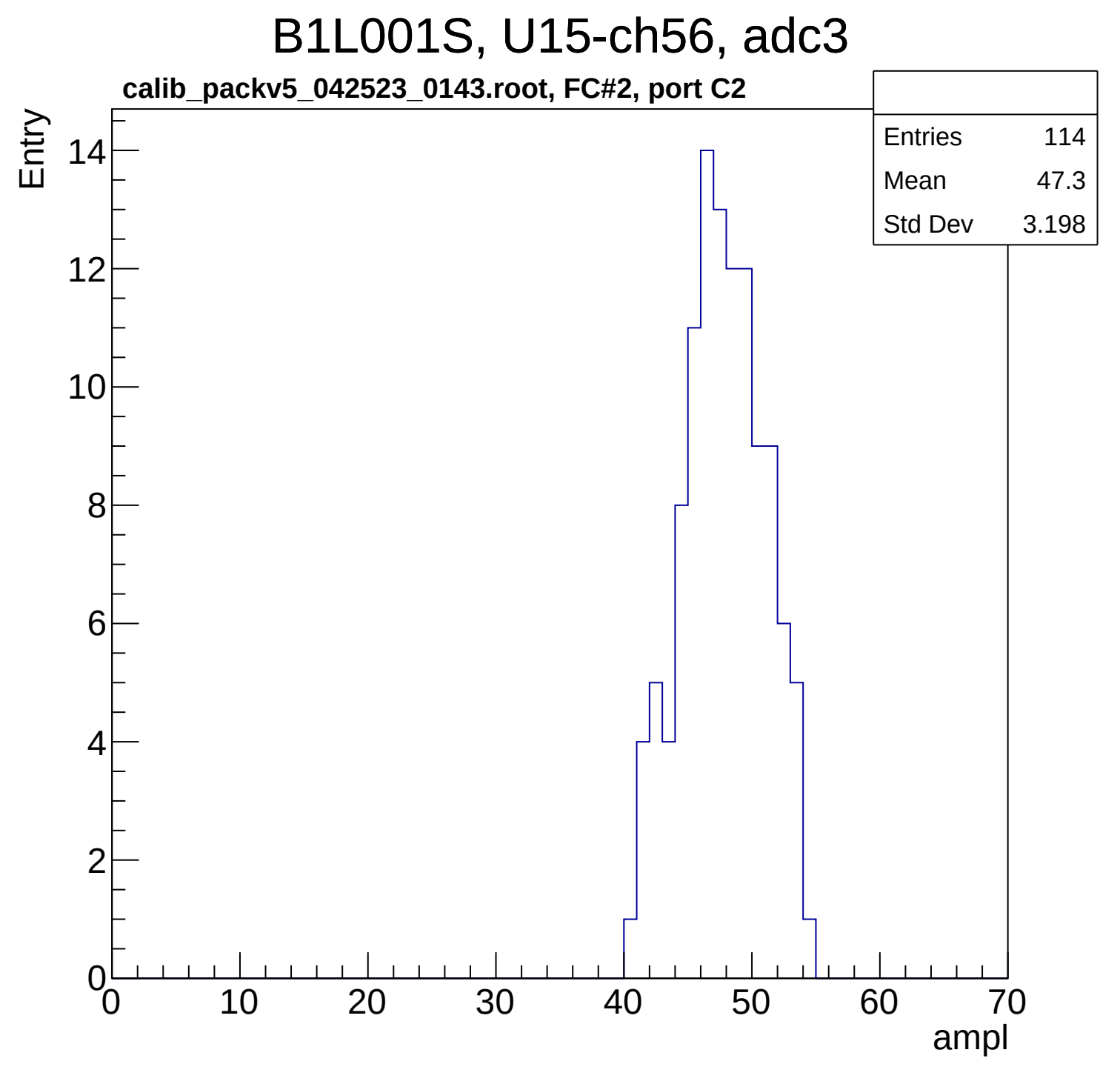
B1L001S, U15-ch56, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 47.3
Std Dev 3.198
Entry
ampl
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2
4
6
8
10
12
14
0
10
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60
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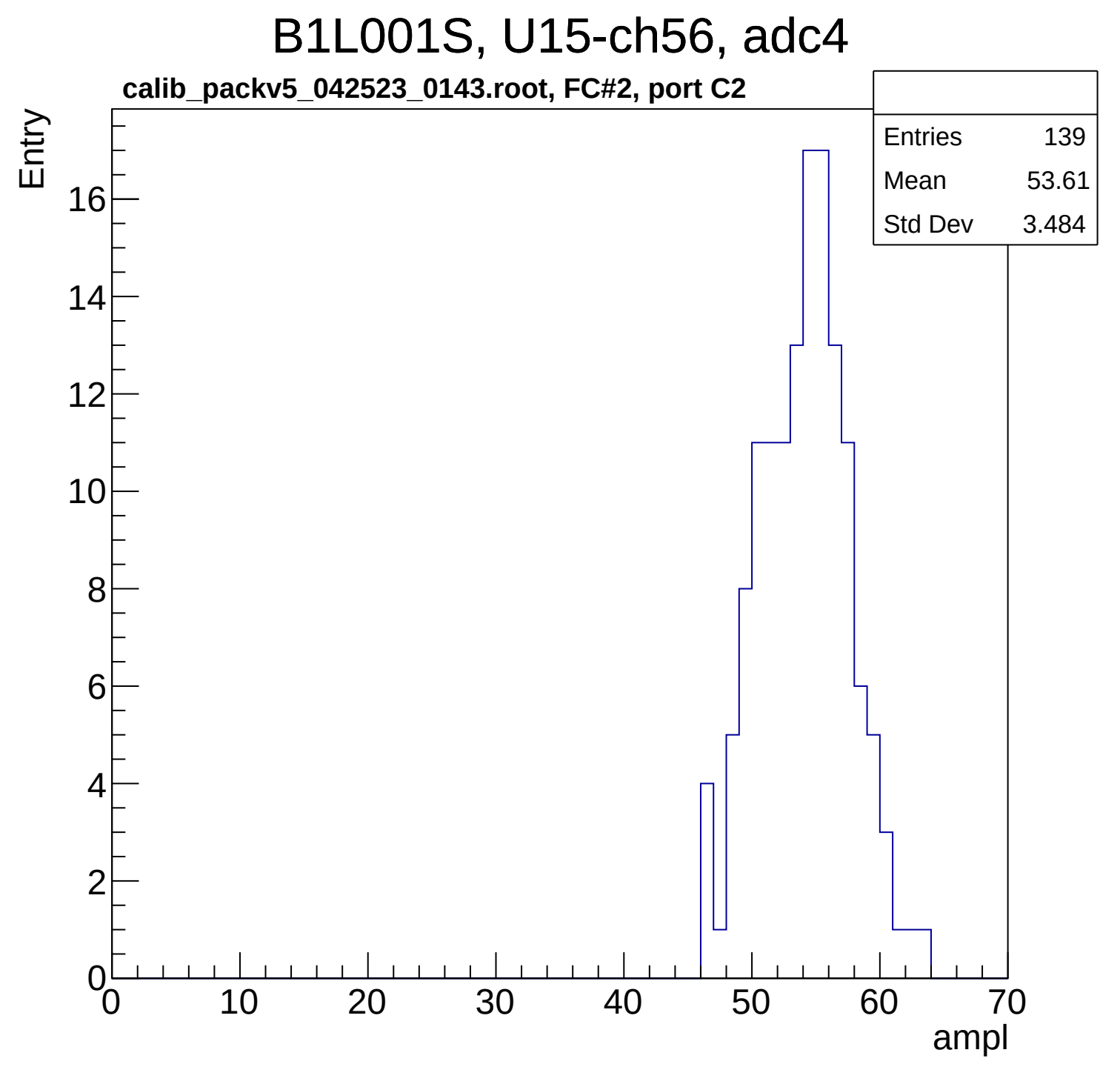
B1L001S, U15-ch56, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 53.61
Std Dev 3.484
Entry
ampl
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4
6
8
10
12
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16
0
10
20
30
40
50
60
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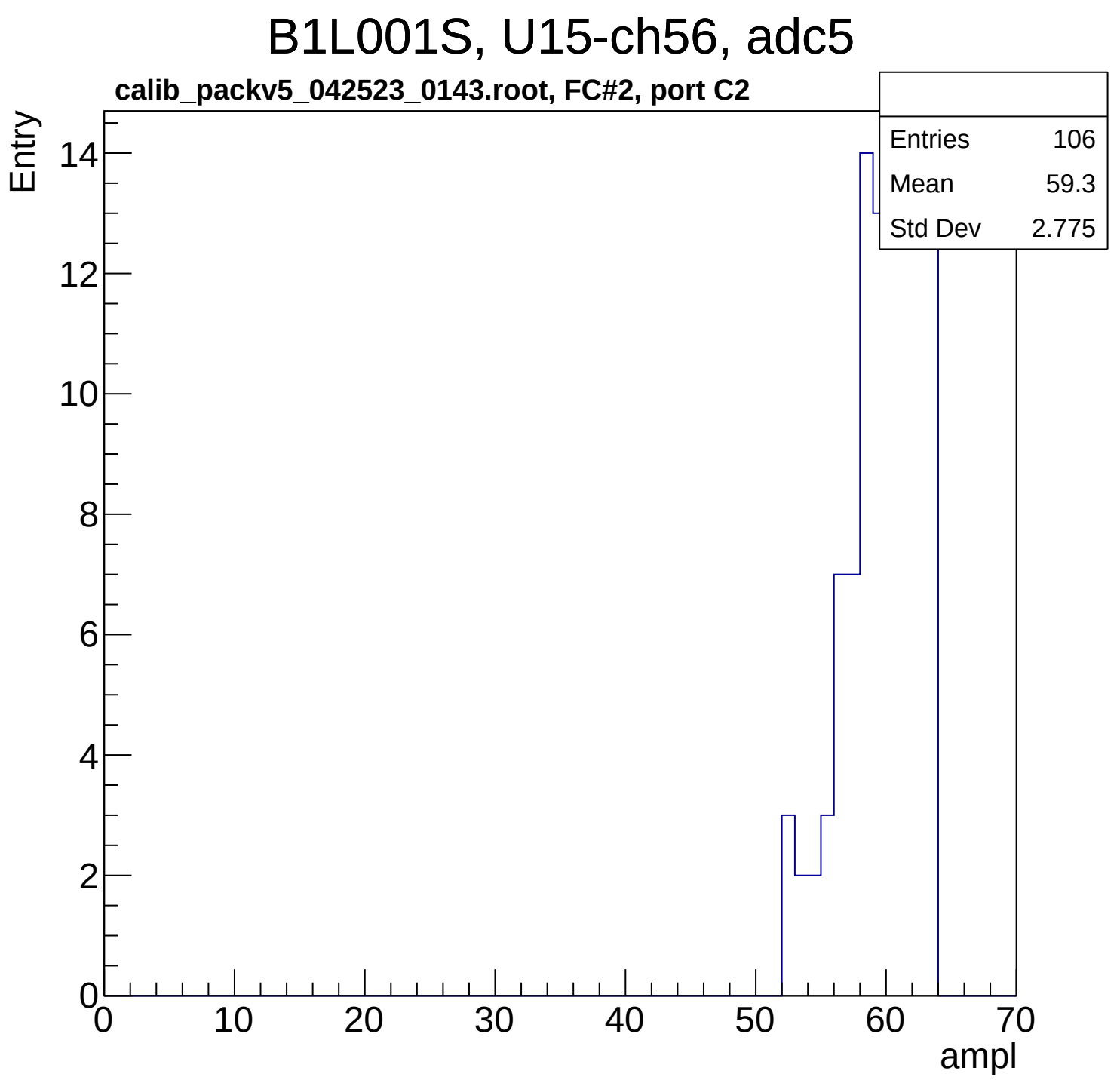
B1L001S, U15-ch56, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 59.3
Std Dev 2.775
Entry
ampl
0
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4
6
8
10
12
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0
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20
30
40
50
60
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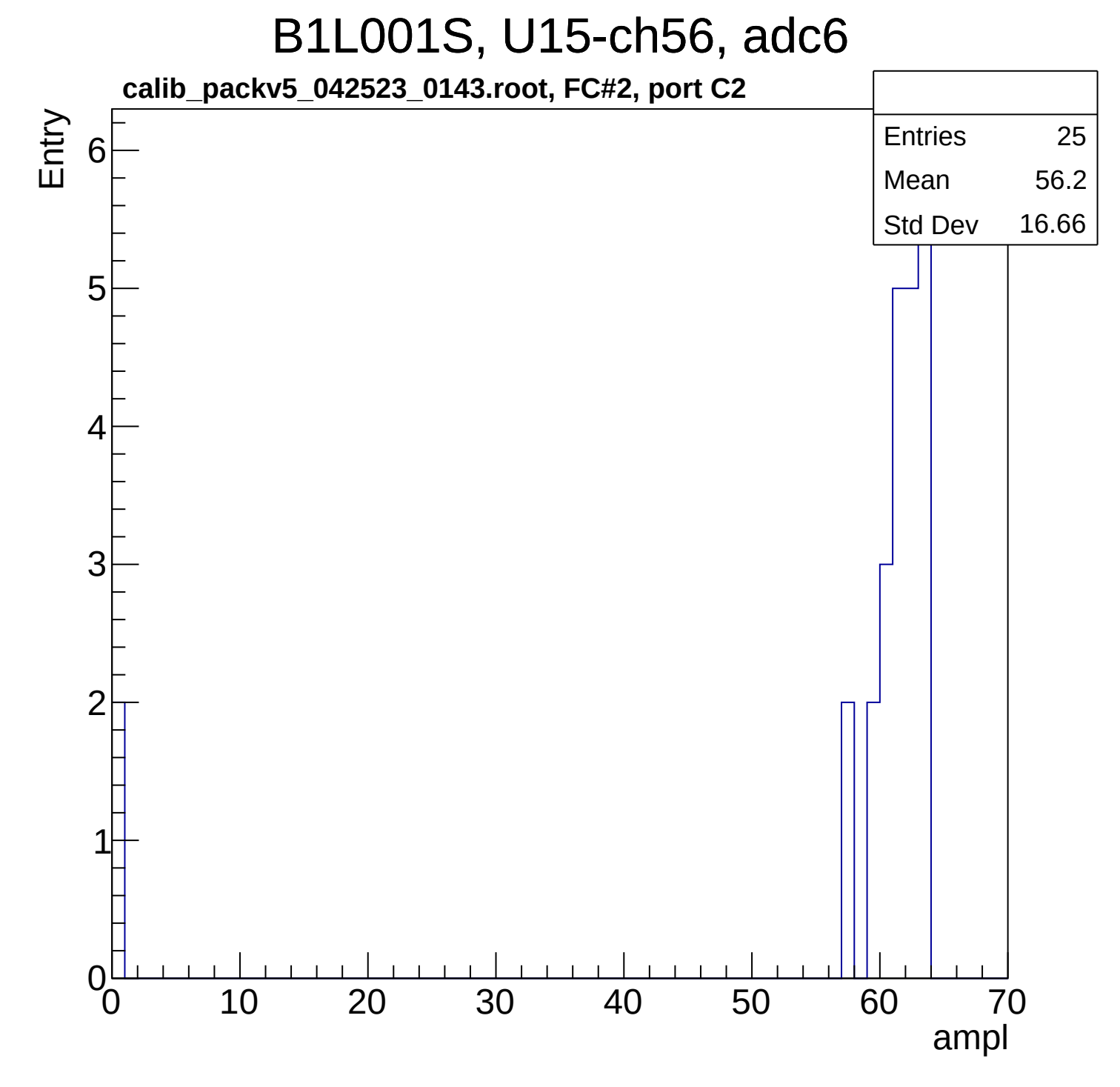
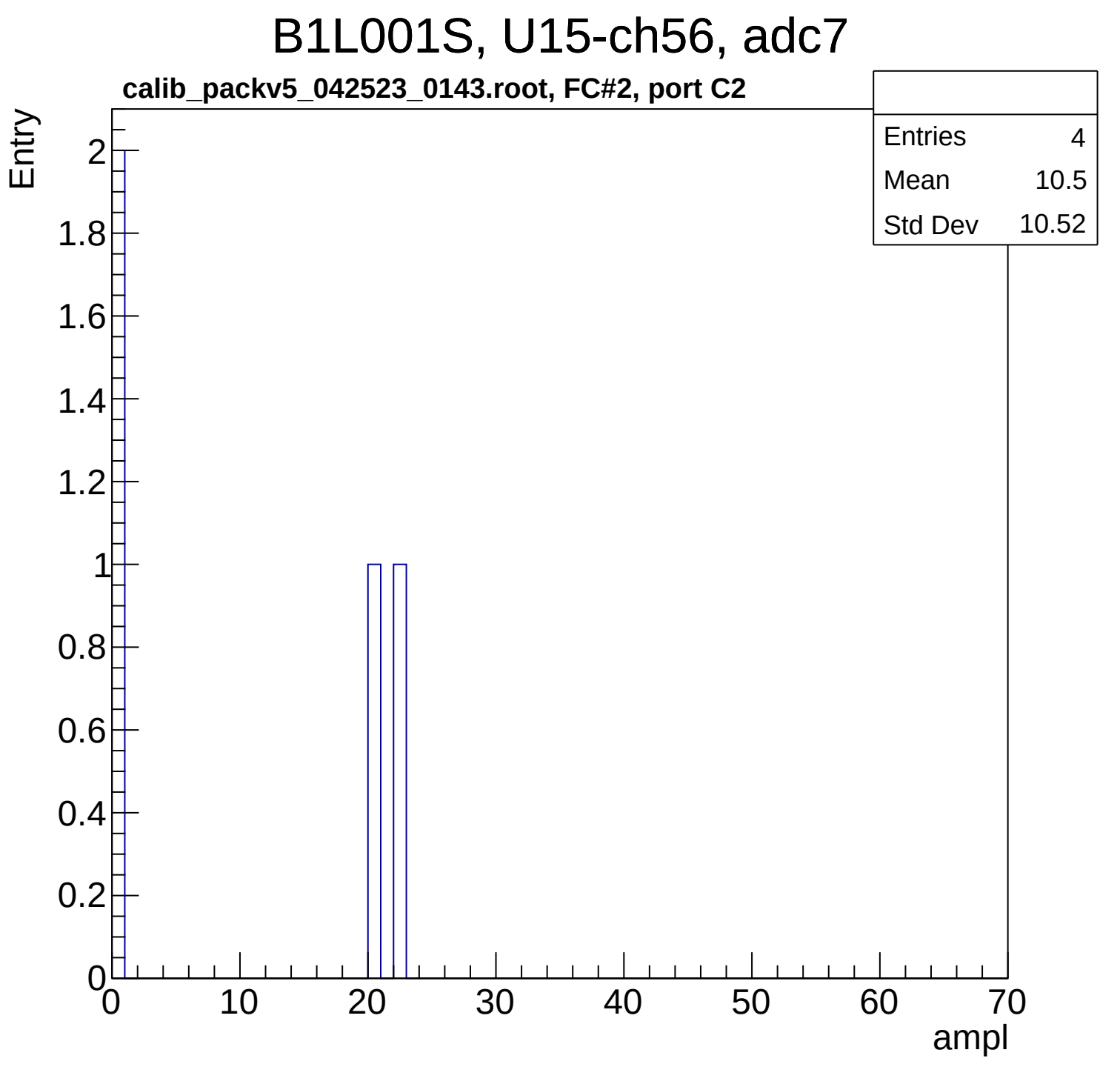

B1L001S, U15-ch56, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 25
Mean 56.2
Std Dev 16.66
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch56, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 10.5
Std Dev 10.52
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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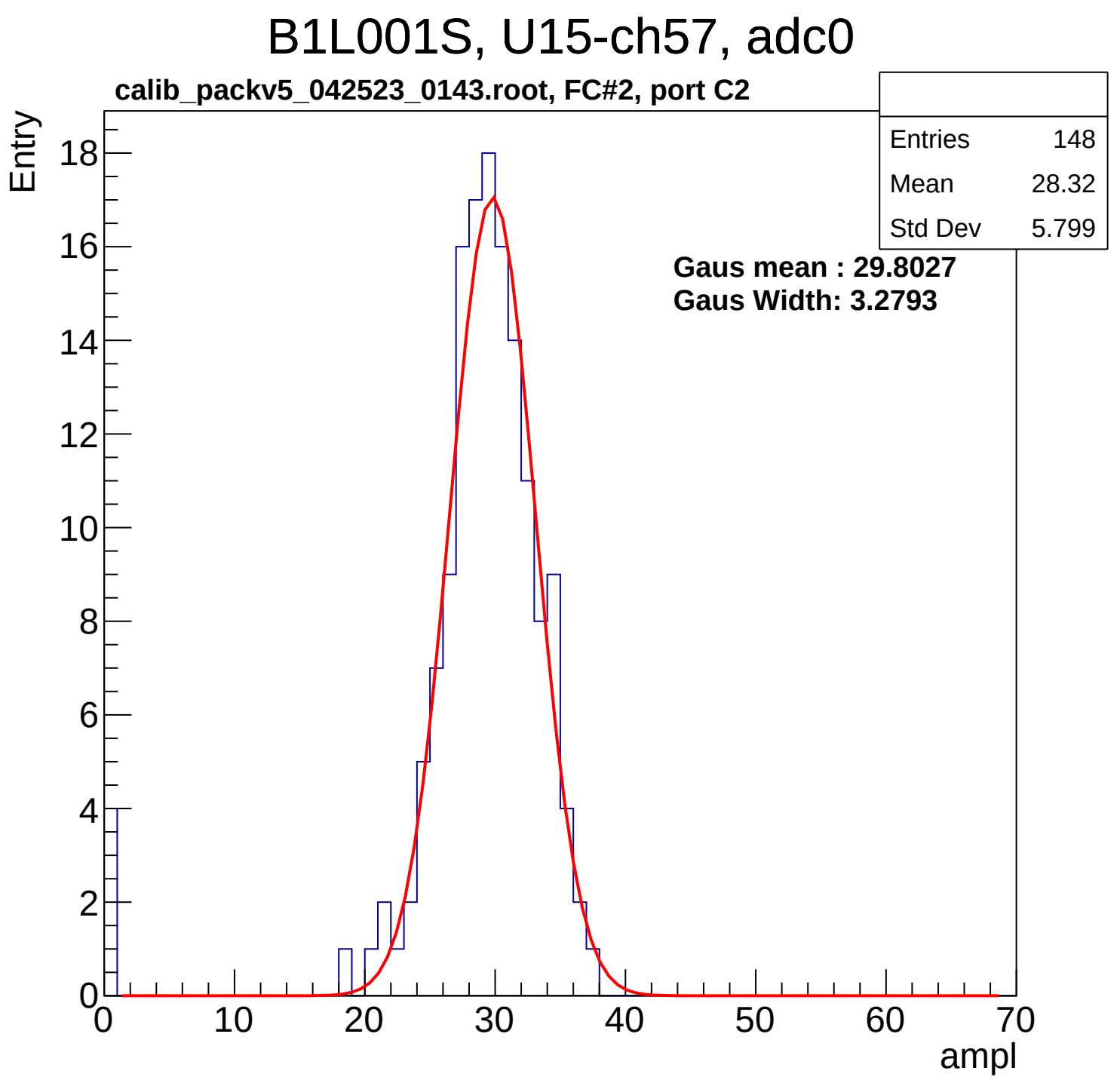
B1L001S, U15-ch57, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 28.32
Std Dev 5.799
Gaus mean : 29.8027
Gaus Width: 3.2793
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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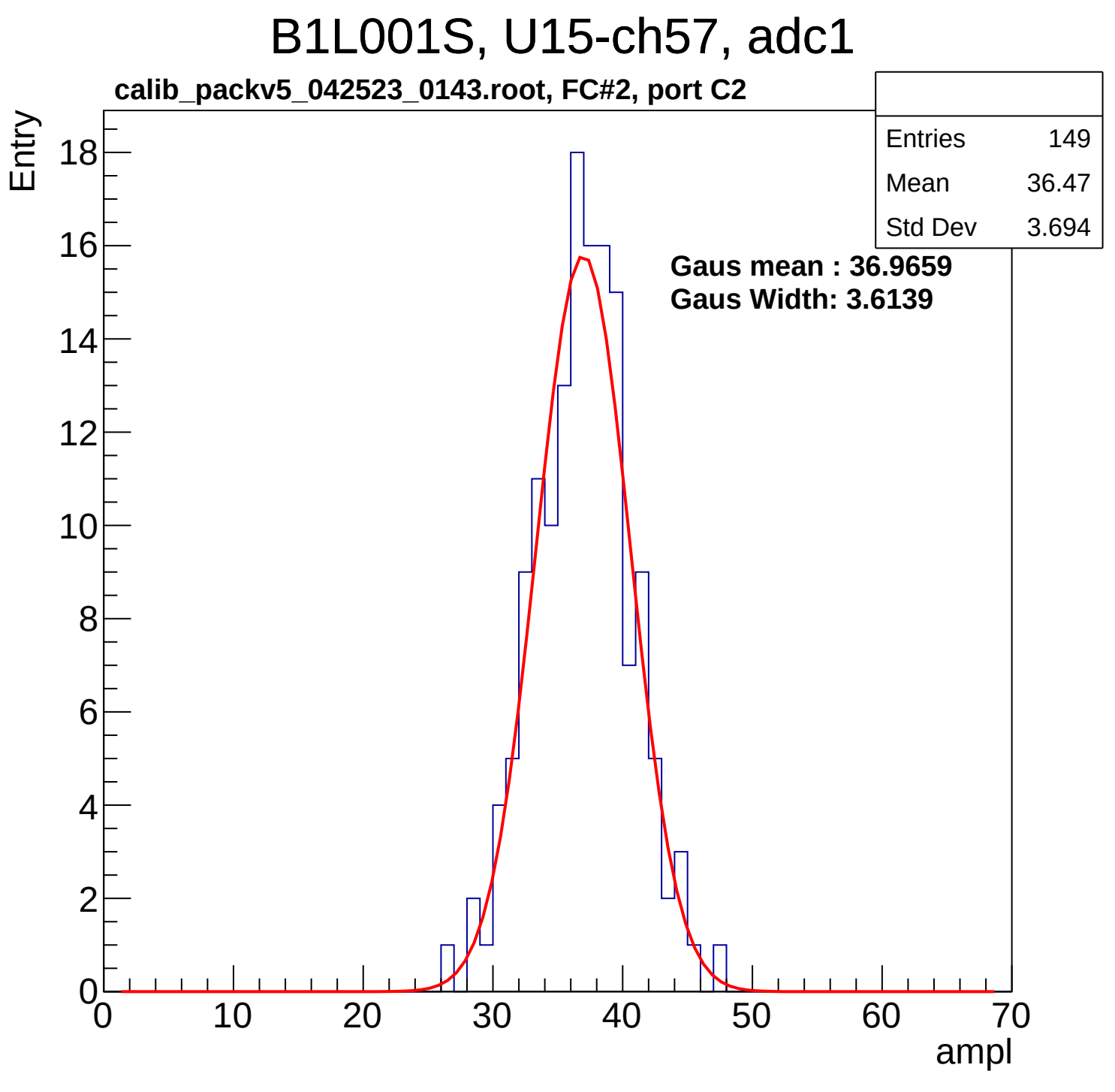
B1L001S, U15-ch57, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 36.47
Std Dev 3.694
Gaus mean : 36.9659
Gaus Width: 3.6139
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
50
60
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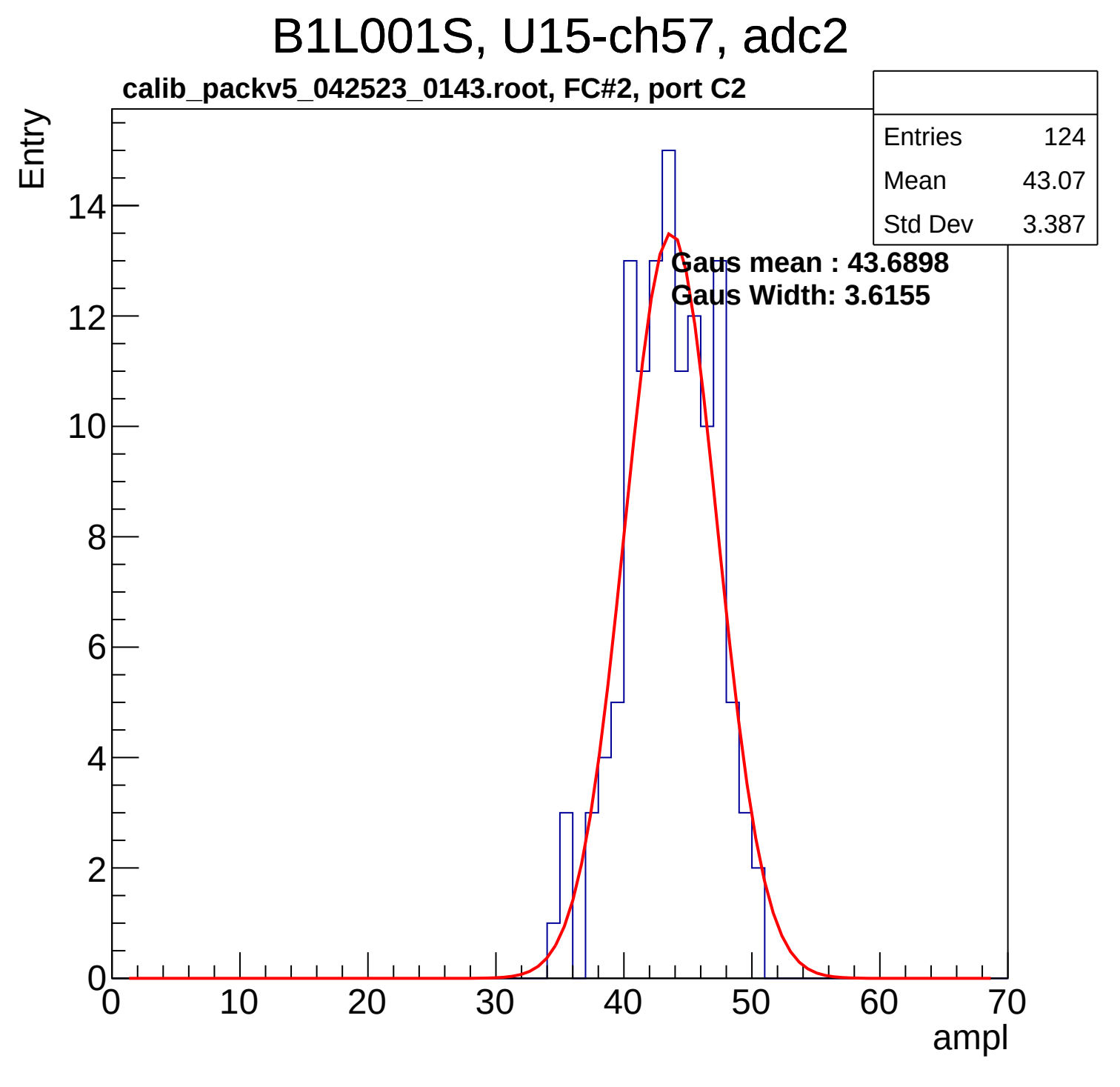
B1L001S, U15-ch57, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 43.07
Std Dev 3.387
Gaus mean : 43.6898
Gaus Width: 3.6155
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
50
60
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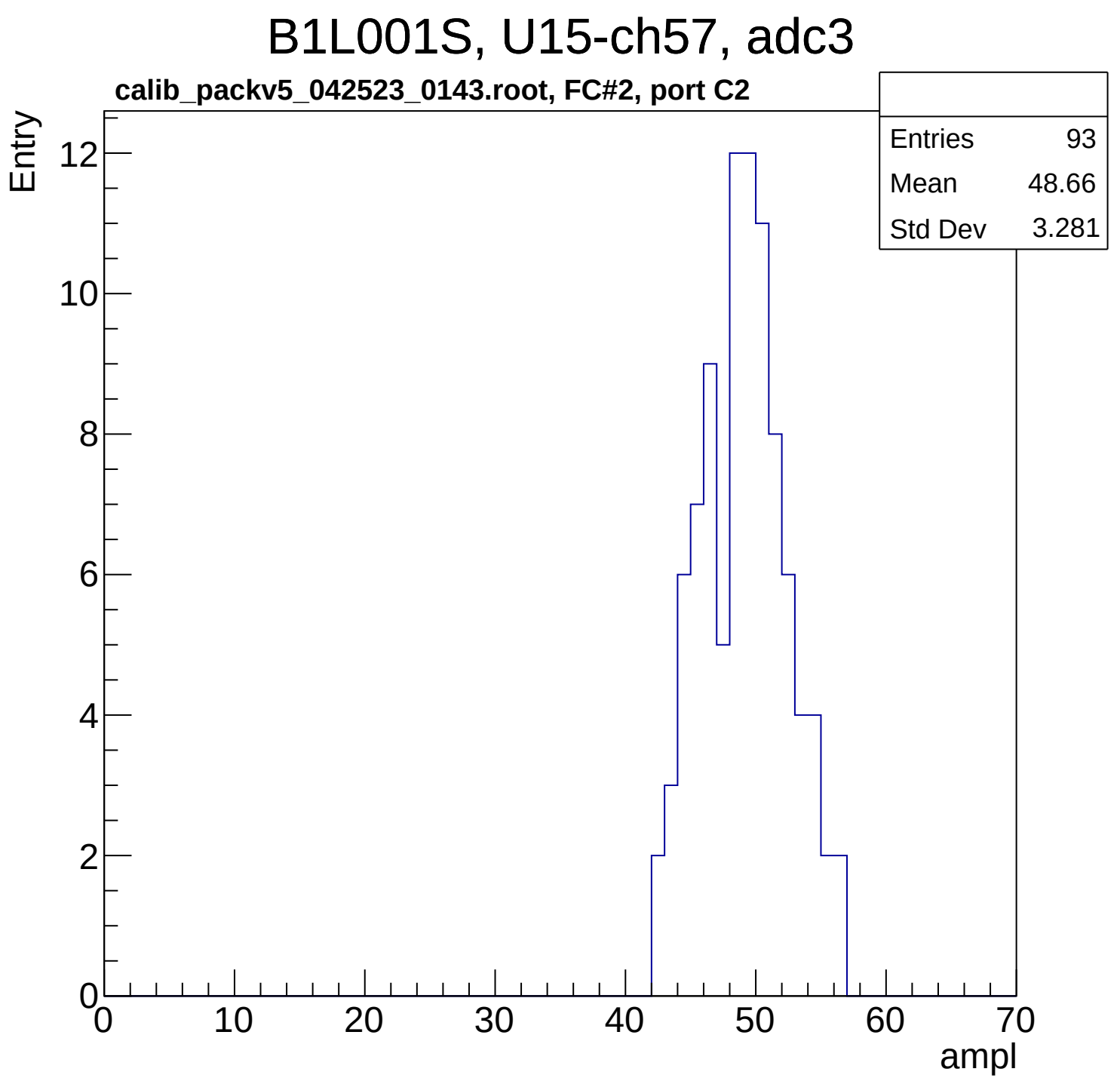
B1L001S, U15-ch57, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 48.66
Std Dev 3.281
Entry
0
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4
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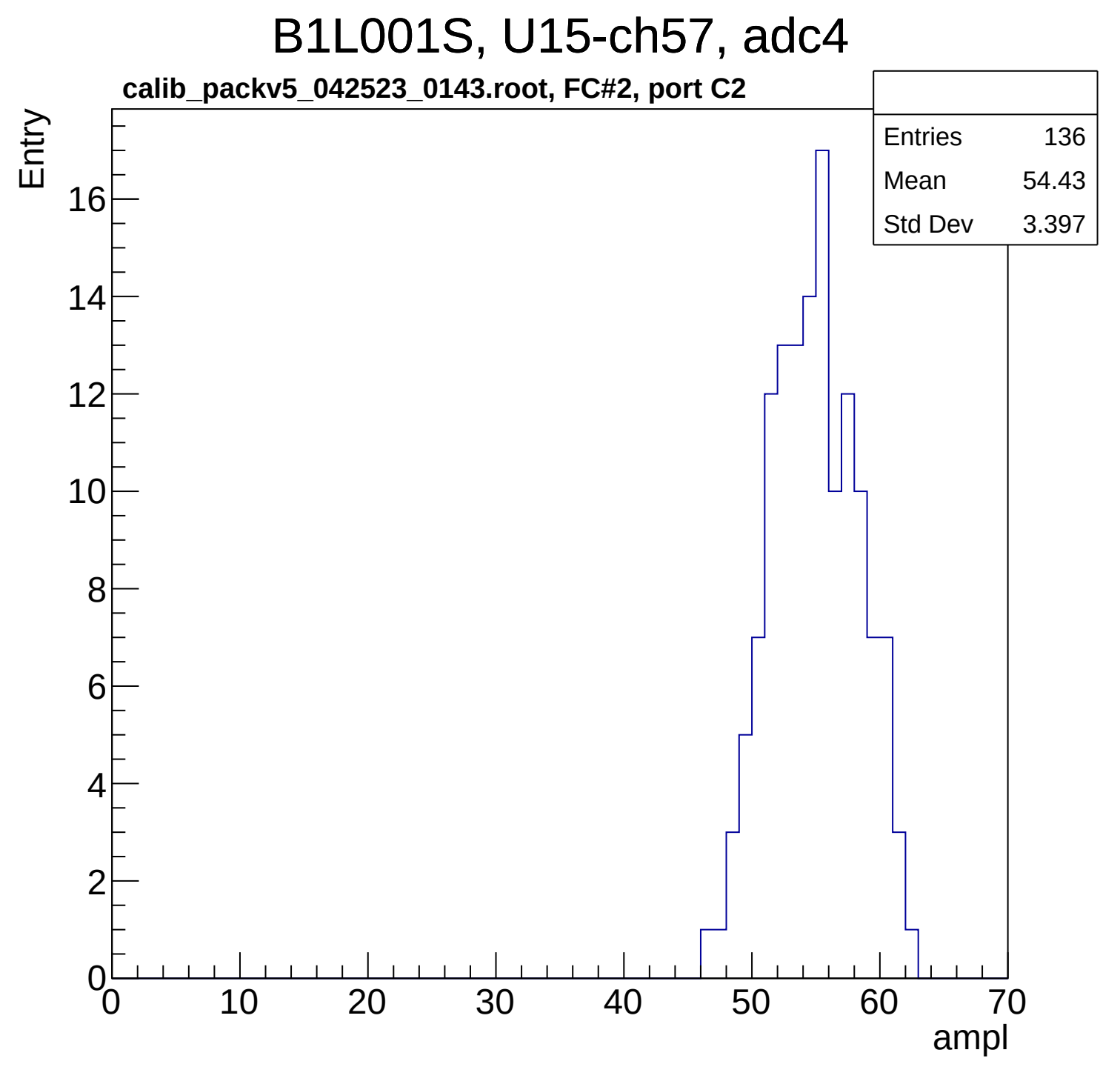
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch57, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 54.43
Std Dev 3.397
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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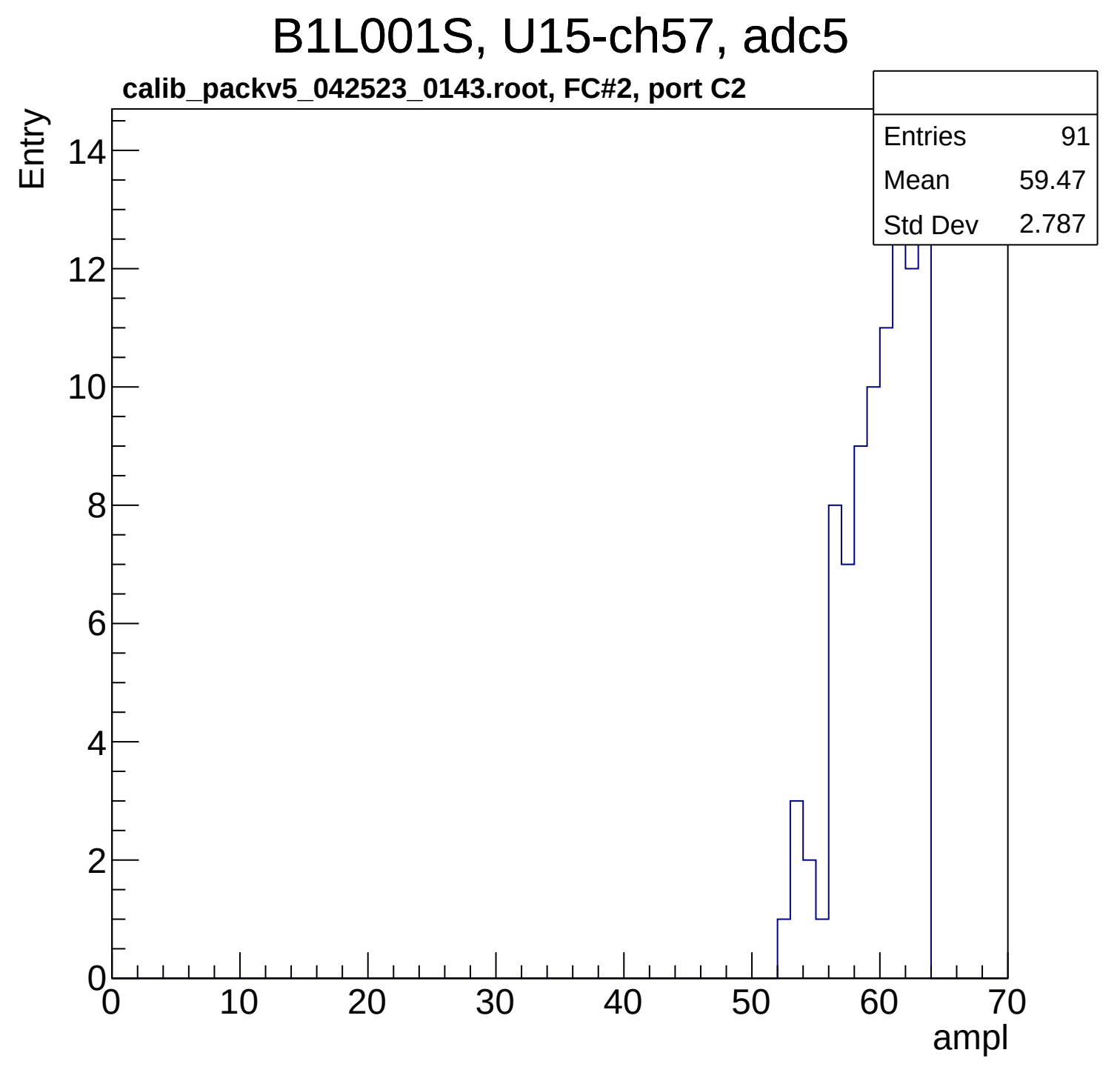
B1L001S, U15-ch57, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.47
Std Dev 2.787
Entry
ampl
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4
6
8
10
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14
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60
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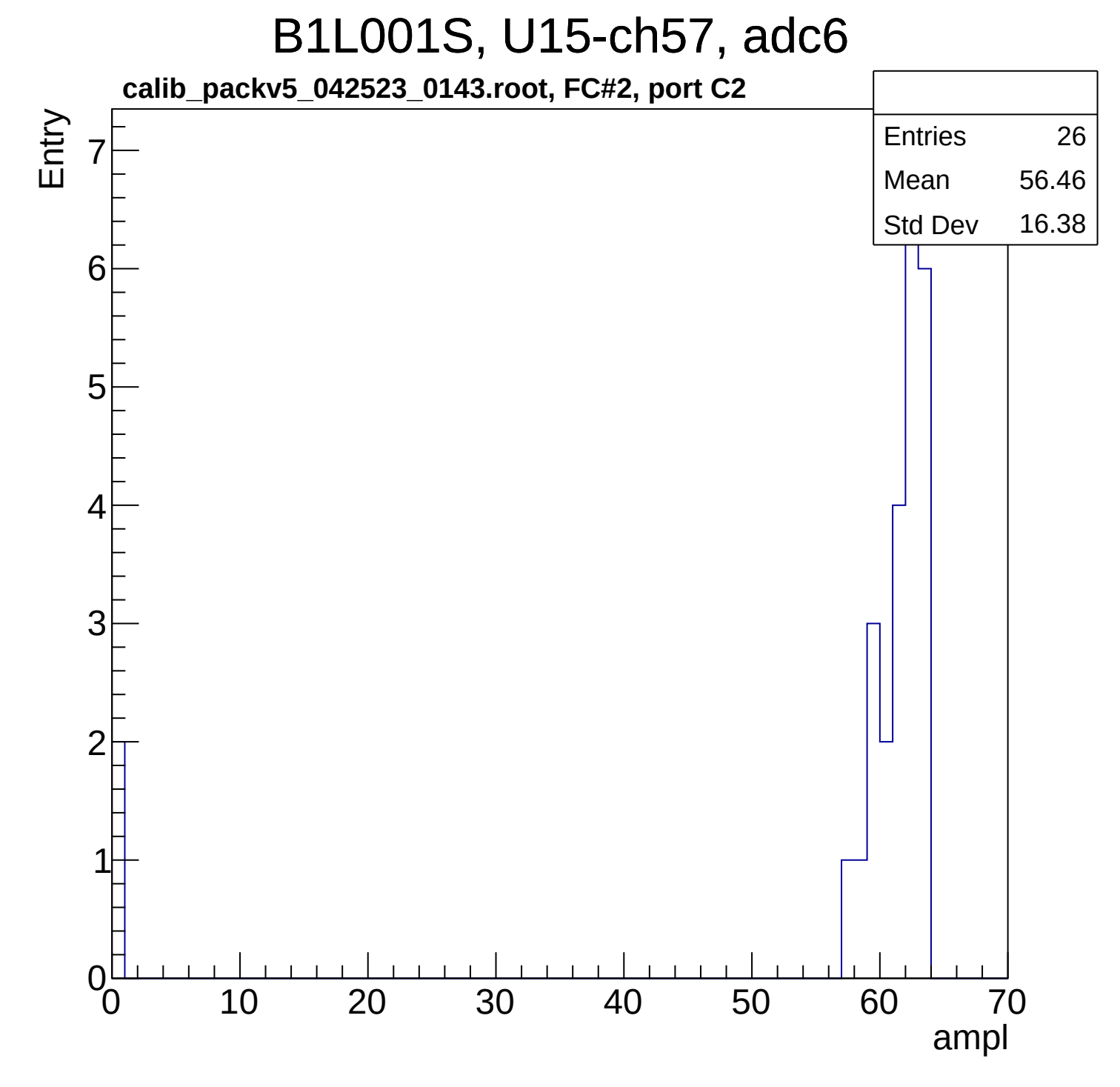
B1L001S, U15-ch57, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 56.46
Std Dev 16.38
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
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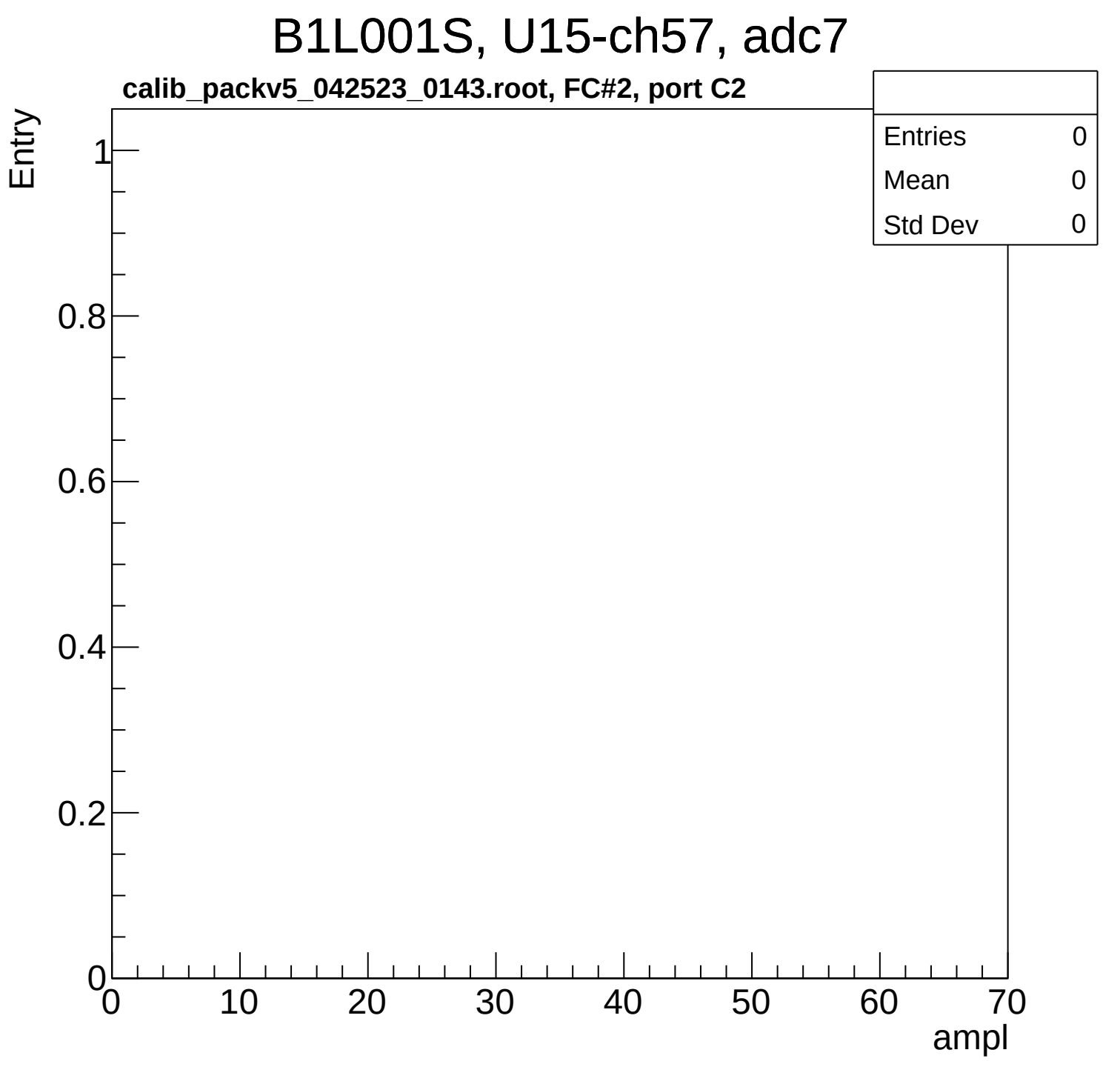
B1L001S, U15-ch57, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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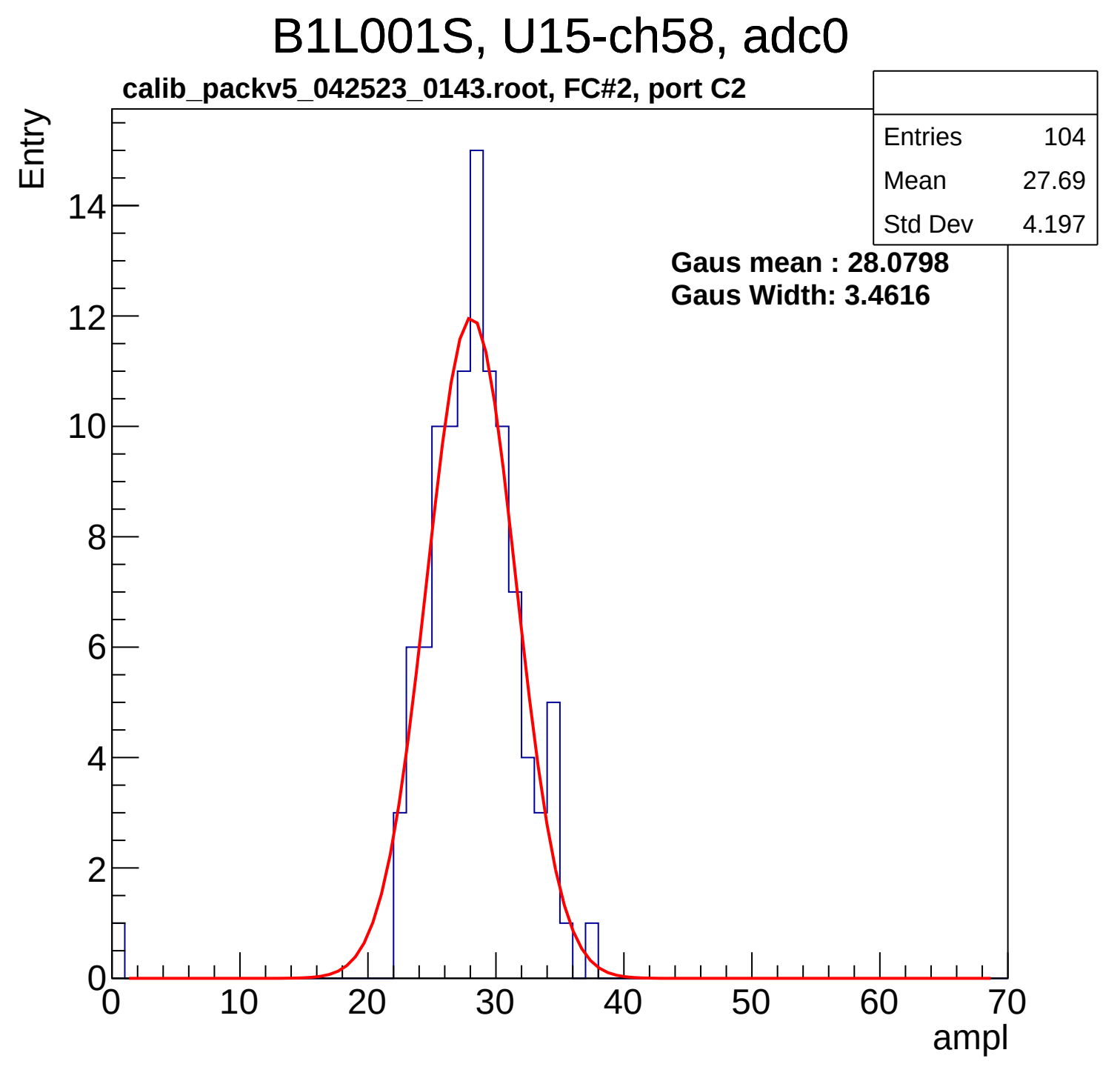
ampl


B1L001S, U15-ch58, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 27.69
Std Dev 4.197
Gaus mean : 28.0798
Gaus Width: 3.4616
Entry
ampl
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2
4
6
8
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0
10
20
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40
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60
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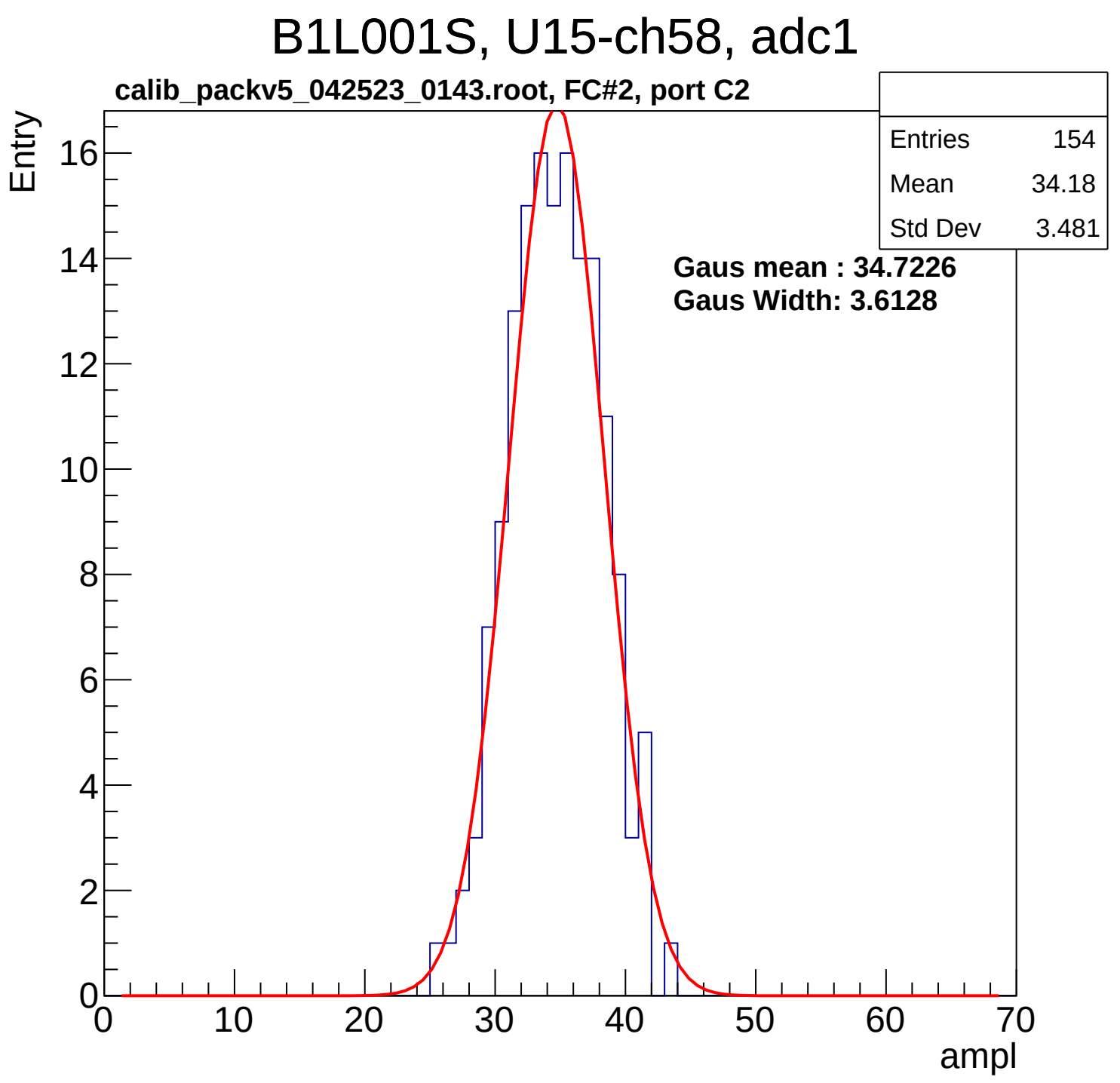
B1L001S, U15-ch58, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 34.18
Std Dev 3.481
Gaus mean : 34.7226
Gaus Width: 3.6128
Entry
ampl
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4
6
8
10
12
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16
0
10
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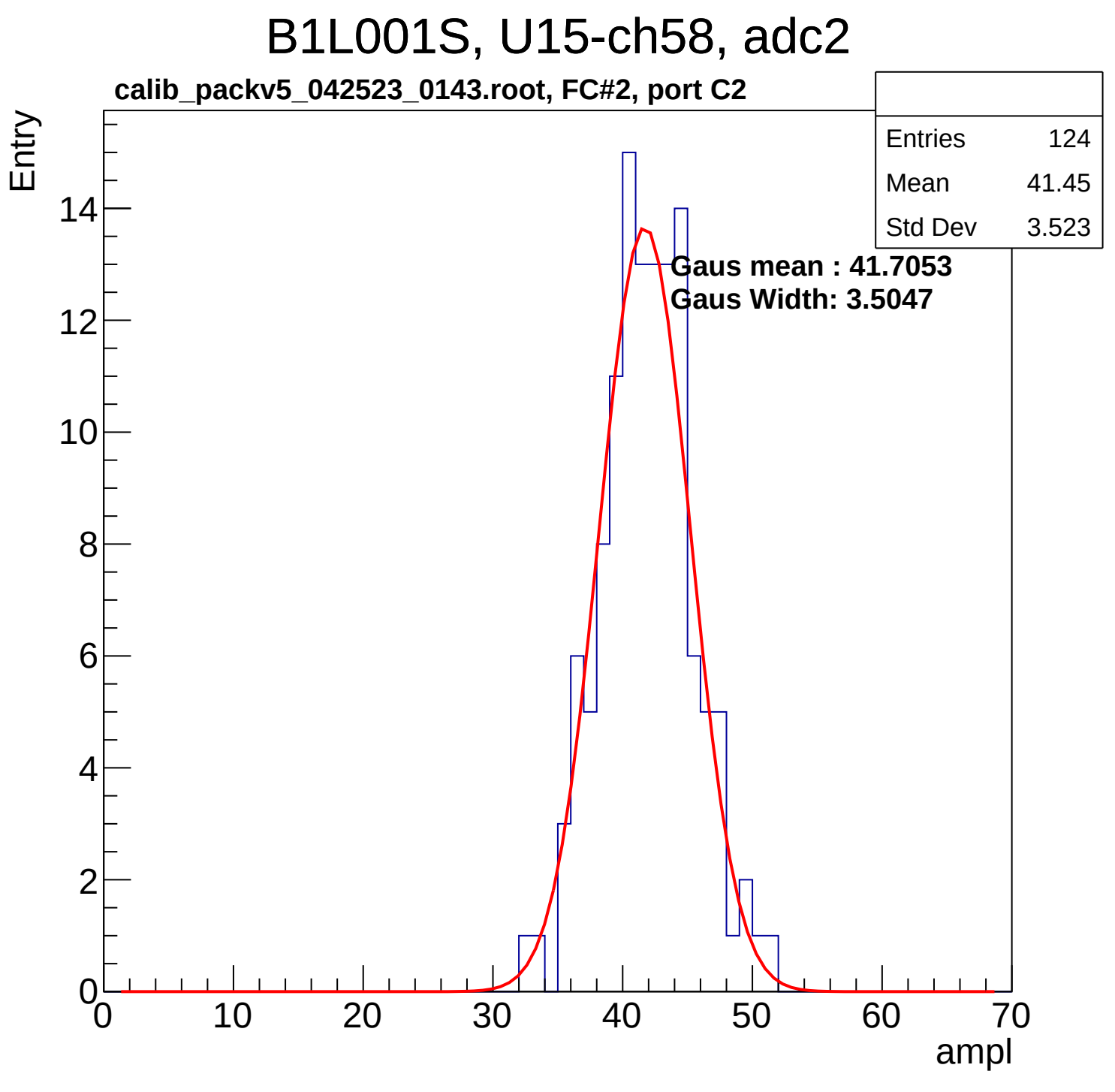
B1L001S, U15-ch58, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 41.45
Std Dev 3.523
Gaus mean : 41.7053
Gaus Width: 3.5047
Entry
ampl
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6
8
10
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0
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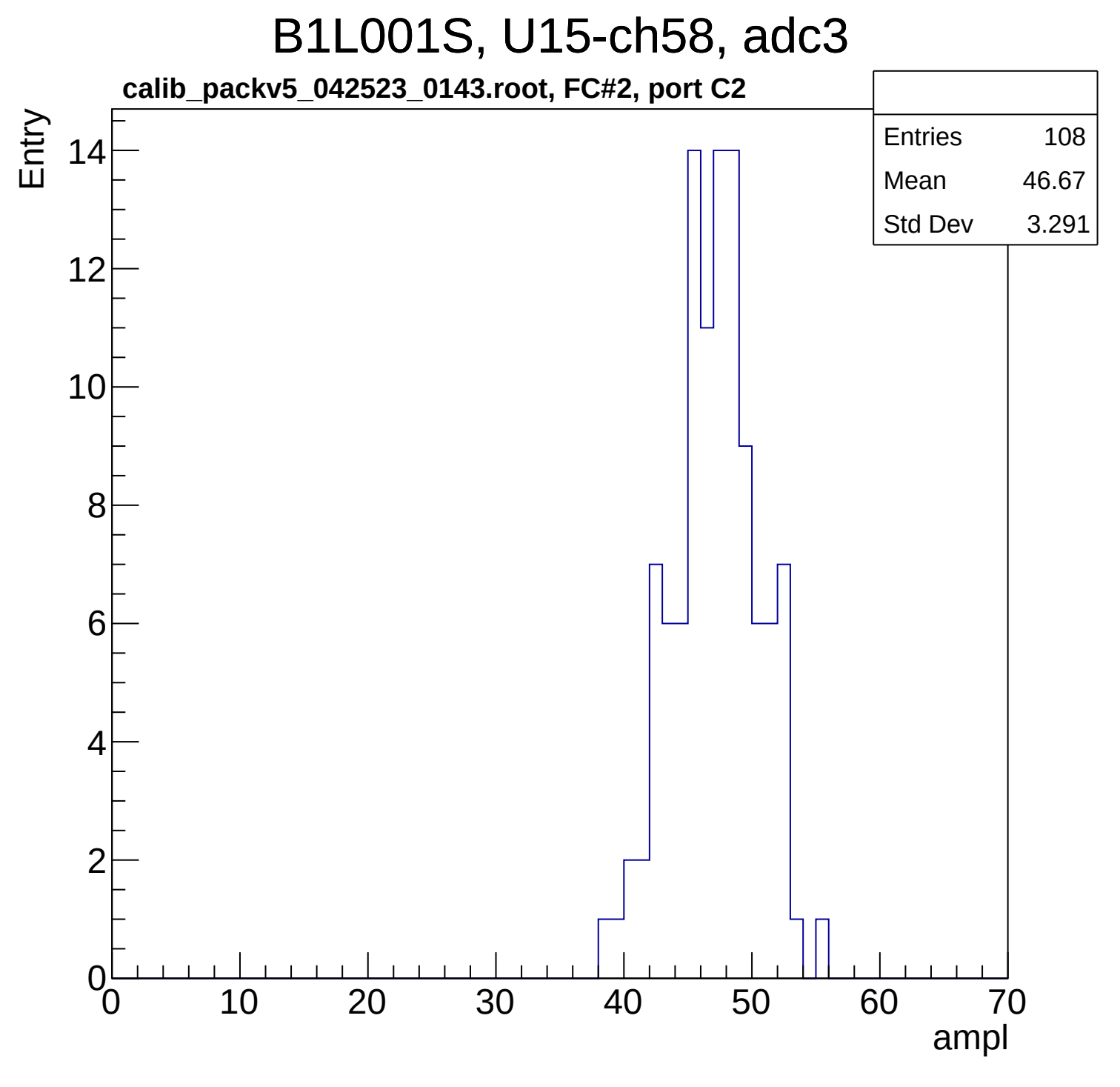
B1L001S, U15-ch58, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 46.67
Std Dev 3.291
Entry
ampl
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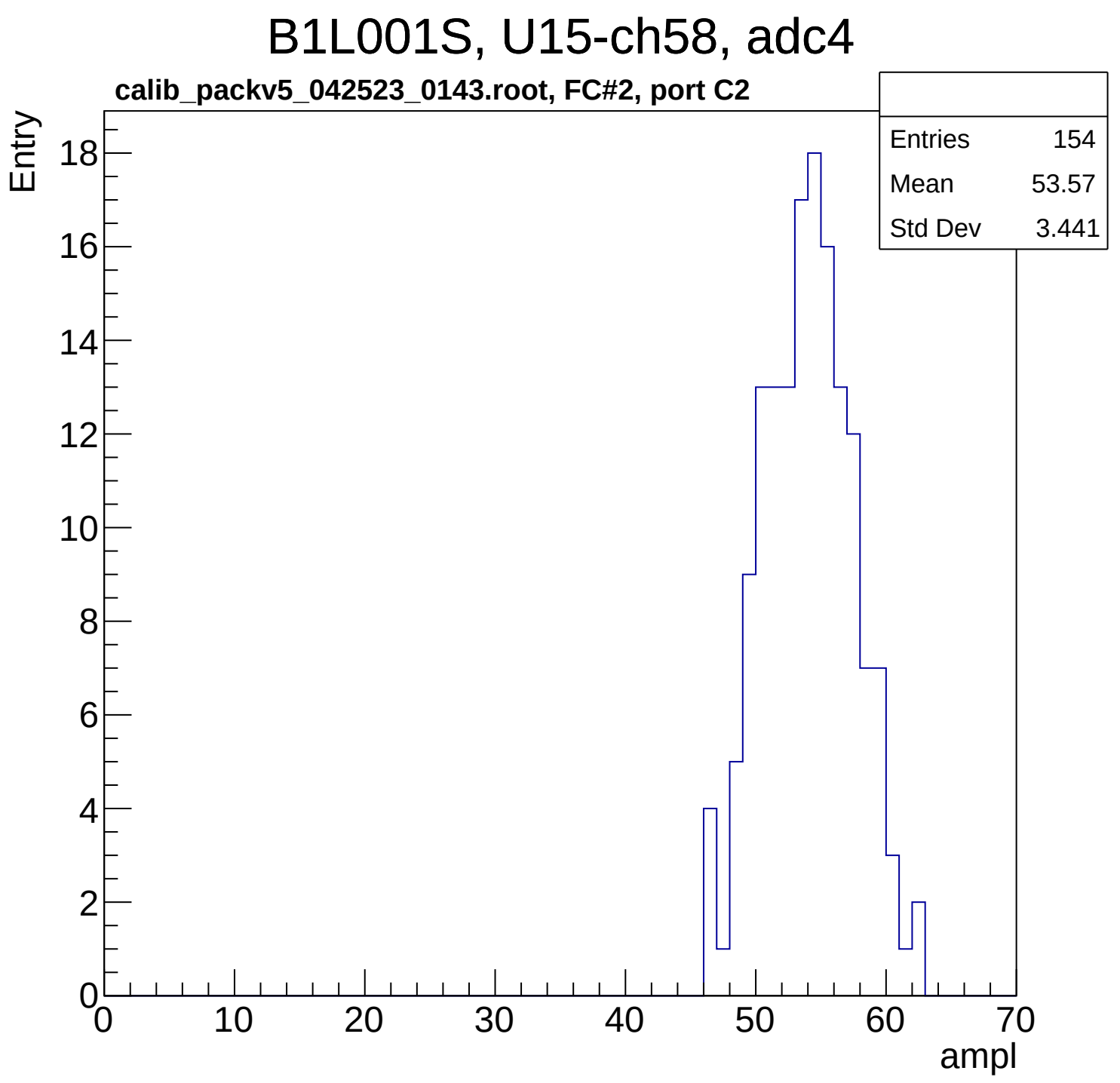
B1L001S, U15-ch58, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 53.57
Std Dev 3.441
Entry
ampl
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6
8
10
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14
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0
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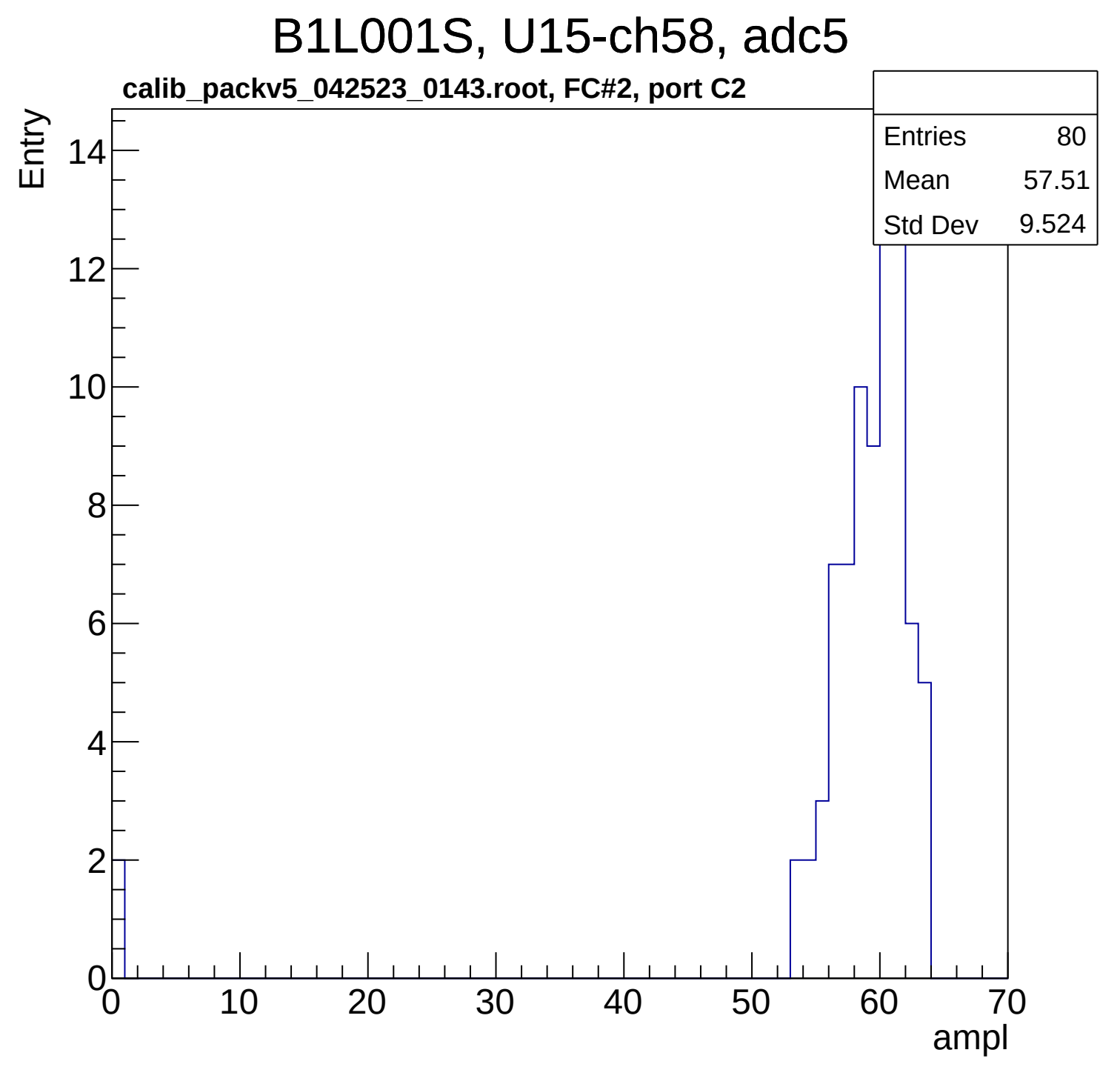
B1L001S, U15-ch58, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 57.51
Std Dev 9.524
Entry
ampl
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2
4
6
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0
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40
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60
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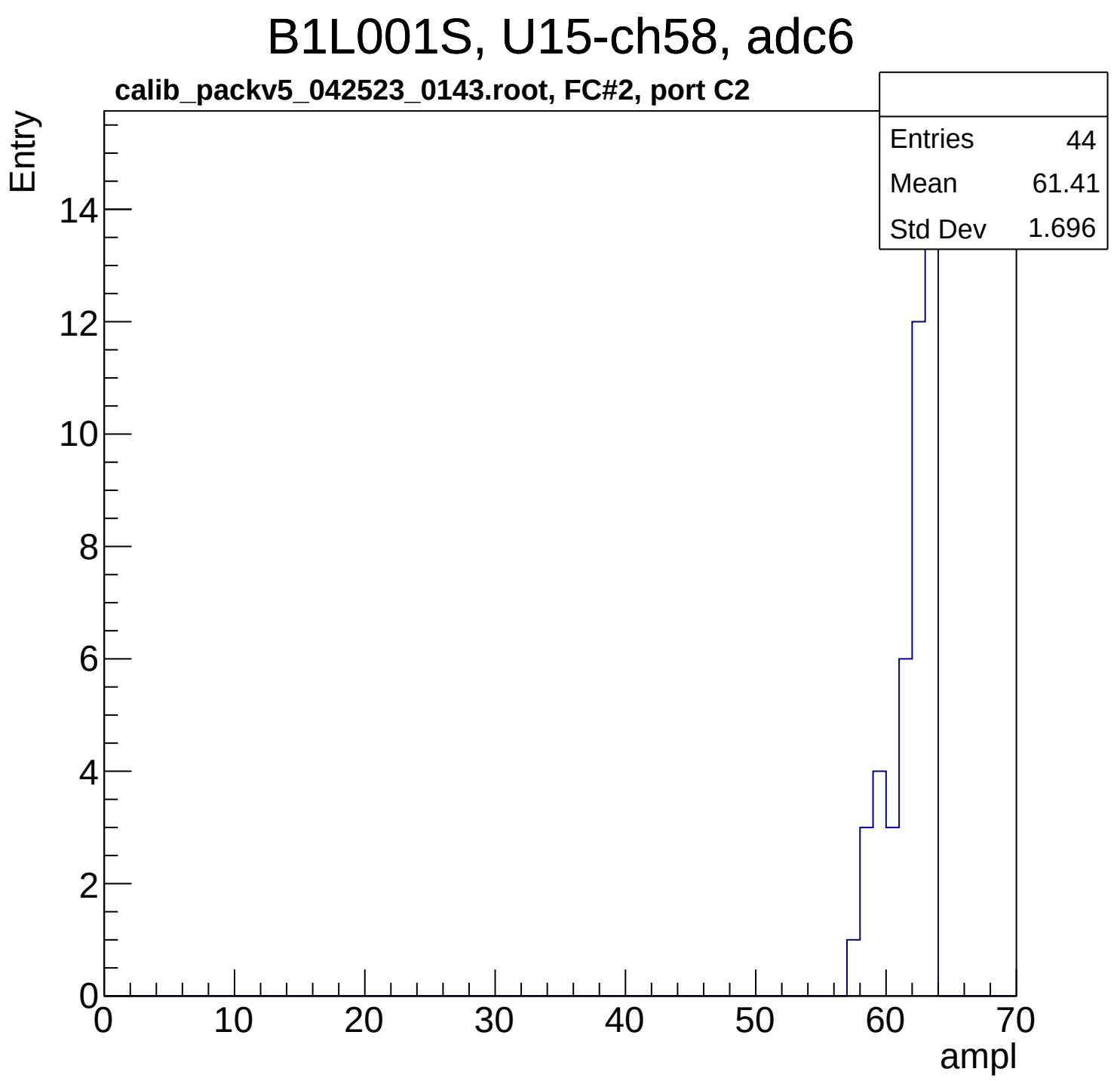
B1L001S, U15-ch58, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 44
Mean 61.41
Std Dev 1.696
Entry
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4
6
8
10
12
14
0
10
20
30
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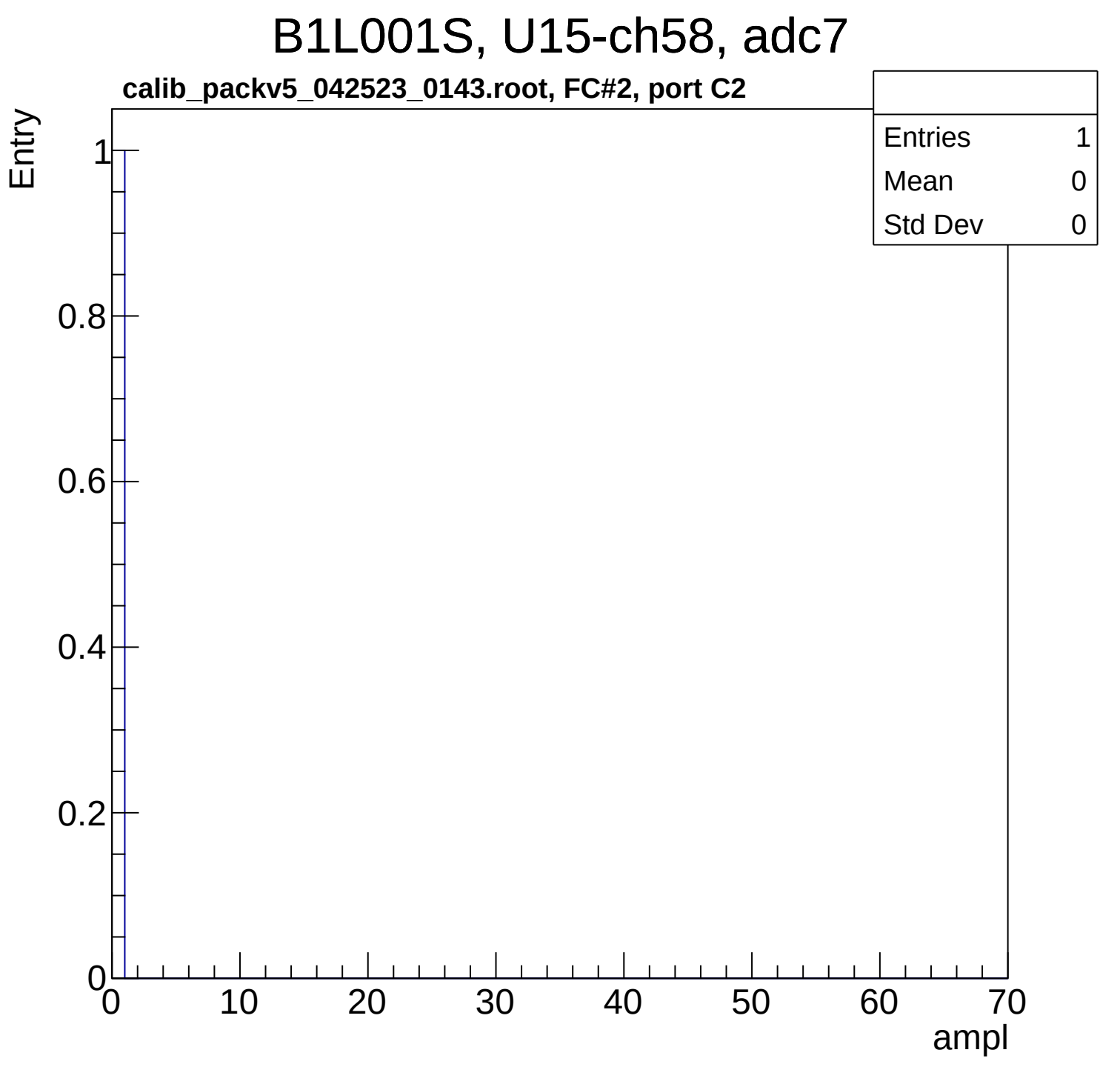
50
60
70
ampl


B1L001S, U15-ch58, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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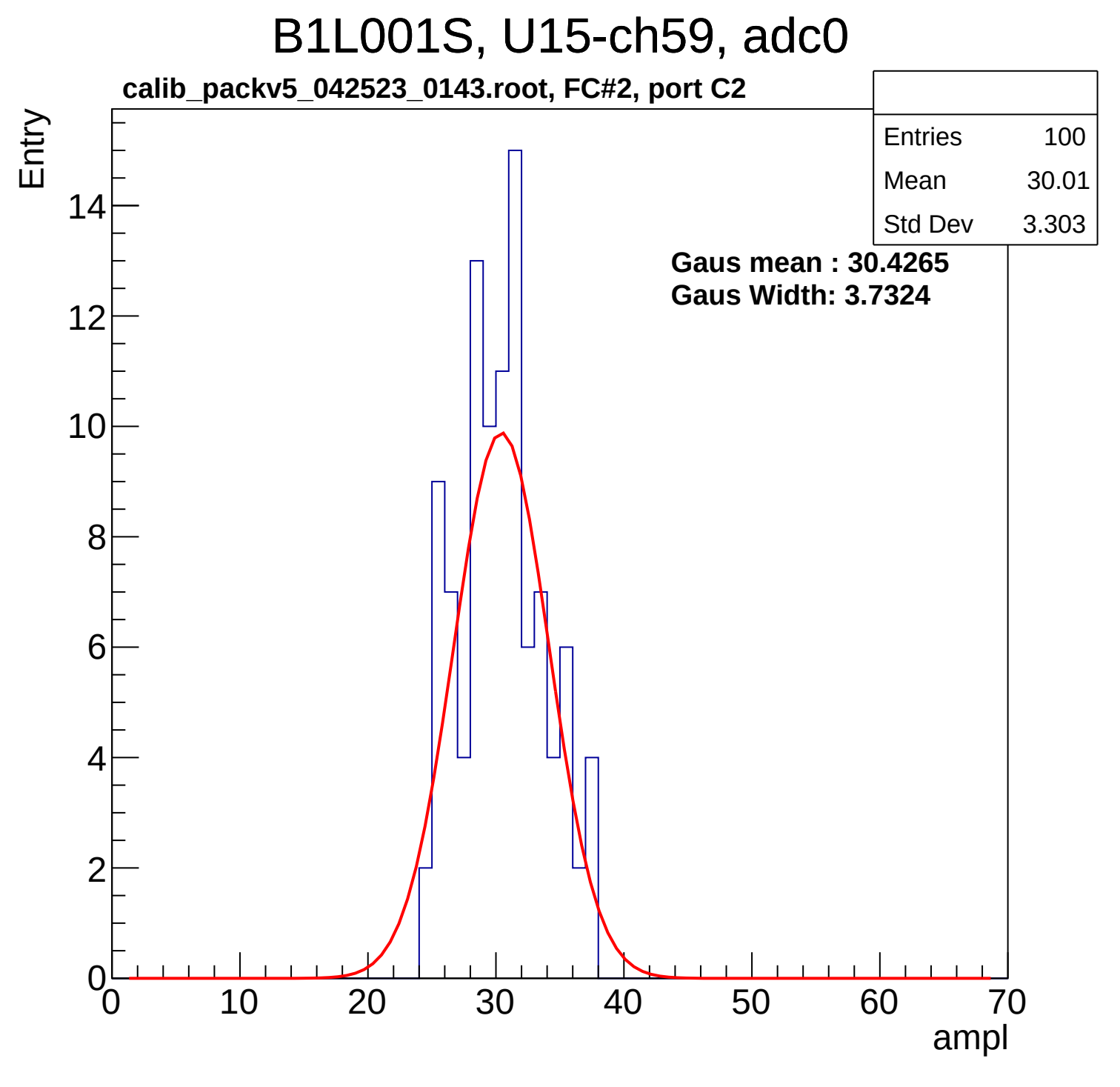
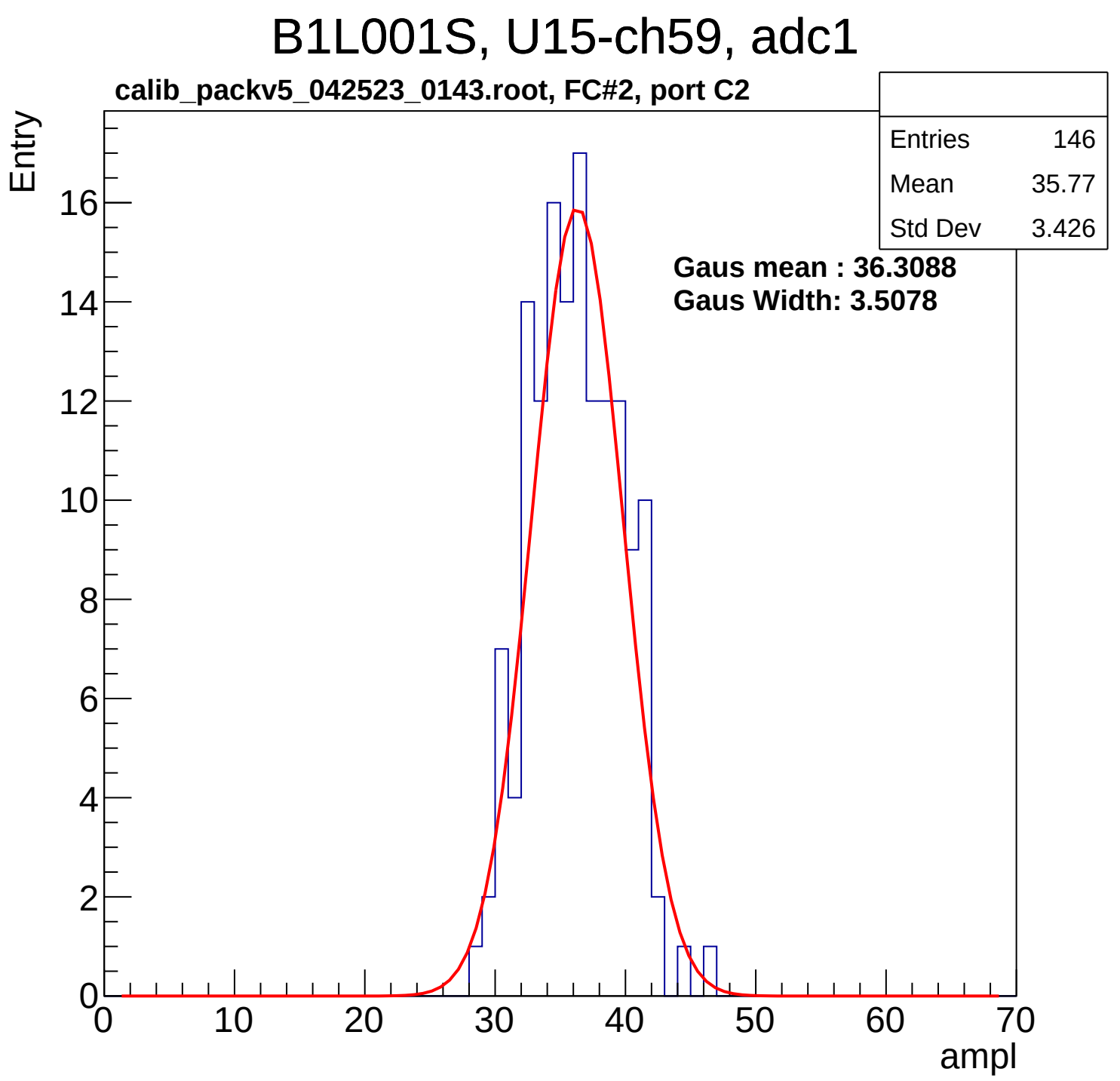
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch59, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 30.01
Std Dev 3.303
Gaus mean : 30.4265
Gaus Width: 3.7324
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch59, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 35.77
Std Dev 3.426
Gaus mean : 36.3088
Gaus Width: 3.5078
Entry
ampl
0
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4
6
8
10
12
14
16
0
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20
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40
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60
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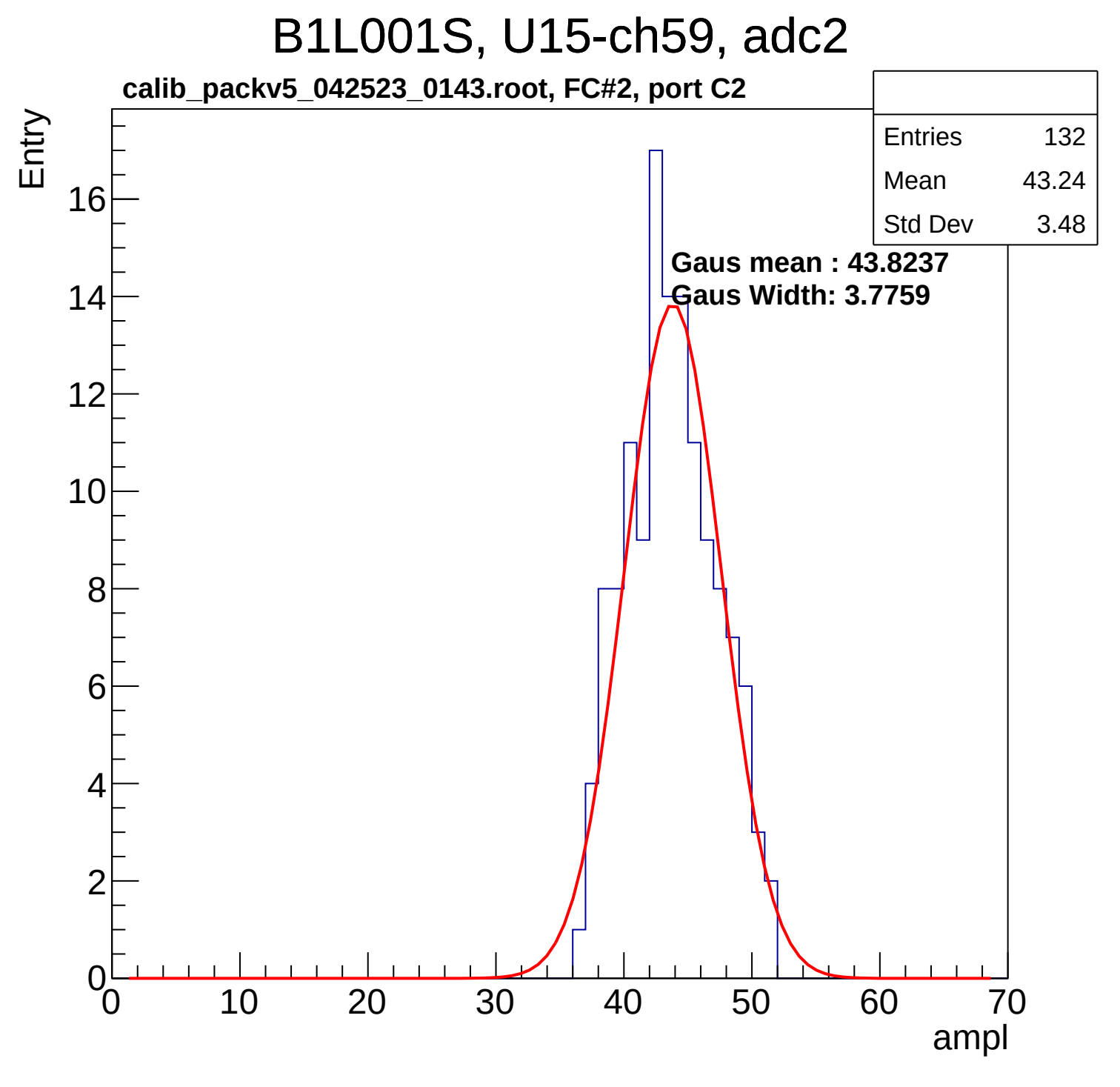
B1L001S, U15-ch59, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 43.24
Std Dev 3.48
Gaus mean : 43.8237
Gaus Width: 3.7759
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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40
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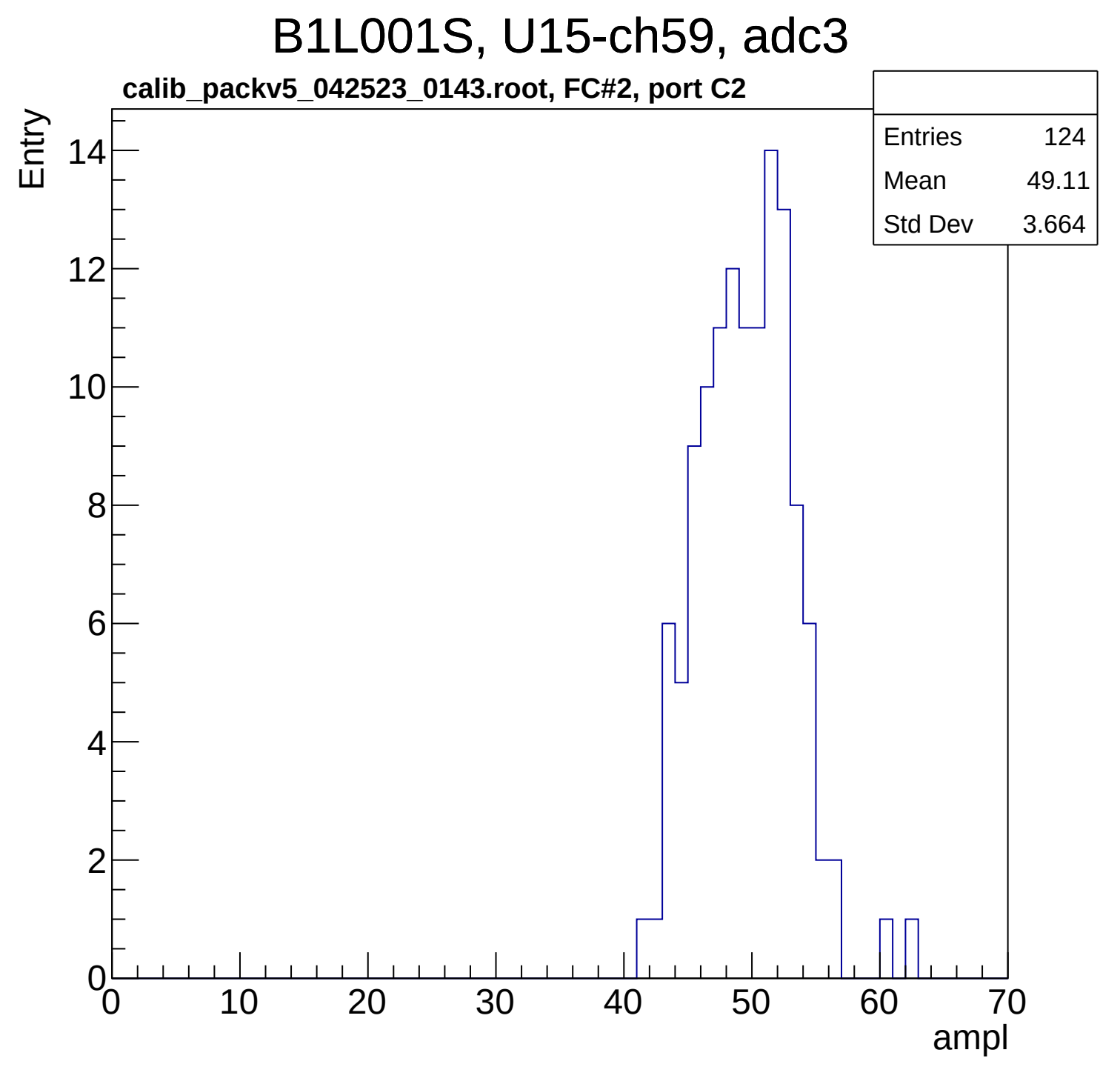
B1L001S, U15-ch59, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 49.11
Std Dev 3.664
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
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60
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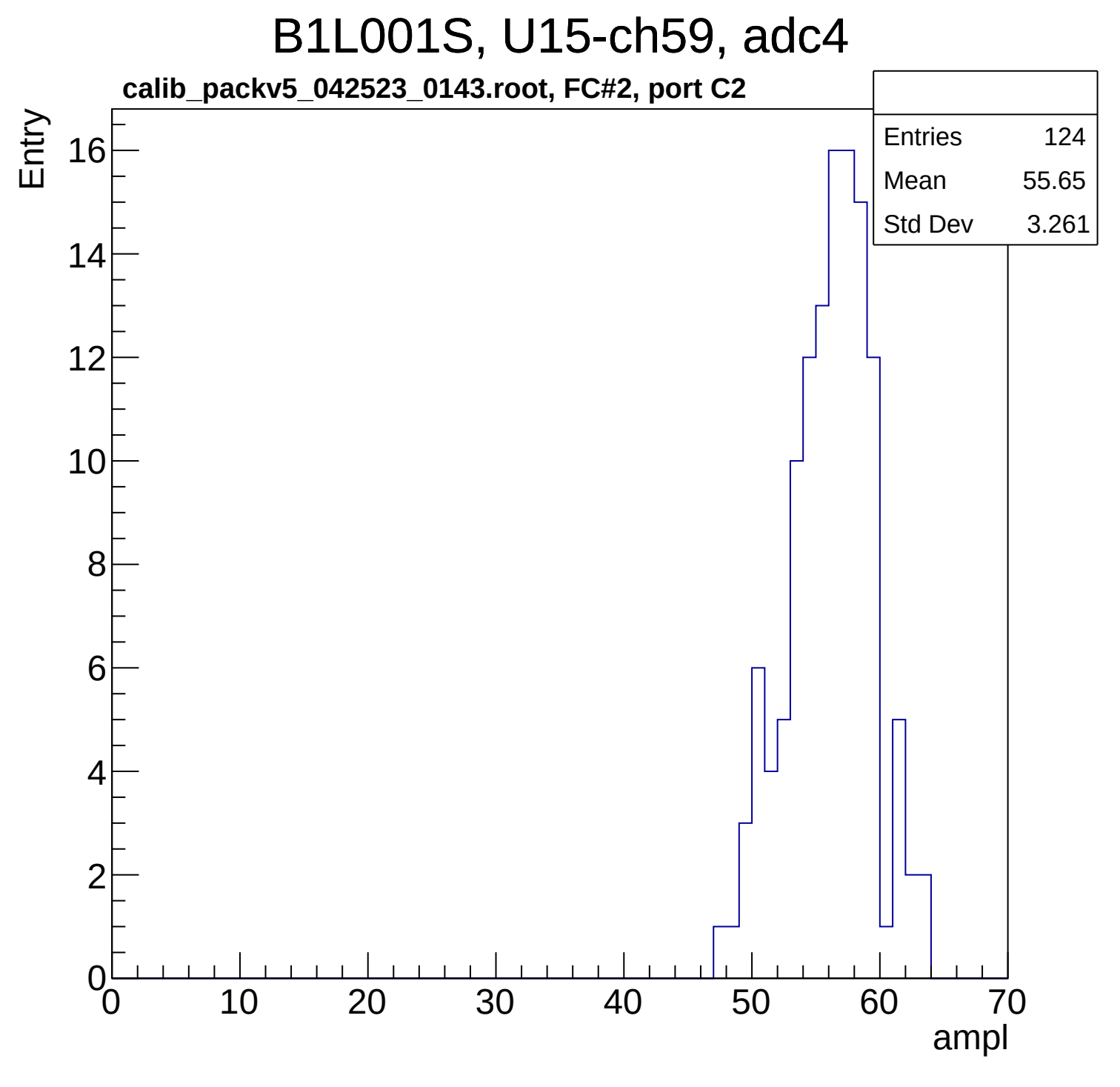
B1L001S, U15-ch59, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.65
Std Dev 3.261
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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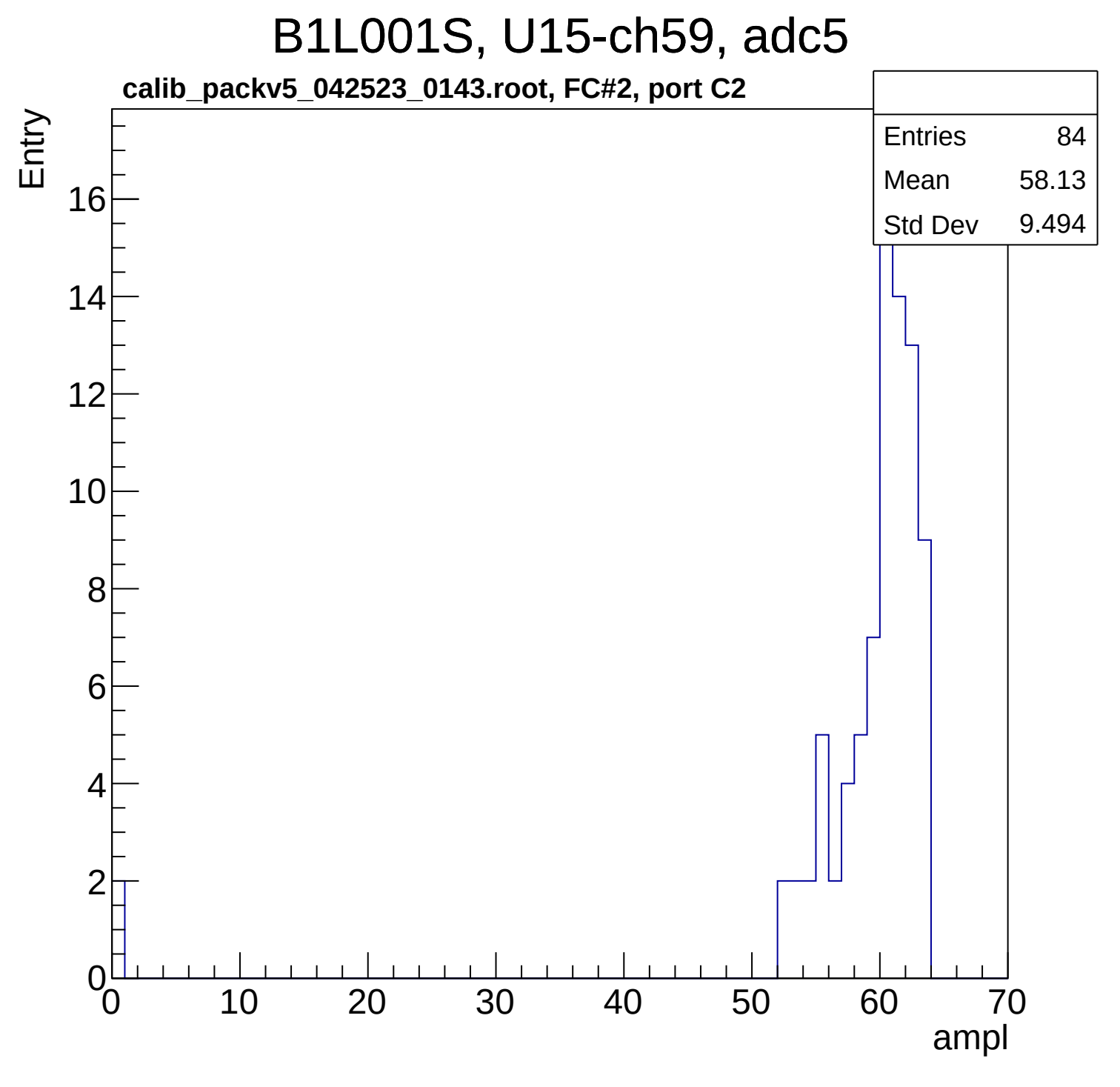
B1L001S, U15-ch59, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 58.13
Std Dev 9.494
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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60
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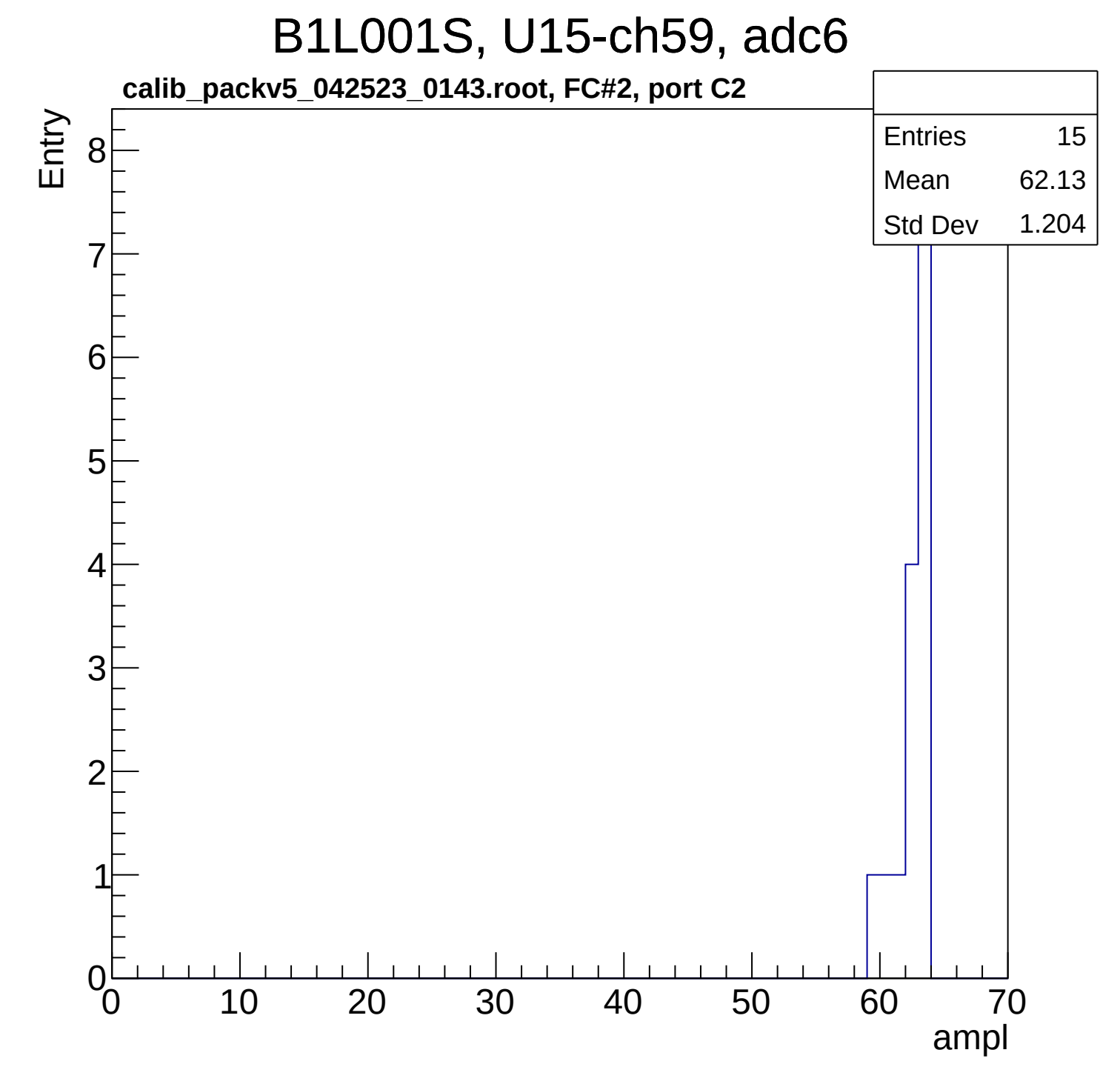
B1L001S, U15-ch59, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 62.13
Std Dev 1.204
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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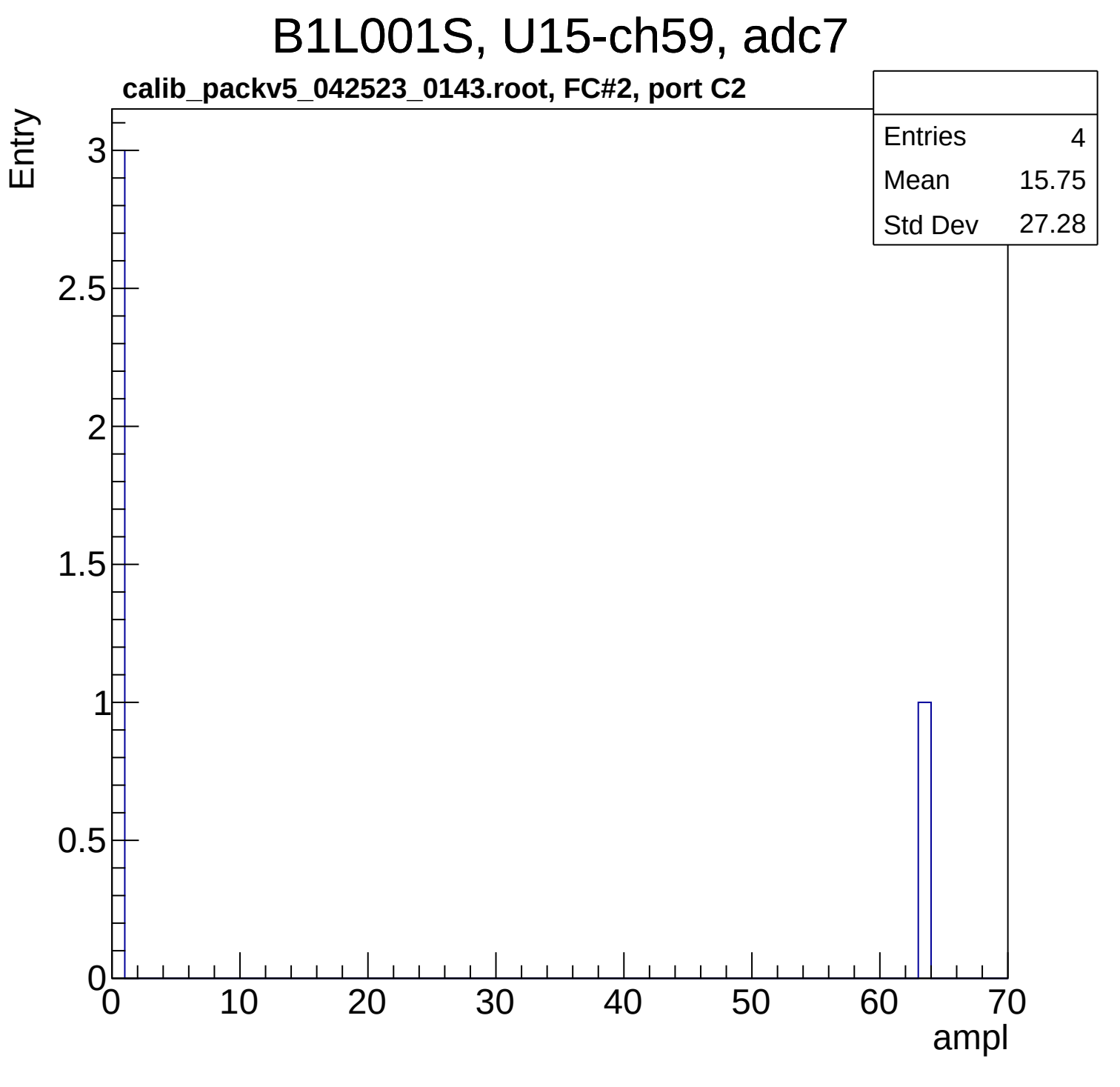
B1L001S, U15-ch59, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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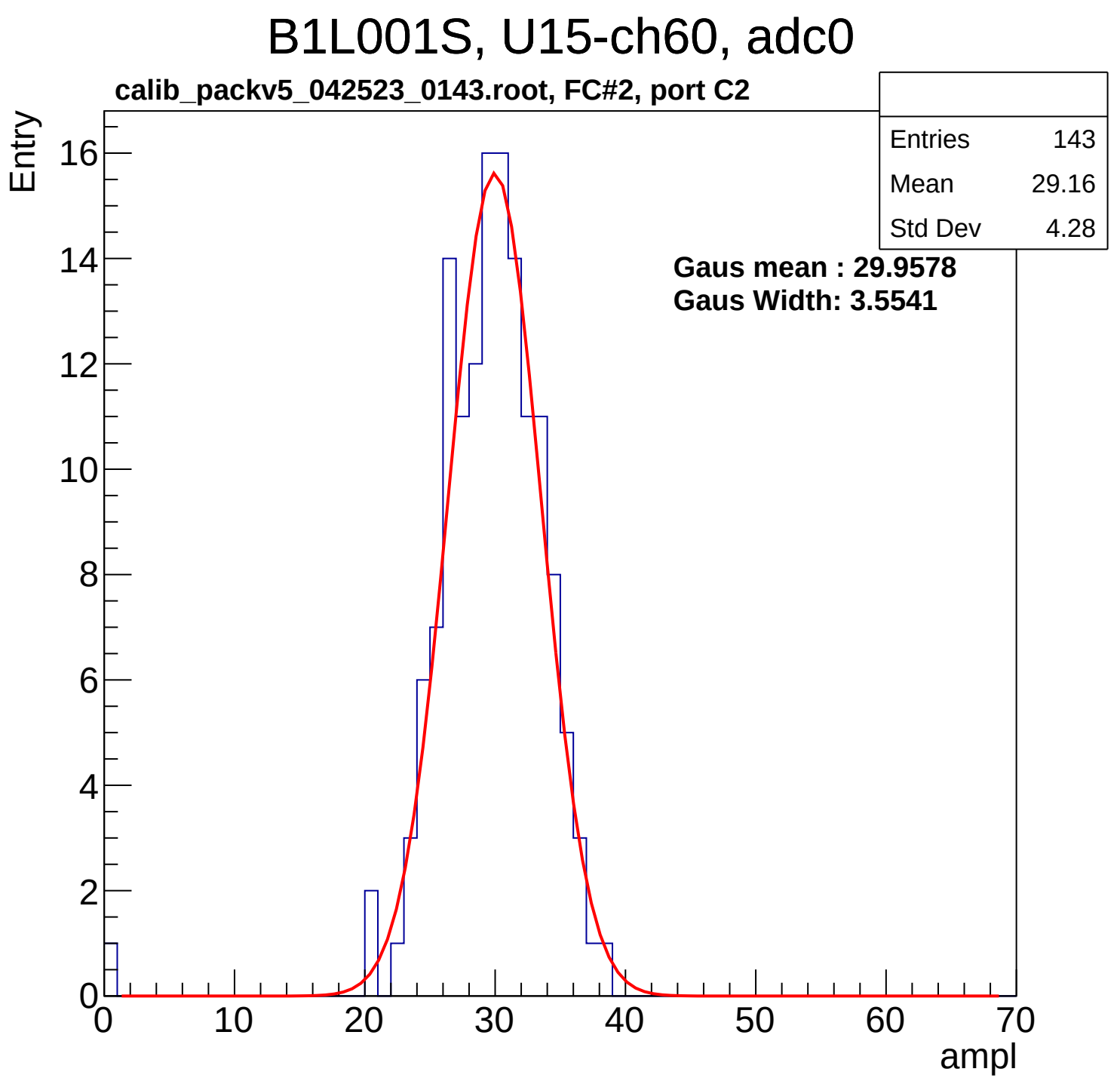
B1L001S, U15-ch60, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 29.16
Std Dev 4.28
Gaus mean : 29.9578
Gaus Width: 3.5541
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
30
40
50
60
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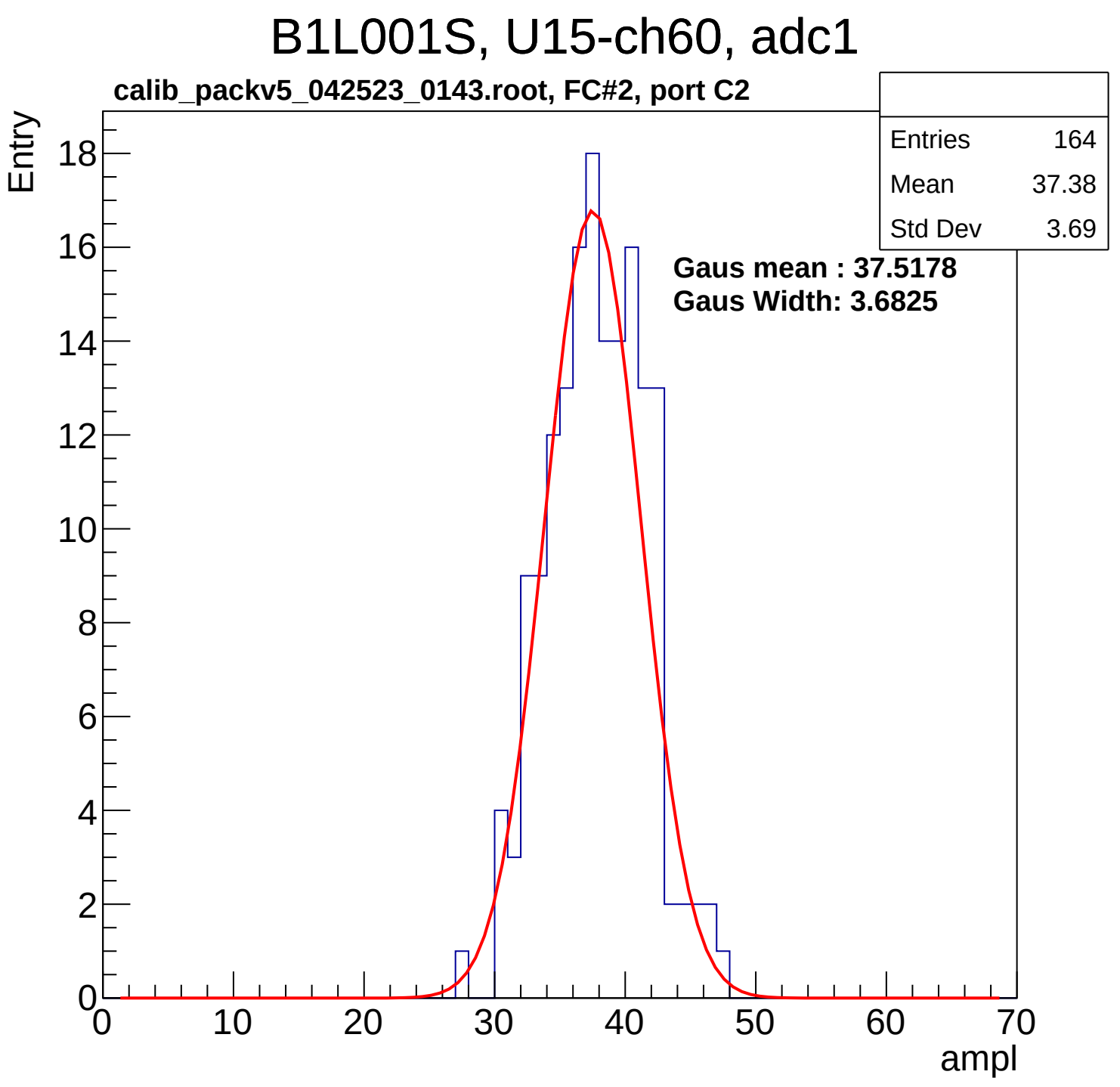
B1L001S, U15-ch60, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 164
Mean 37.38
Std Dev 3.69
Gaus mean : 37.5178
Gaus Width: 3.6825
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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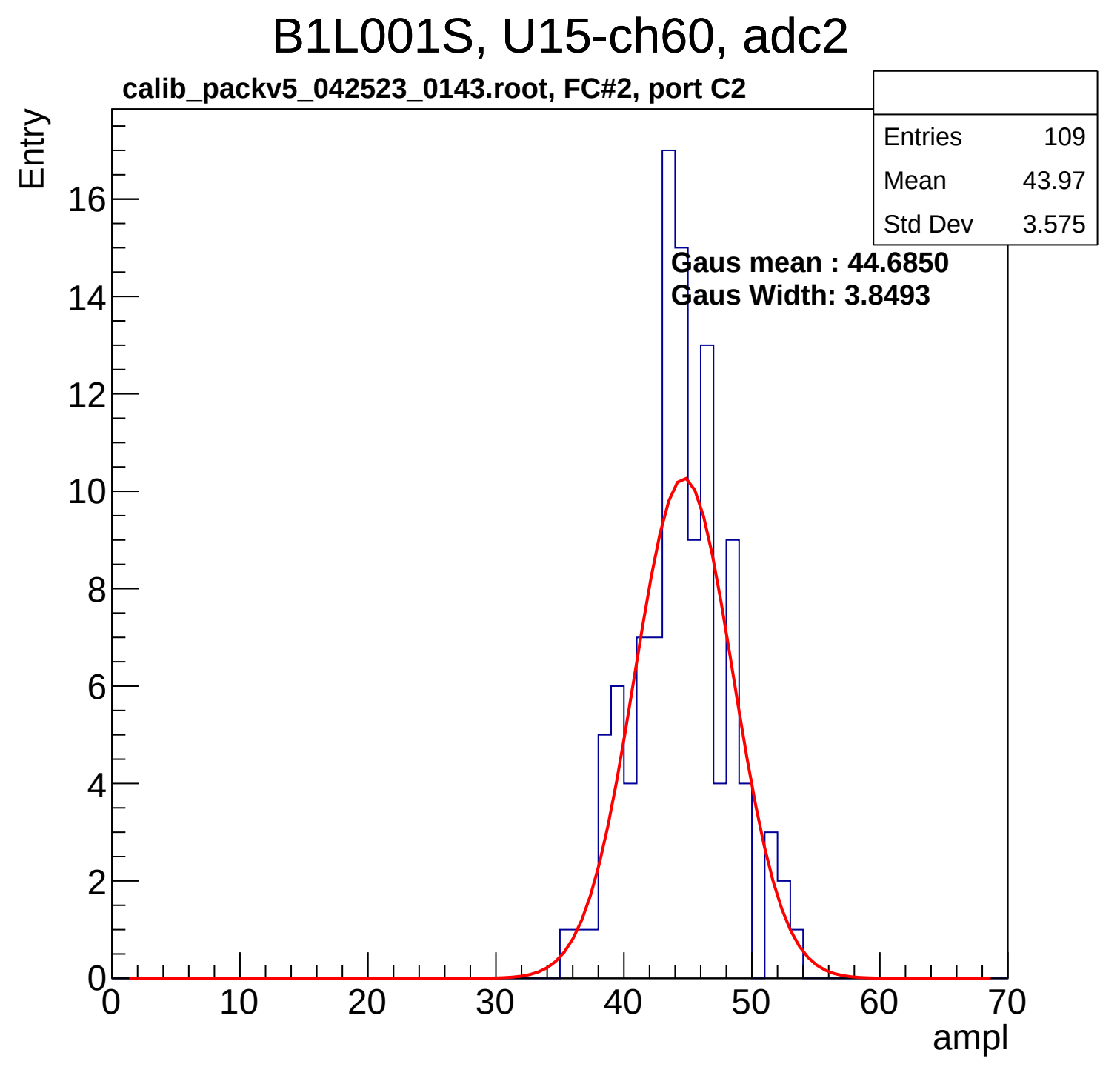
B1L001S, U15-ch60, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 43.97
Std Dev 3.575
Gaus mean : 44.6850
Gaus Width: 3.8493
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
30
40
50
60
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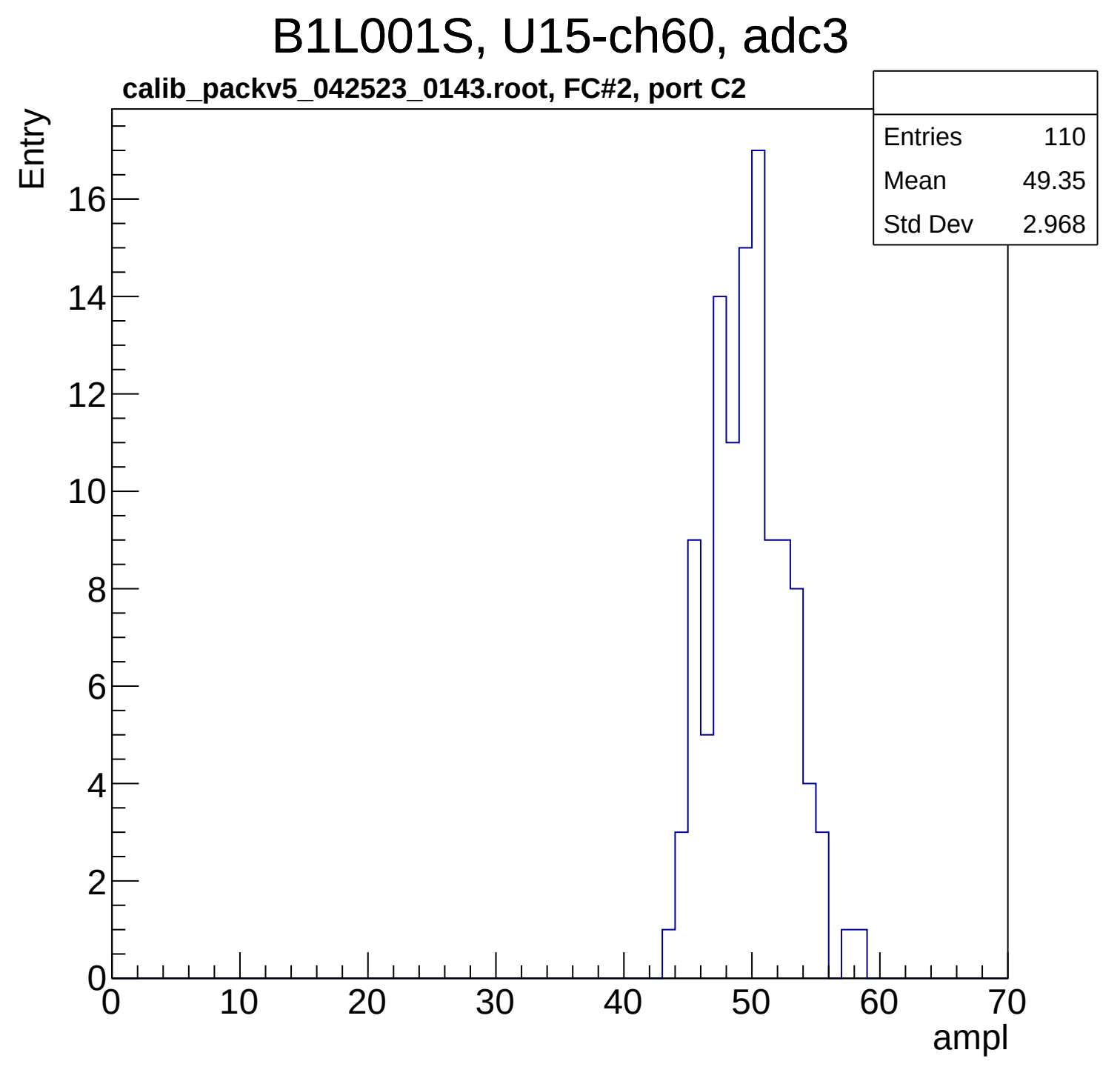
B1L001S, U15-ch60, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.35
Std Dev 2.968
Entry
ampl
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8
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50
60
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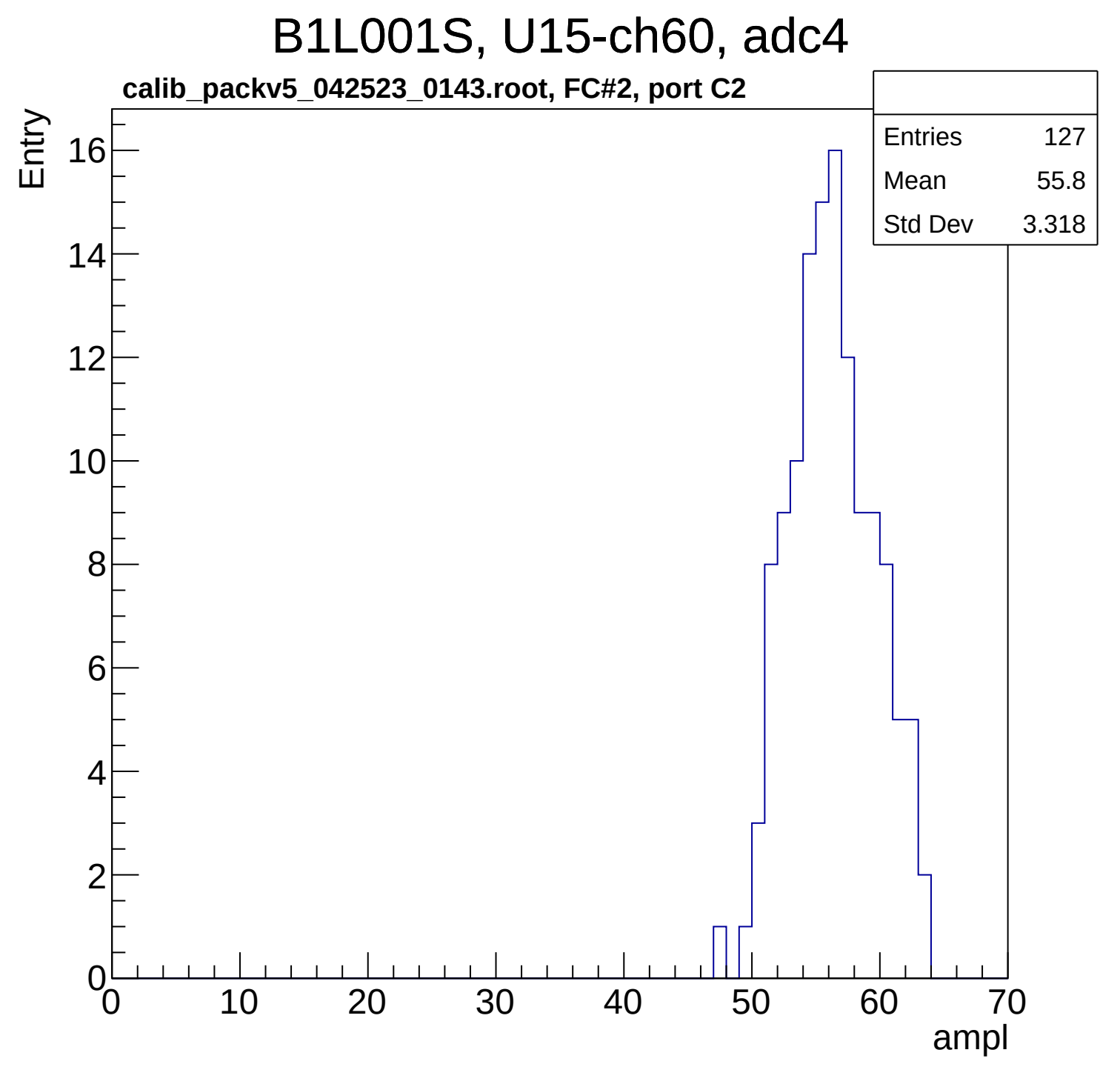
B1L001S, U15-ch60, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 55.8
Std Dev 3.318
Entry
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6
8
10
12
14
16
0
10
20
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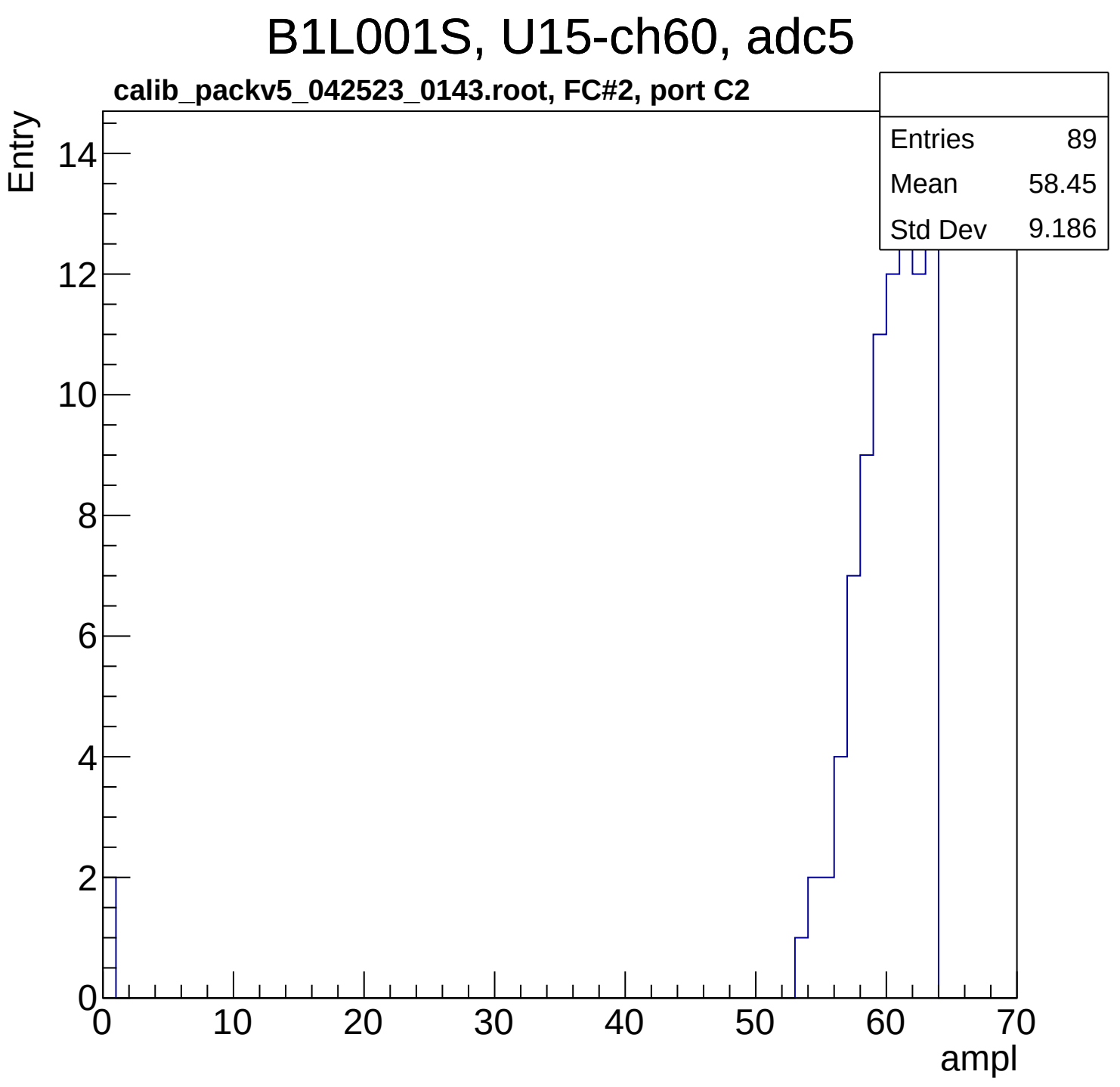
50
60
70
ampl


B1L001S, U15-ch60, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 58.45
Std Dev 9.186
Entry
ampl
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6
8
10
12
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0
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40
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60
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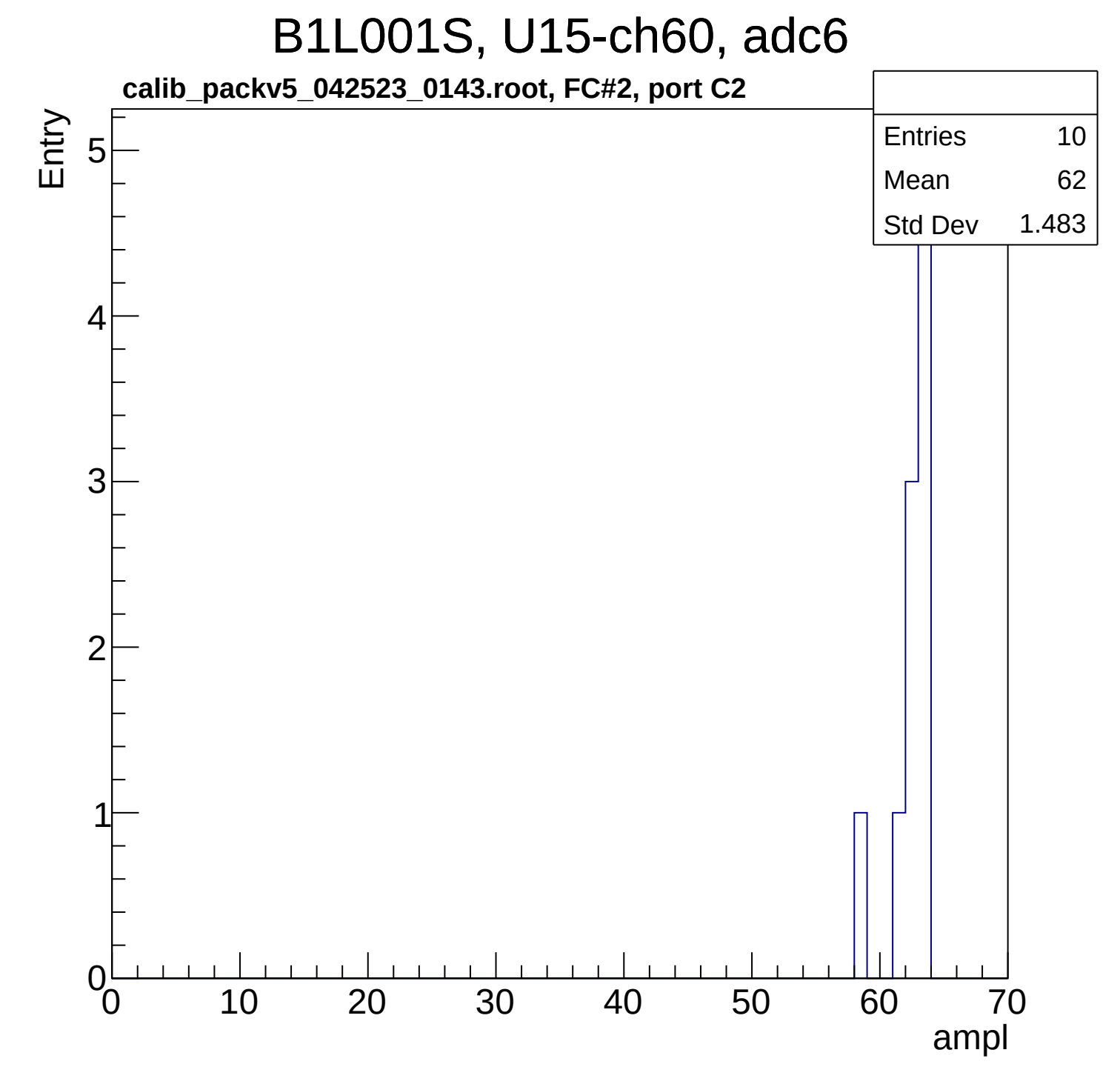
B1L001S, U15-ch60, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 62
Std Dev 1.483
Entry
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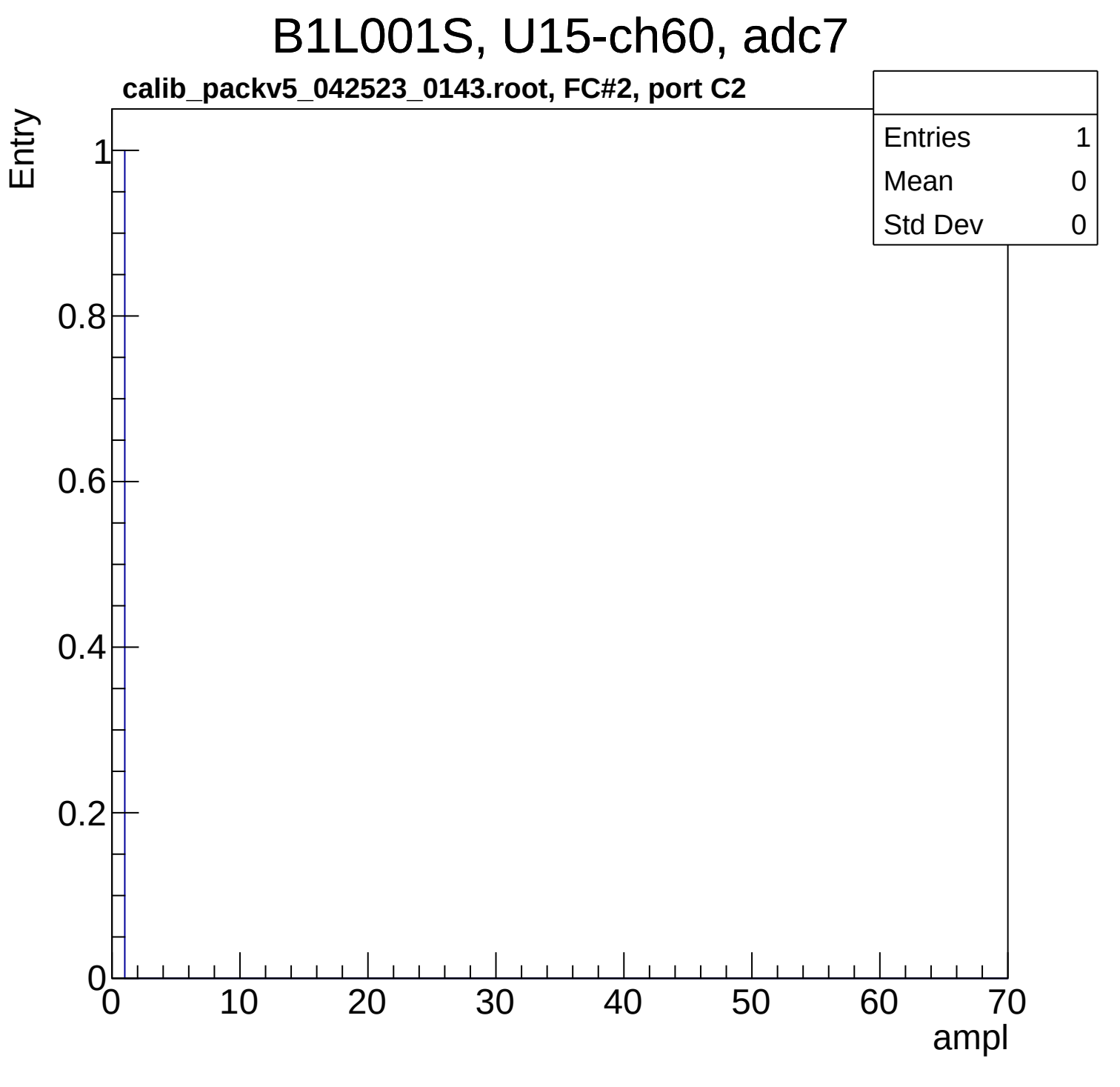
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch60, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.4
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0
0
10
20
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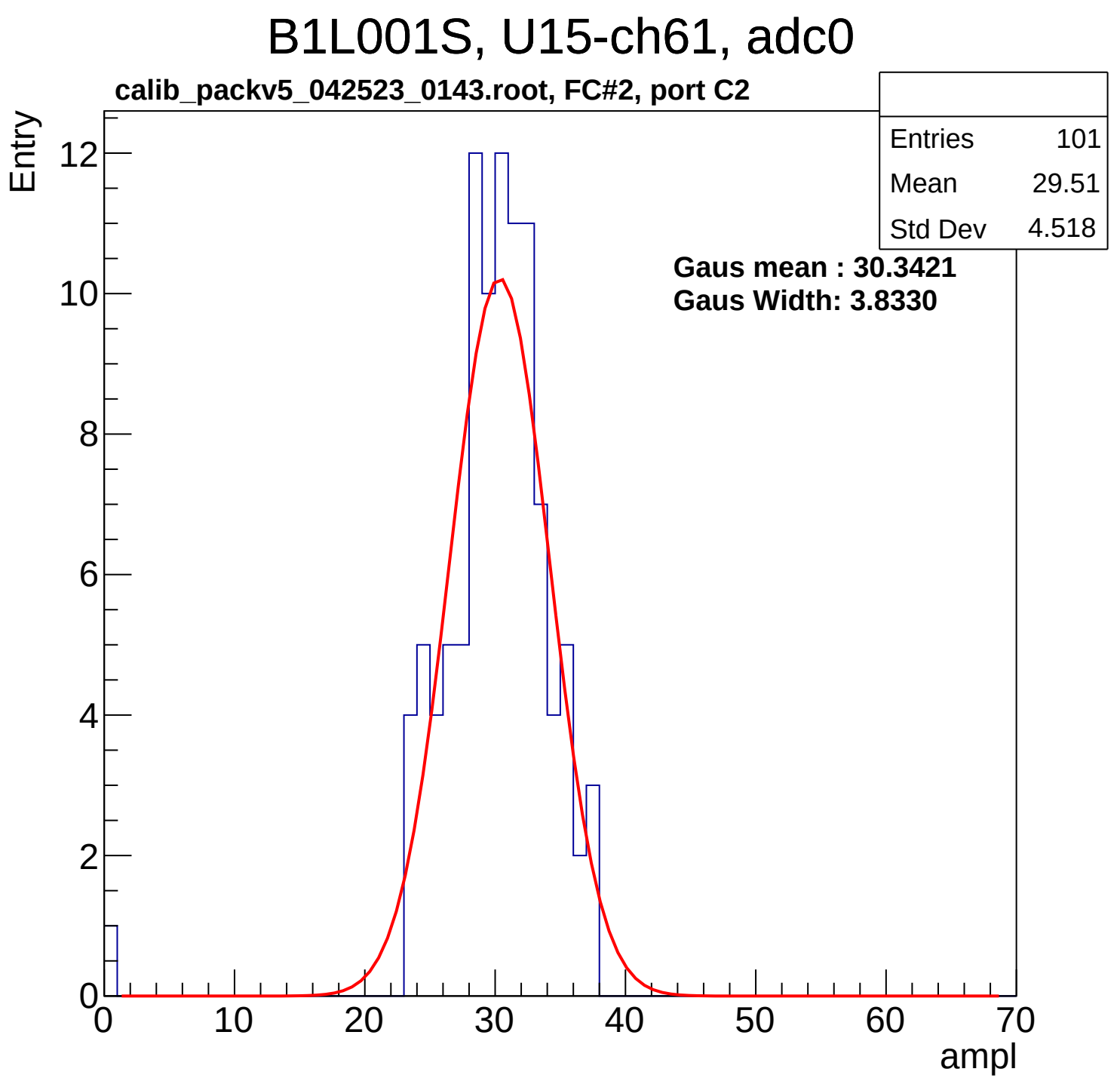
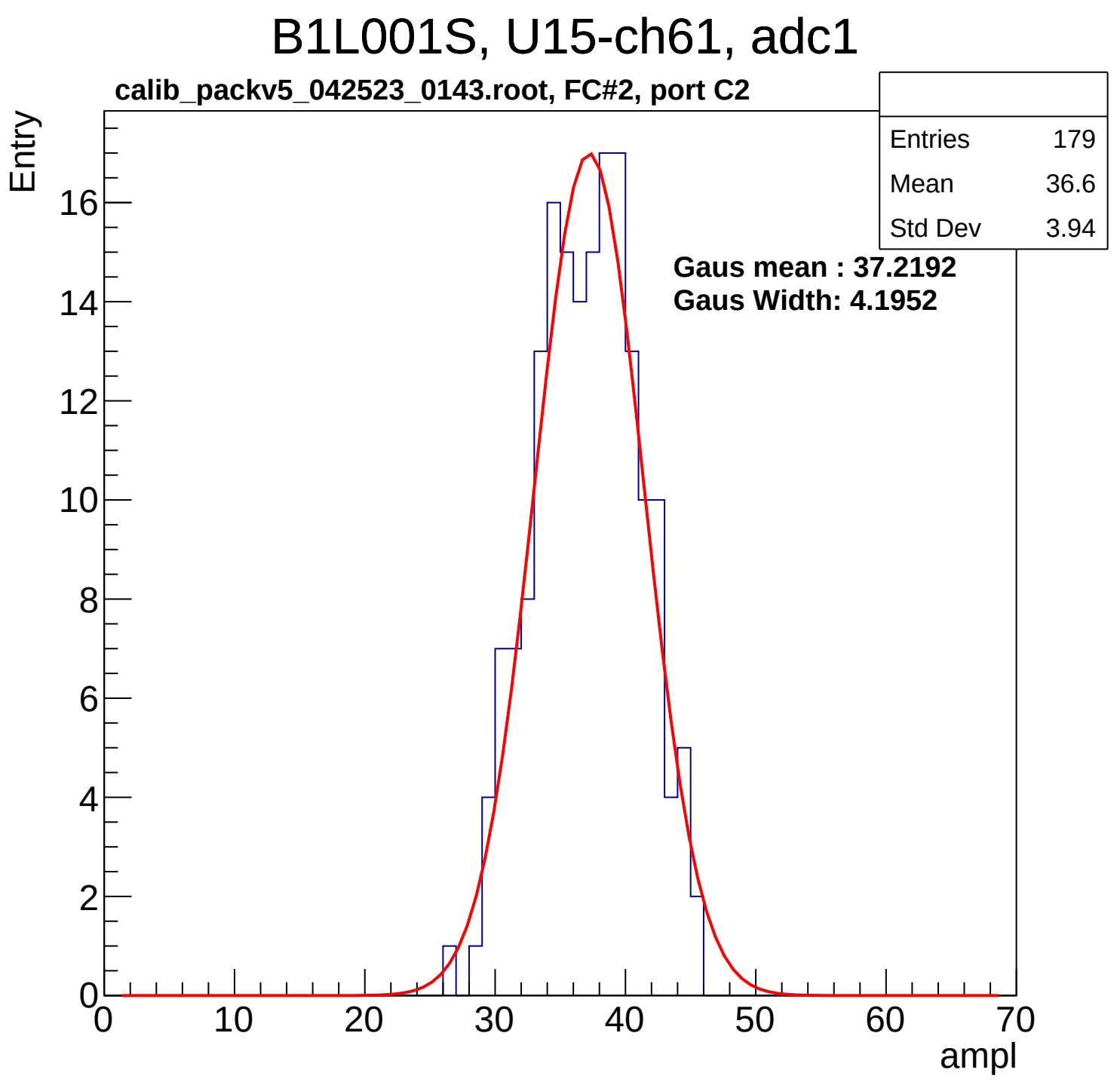
50
60
70
ampl


B1L001S, U15-ch61, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 29.51
Std Dev 4.518
Gaus mean : 30.3421
Gaus Width: 3.8330
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch61, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 179
Mean 36.6
Std Dev 3.94
Gaus mean : 37.2192
Gaus Width: 4.1952
Entry
ampl
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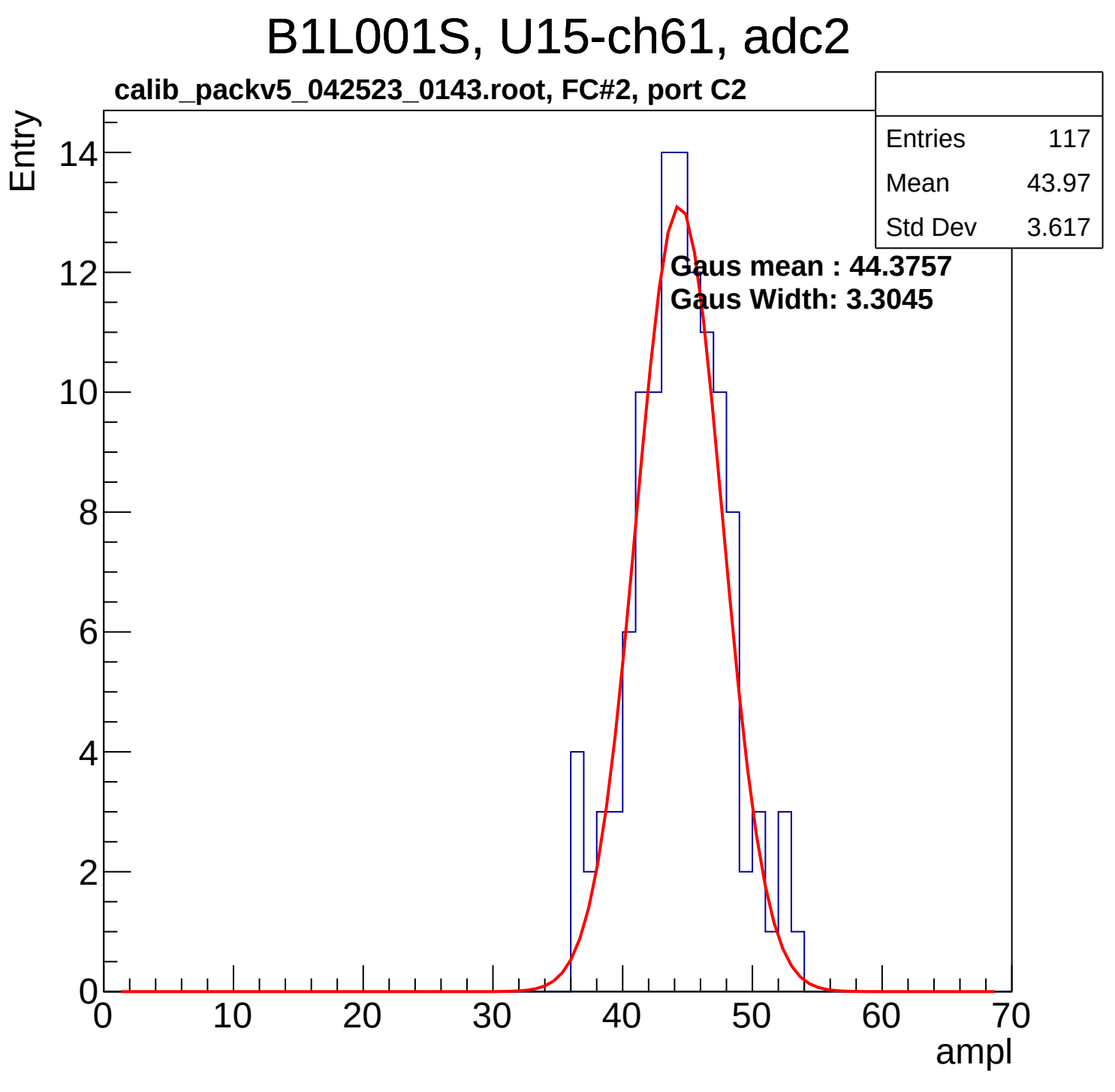
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch61, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 43.97
Std Dev 3.617
Gaus mean : 44.3757
Gaus Width: 3.3045
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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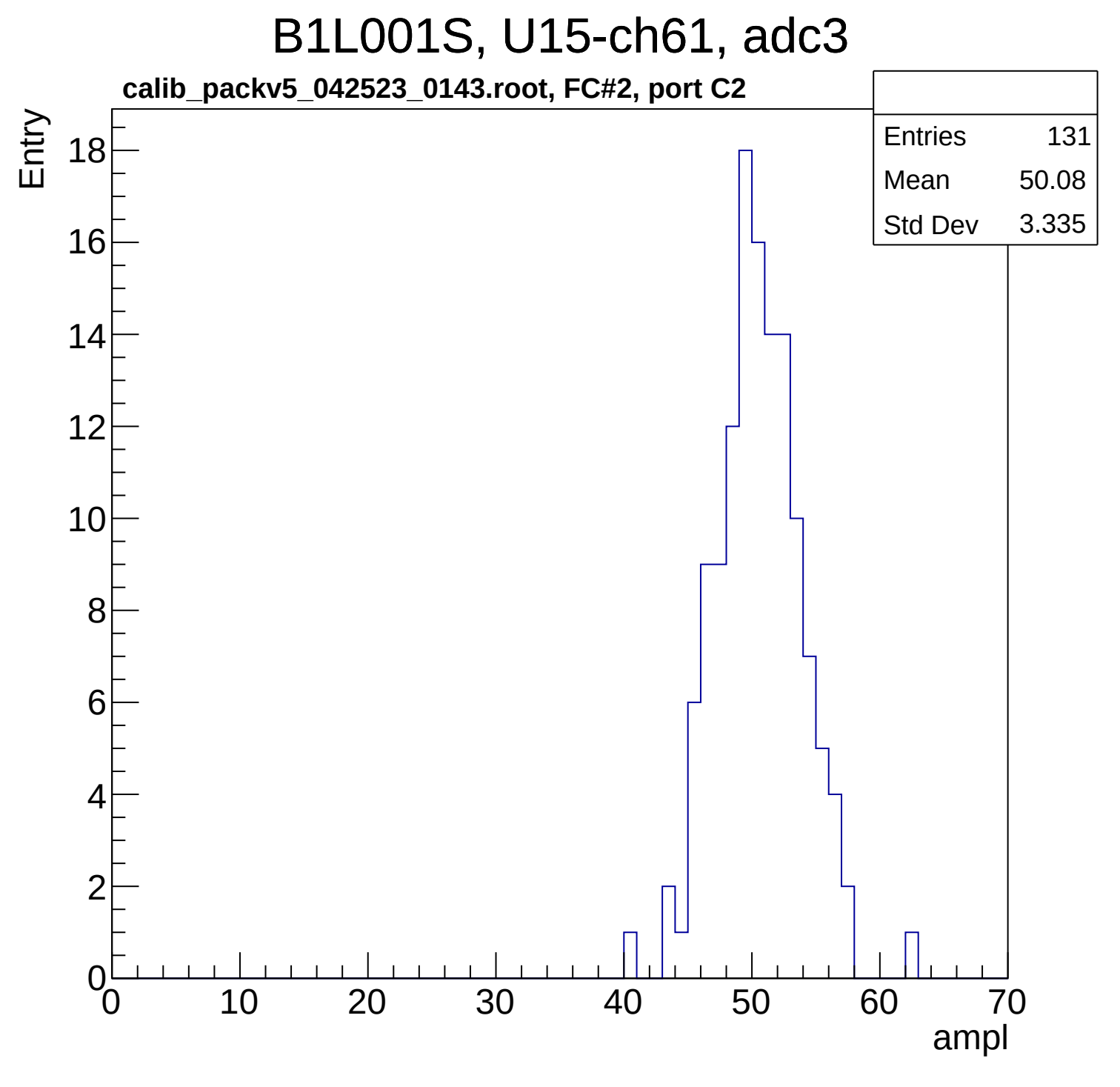
B1L001S, U15-ch61, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 50.08
Std Dev 3.335
Entry
ampl
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6
8
10
12
14
16
18
0
10
20
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40
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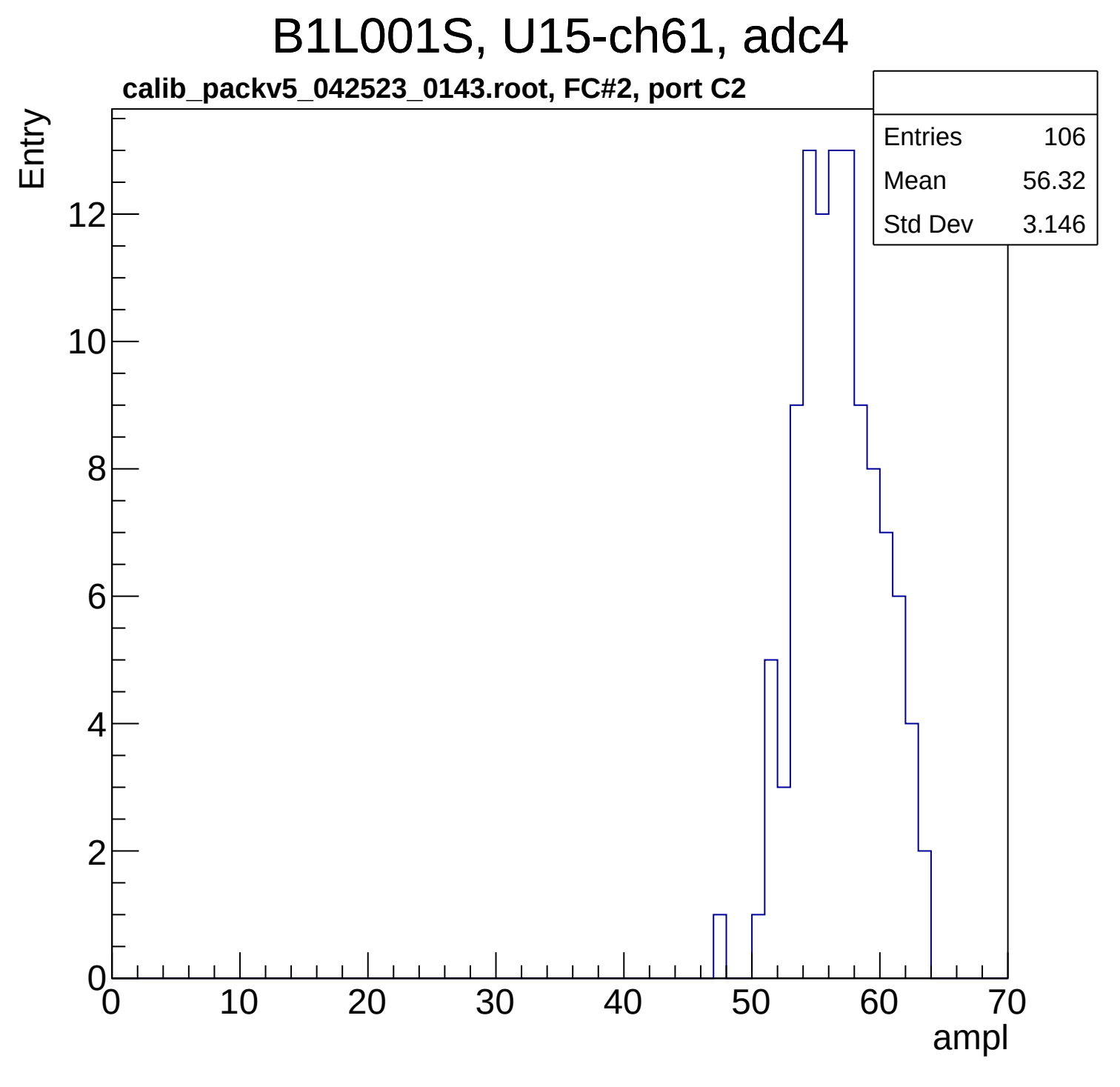
B1L001S, U15-ch61, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 56.32
Std Dev 3.146
Entry
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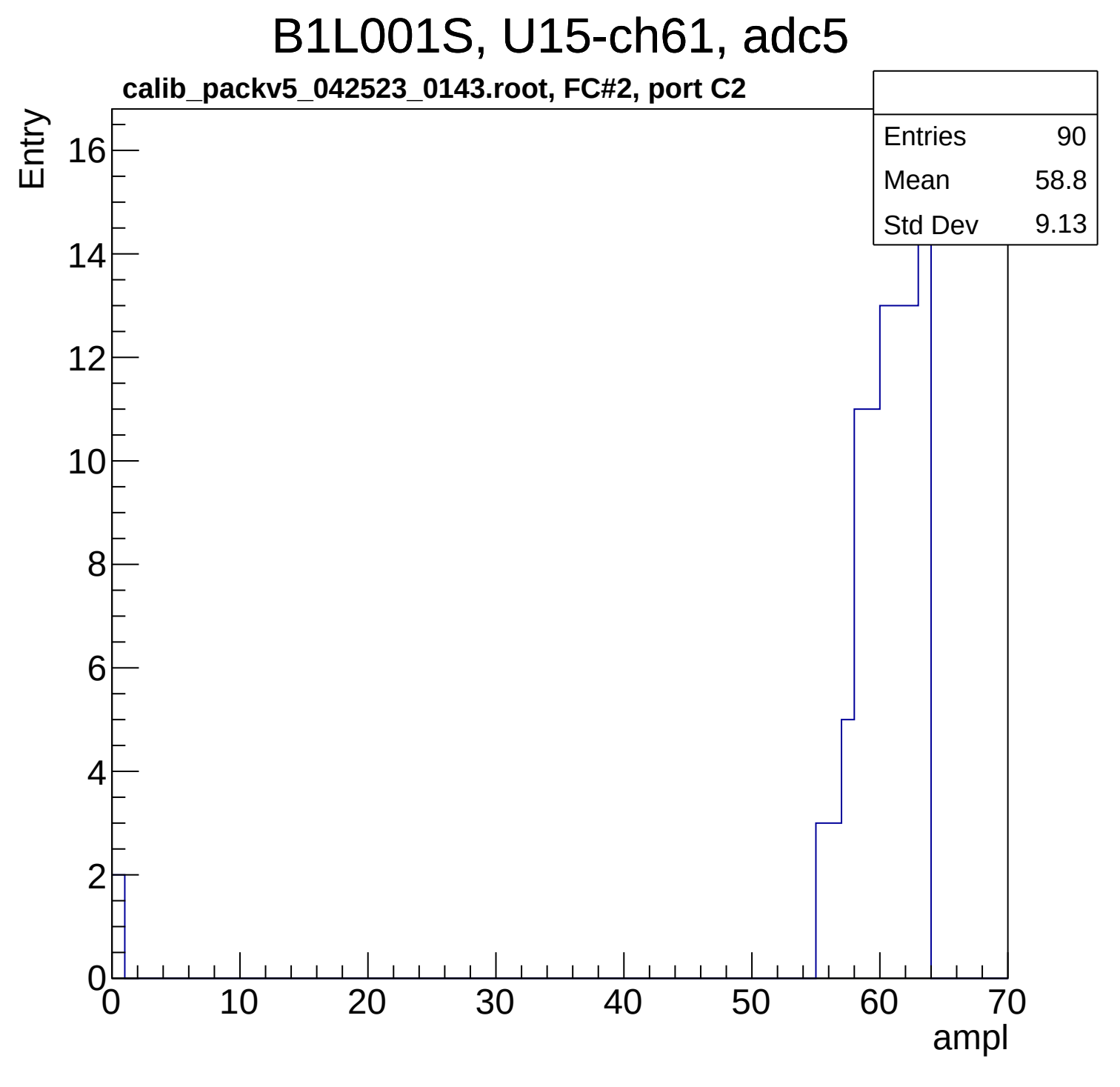
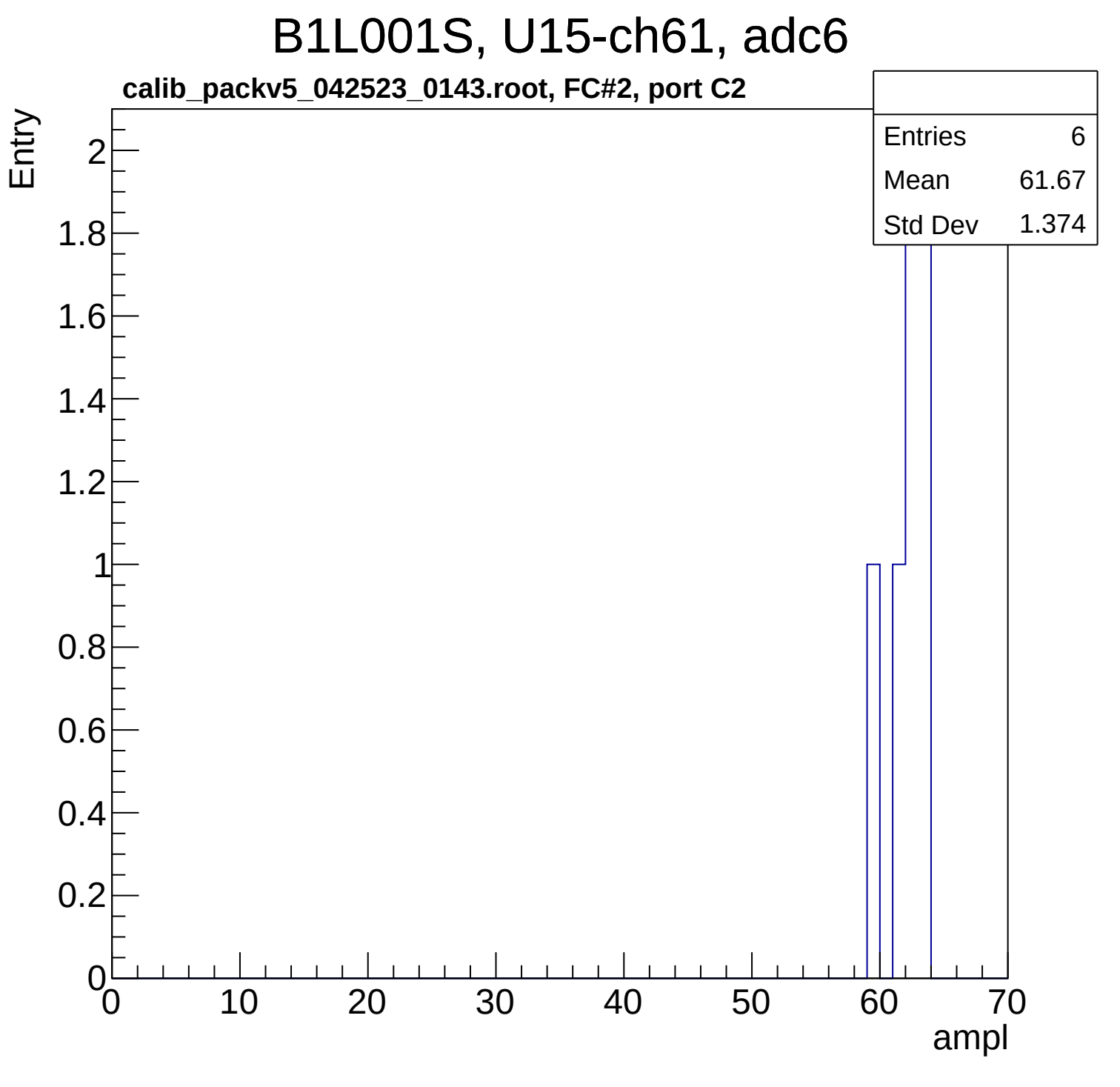
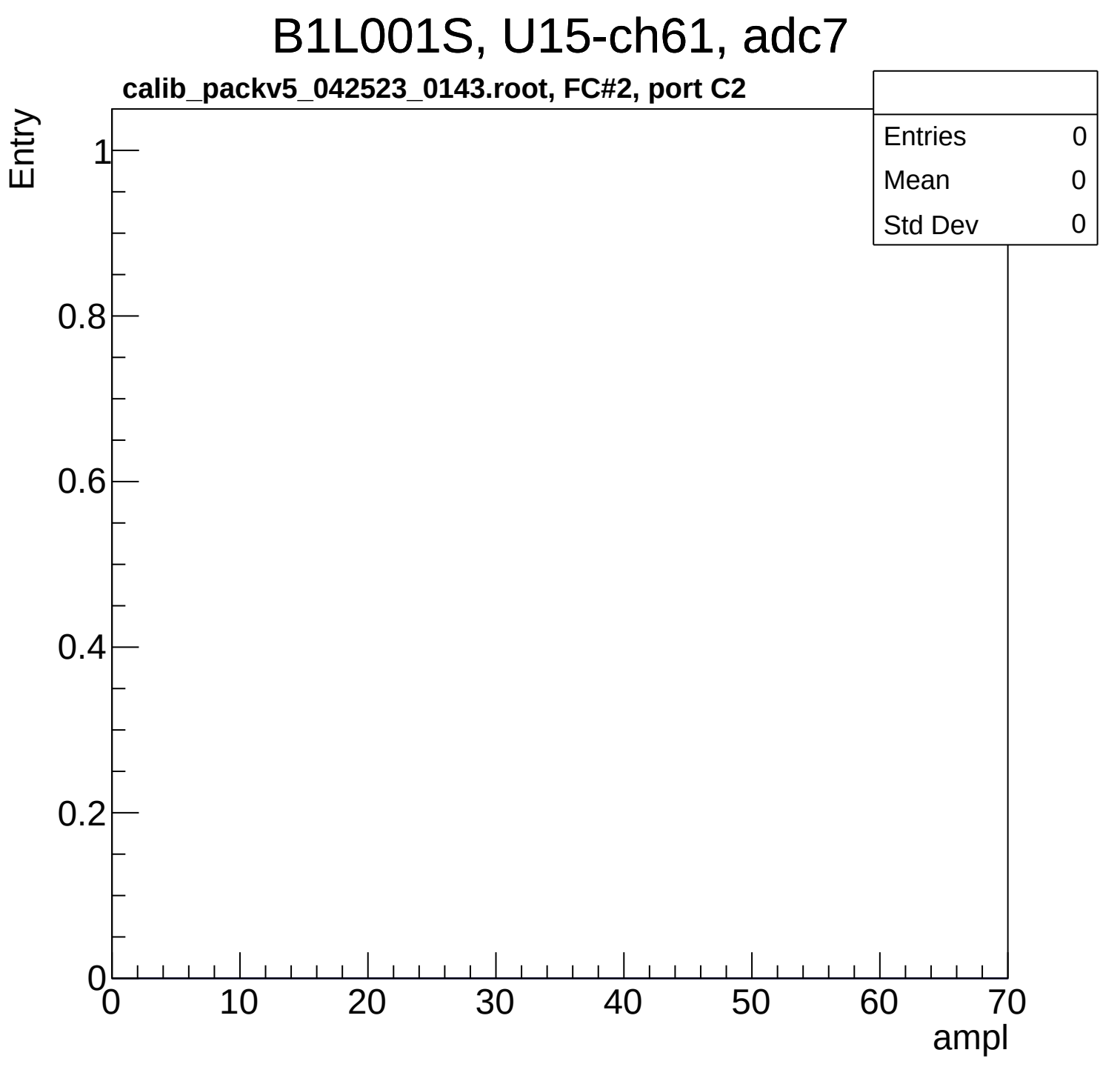
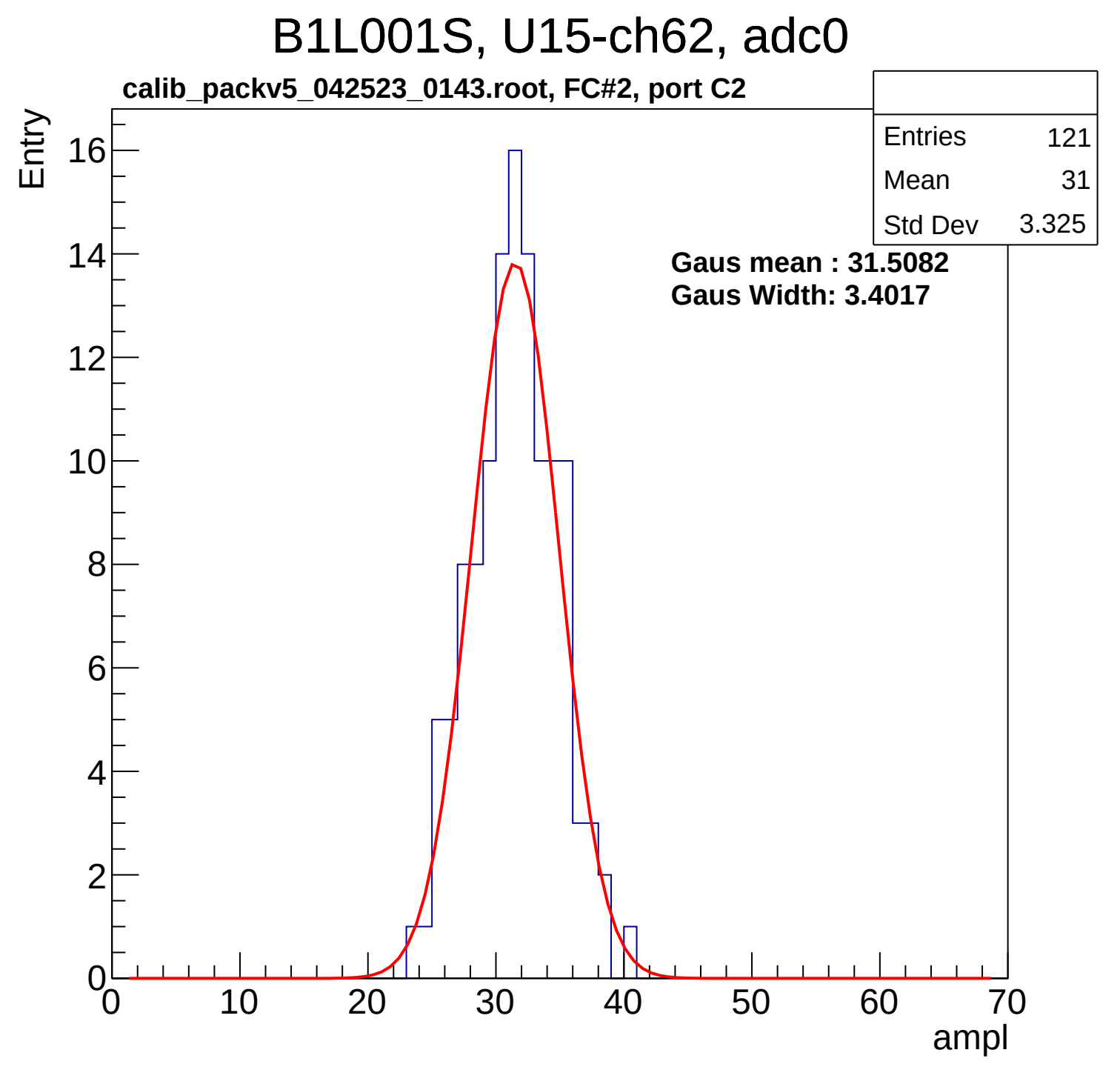
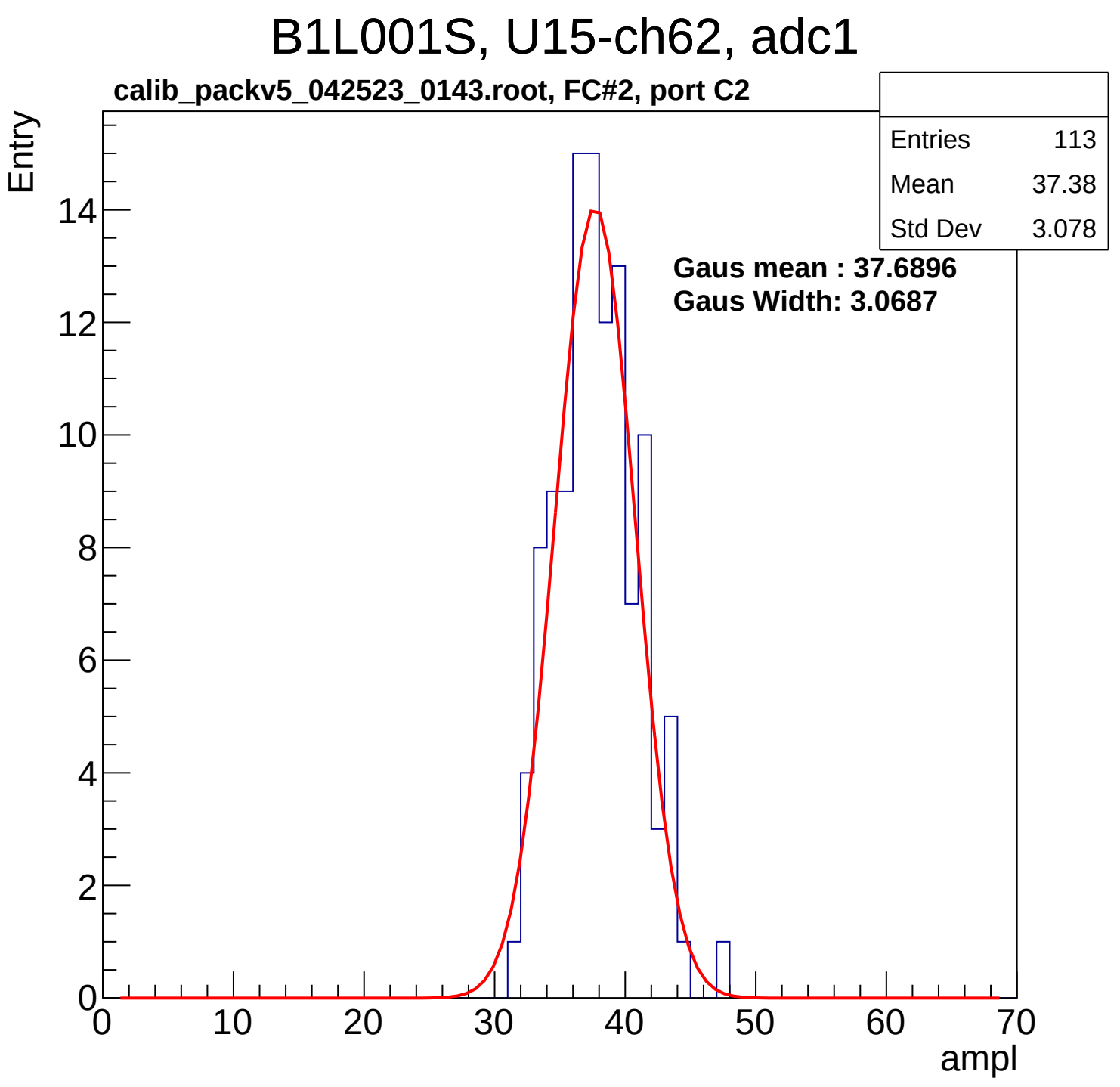
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

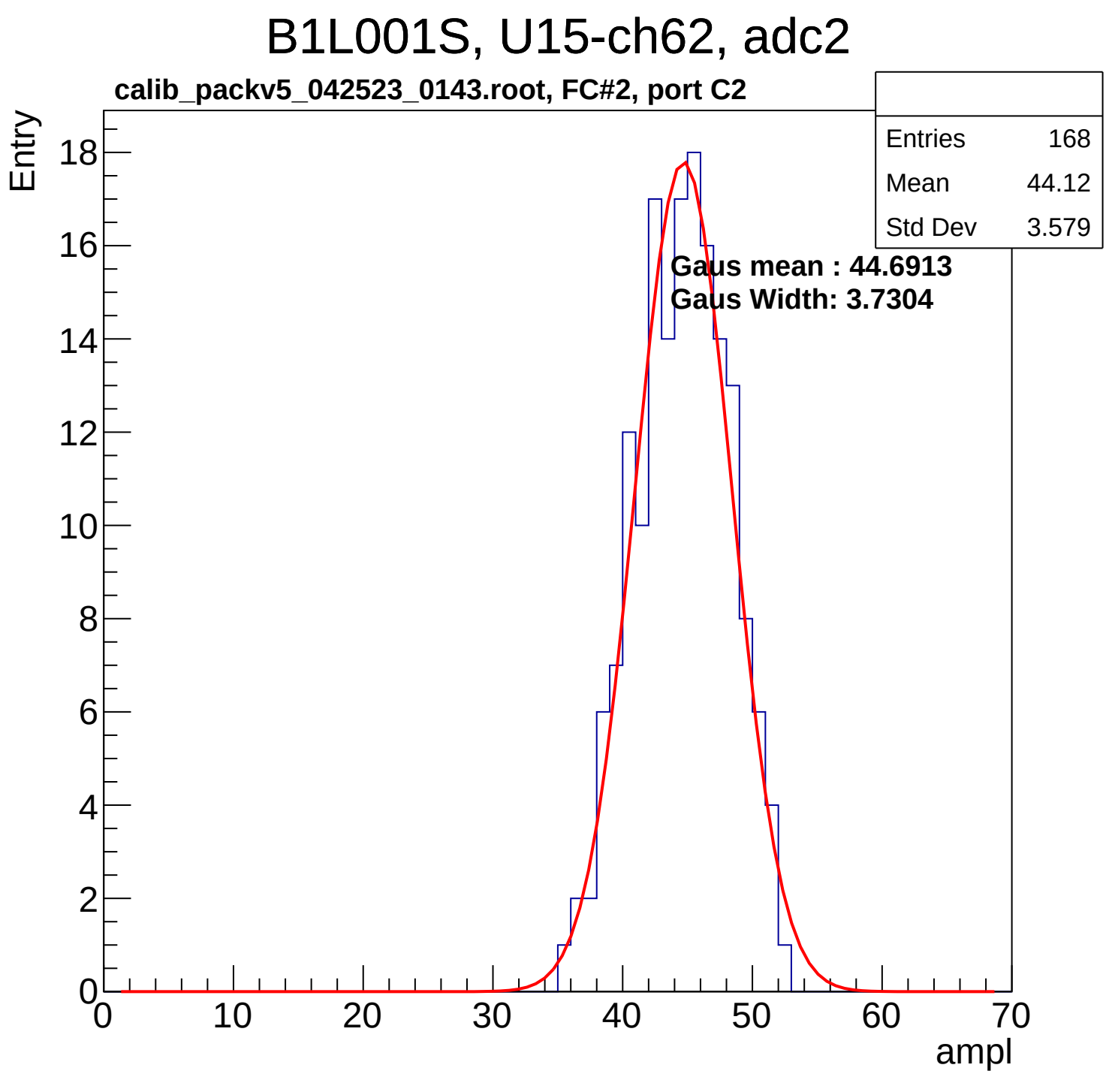

B1L001S, U15-ch61, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 58.8
Std Dev 9.13
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch61, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch61, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch62, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 31
Std Dev 3.325
Gaus mean : 31.5082
Gaus Width: 3.4017
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch62, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 37.38
Std Dev 3.078
Gaus mean : 37.6896
Gaus Width: 3.0687
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch62, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 168
Mean 44.12
Std Dev 3.579
Gaus mean : 44.6913
Gaus Width: 3.7304
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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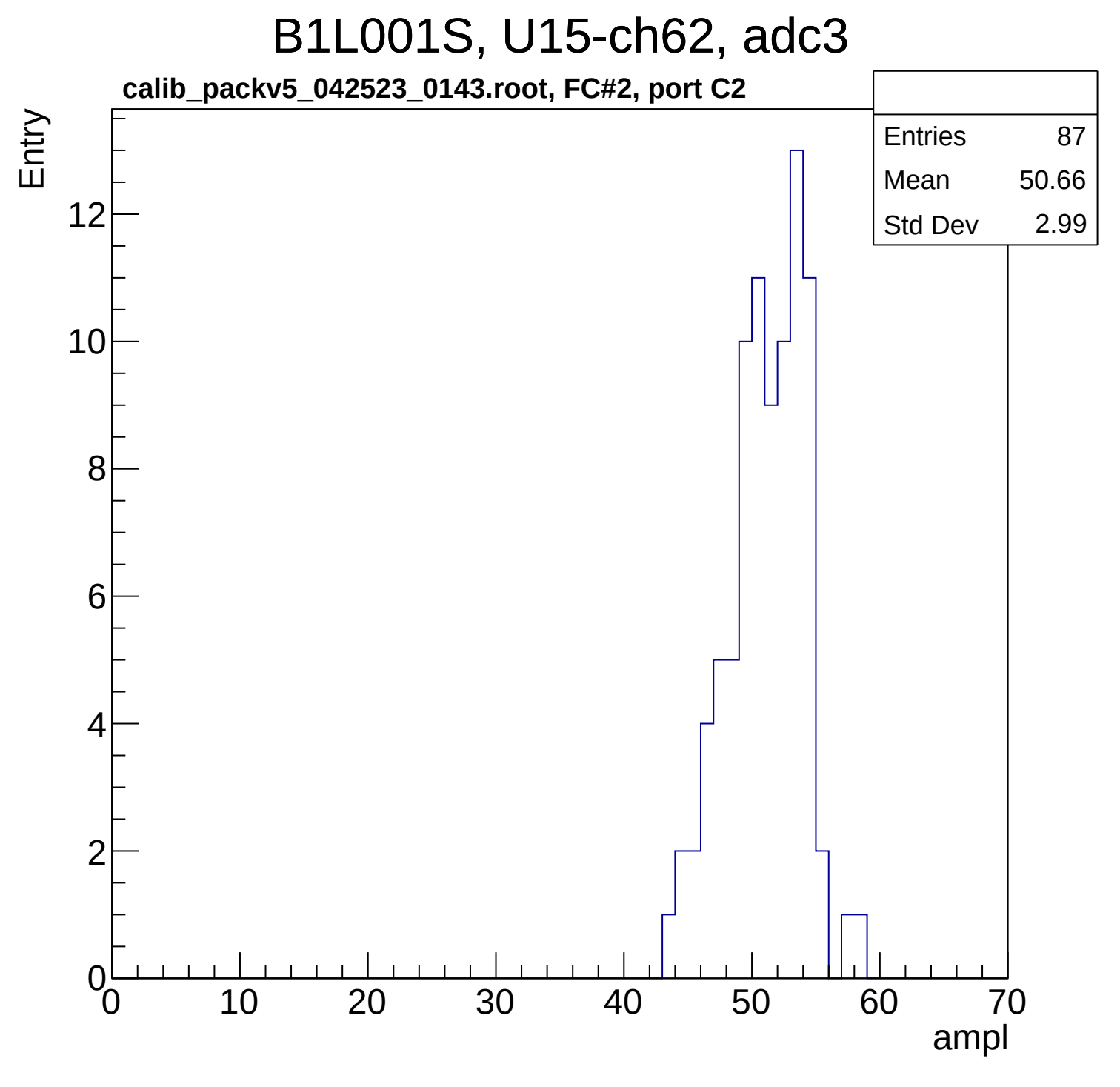
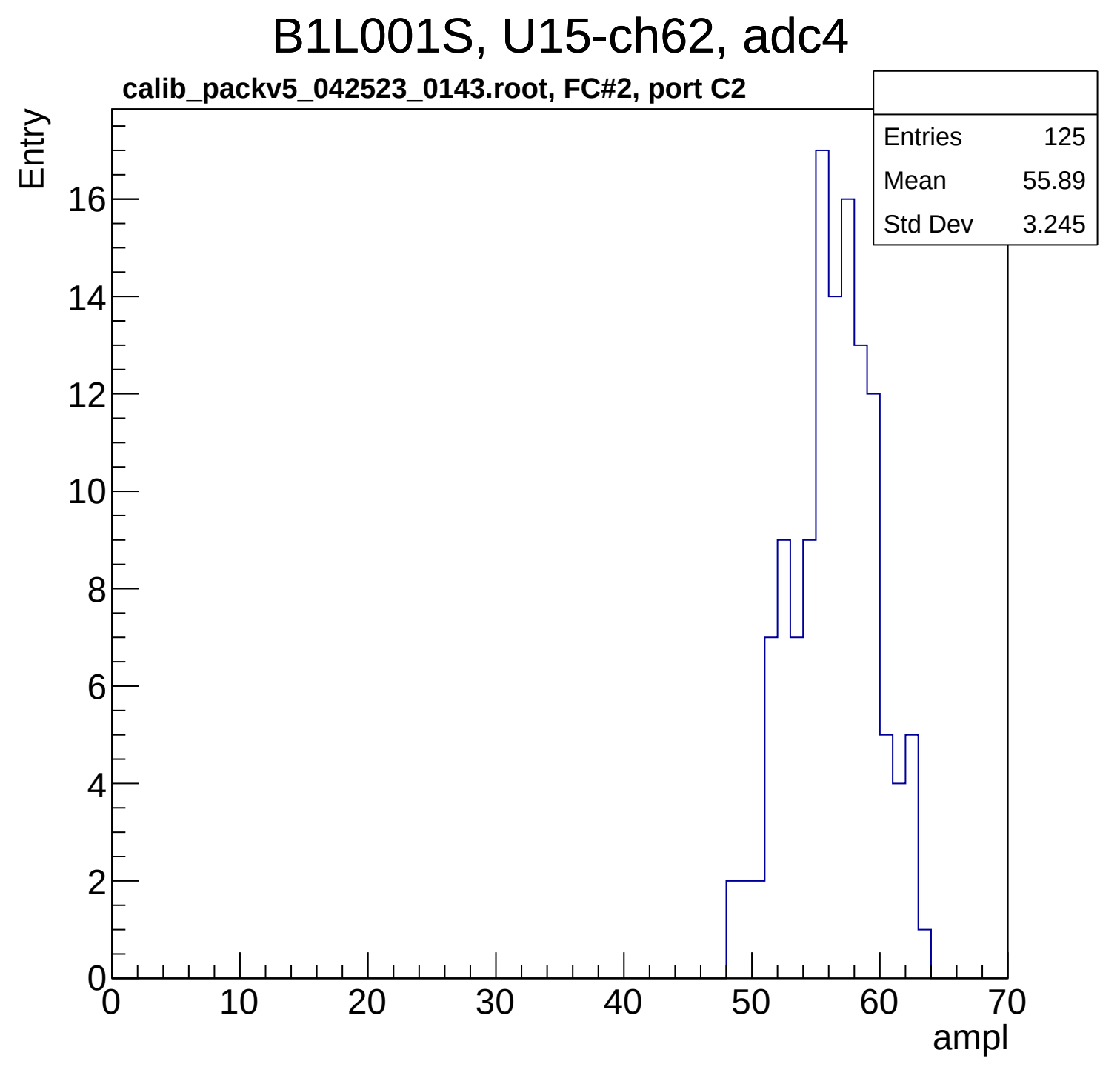
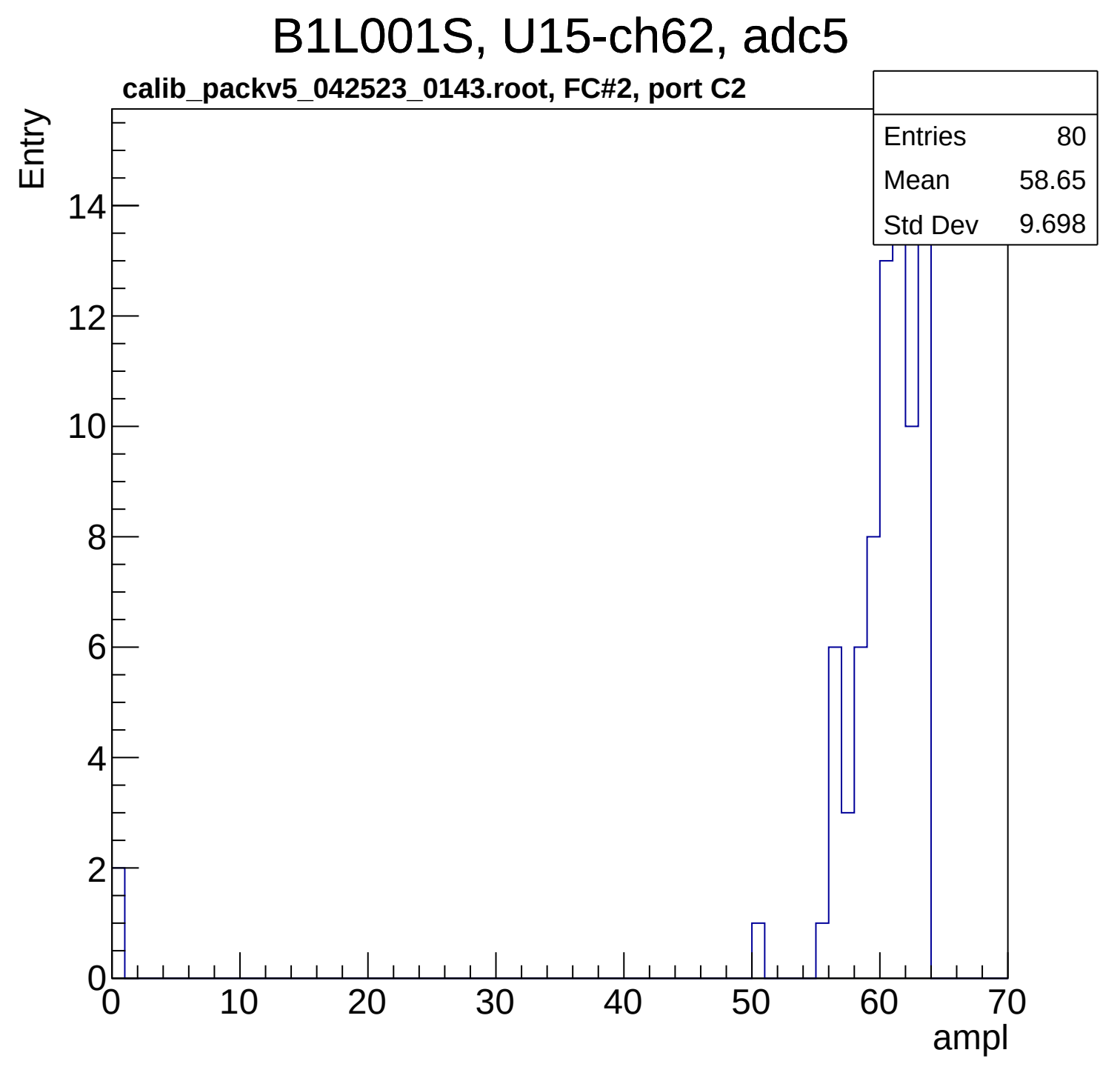
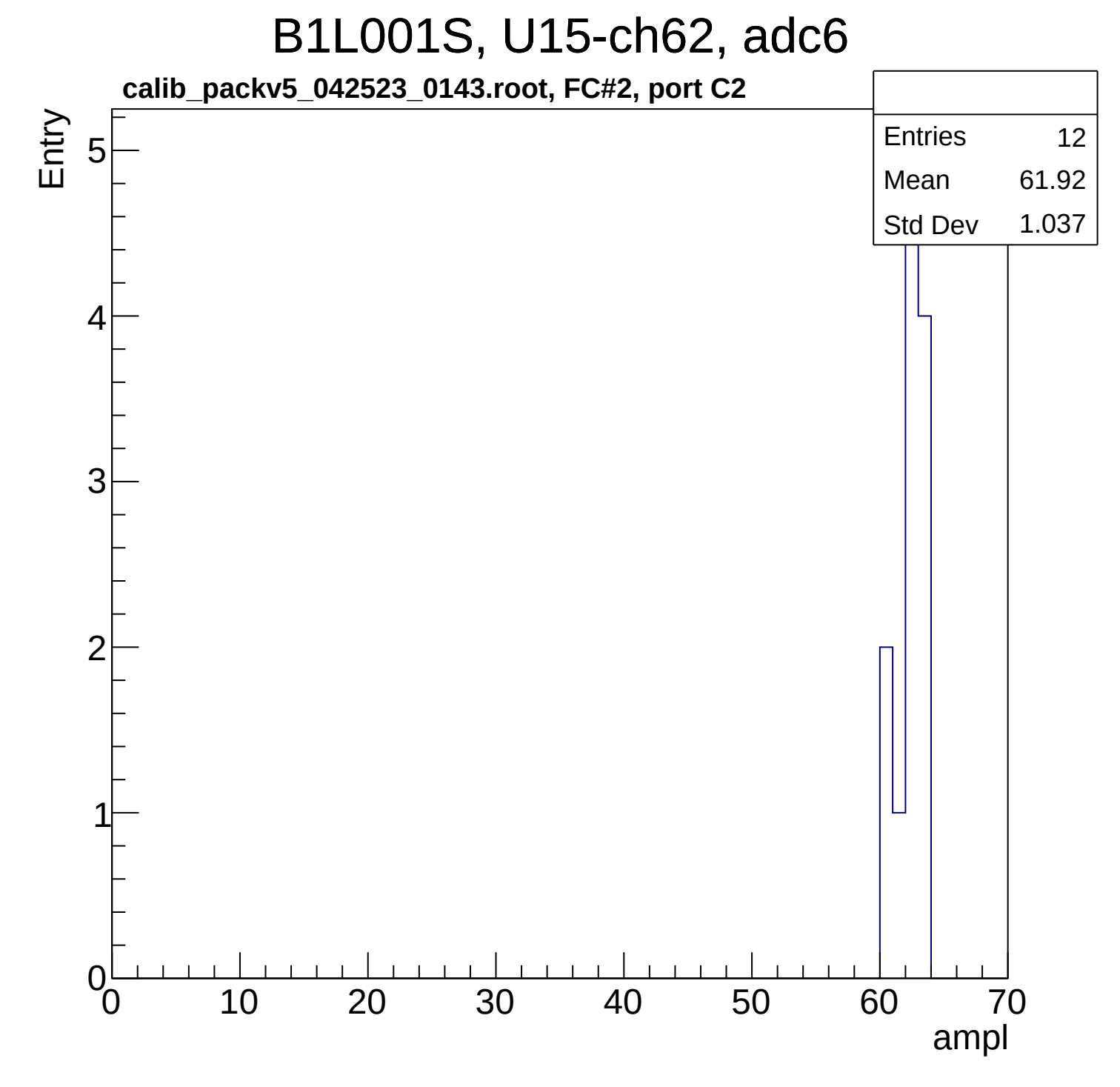
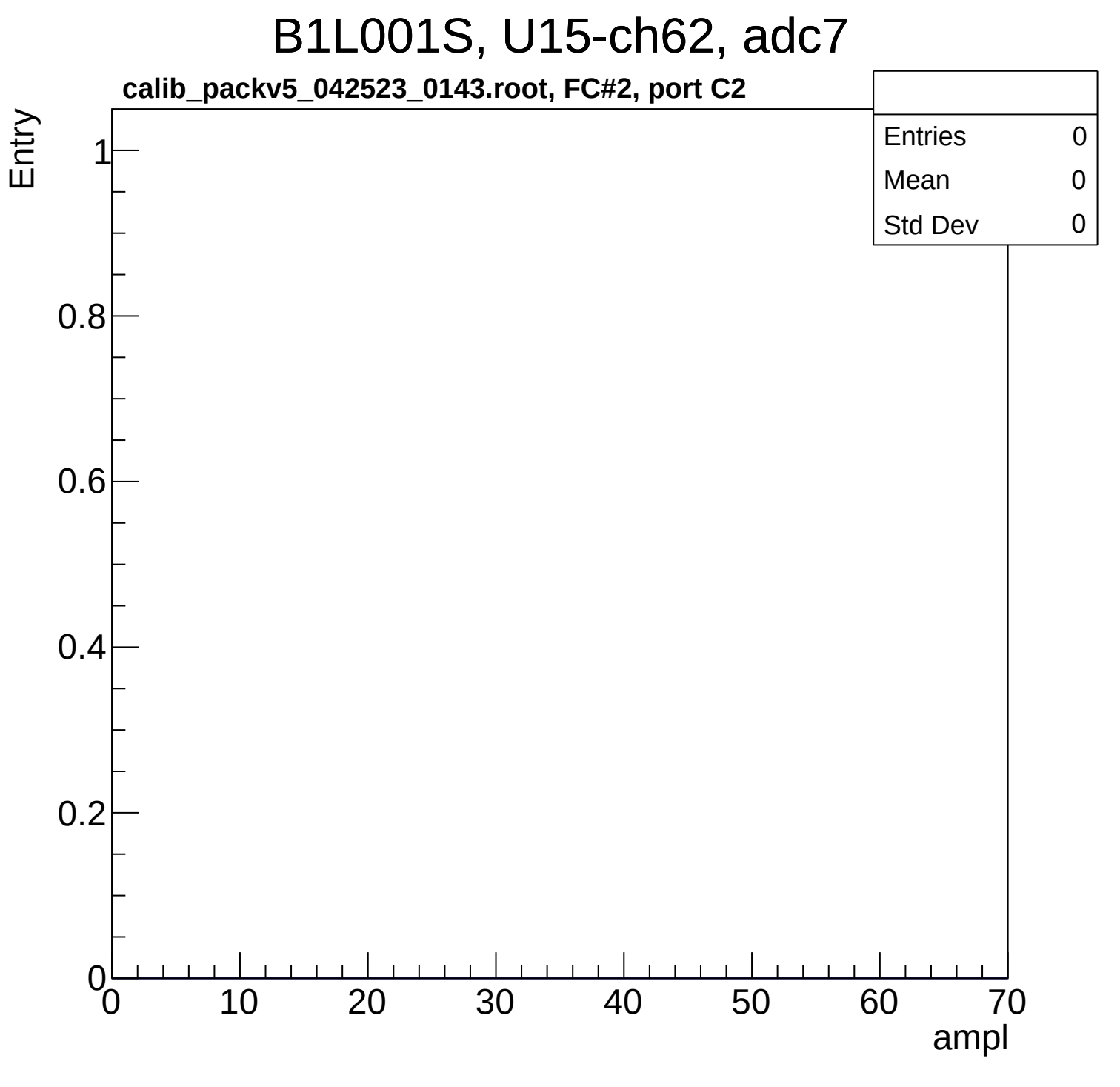
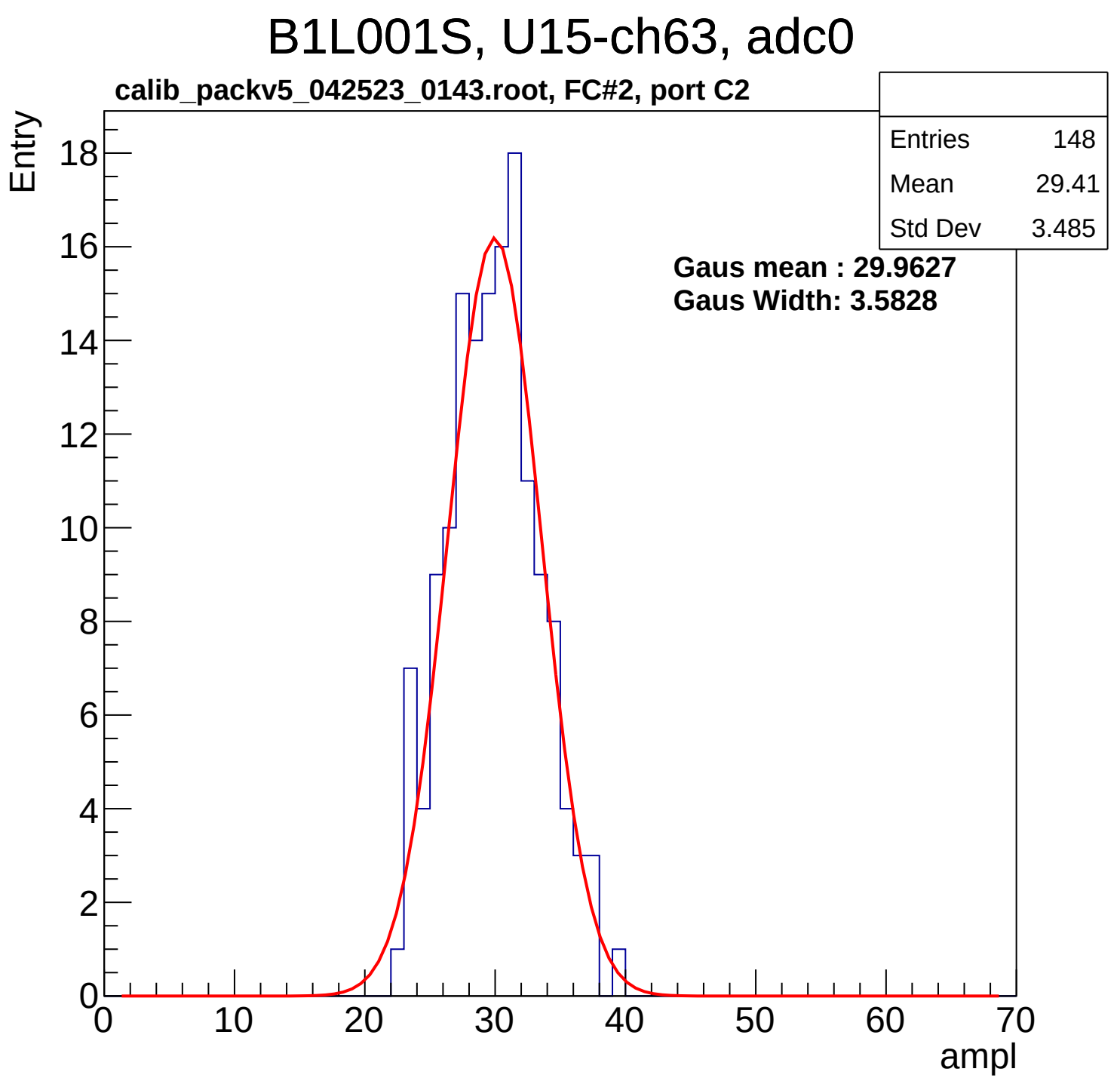

B1L001S, U15-ch62, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 50.66
Std Dev 2.99
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch62, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 55.89
Std Dev 3.245
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch62, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 58.65
Std Dev 9.698
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch62, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 61.92
Std Dev 1.037
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L001S, U15-ch62, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch63, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 29.41
Std Dev 3.485
Gaus mean : 29.9627
Gaus Width: 3.5828
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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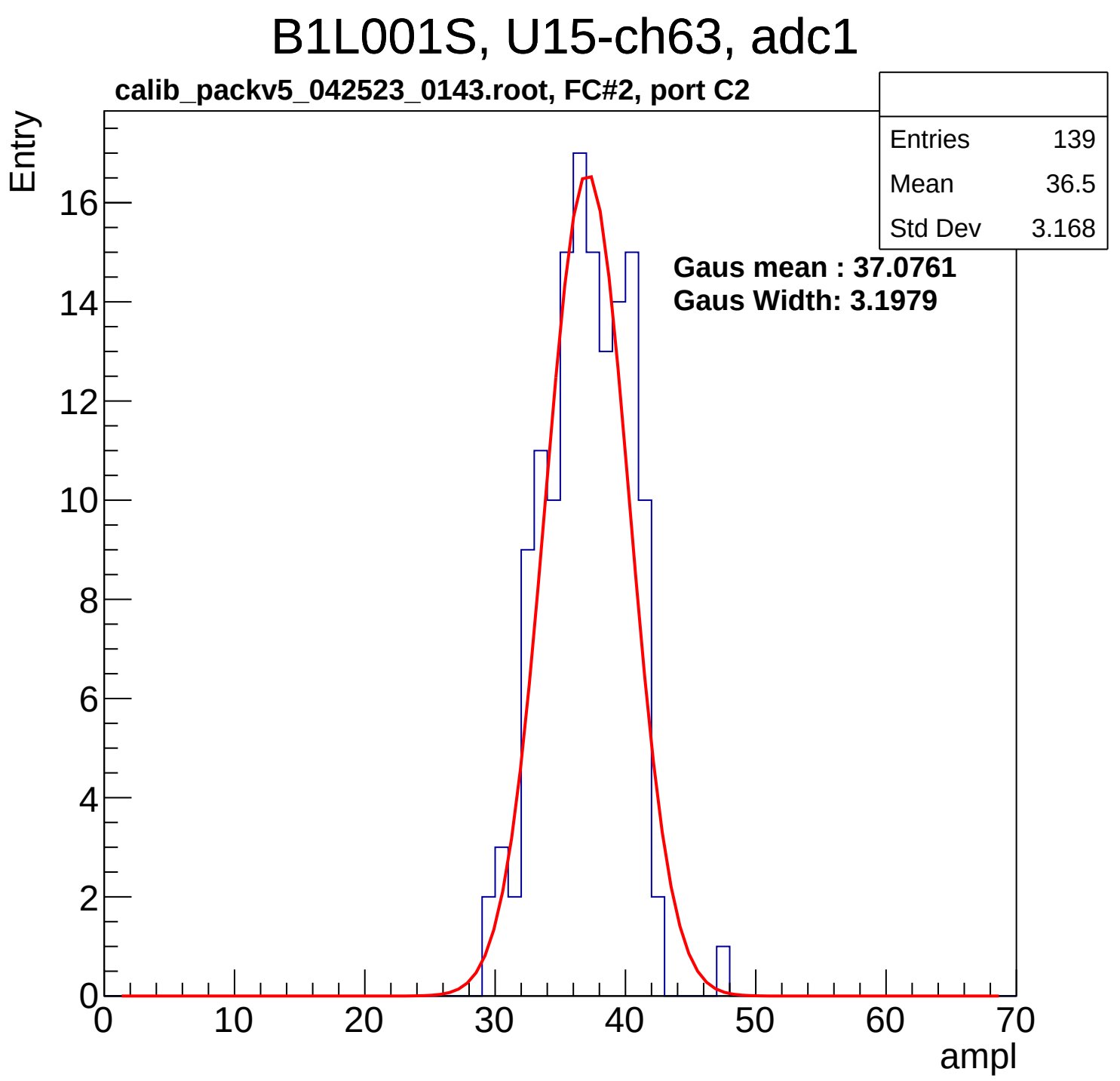
B1L001S, U15-ch63, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 36.5
Std Dev 3.168
Gaus mean : 37.0761
Gaus Width: 3.1979
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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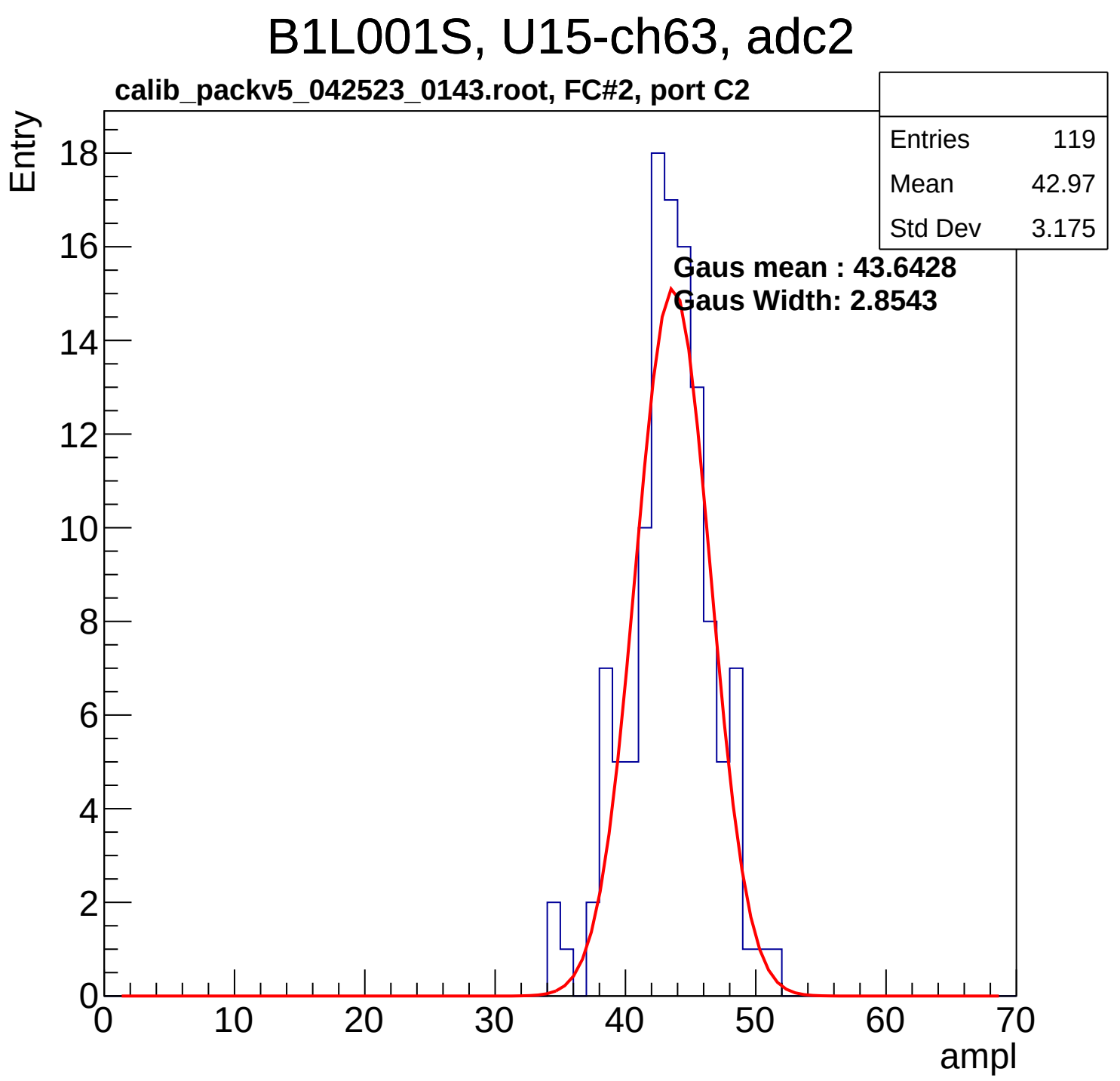
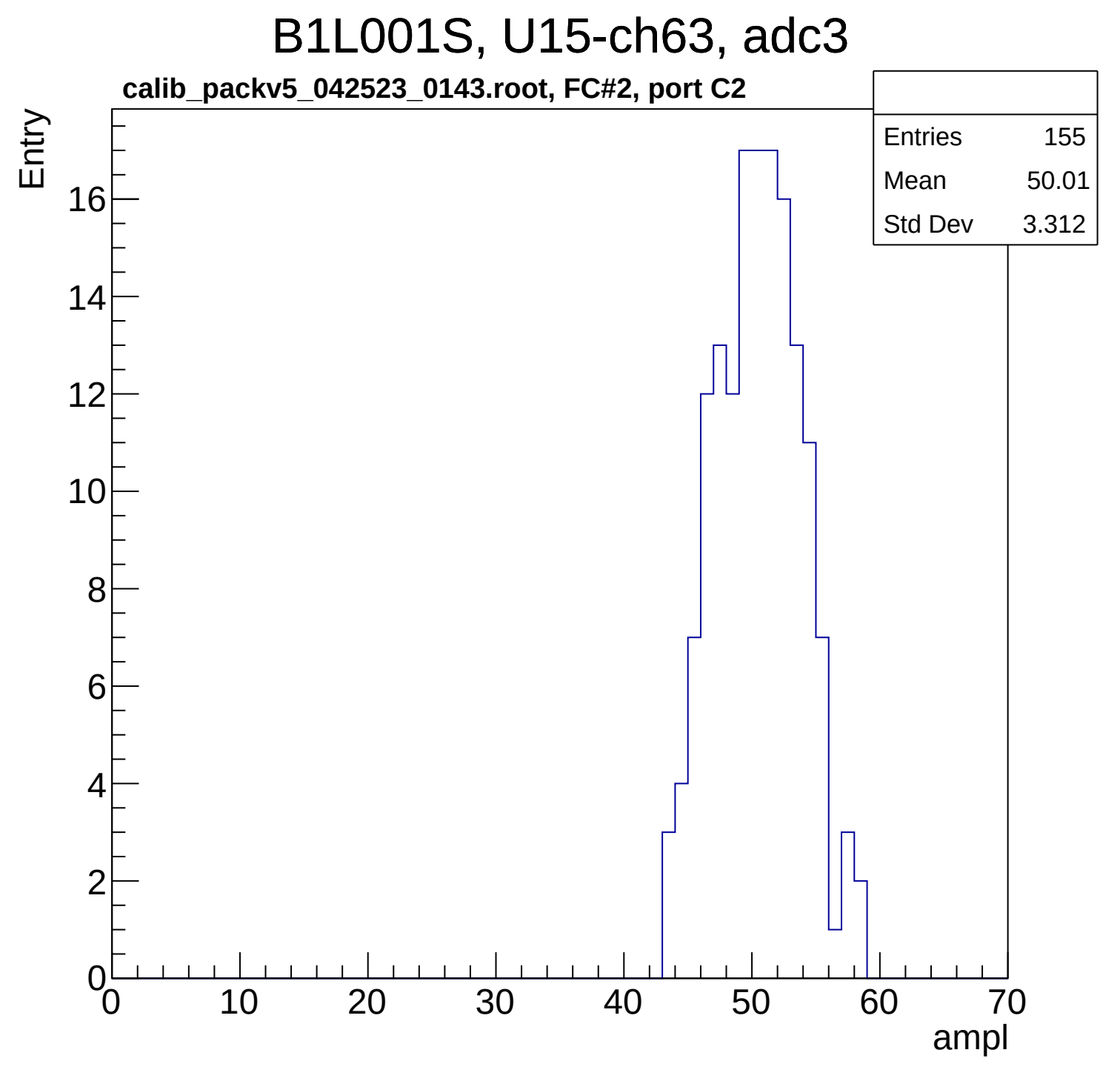

B1L001S, U15-ch63, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 42.97
Std Dev 3.175
Gaus mean : 43.6428
Gaus Width: 2.8543
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch63, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 50.01
Std Dev 3.312
Entry
ampl
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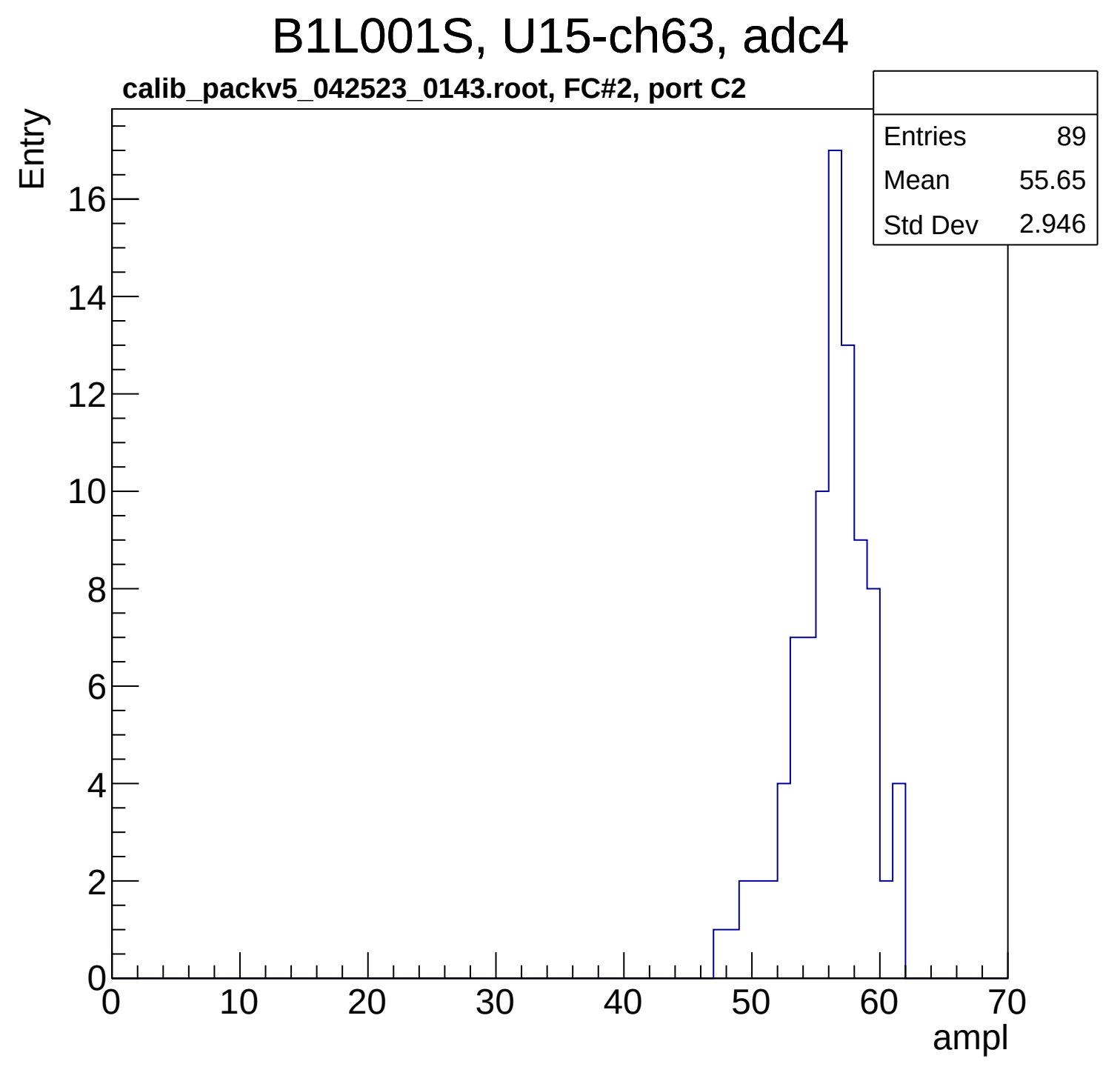
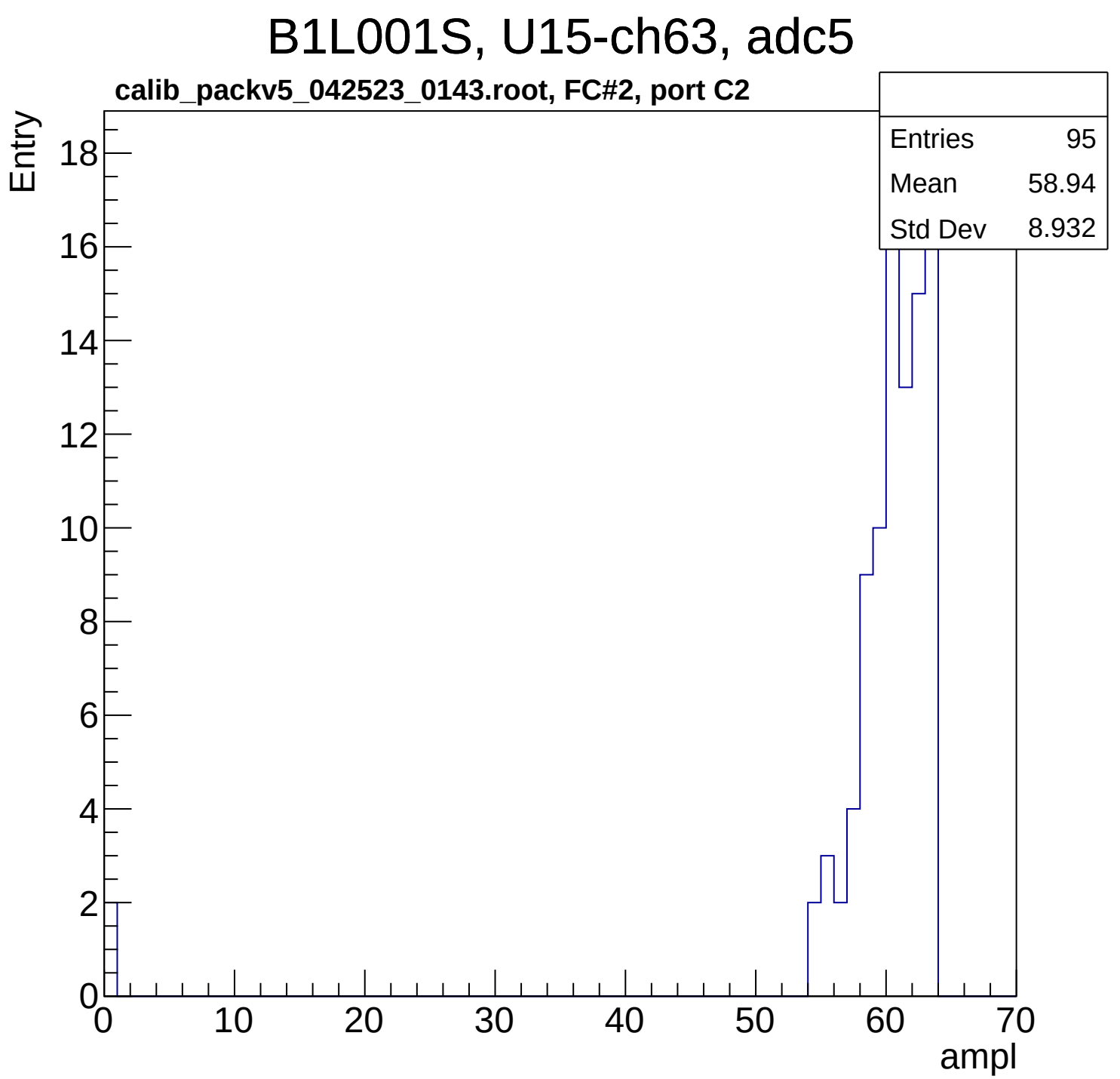
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch63, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 55.65
Std Dev 2.946
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch63, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 58.94
Std Dev 8.932
Entry
ampl
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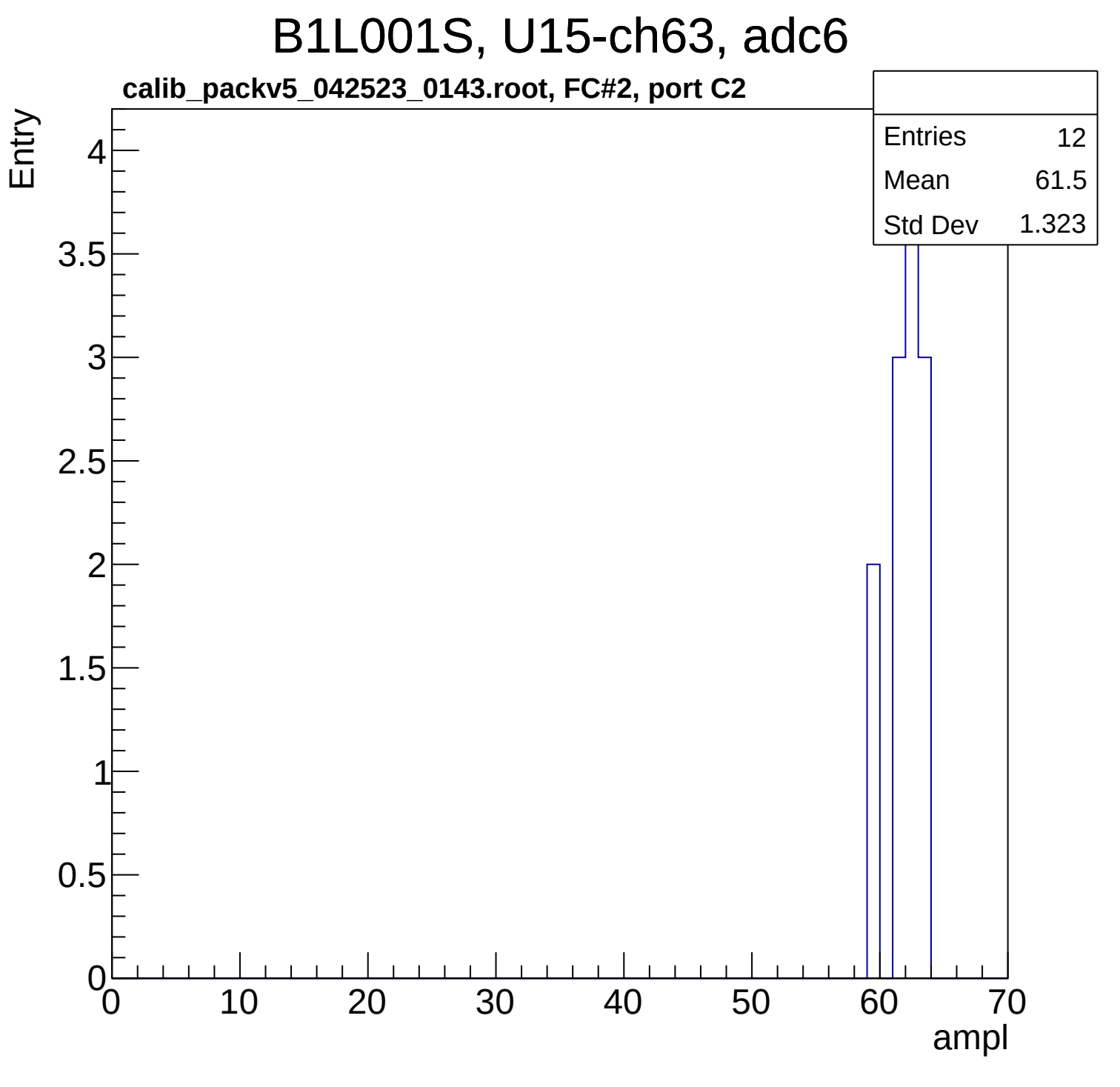
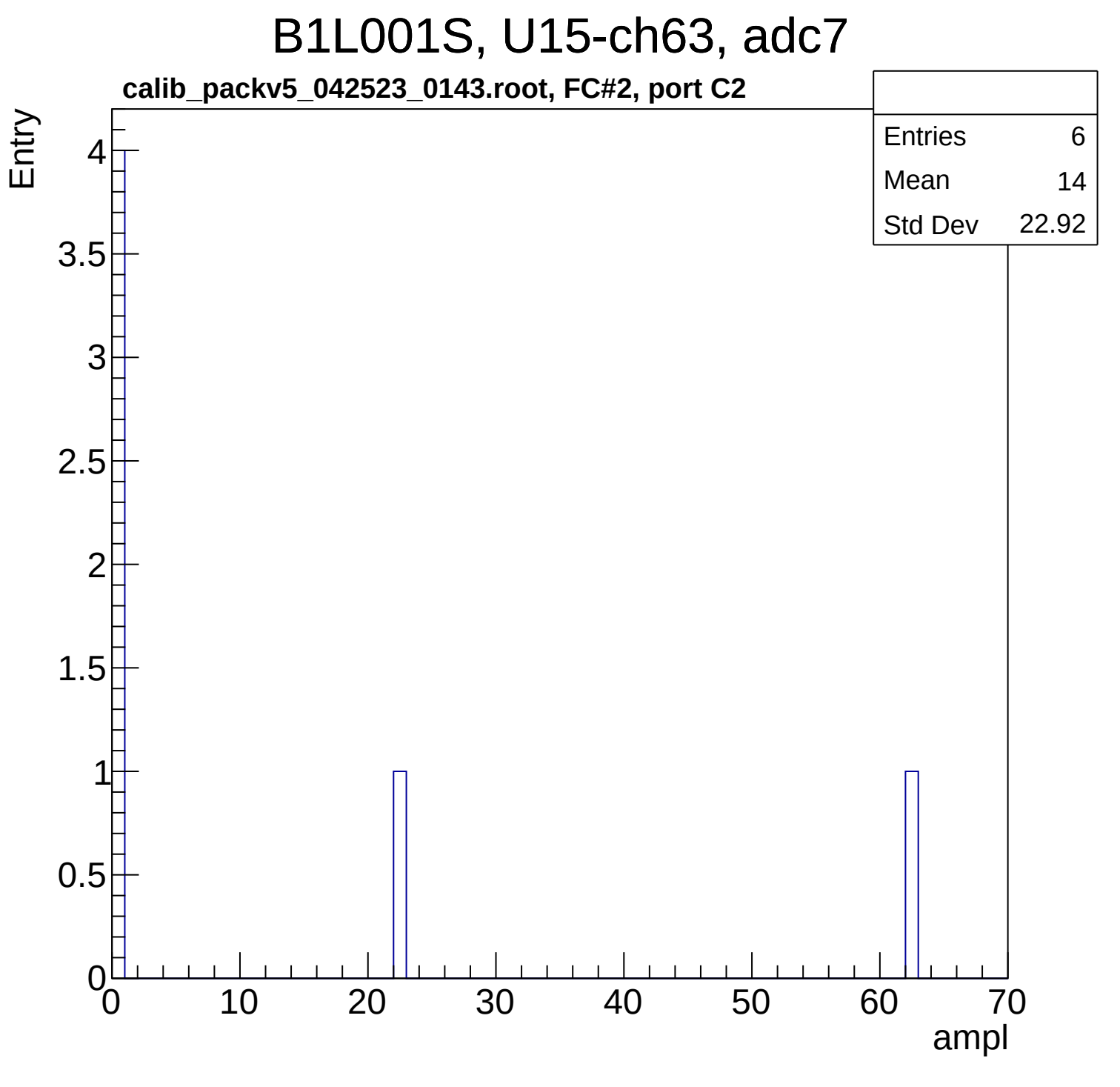
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch63, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 61.5
Std Dev 1.323
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch63, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 14
Std Dev 22.92
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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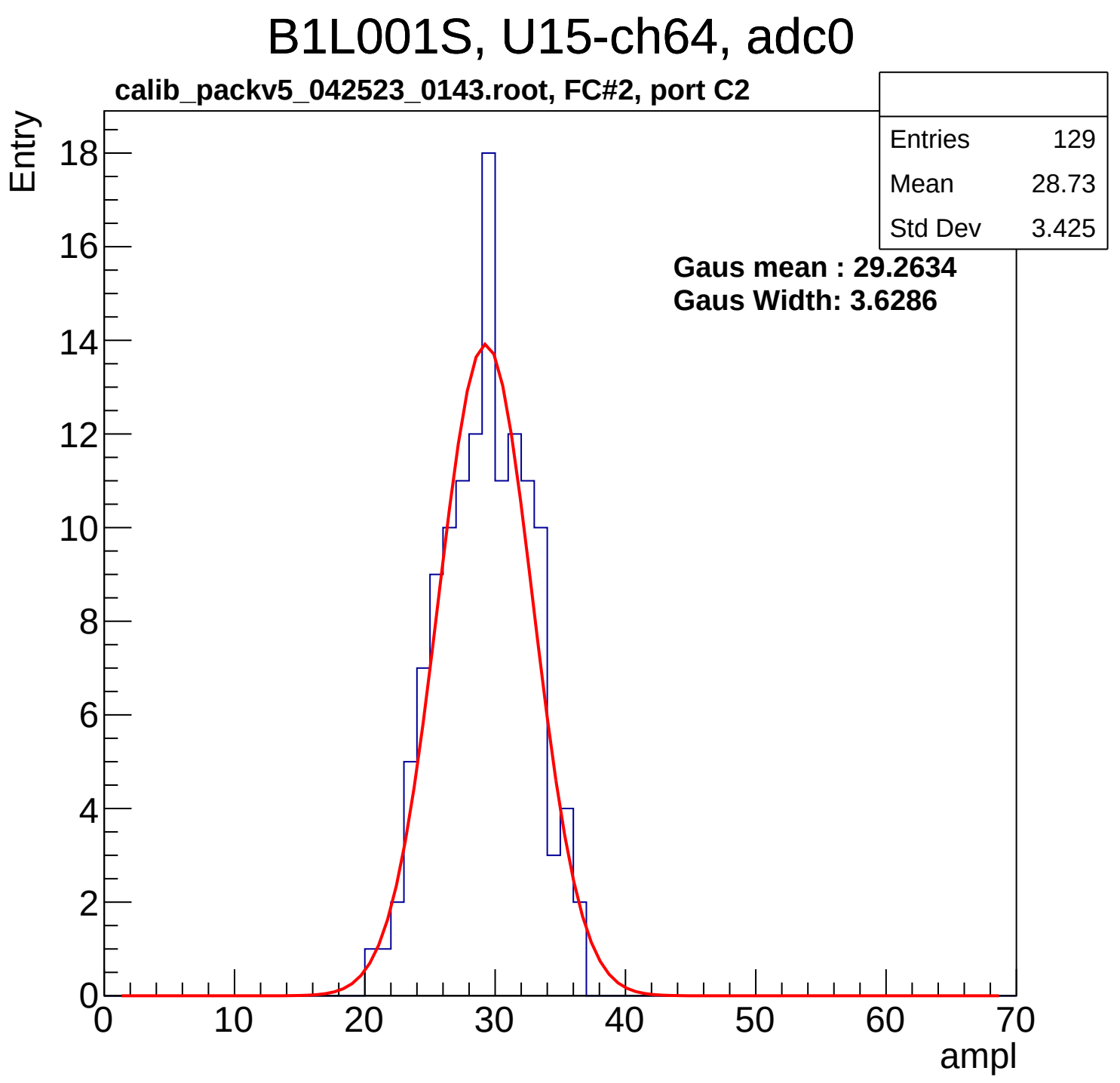
B1L001S, U15-ch64, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 28.73
Std Dev 3.425
Gaus mean : 29.2634
Gaus Width: 3.6286
Entry
ampl
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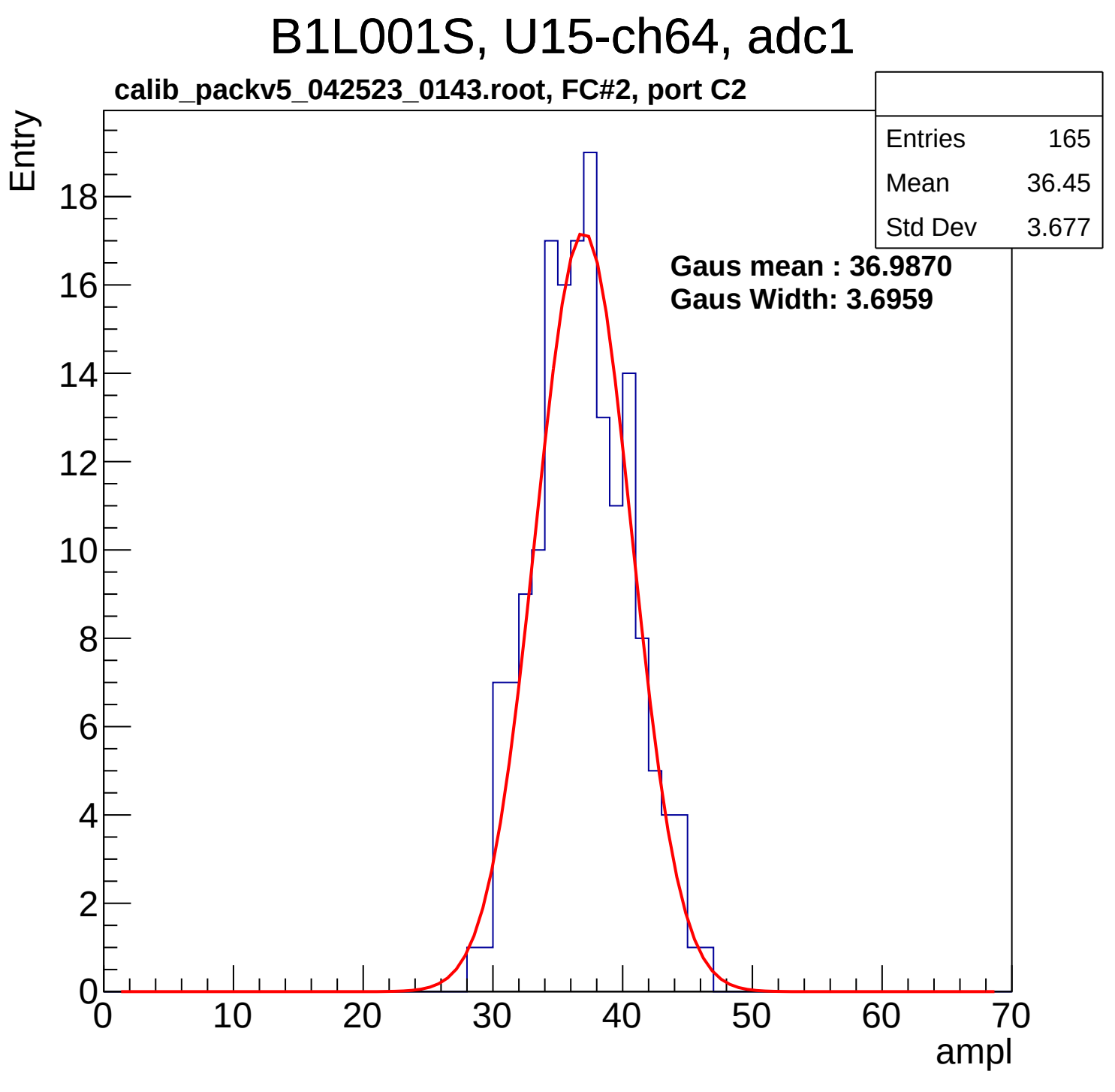
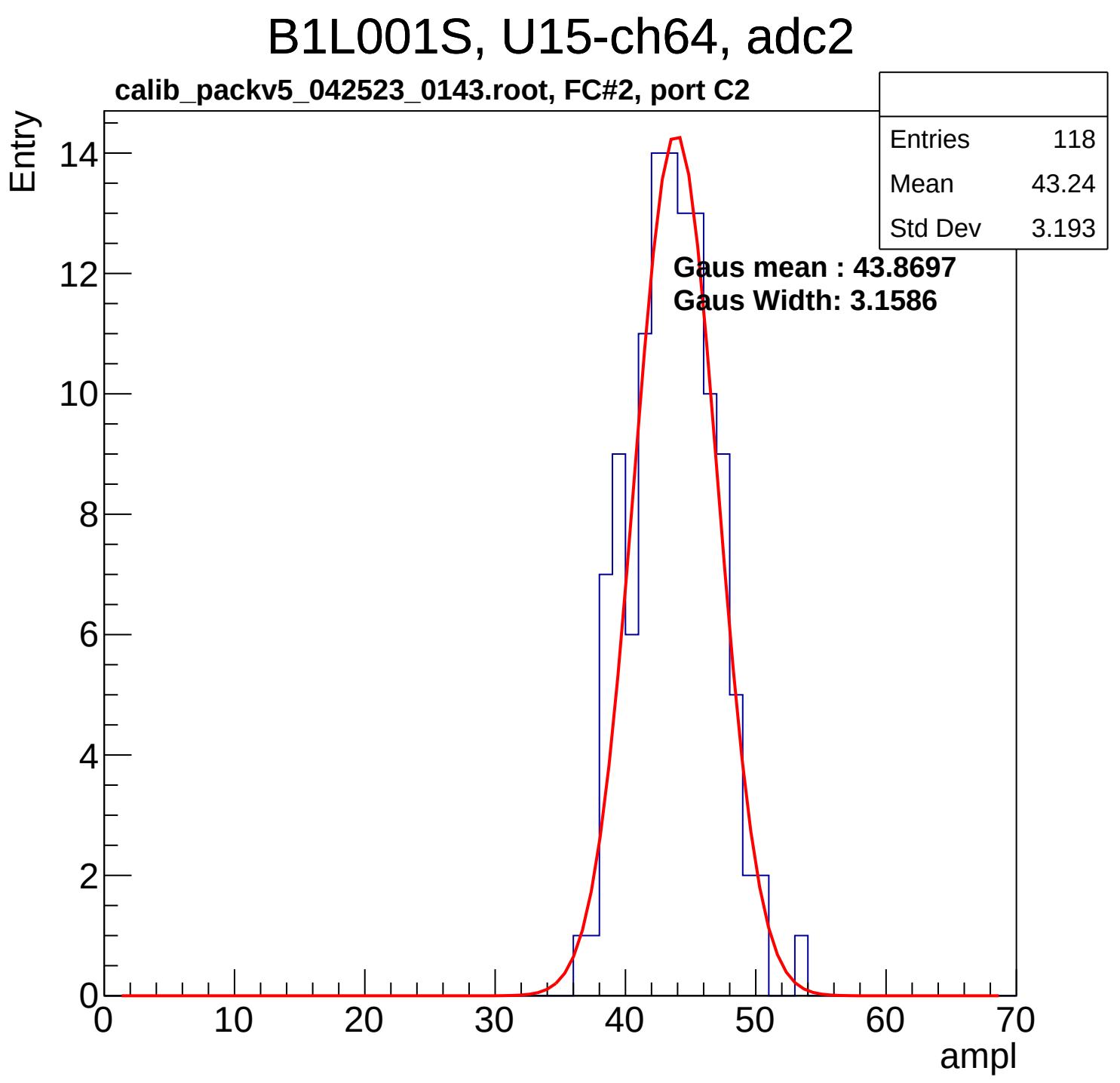
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch64, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 165
Mean 36.45
Std Dev 3.677
Gaus mean : 36.9870
Gaus Width: 3.6959
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch64, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 43.24
Std Dev 3.193
Gaus mean : 43.8697
Gaus Width: 3.1586
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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60
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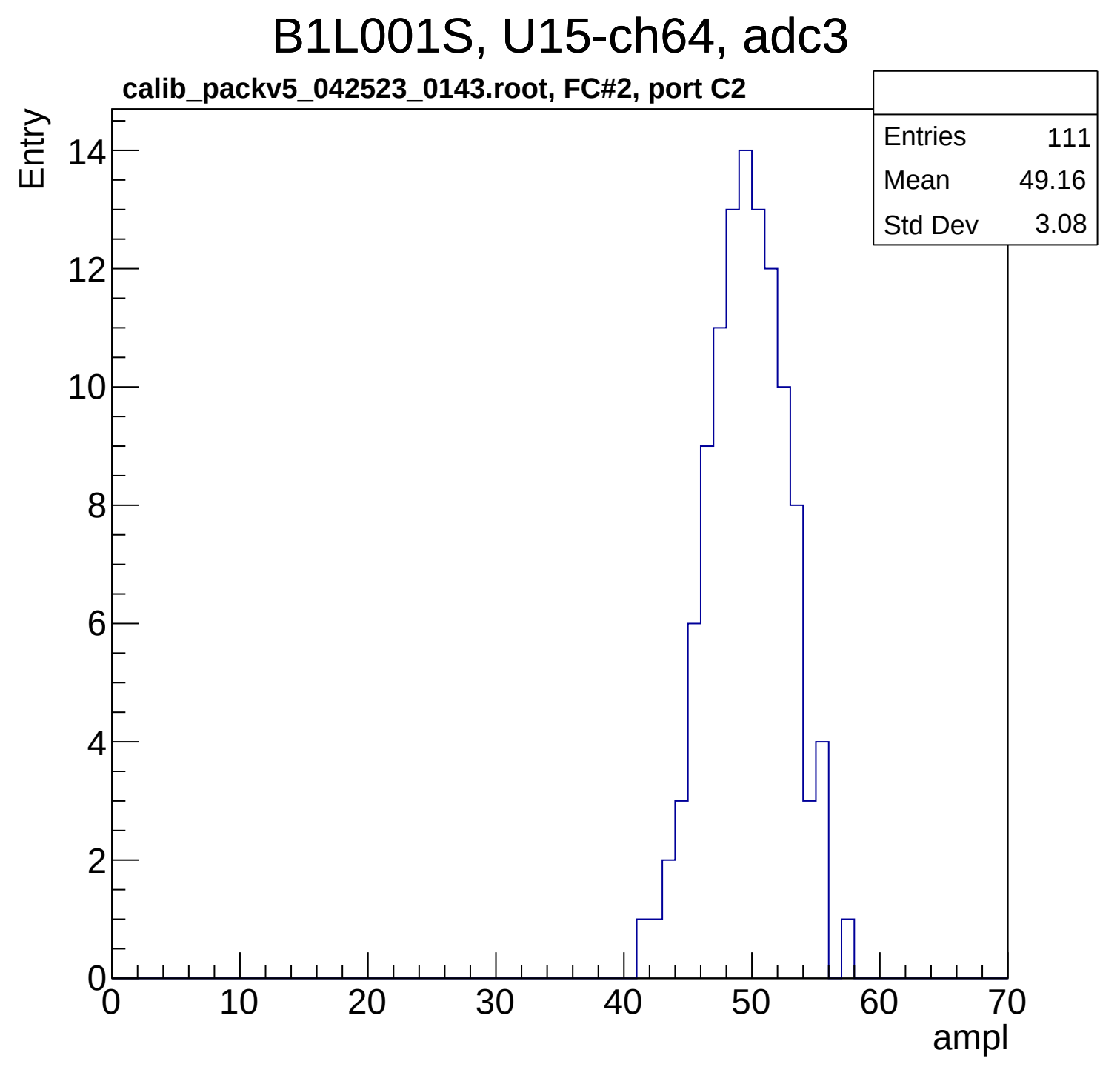
B1L001S, U15-ch64, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 49.16
Std Dev 3.08
Entry
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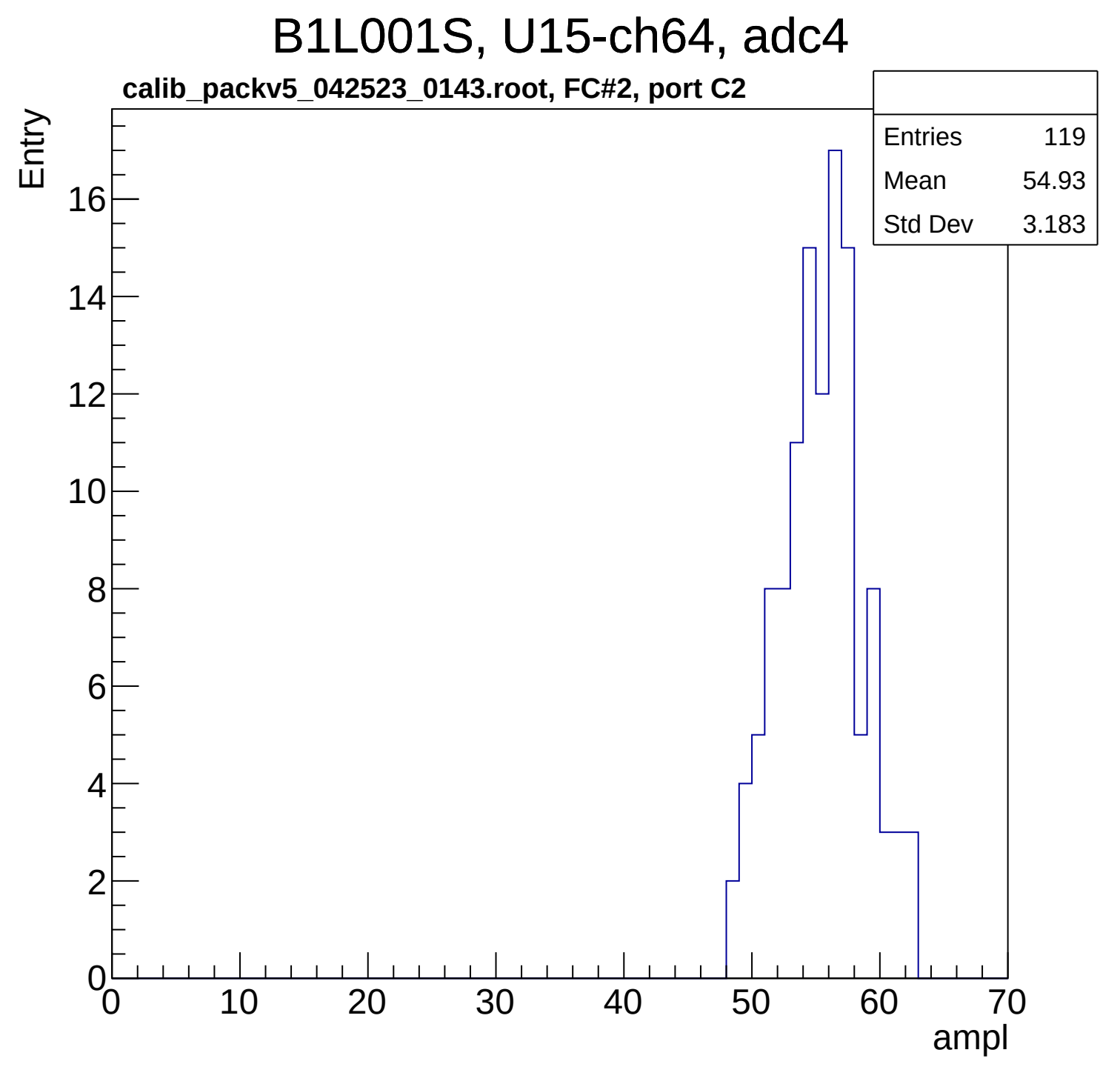
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch64, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 54.93
Std Dev 3.183
Entry
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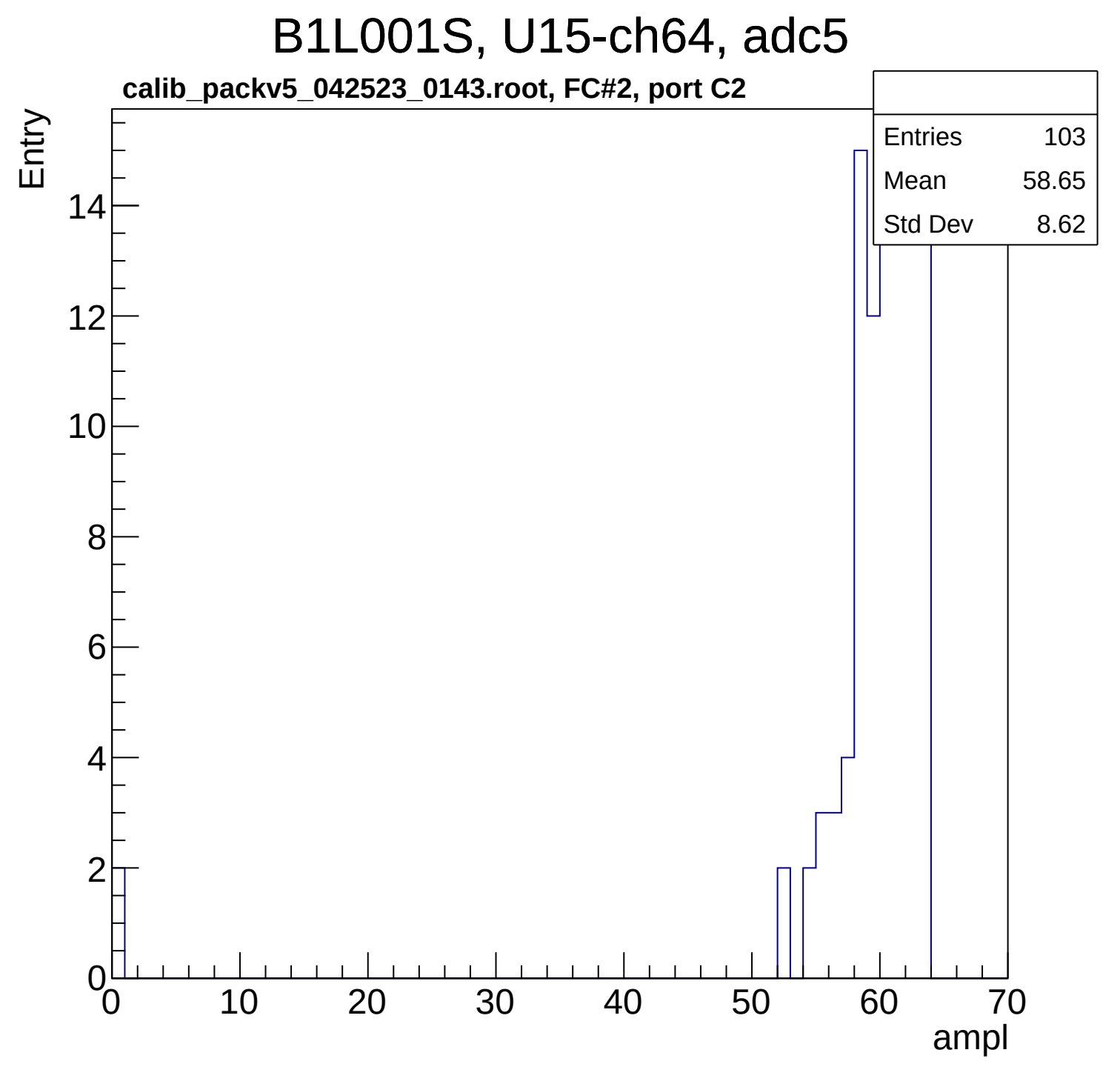
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch64, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.65
Std Dev 8.62
Entry
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2
4
6
8
10
12
14
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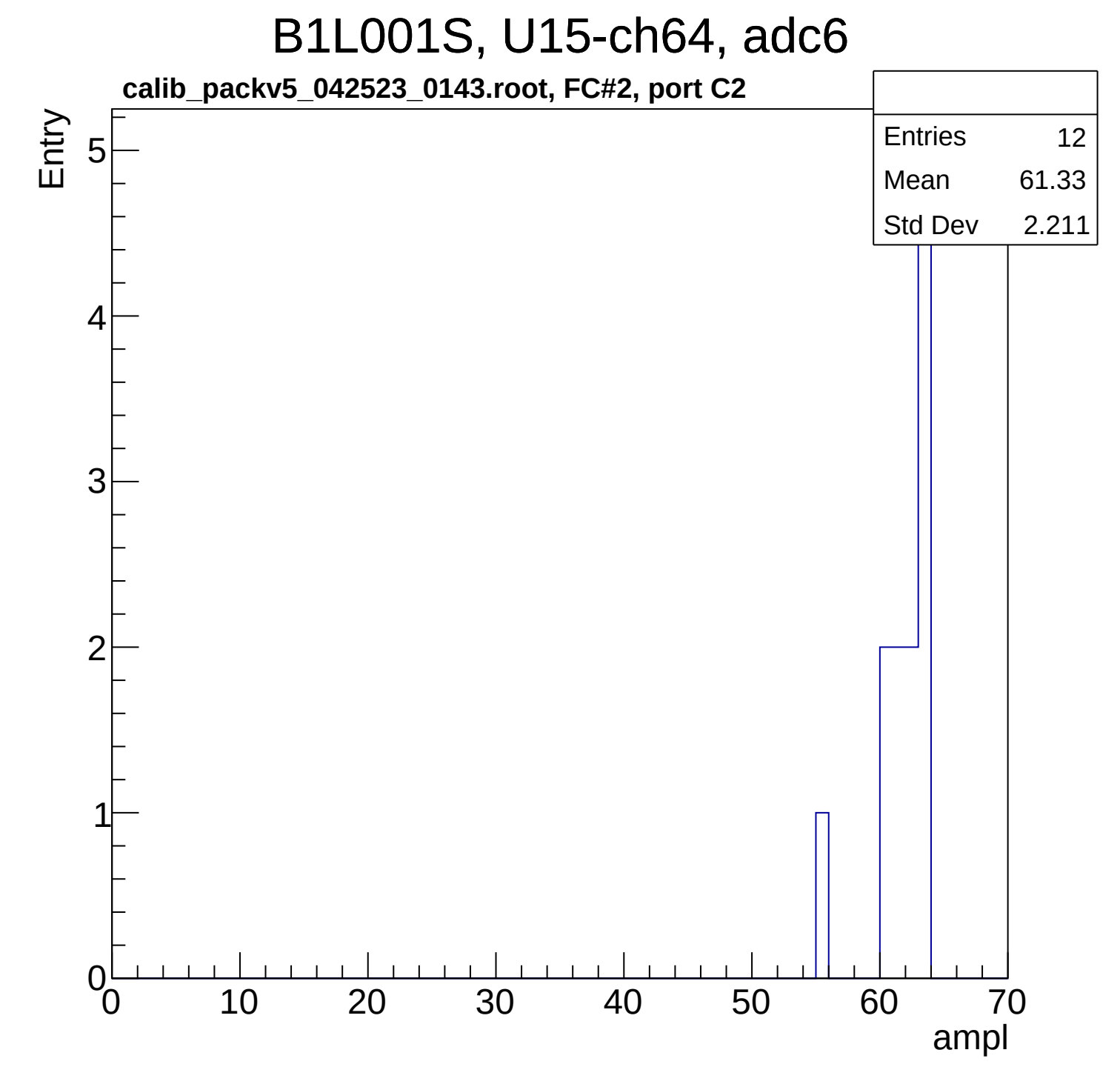
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch64, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 61.33
Std Dev 2.211
Entry
ampl
0
1
2
3
4
5
0
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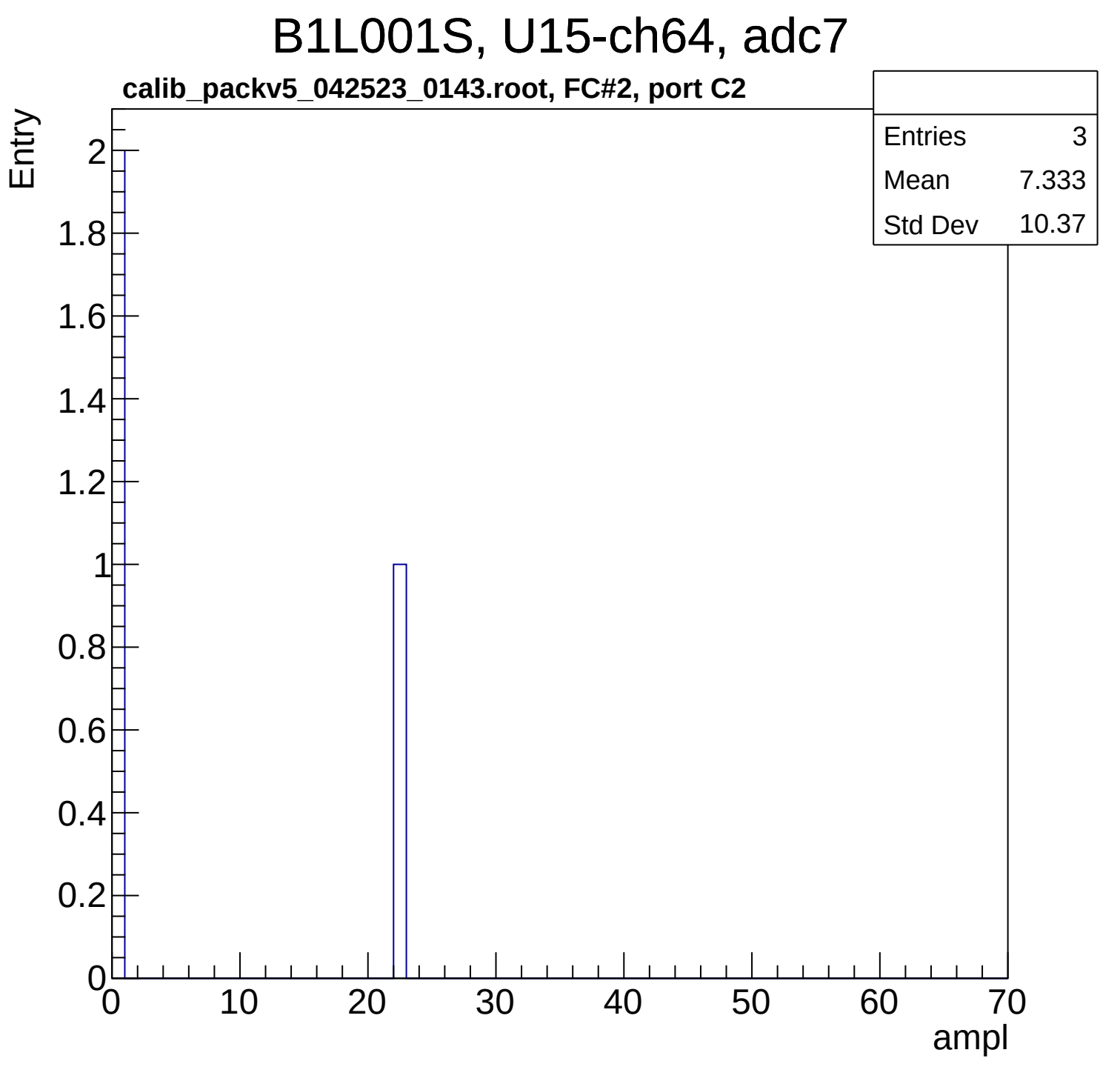
20
30
40
50
60
70


B1L001S, U15-ch64, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
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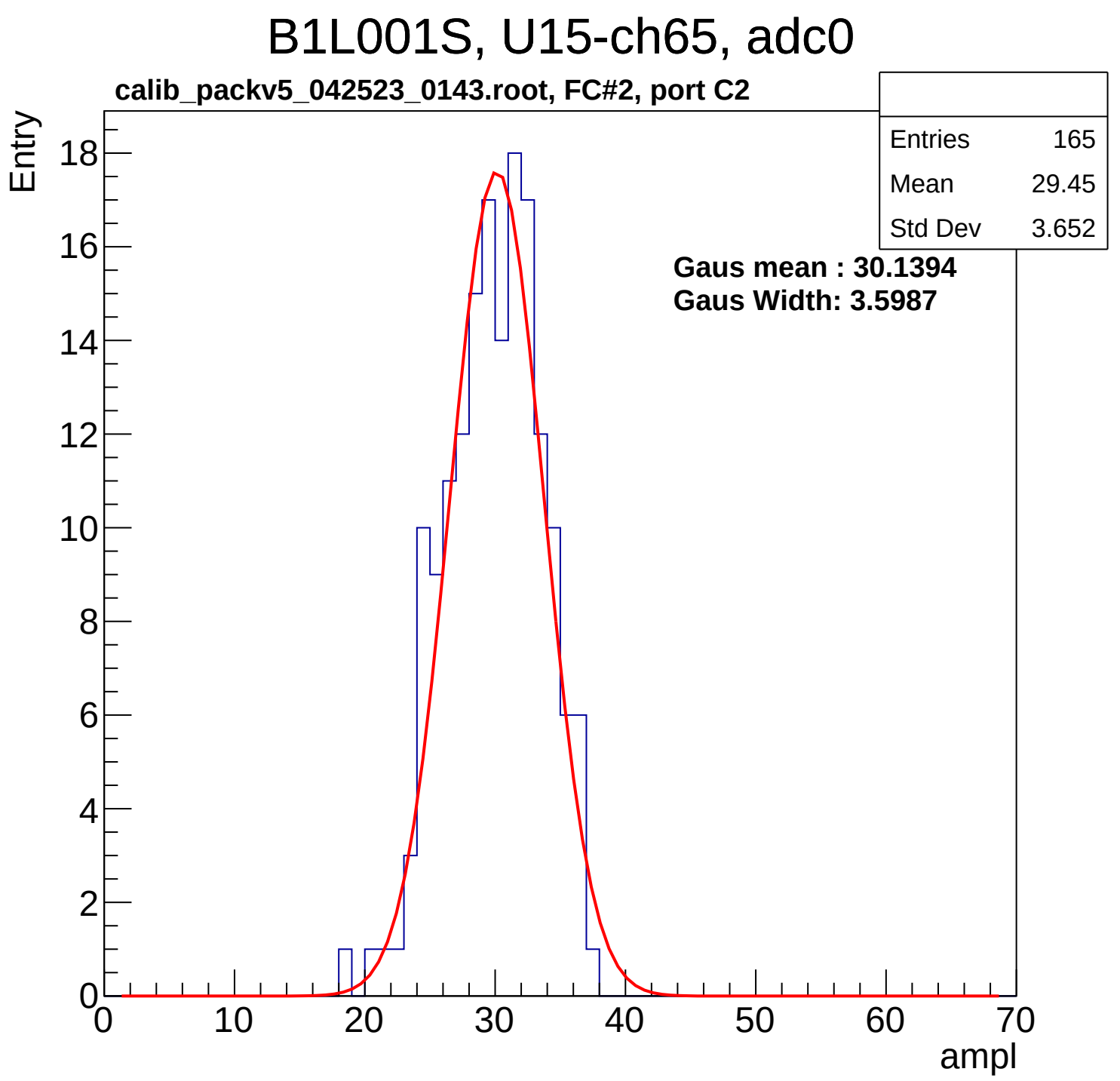
40
50
60
70


B1L001S, U15-ch65, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 165
Mean 29.45
Std Dev 3.652
Gaus mean : 30.1394
Gaus Width: 3.5987
Entry
ampl
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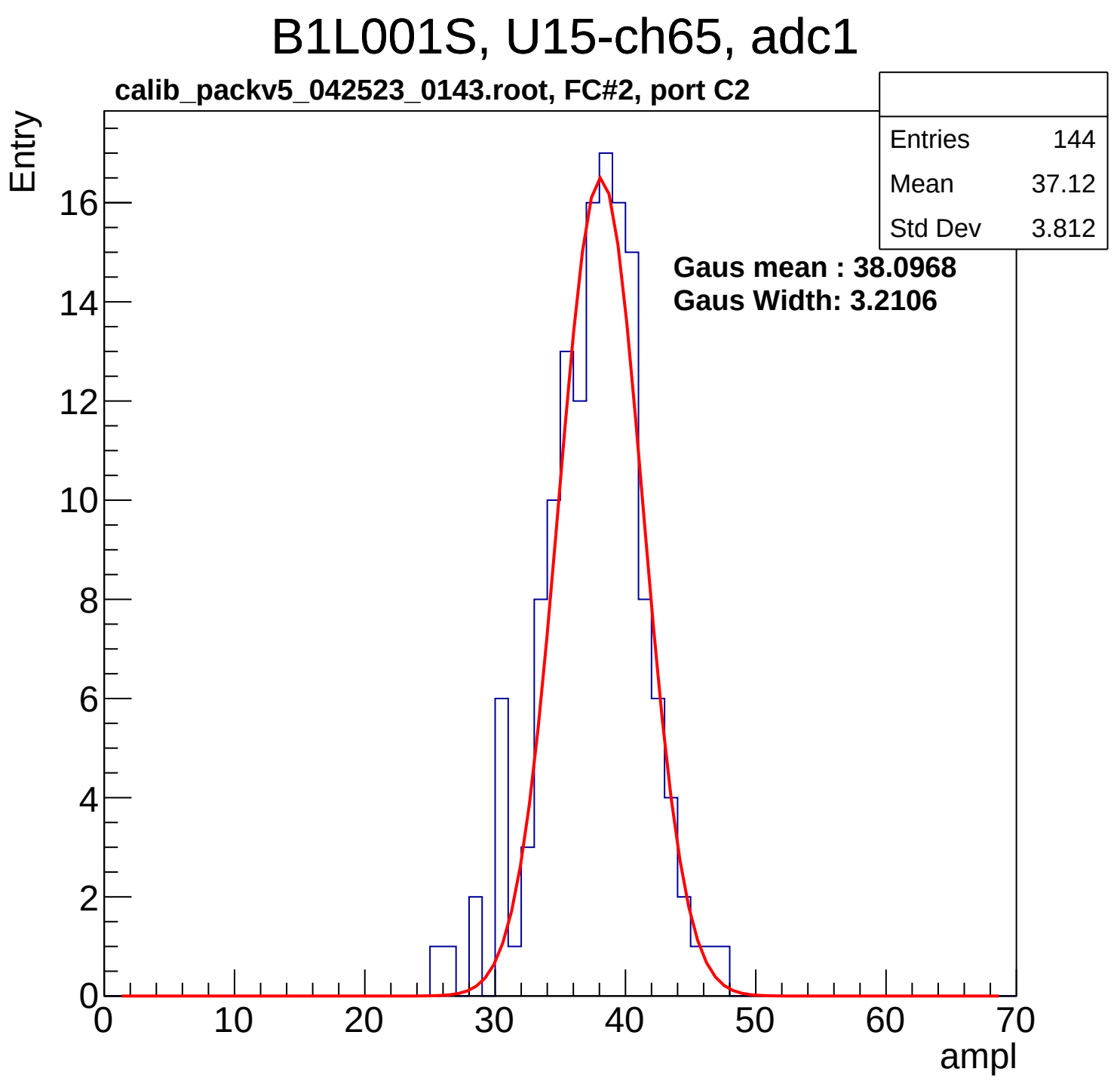
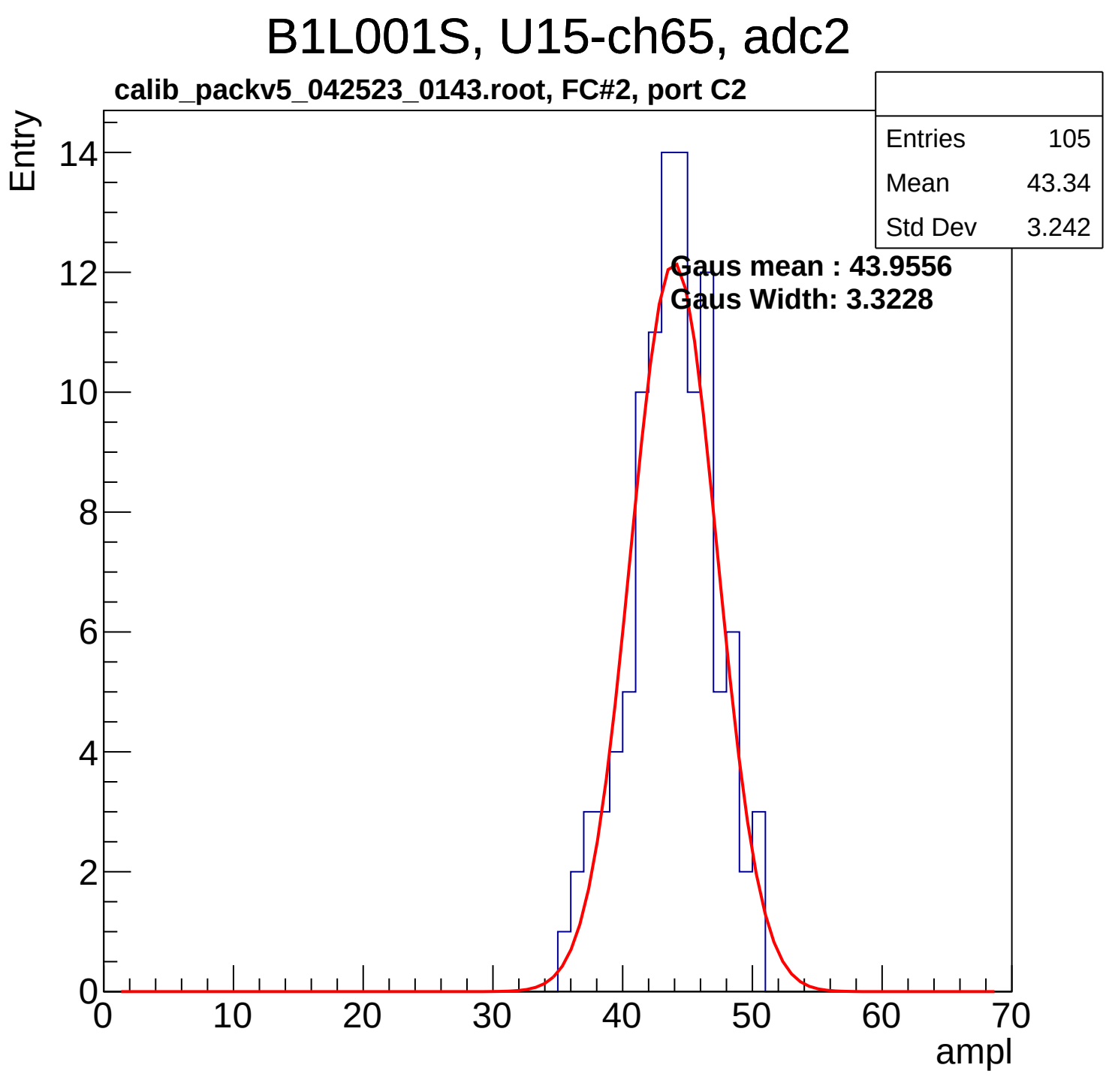
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch65, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 37.12
Std Dev 3.812
Gaus mean : 38.0968
Gaus Width: 3.2106
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch65, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 43.34
Std Dev 3.242
Gaus mean : 43.9556
Gaus Width: 3.3228
Entry
ampl
0
2
4
6
8
10
12
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0
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20
30
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50
60
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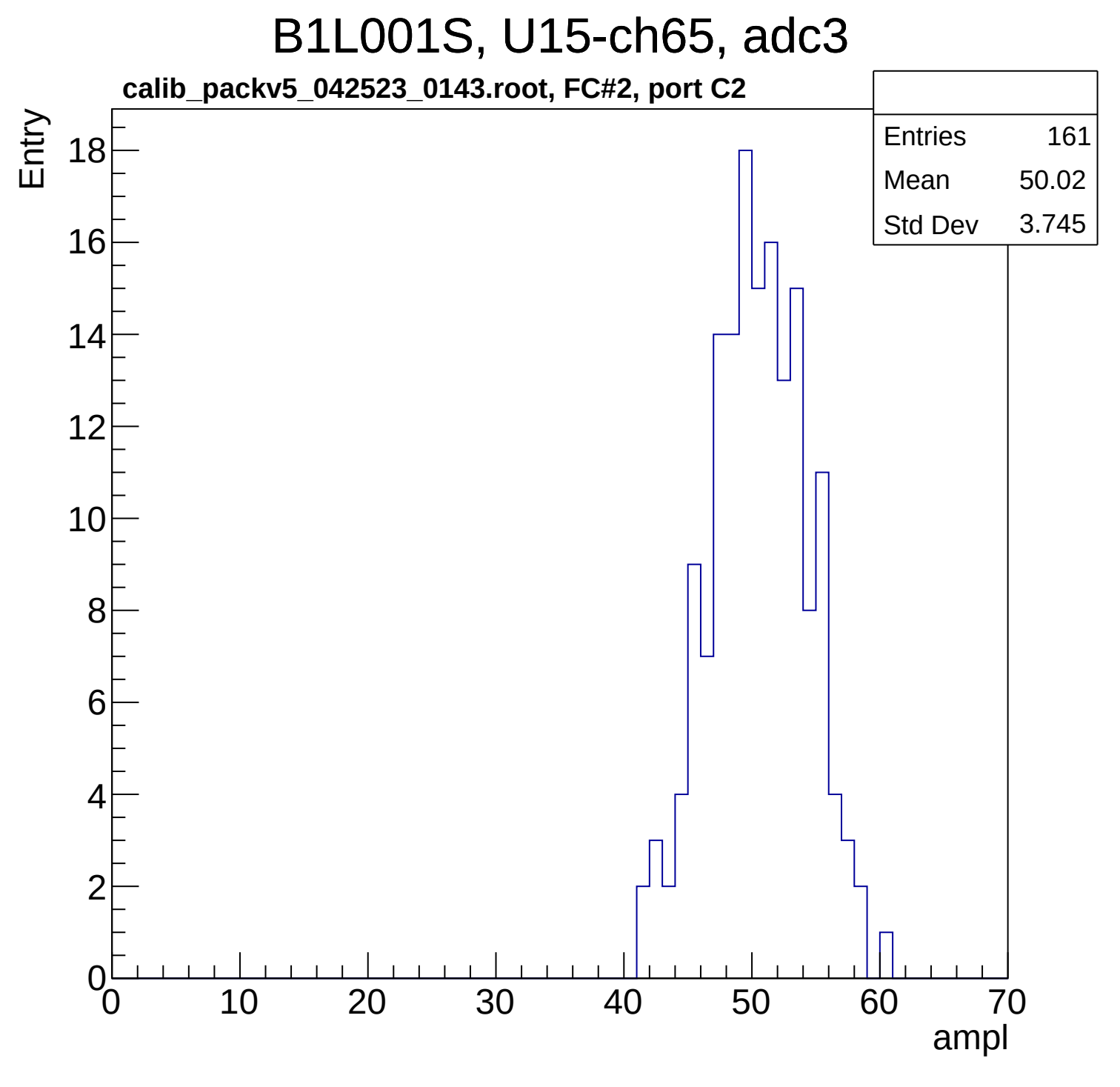
B1L001S, U15-ch65, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 50.02
Std Dev 3.745
Entry
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6
8
10
12
14
16
18
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10
20
30
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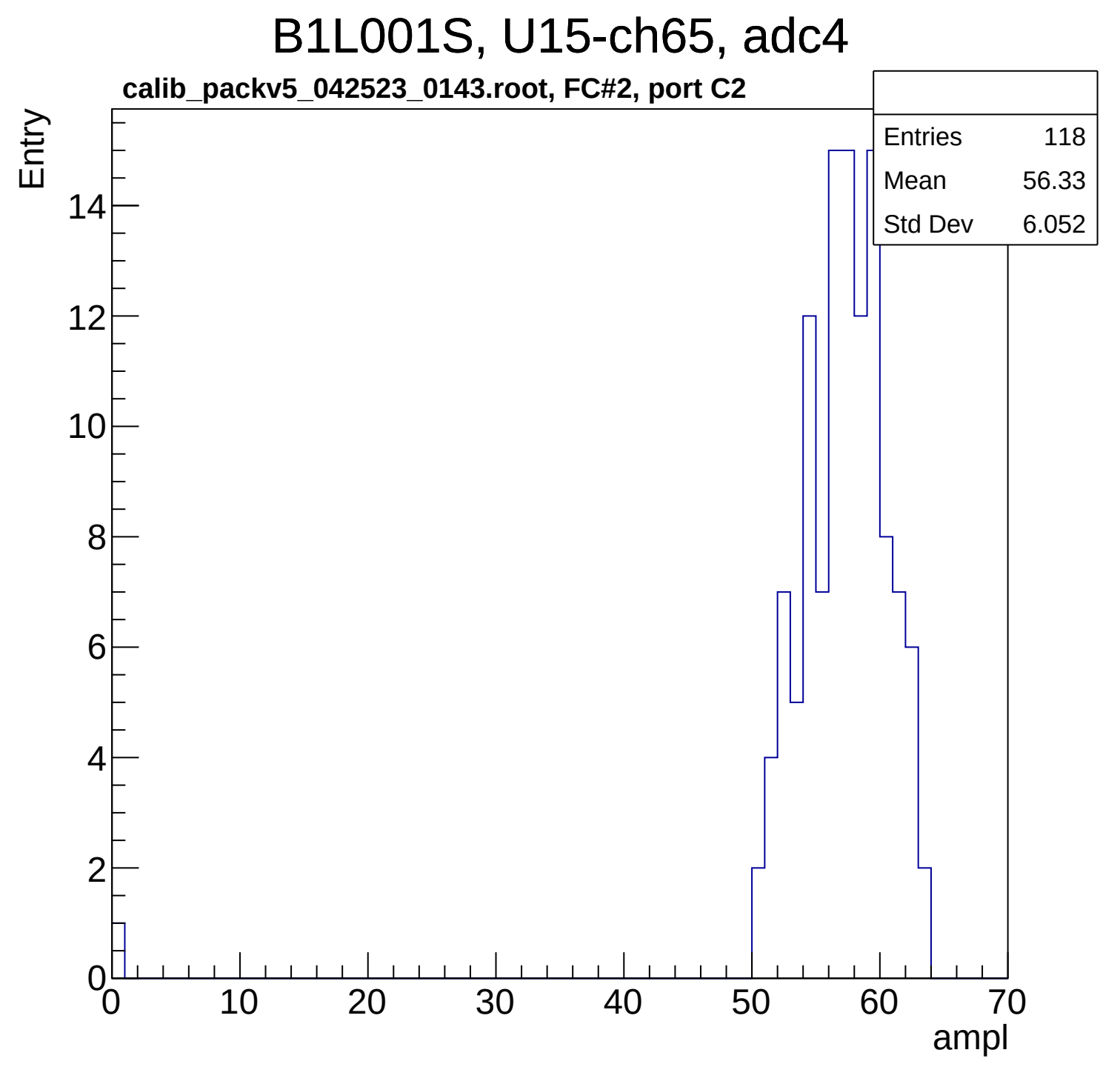
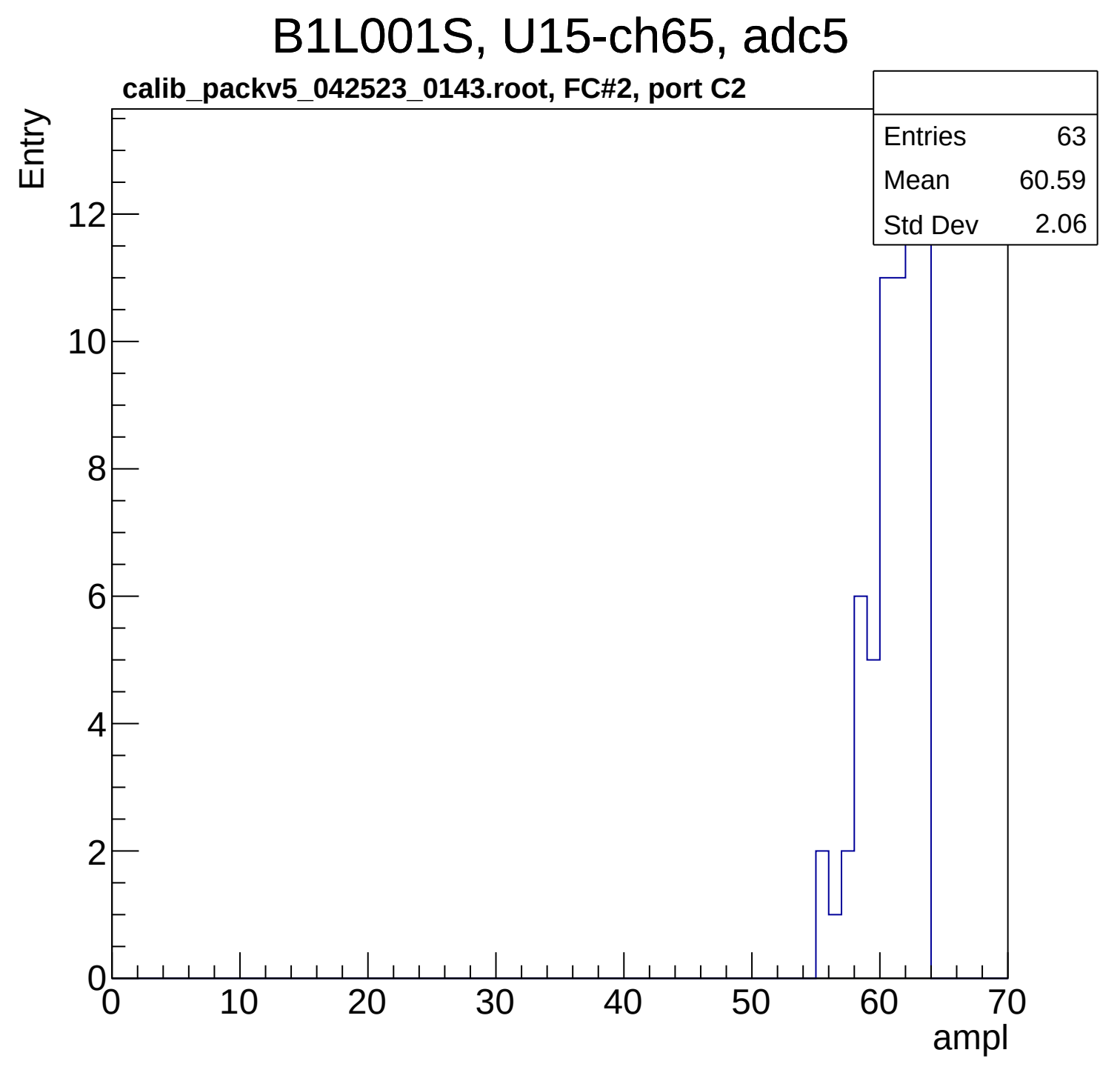
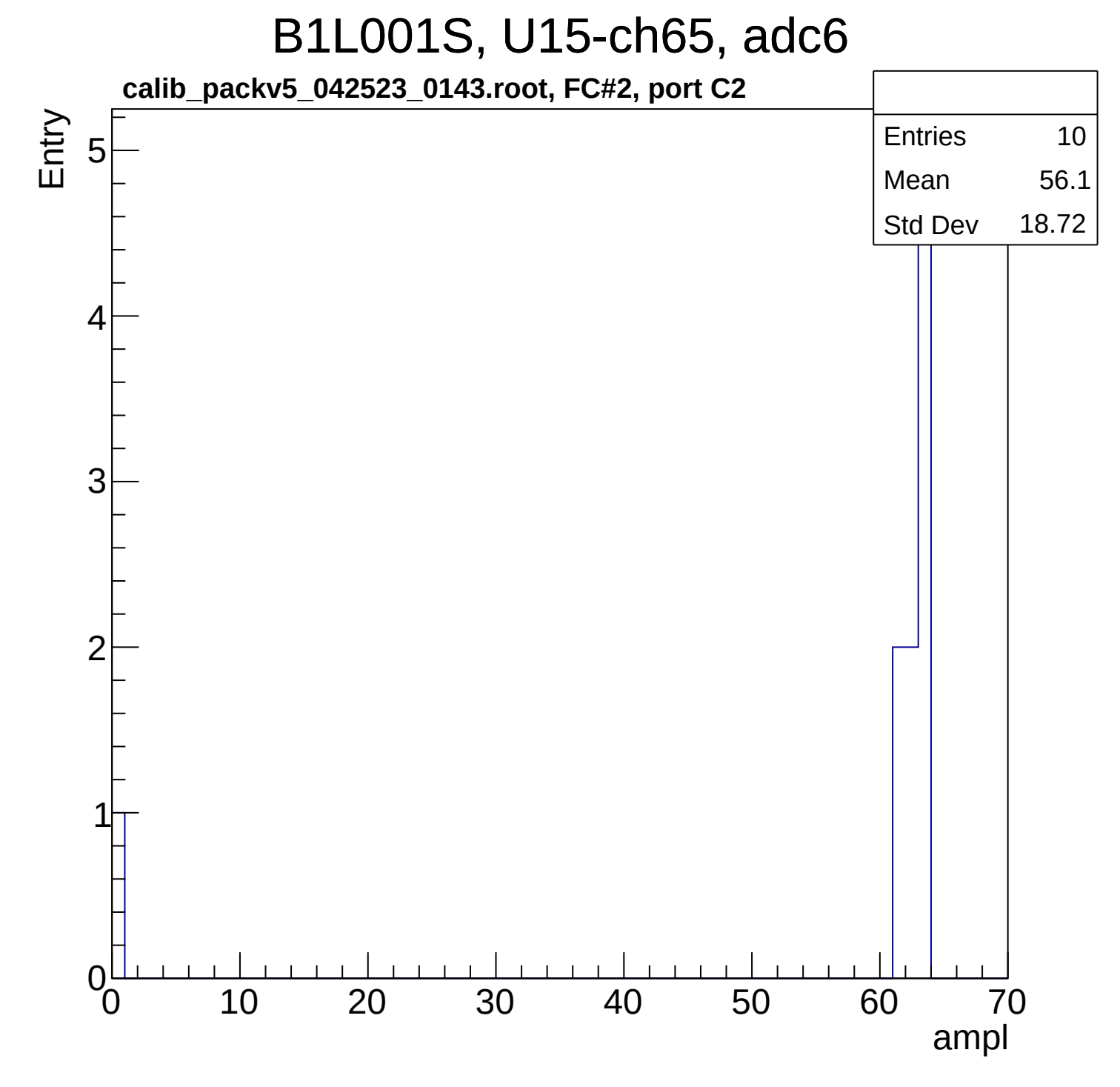
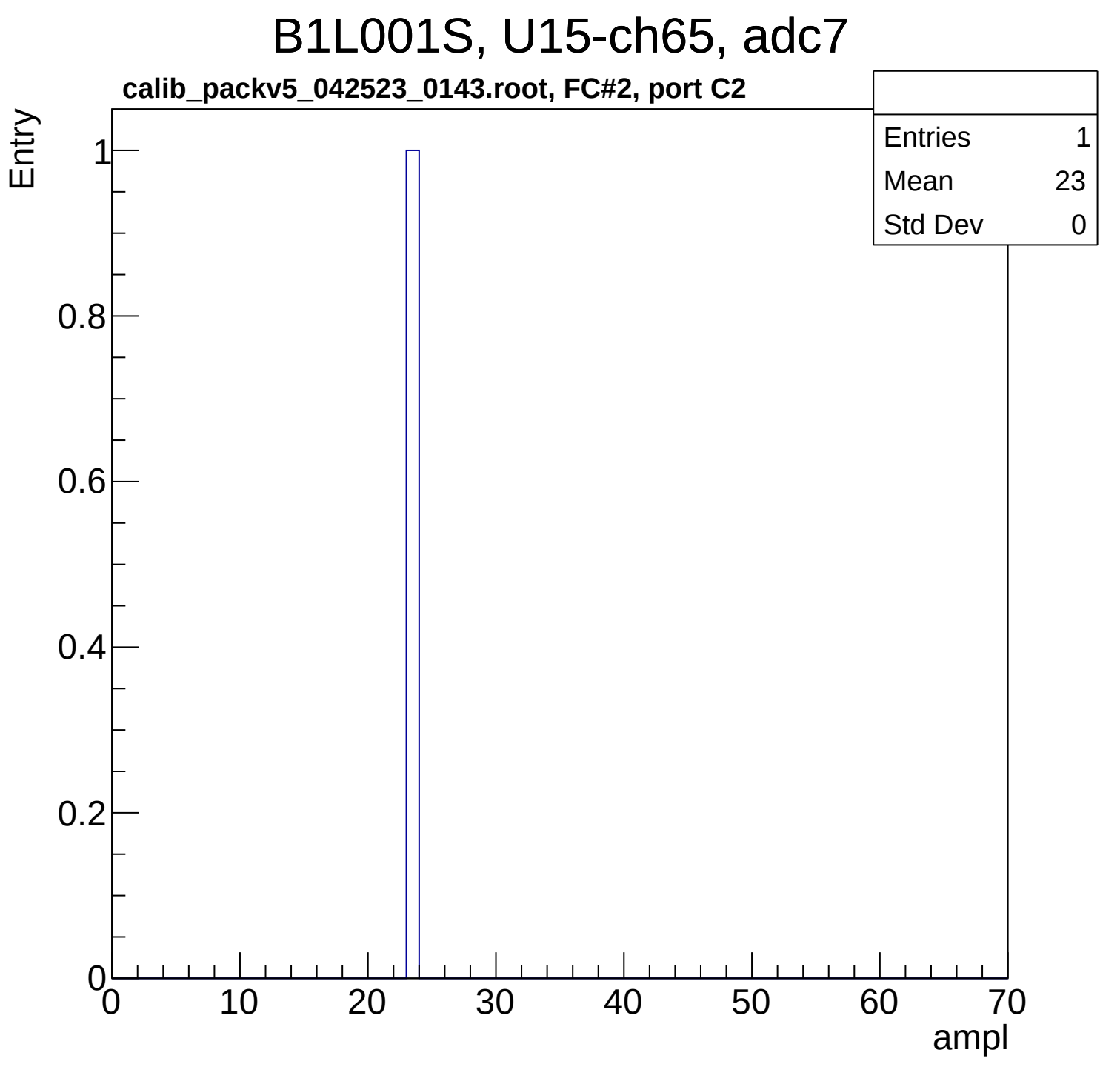
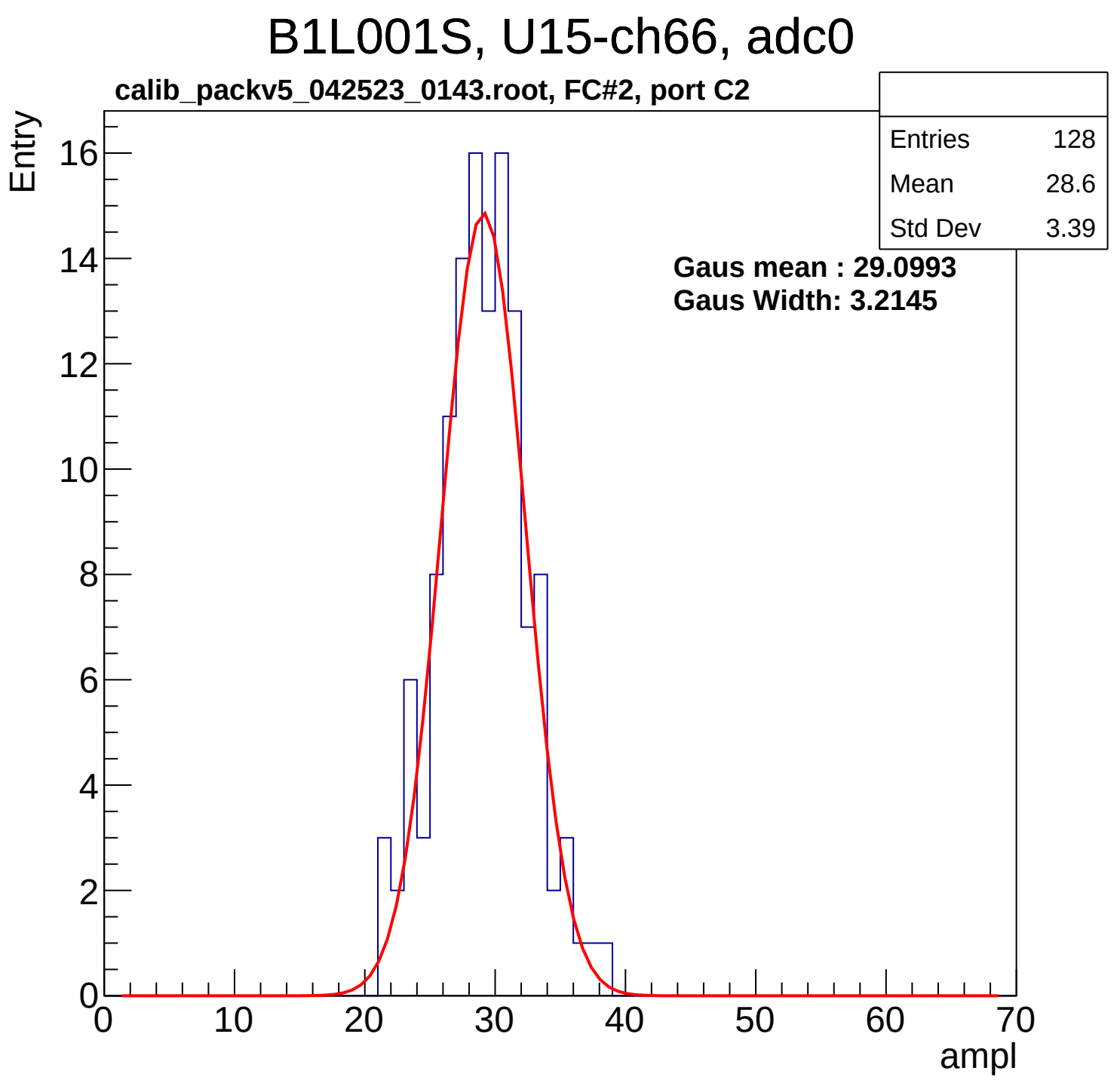
50
60
70
ampl

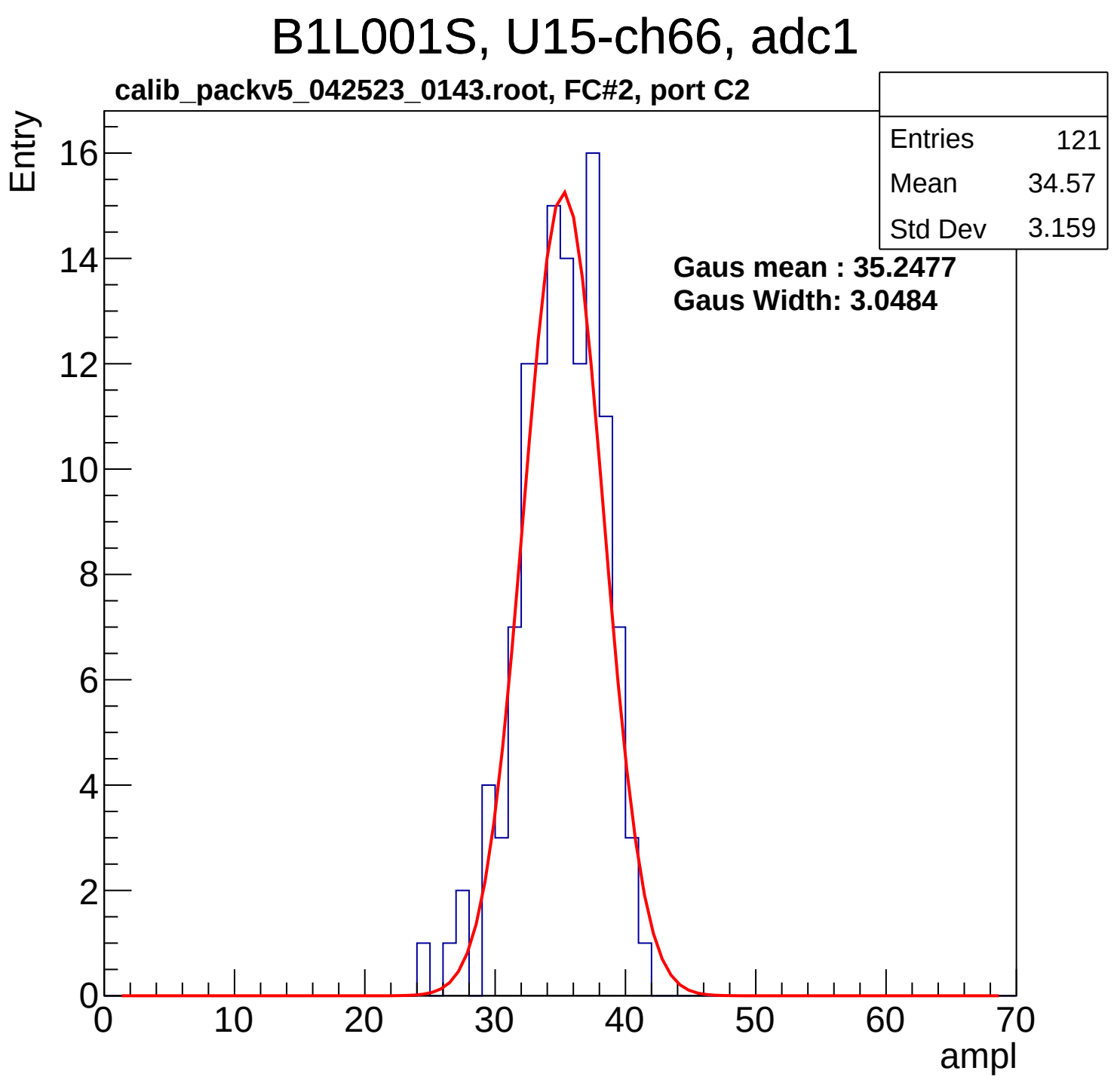

B1L001S, U15-ch65, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 56.33
Std Dev 6.052
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch65, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 63
Mean 60.59
Std Dev 2.06
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch65, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 56.1
Std Dev 18.72
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch65, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch66, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 28.6
Std Dev 3.39
Gaus mean : 29.0993
Gaus Width: 3.2145
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch66, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 34.57
Std Dev 3.159
Gaus mean : 35.2477
Gaus Width: 3.0484
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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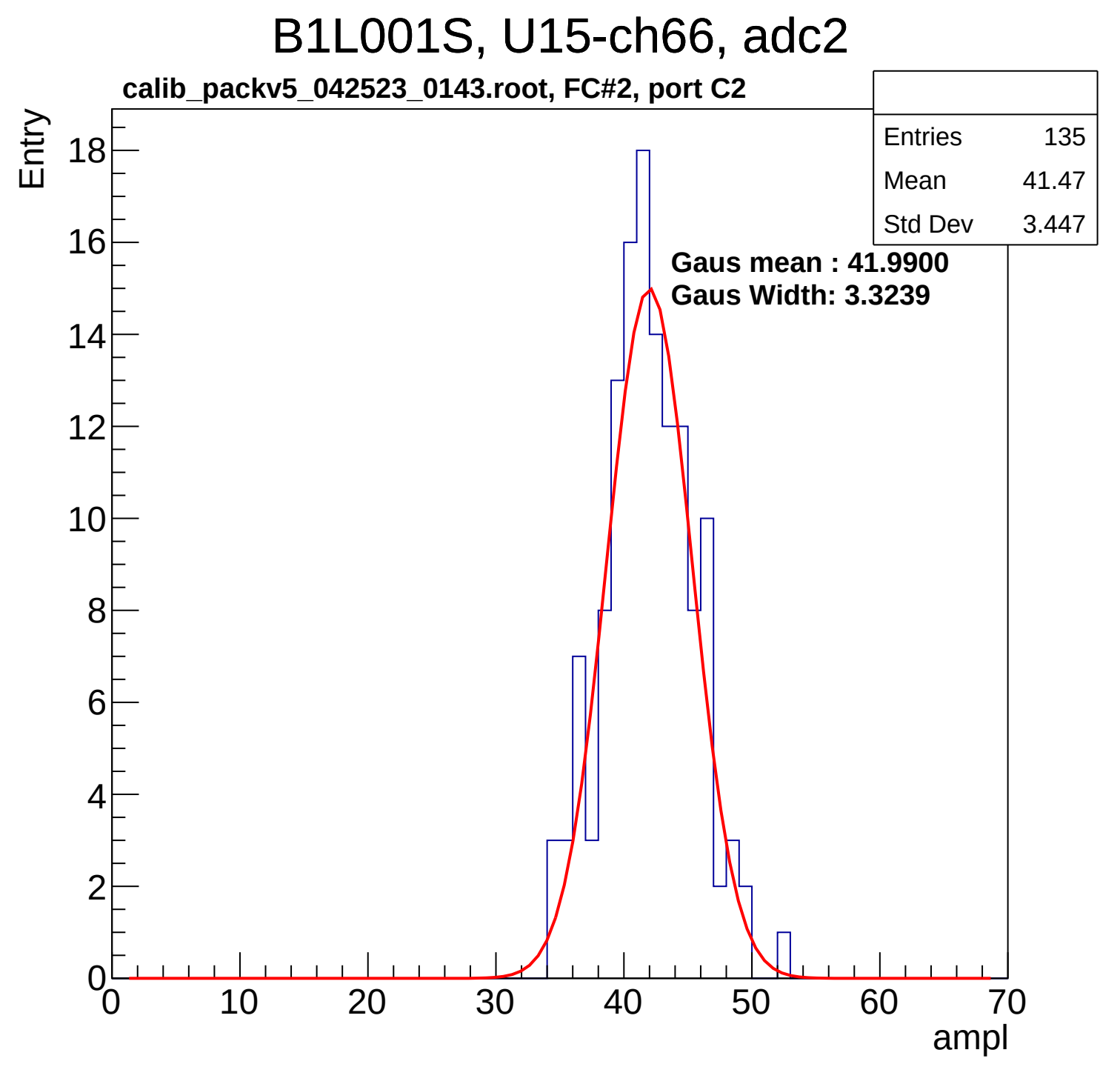
B1L001S, U15-ch66, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 41.47
Std Dev 3.447
Gaus mean : 41.9900
Gaus Width: 3.3239
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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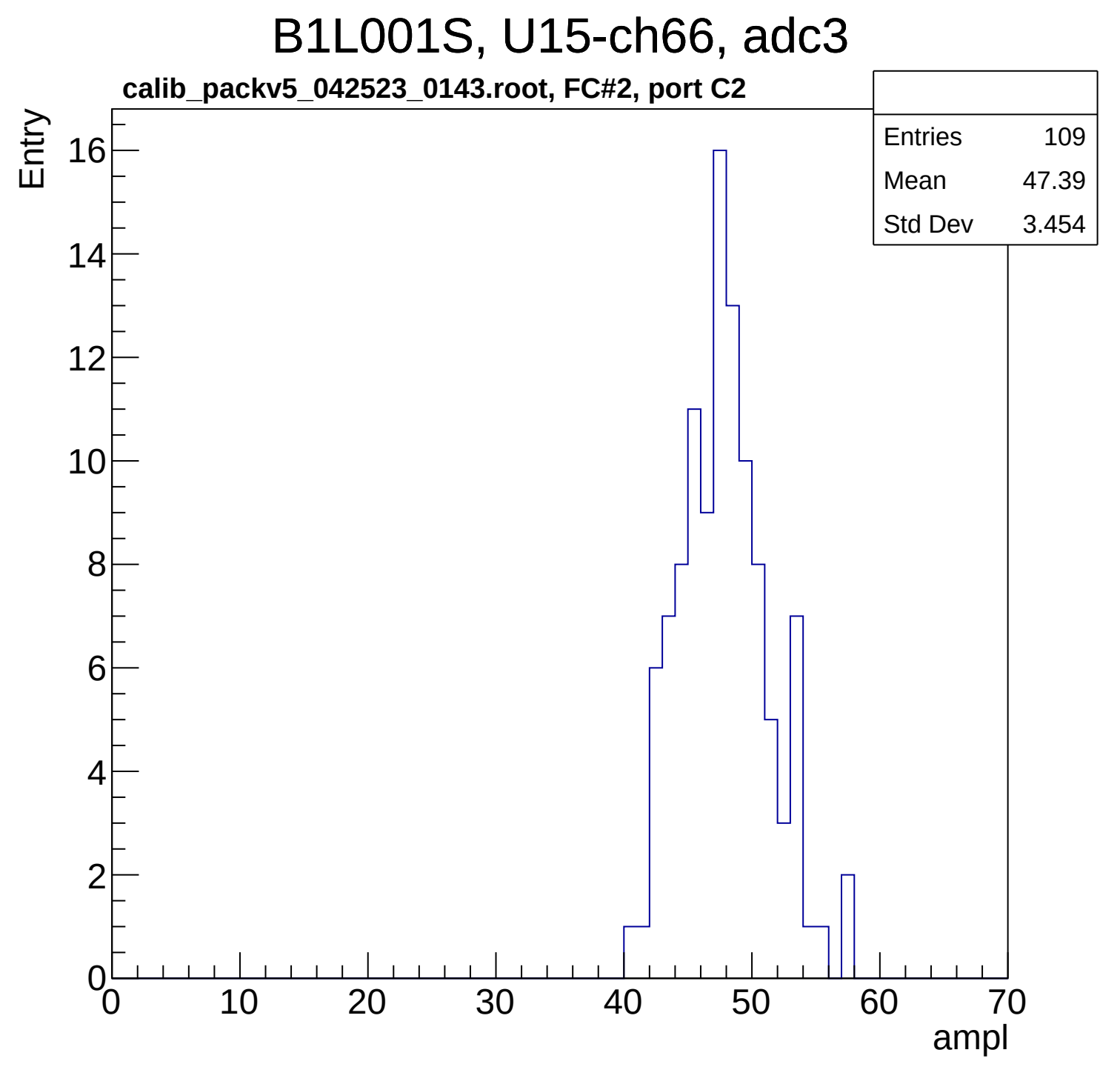
B1L001S, U15-ch66, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 47.39
Std Dev 3.454
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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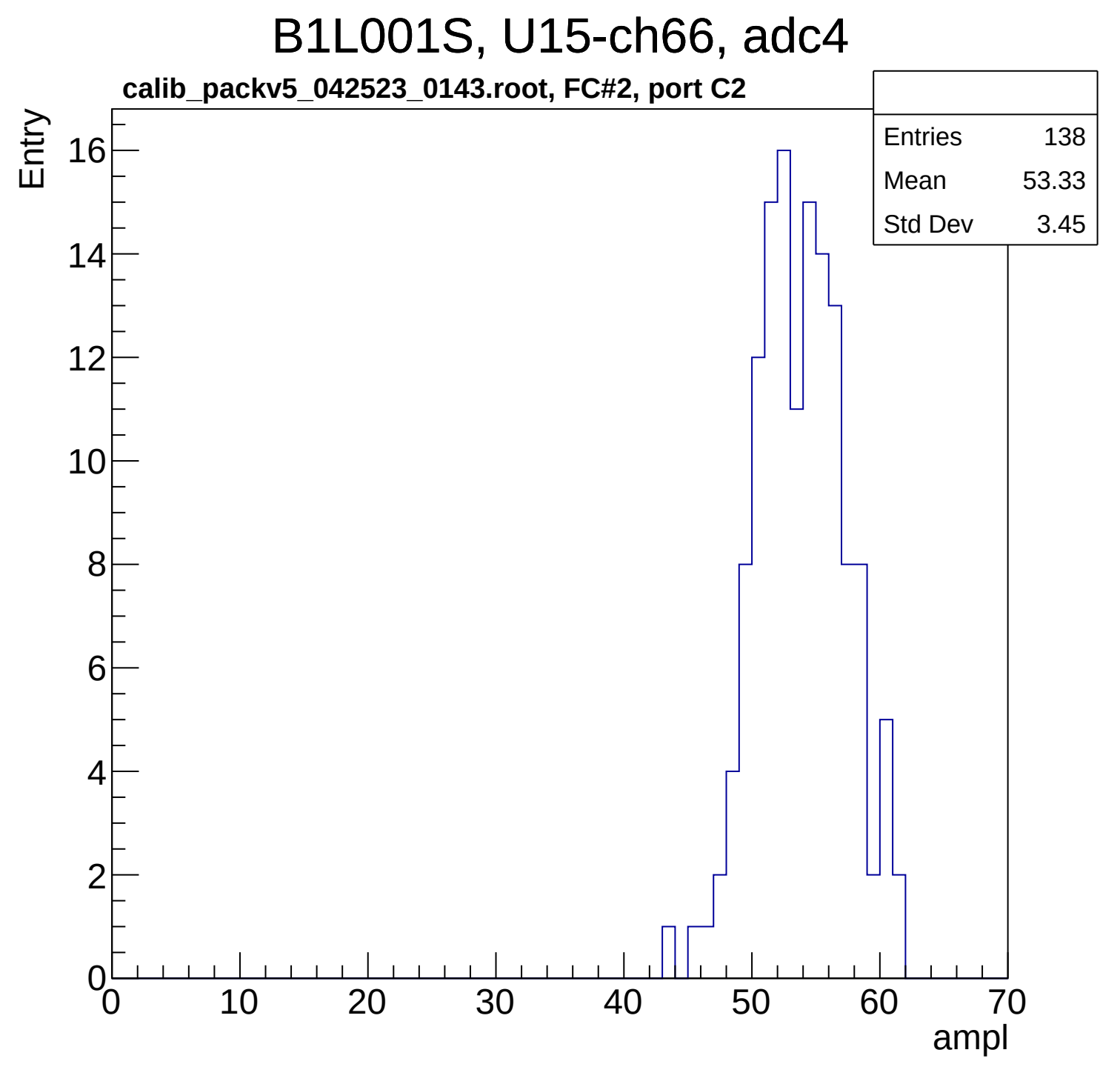
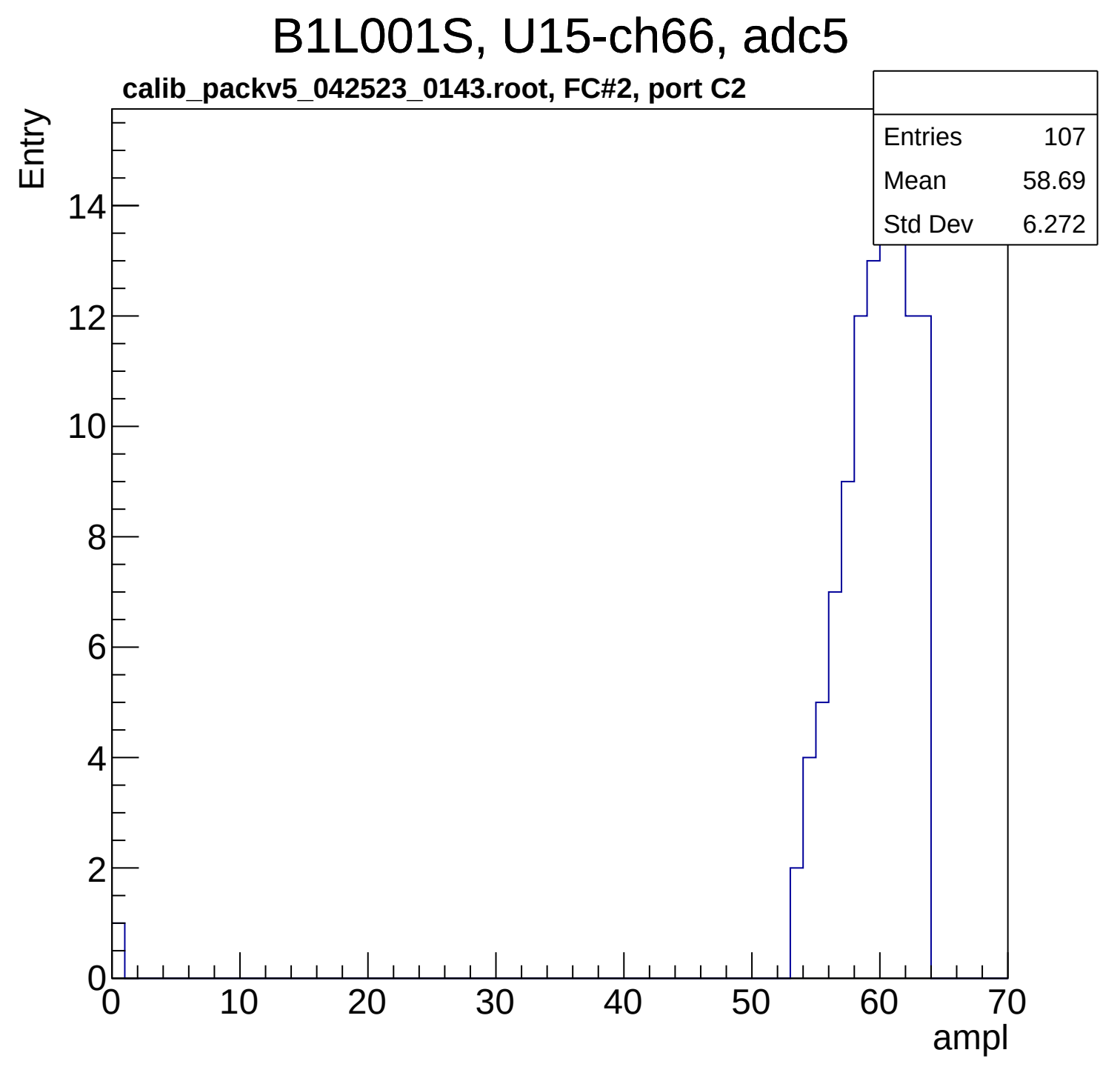
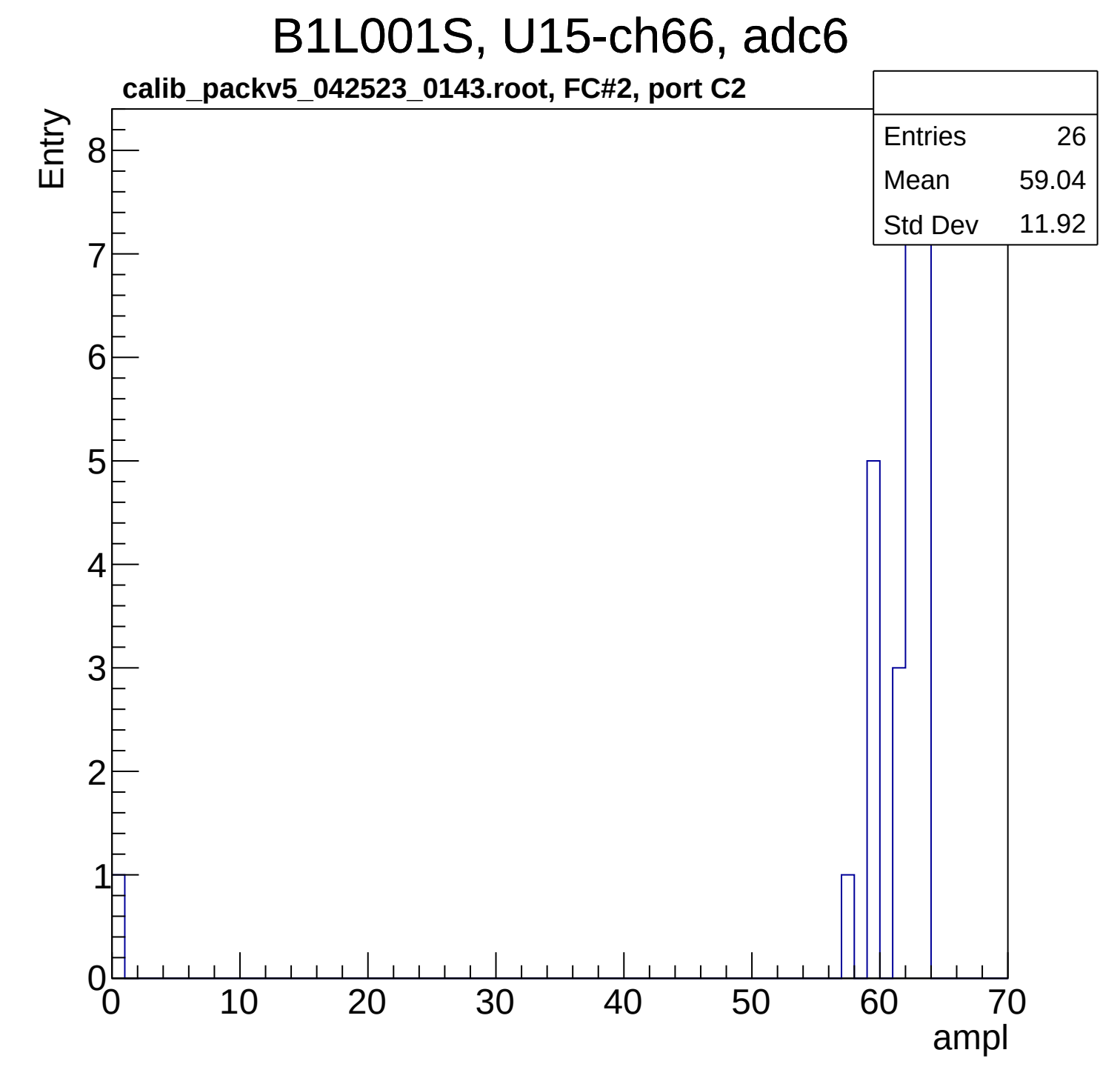
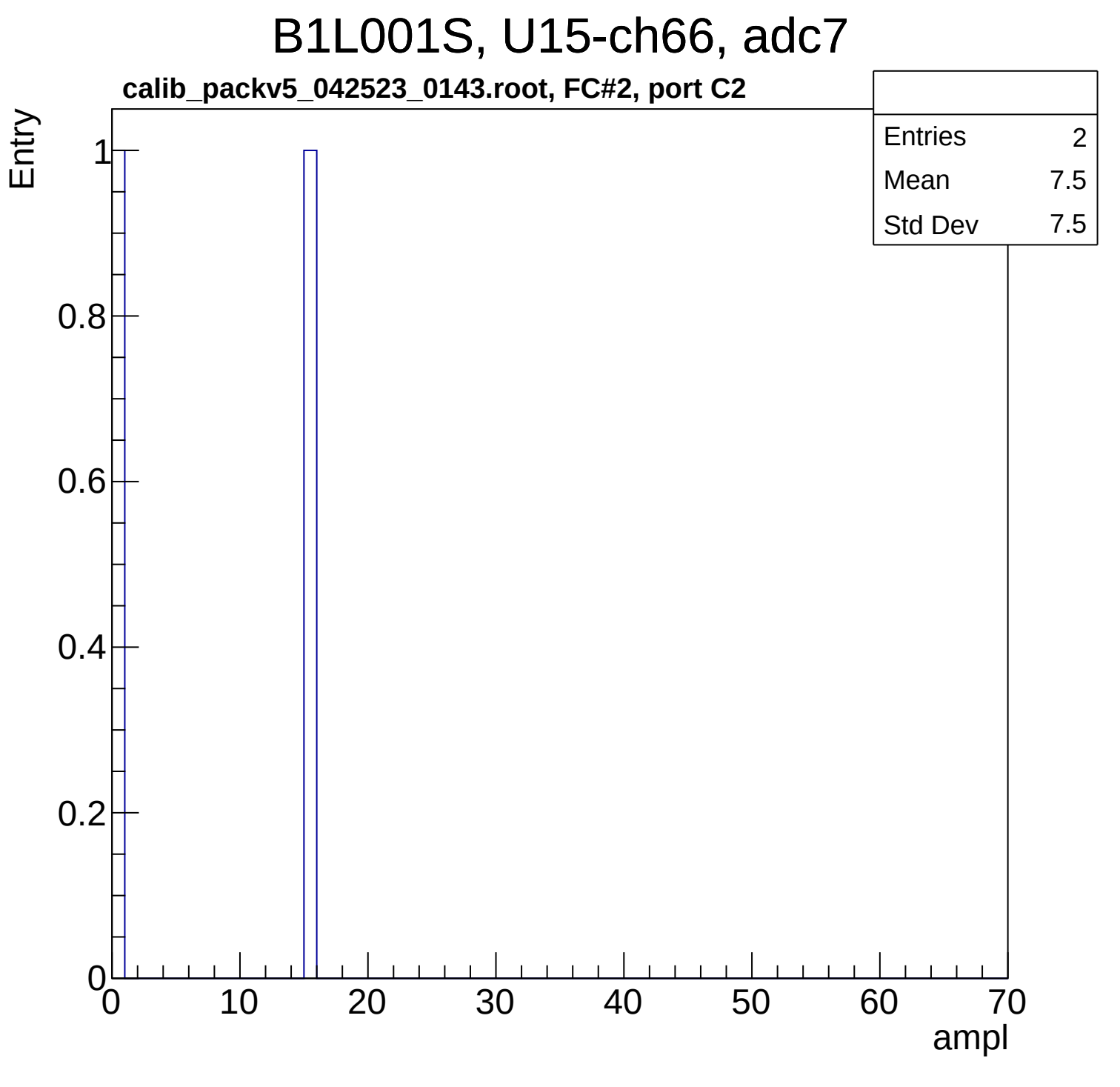
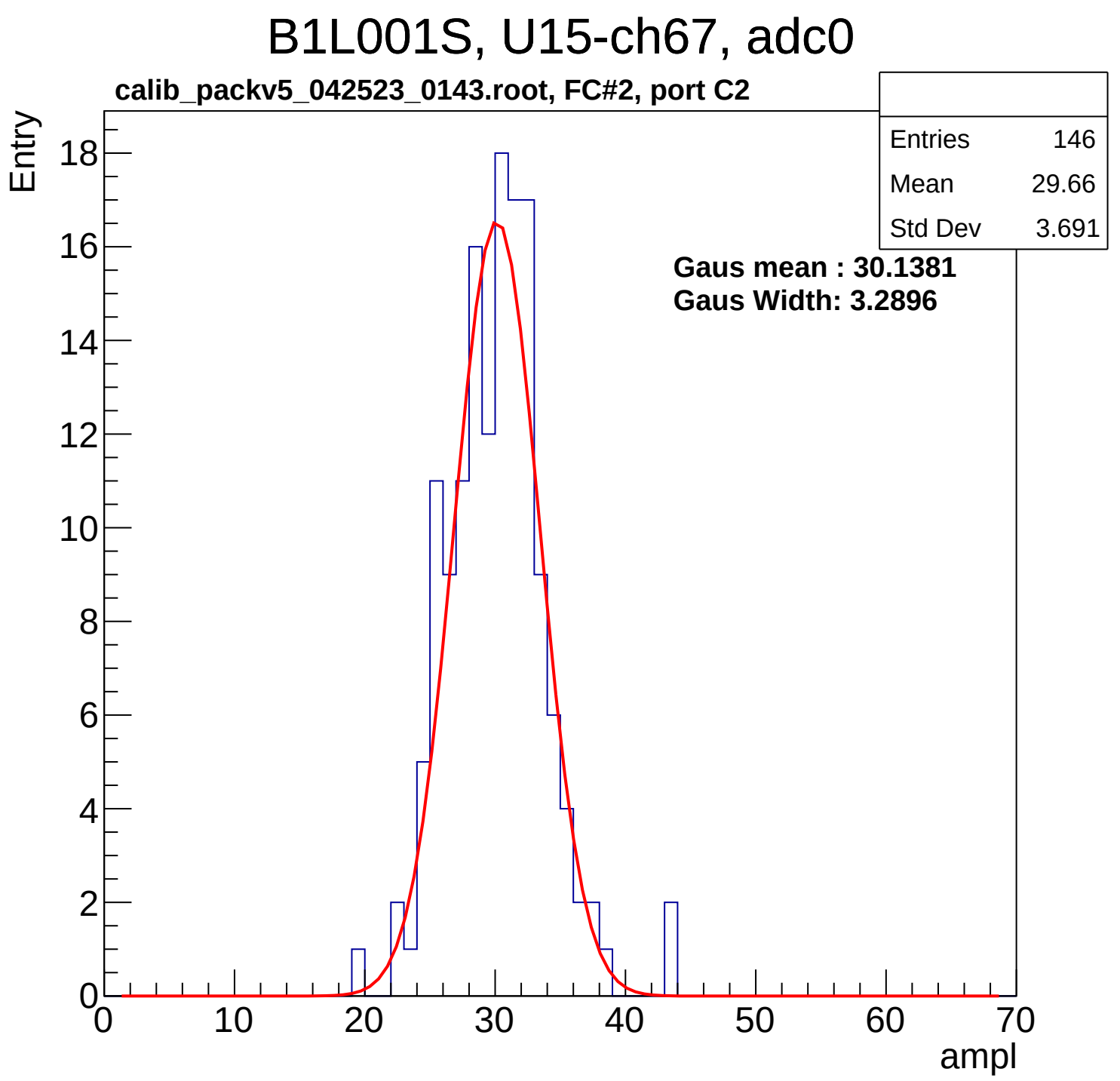
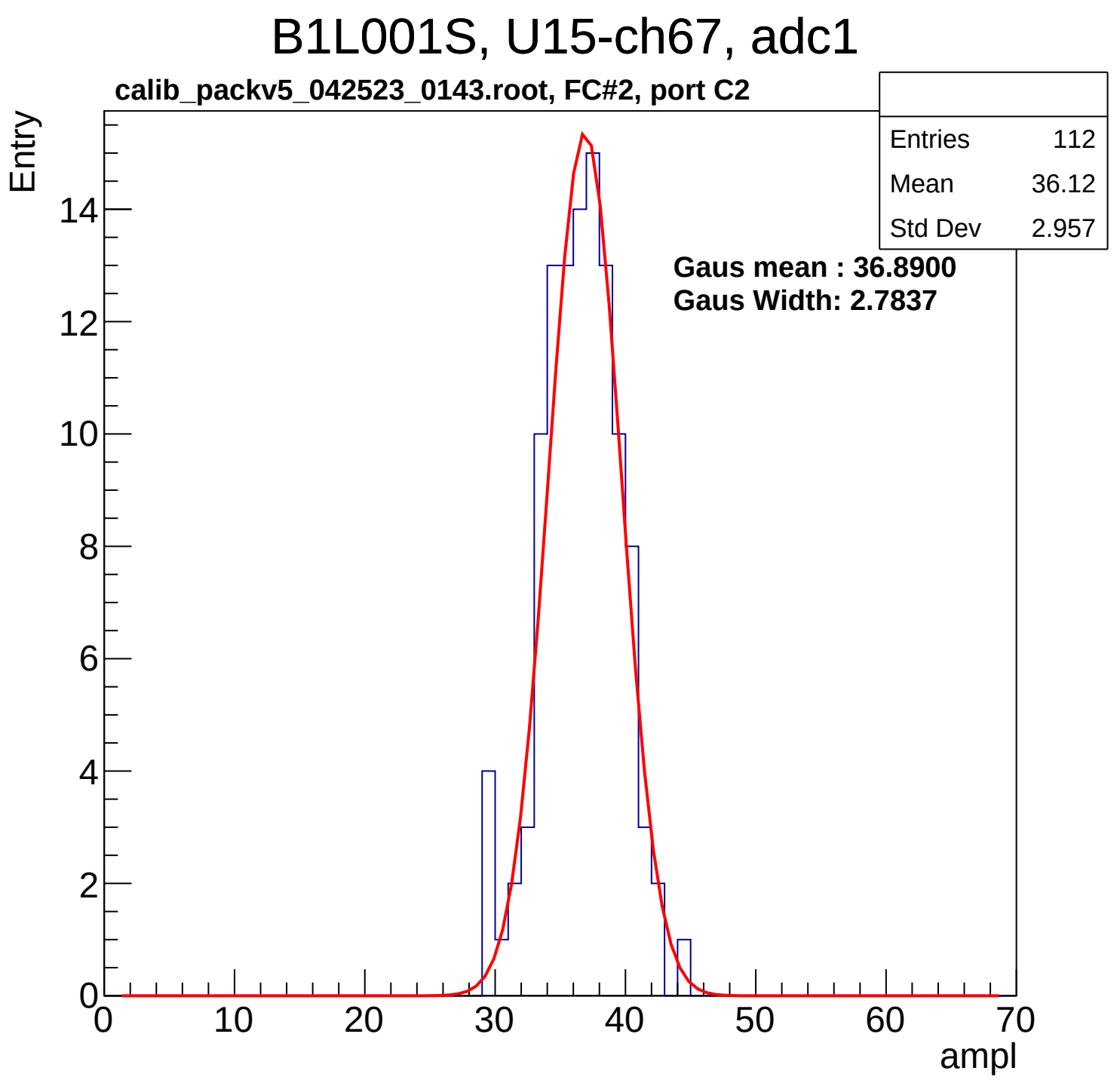

B1L001S, U15-ch66, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 53.33
Std Dev 3.45
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch66, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 58.69
Std Dev 6.272
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch66, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 59.04
Std Dev 11.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch66, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 7.5
Std Dev 7.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch67, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 29.66
Std Dev 3.691
Gaus mean : 30.1381
Gaus Width: 3.2896
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch67, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 36.12
Std Dev 2.957
Gaus mean : 36.8900
Gaus Width: 2.7837
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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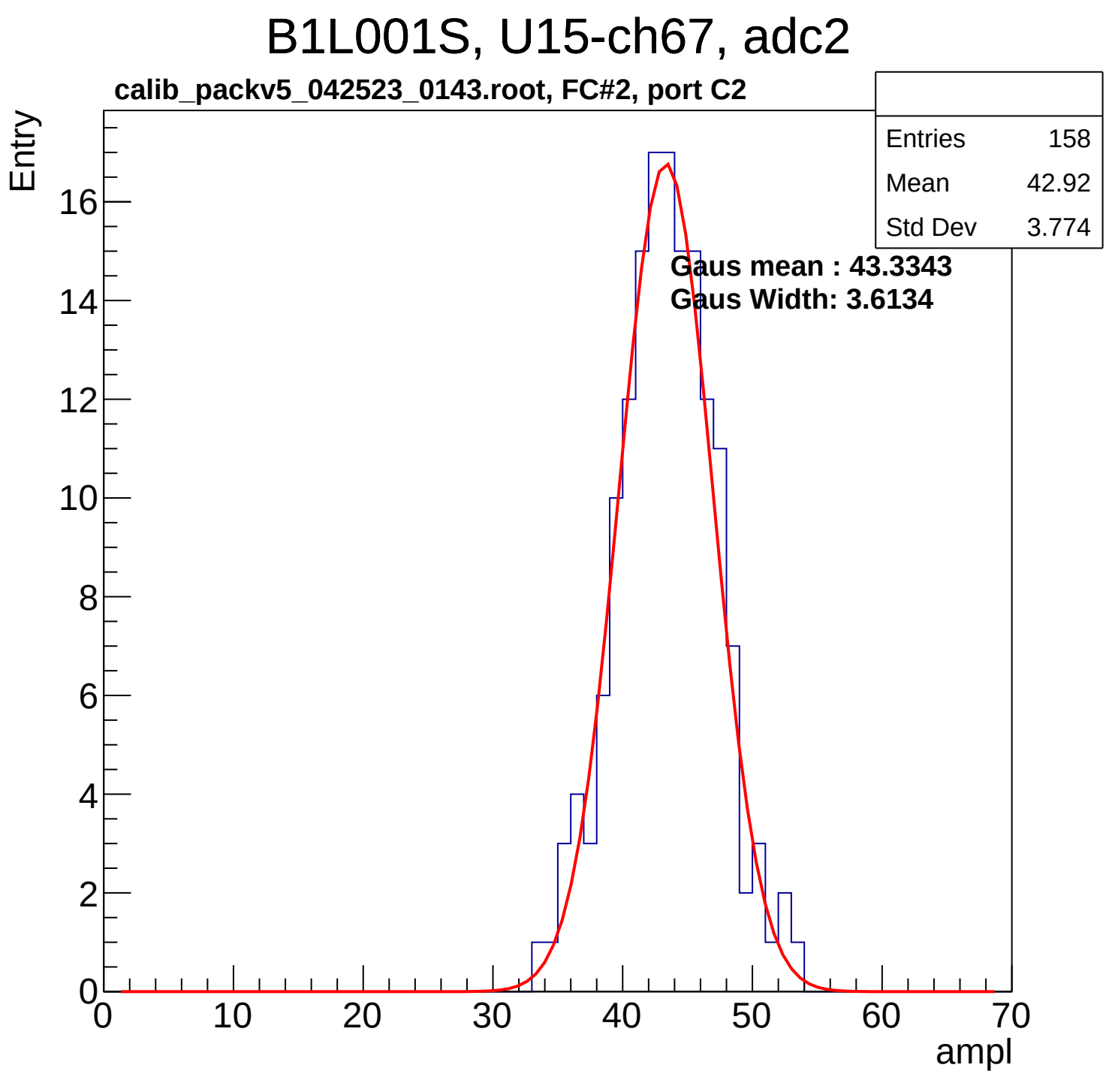
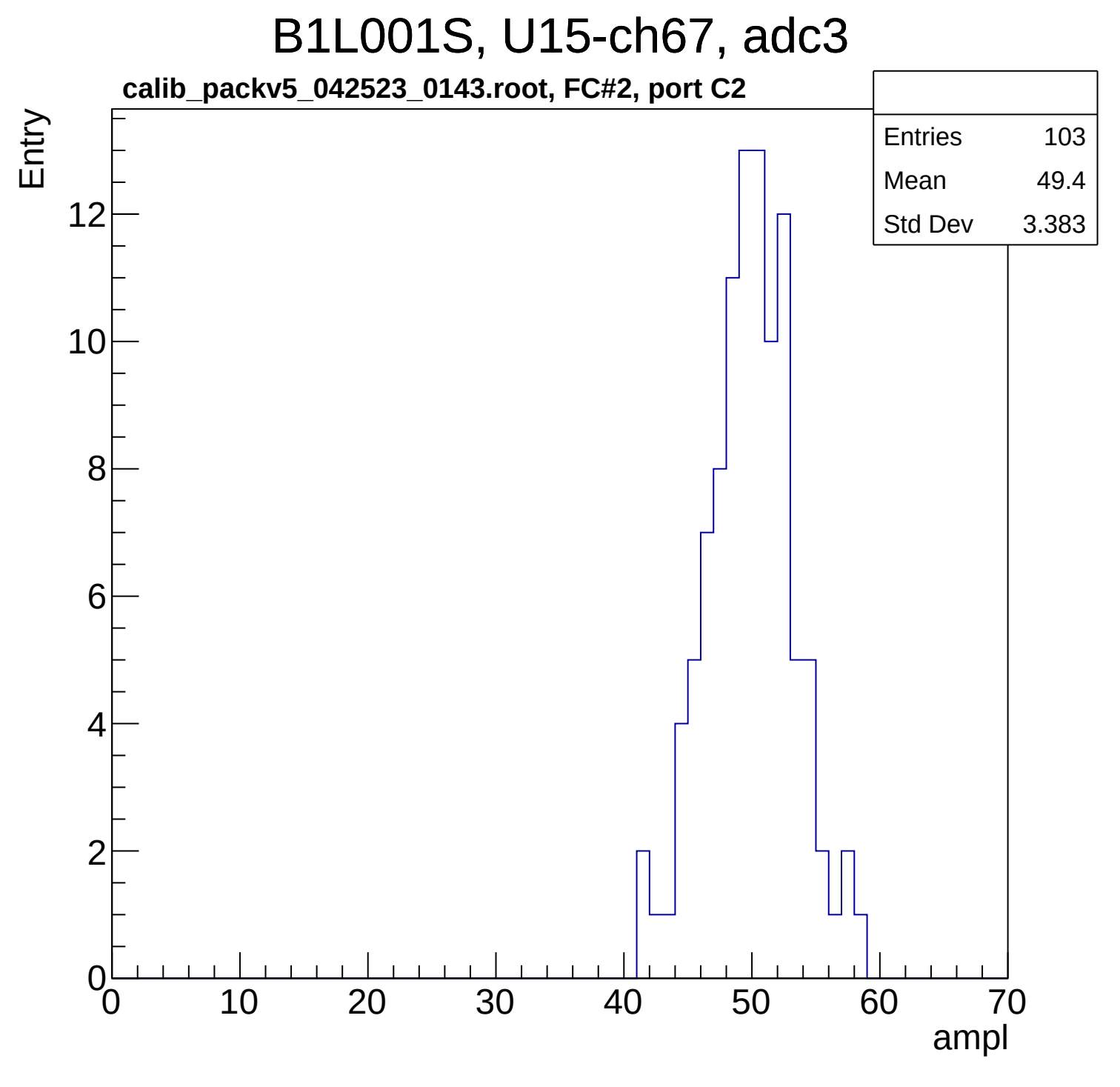
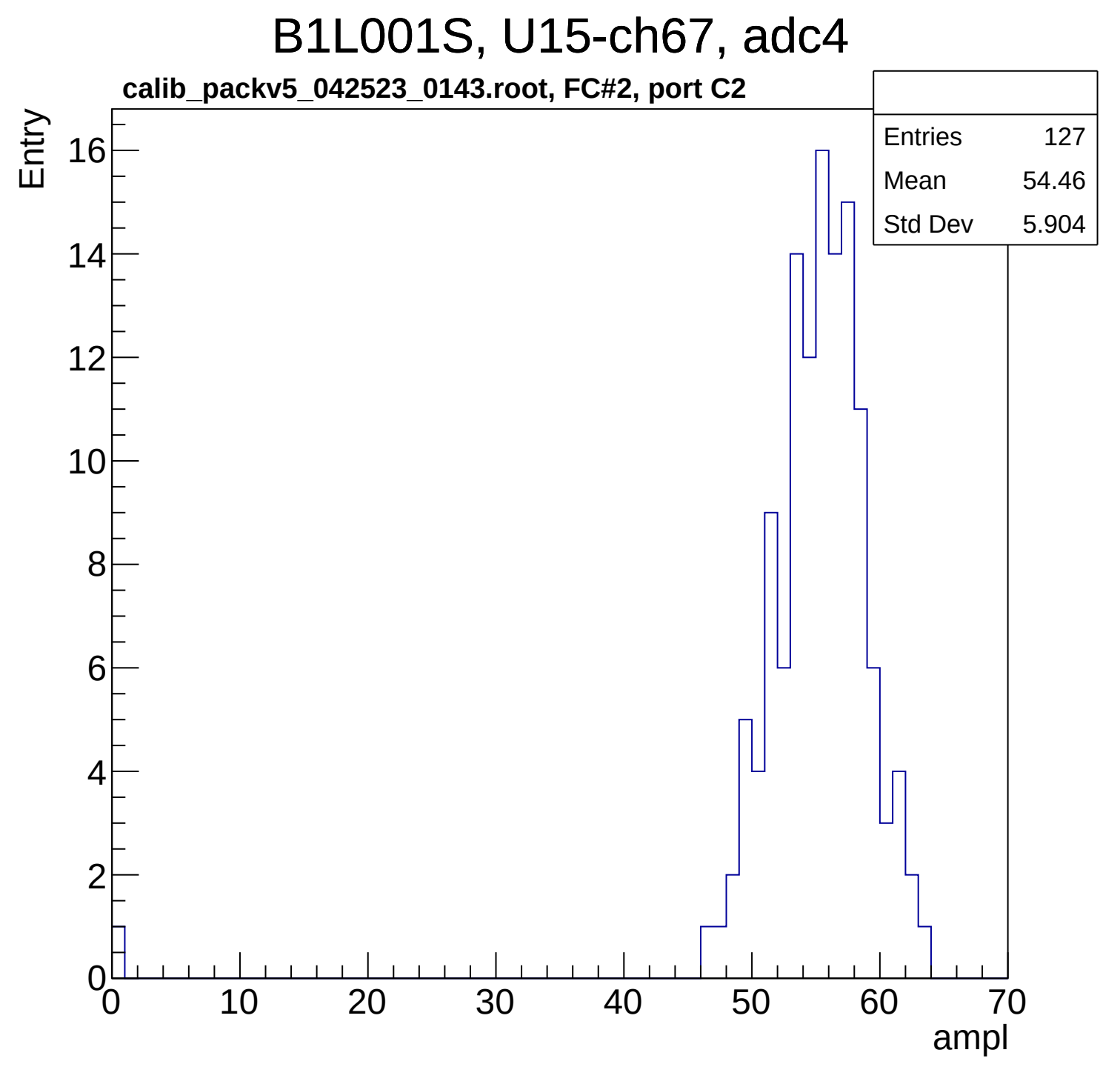
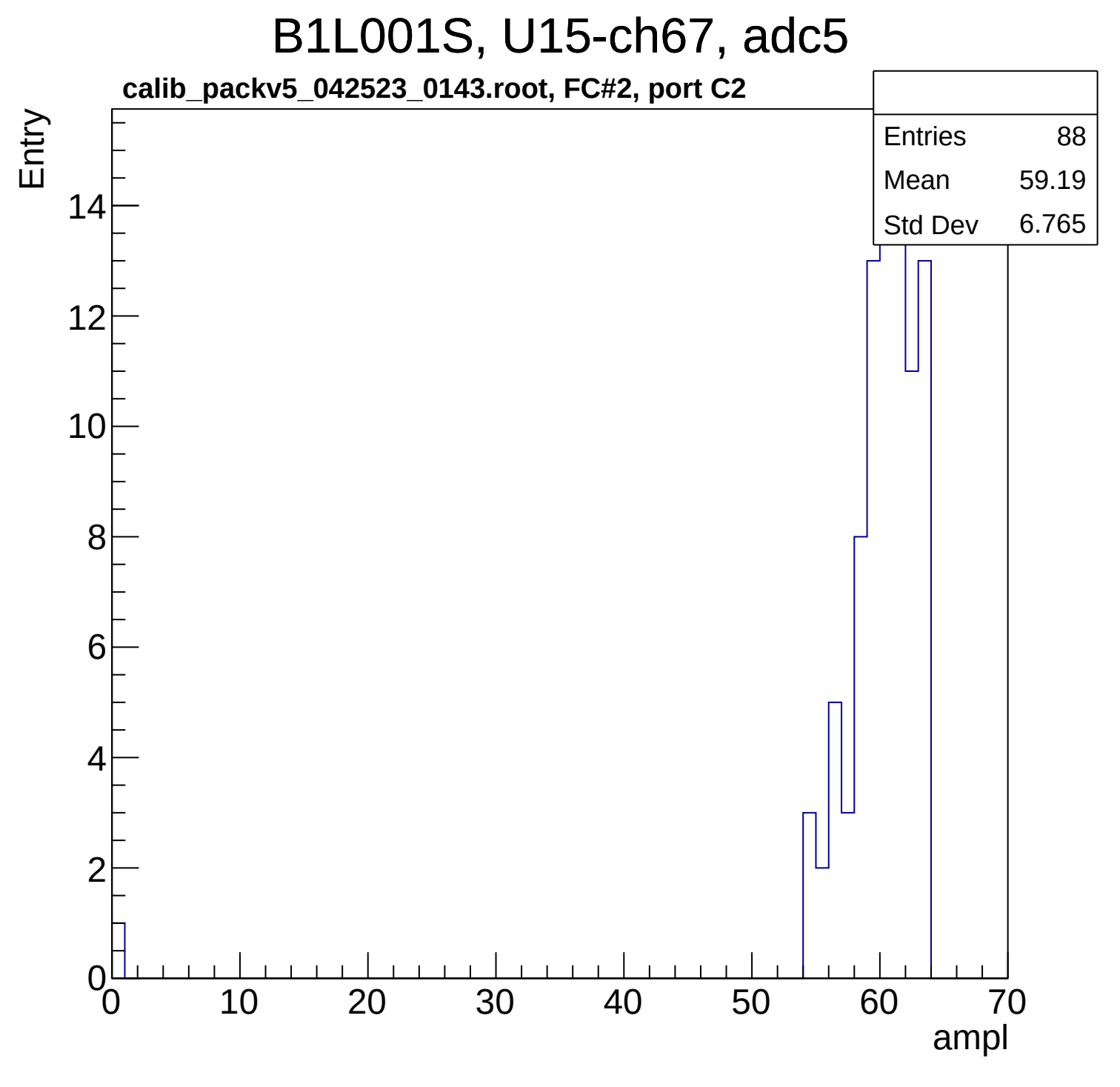
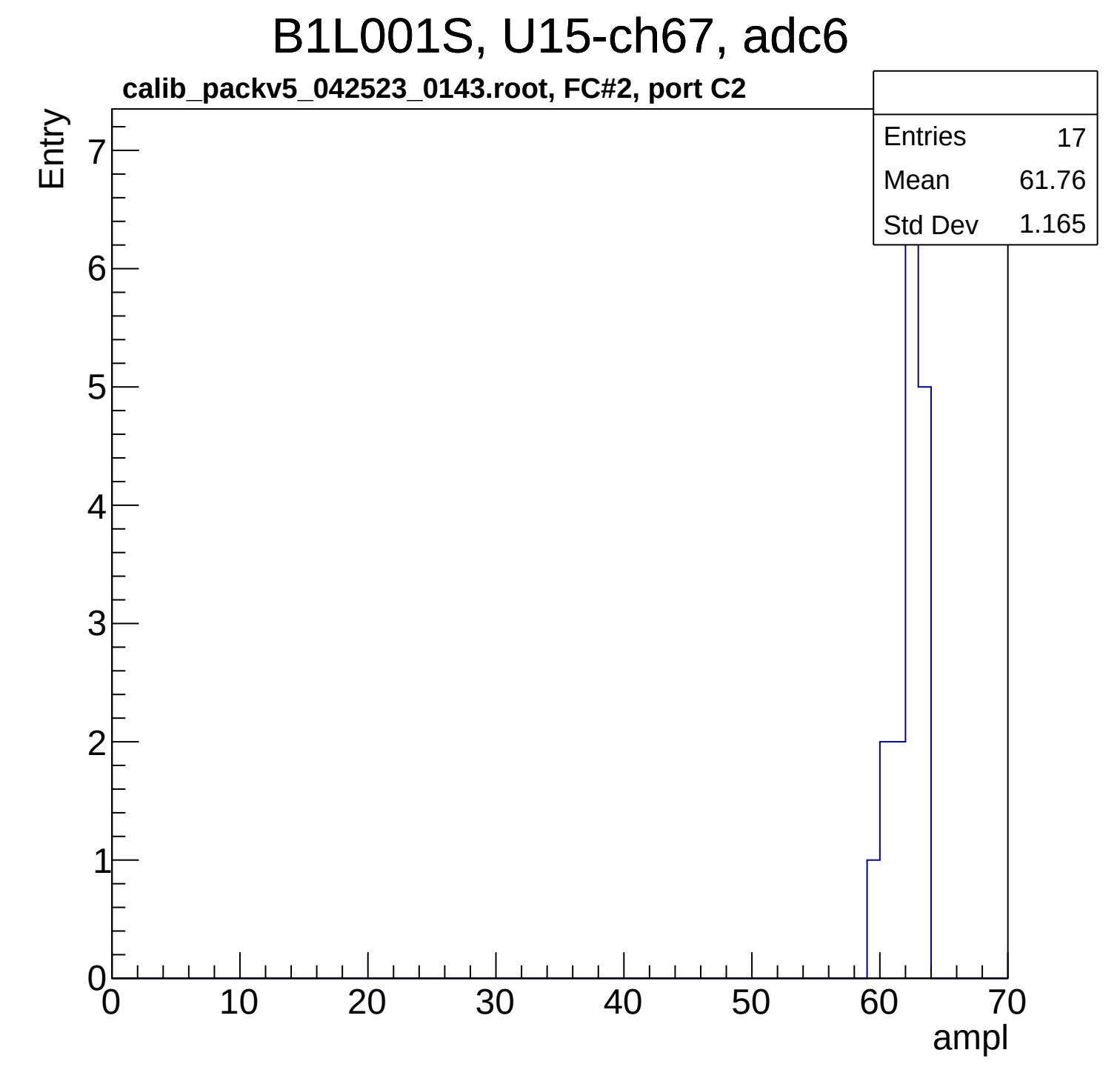
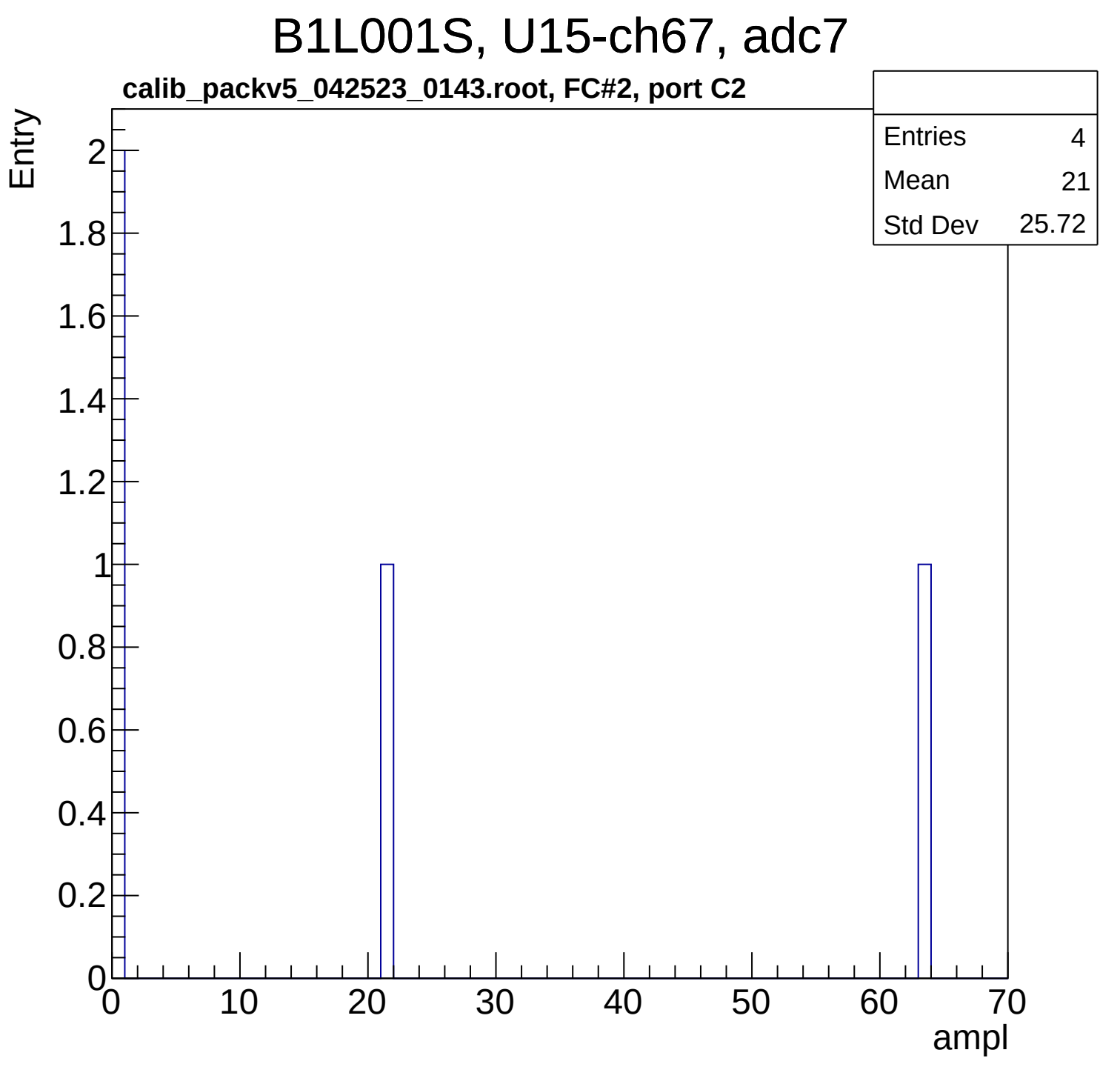

B1L001S, U15-ch67, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 42.92
Std Dev 3.774
Gaus mean : 43.3343
Gaus Width: 3.6134
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch67, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 49.4
Std Dev 3.383
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch67, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 54.46
Std Dev 5.904
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch67, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.19
Std Dev 6.765
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch67, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 17
Mean 61.76
Std Dev 1.165
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch67, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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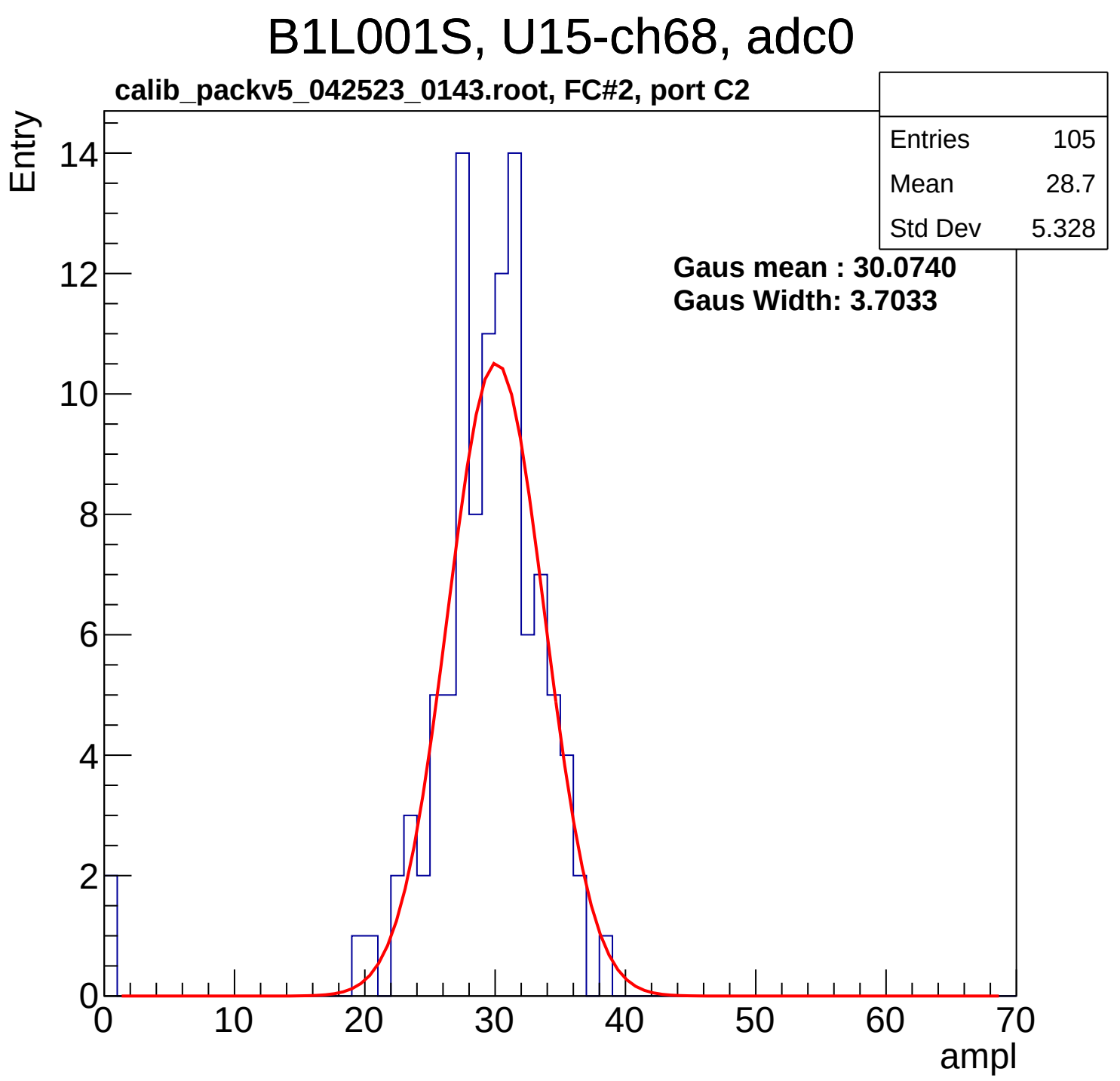
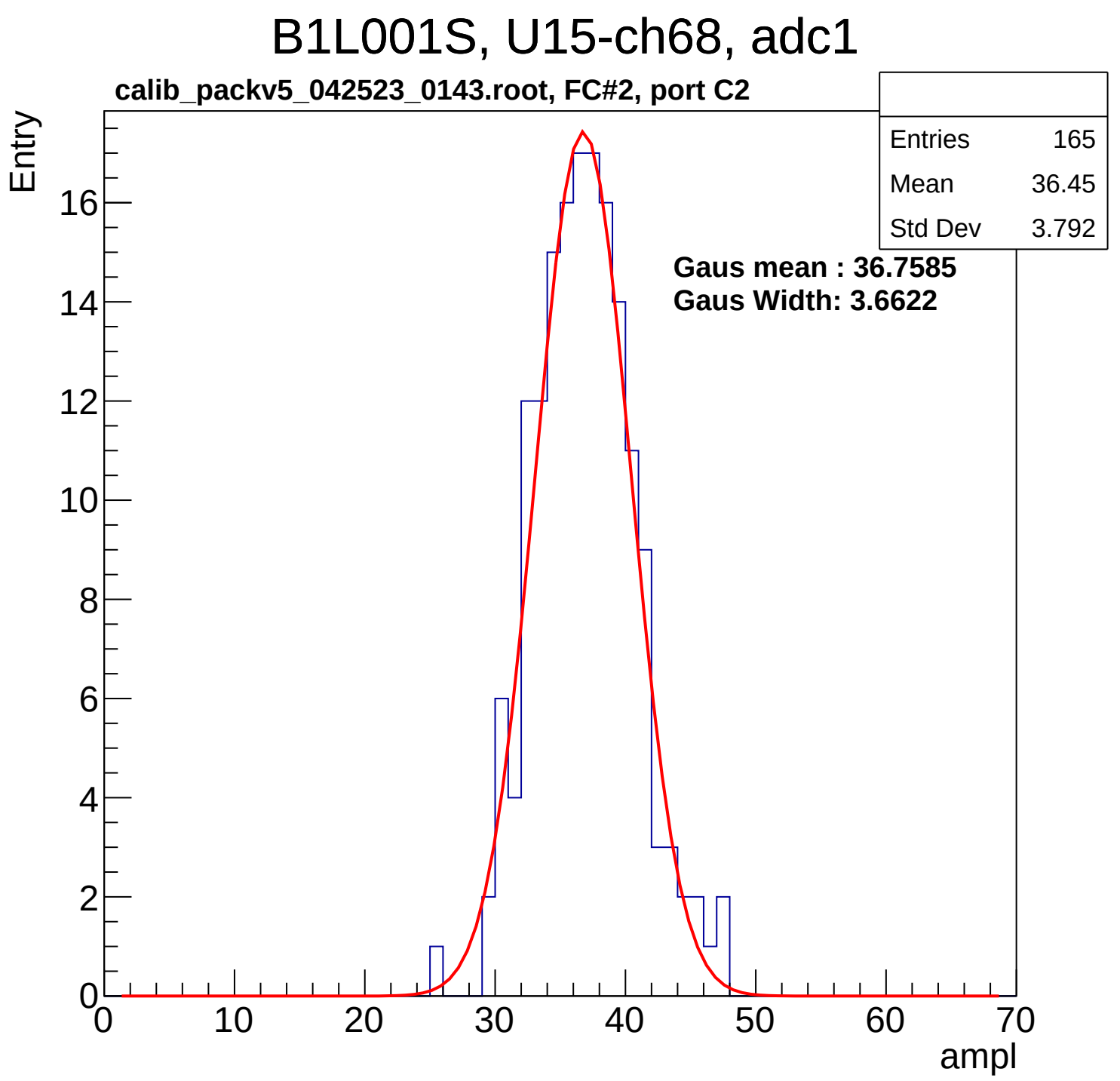
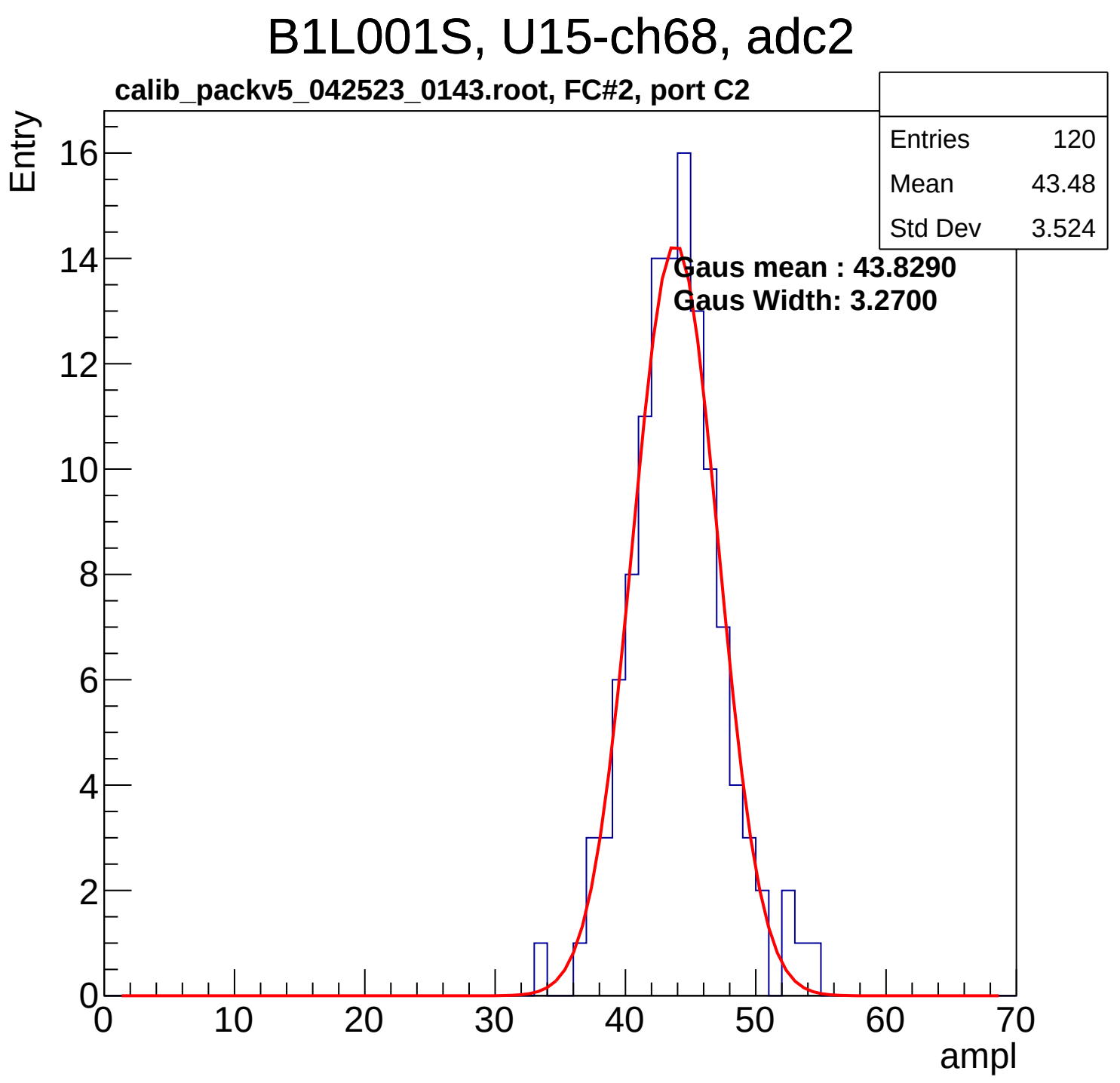
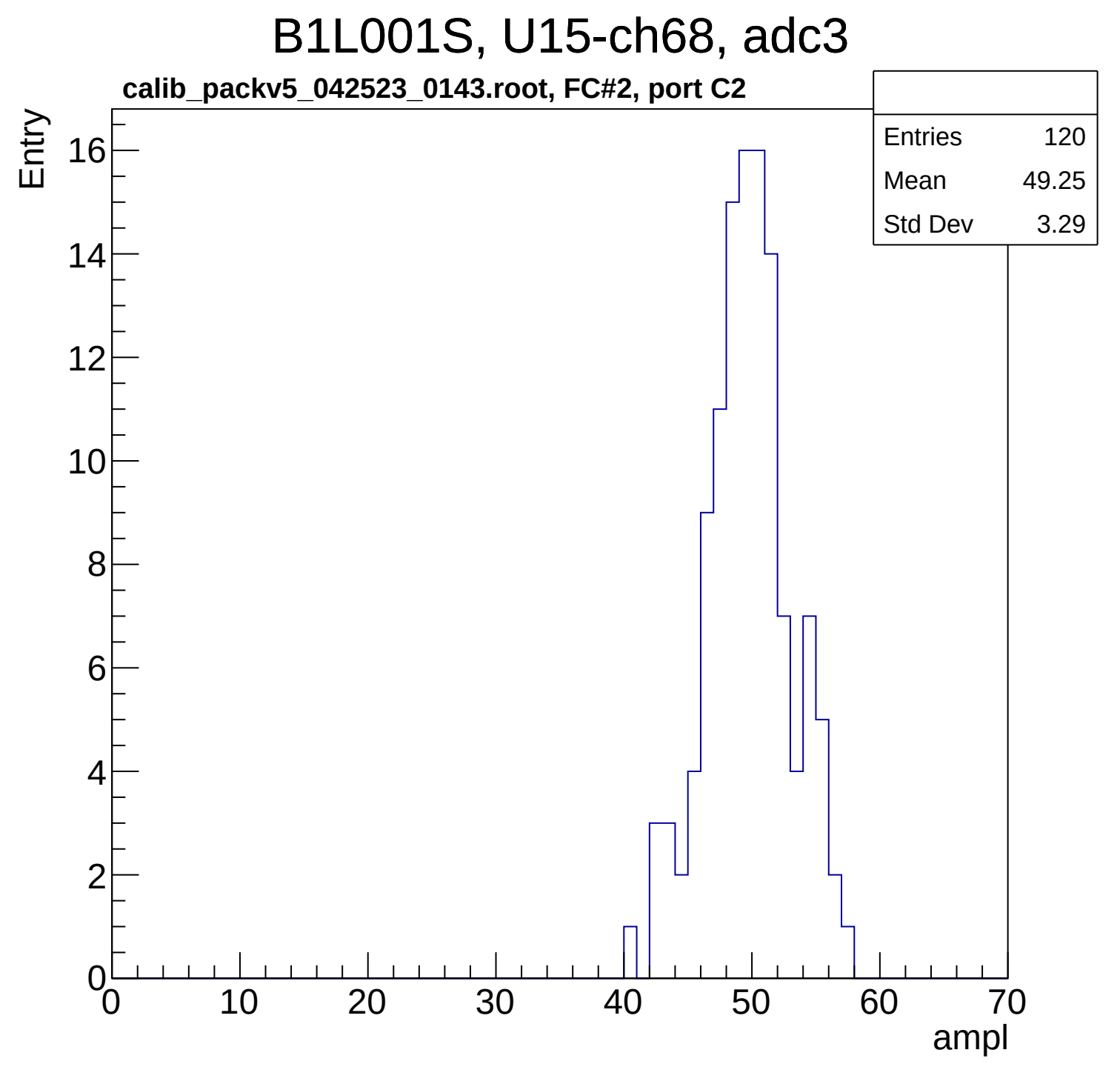
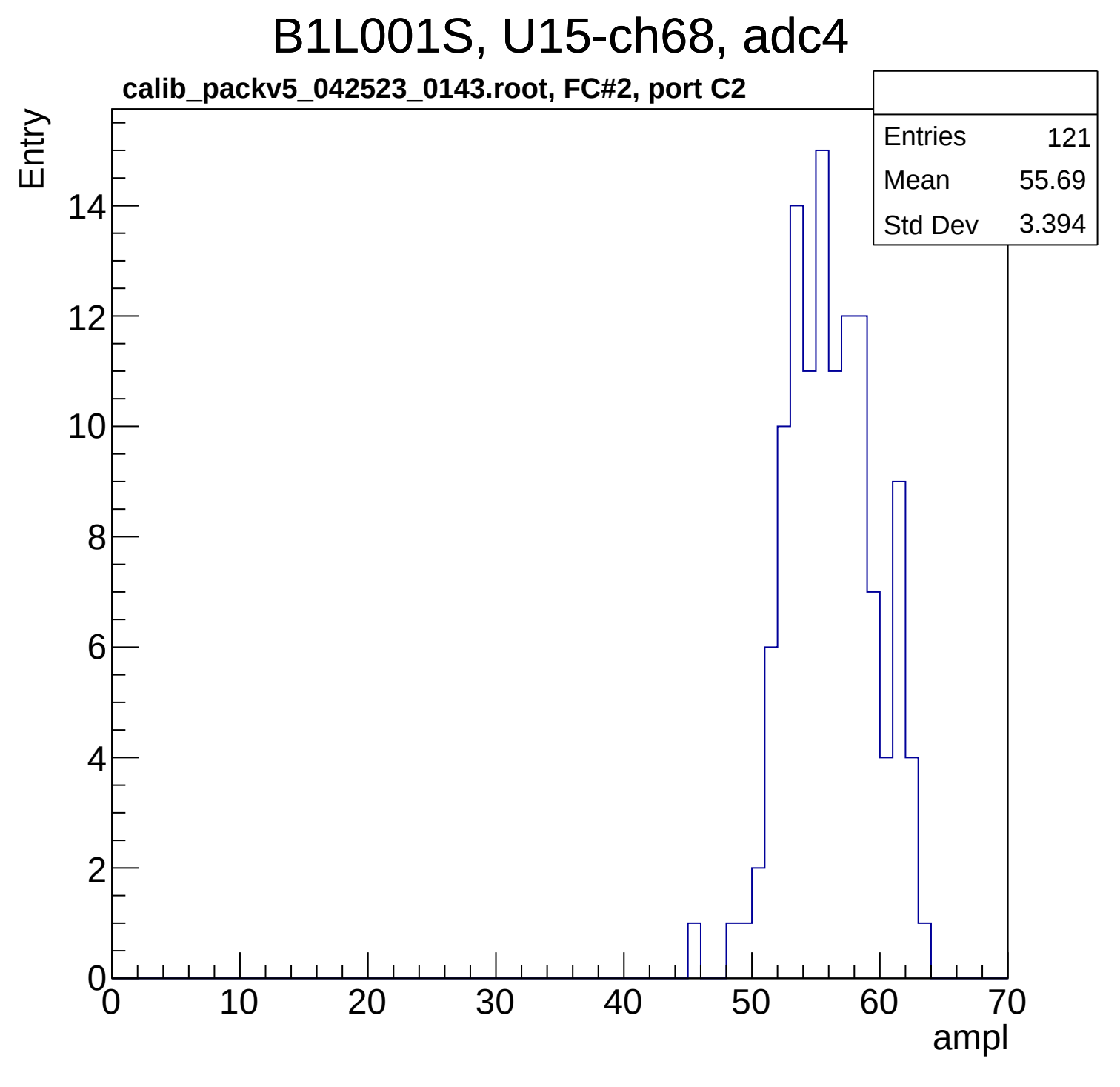
40
50
60
70

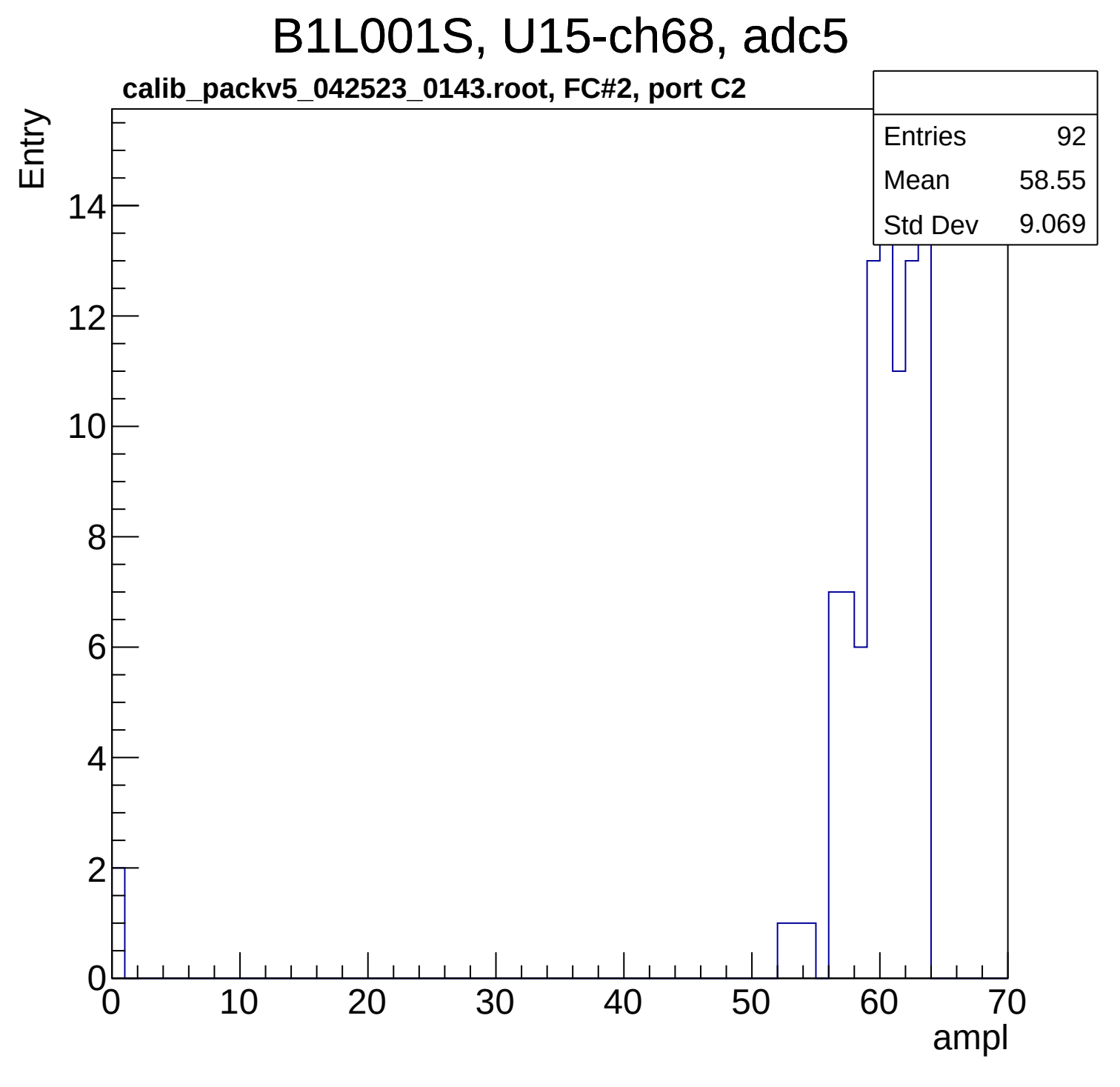

B1L001S, U15-ch68, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 28.7
Std Dev 5.328
Gaus mean : 30.0740
Gaus Width: 3.7033
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

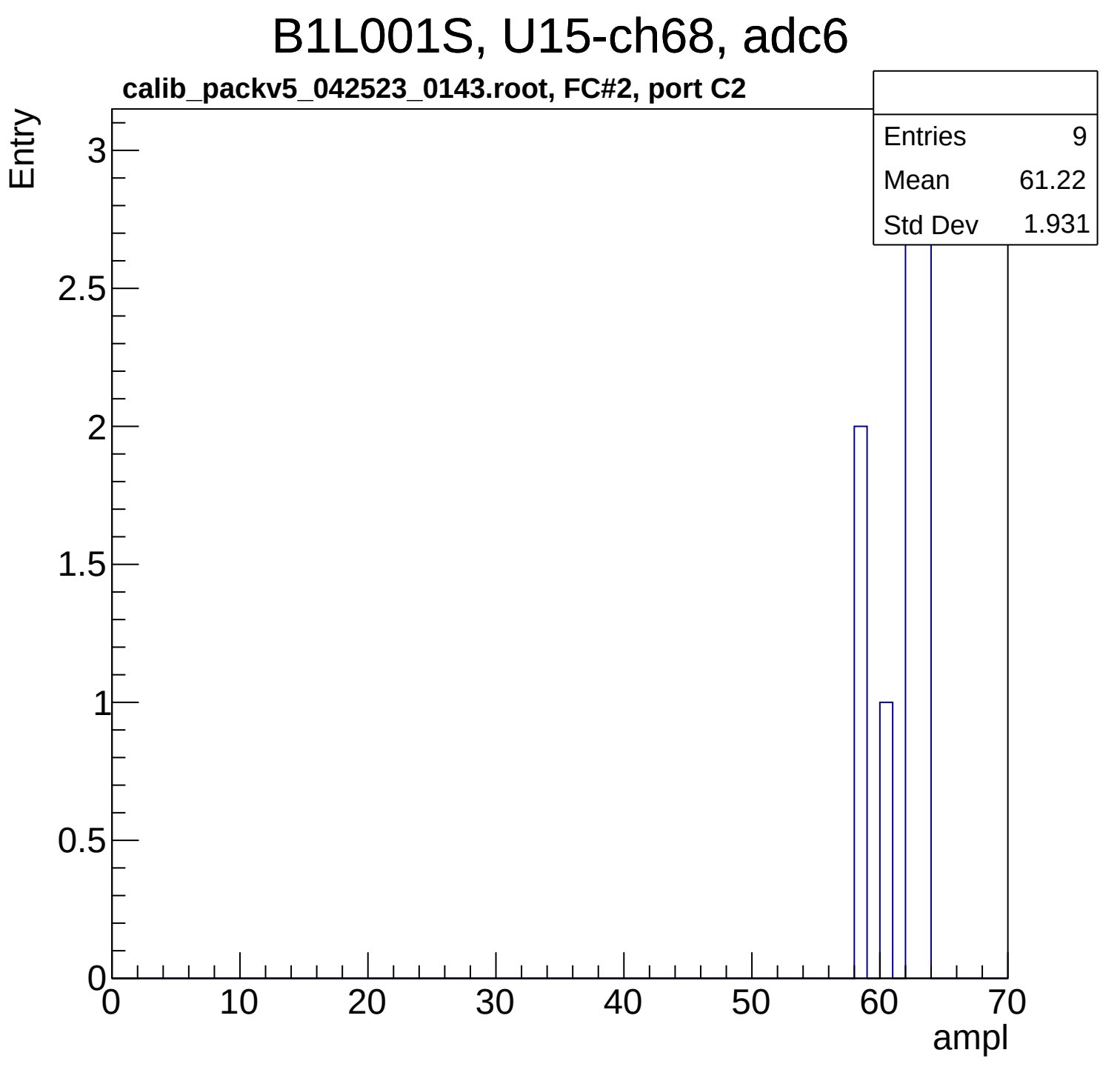

B1L001S, U15-ch68, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 165
Mean 36.45
Std Dev 3.792
Gaus mean : 36.7585
Gaus Width: 3.6622
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

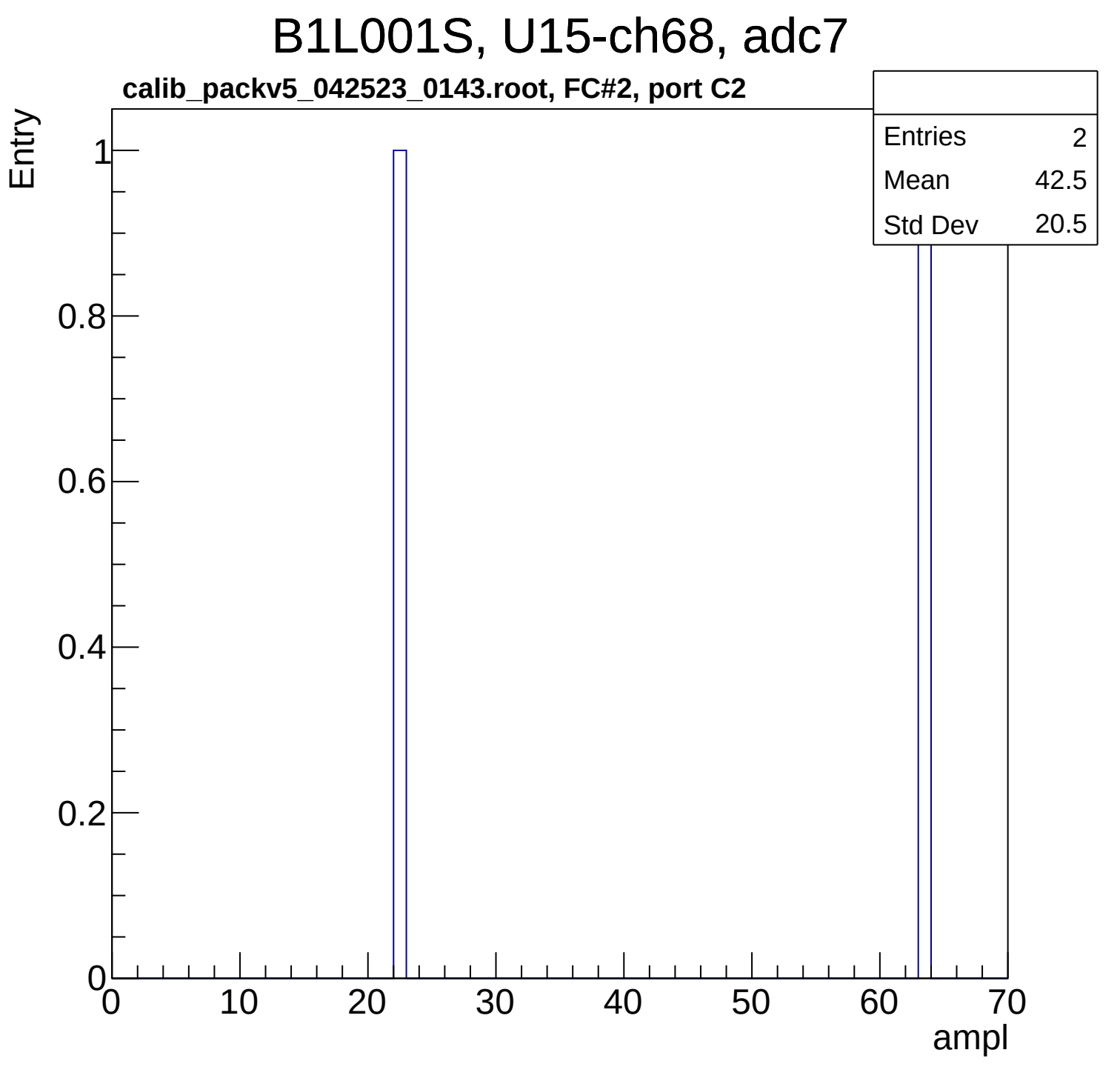

B1L001S, U15-ch68, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 43.48
Std Dev 3.524
Gaus mean : 43.8290
Gaus Width: 3.2700
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

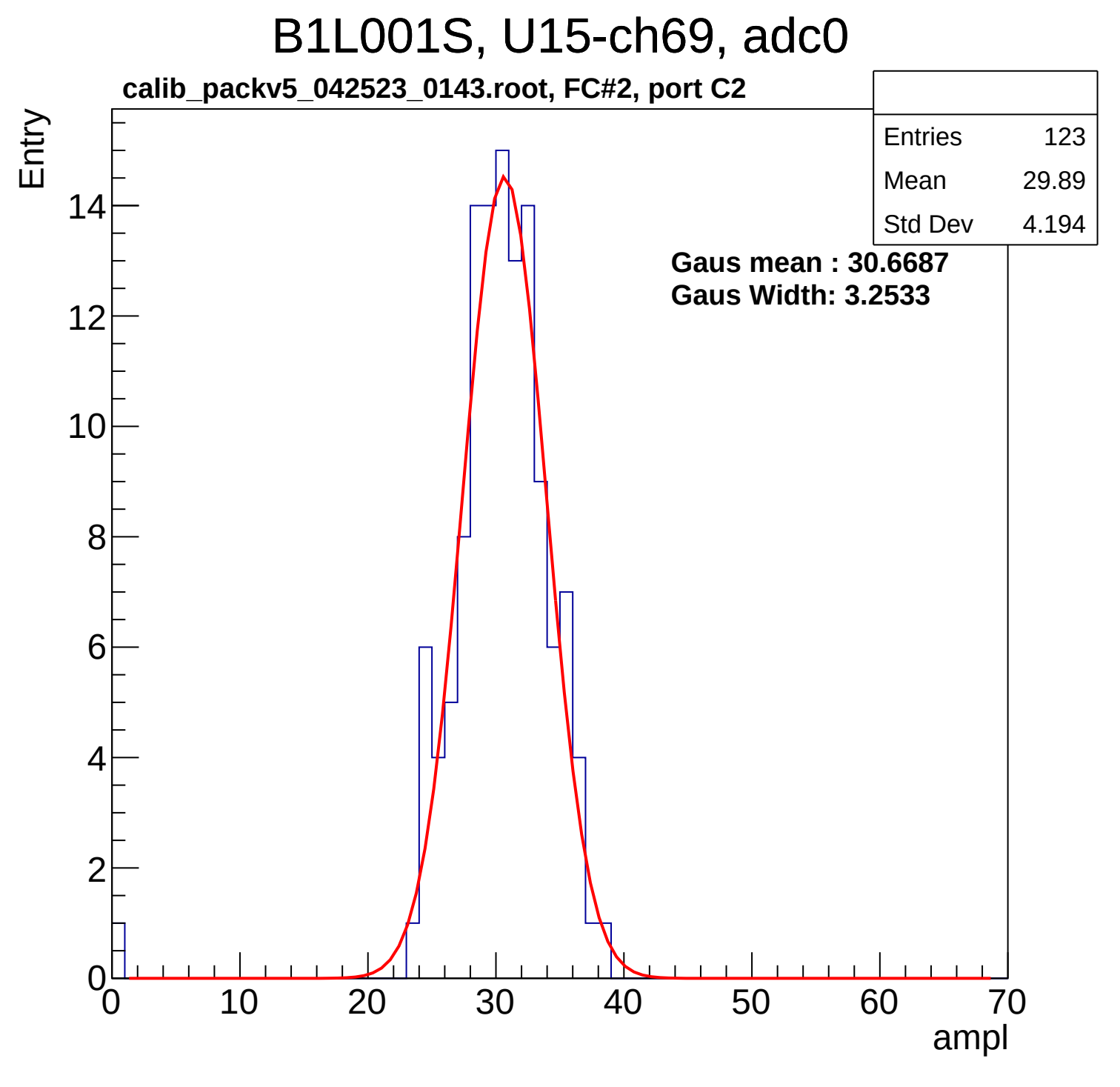

B1L001S, U15-ch68, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 49.25
Std Dev 3.29
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch68, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 55.69
Std Dev 3.394
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch68, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 92
Mean 58.55
Std Dev 9.069
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch68, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 61.22
Std Dev 1.931
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch68, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch69, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 29.89
Std Dev 4.194
Gaus mean : 30.6687
Gaus Width: 3.2533
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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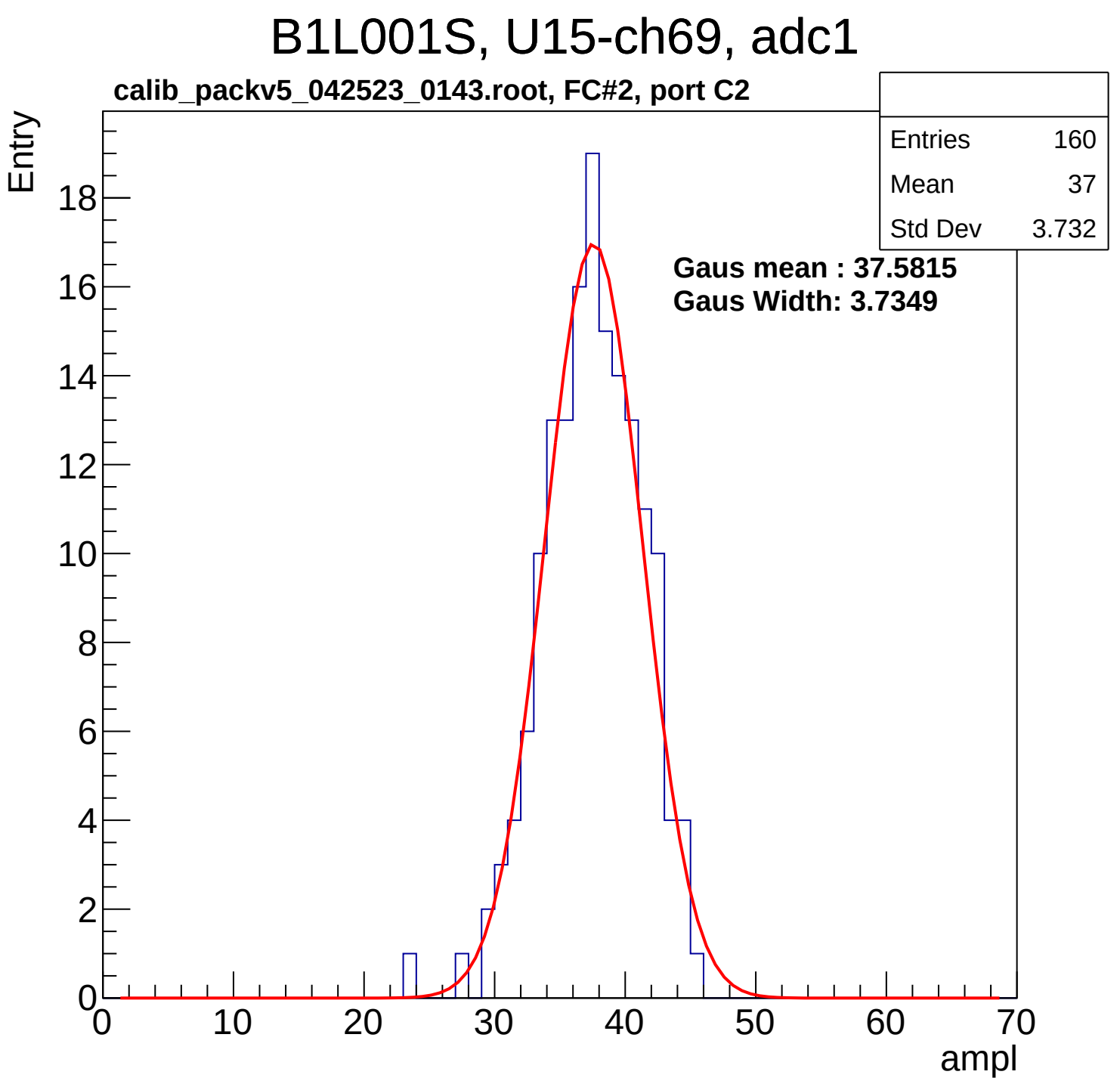
B1L001S, U15-ch69, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 37
Std Dev 3.732
Gaus mean : 37.5815
Gaus Width: 3.7349
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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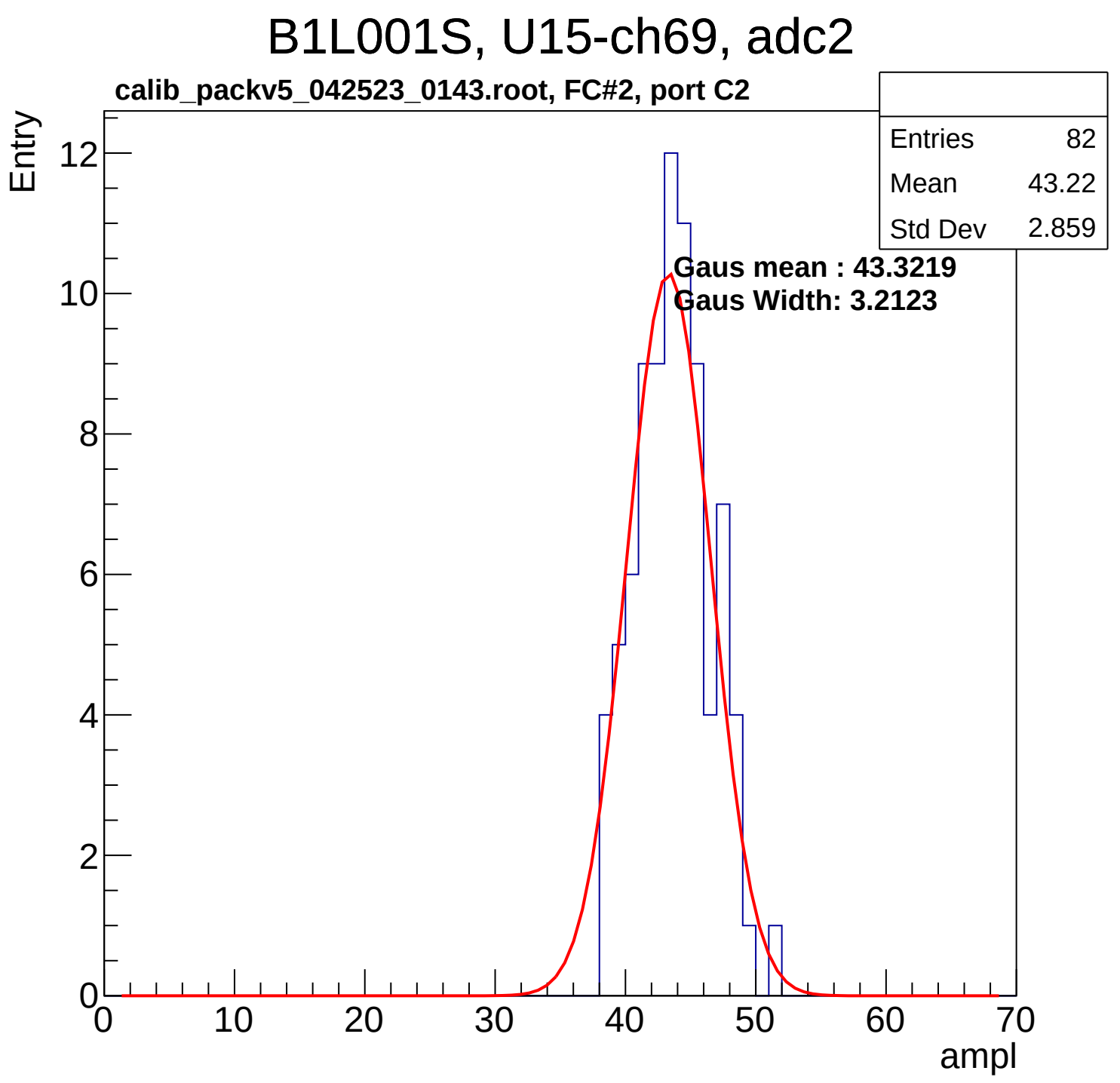
B1L001S, U15-ch69, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 43.22
Std Dev 2.859
Gaus mean : 43.3219
Gaus Width: 3.2123
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
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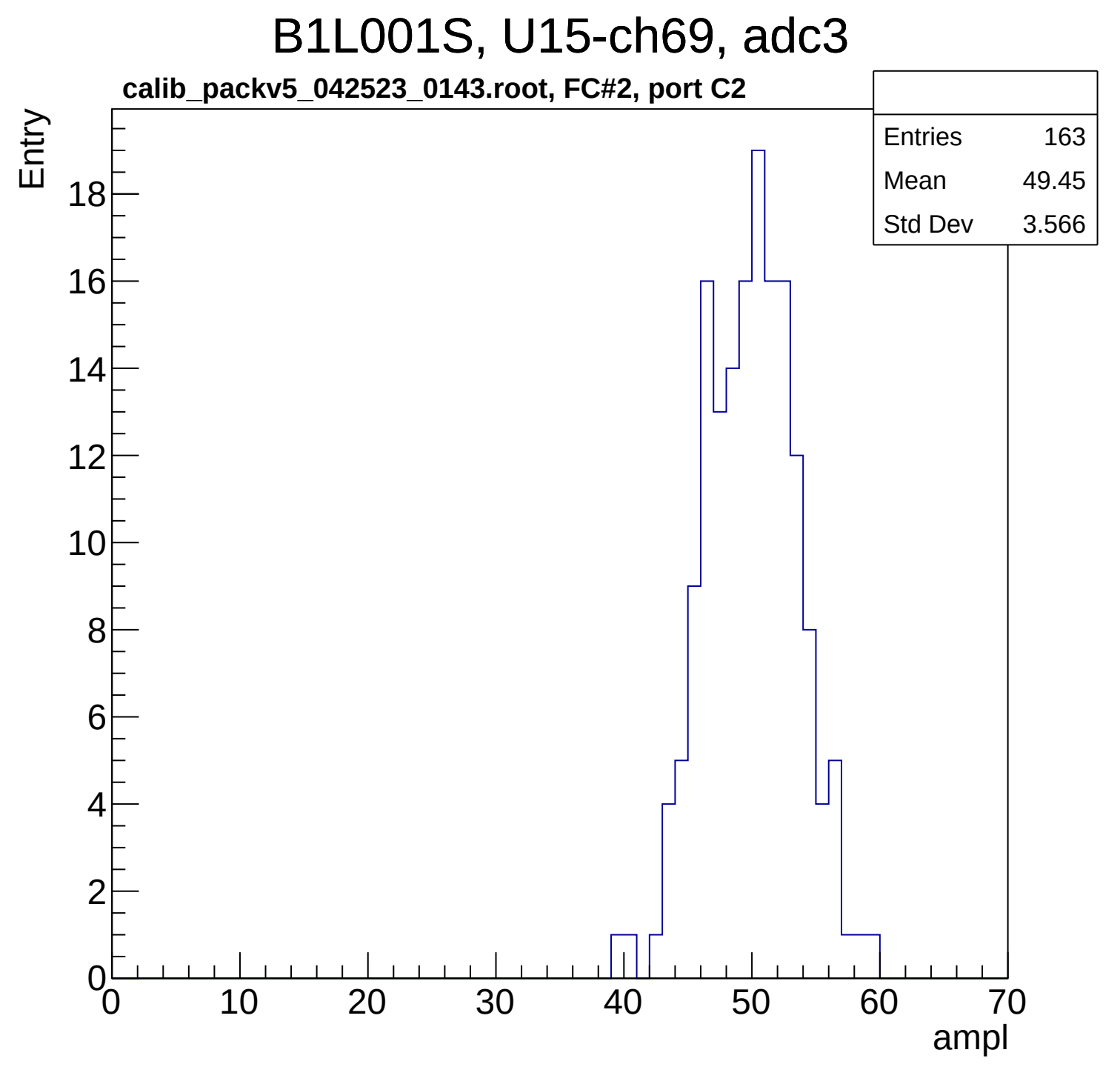
B1L001S, U15-ch69, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 49.45
Std Dev 3.566
Entry
0
2
4
6
8
10
12
14
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18
0
10
20
30
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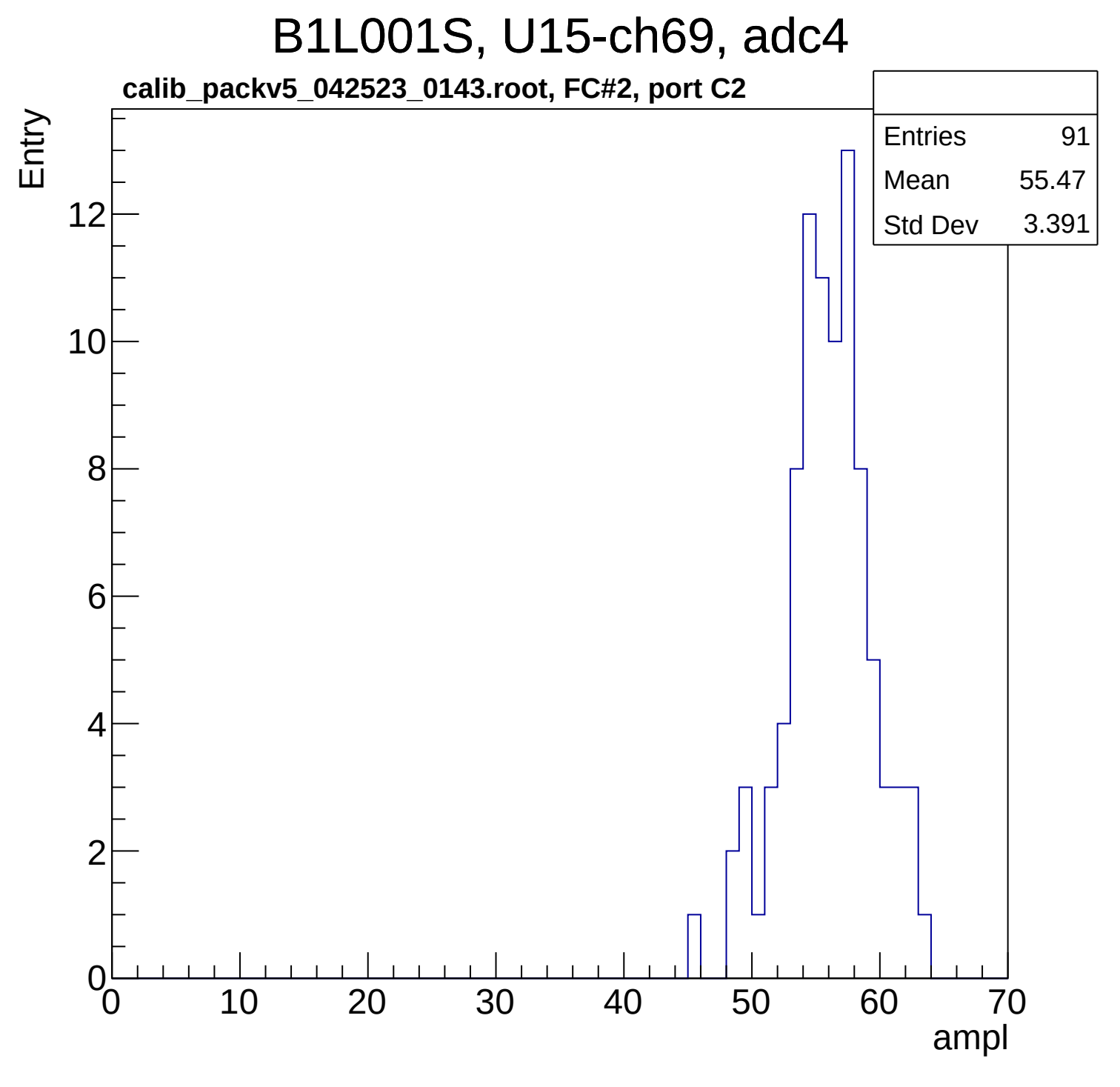
50
60
70
ampl


B1L001S, U15-ch69, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 55.47
Std Dev 3.391
Entry
0
2
4
6
8
10
12
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10
20
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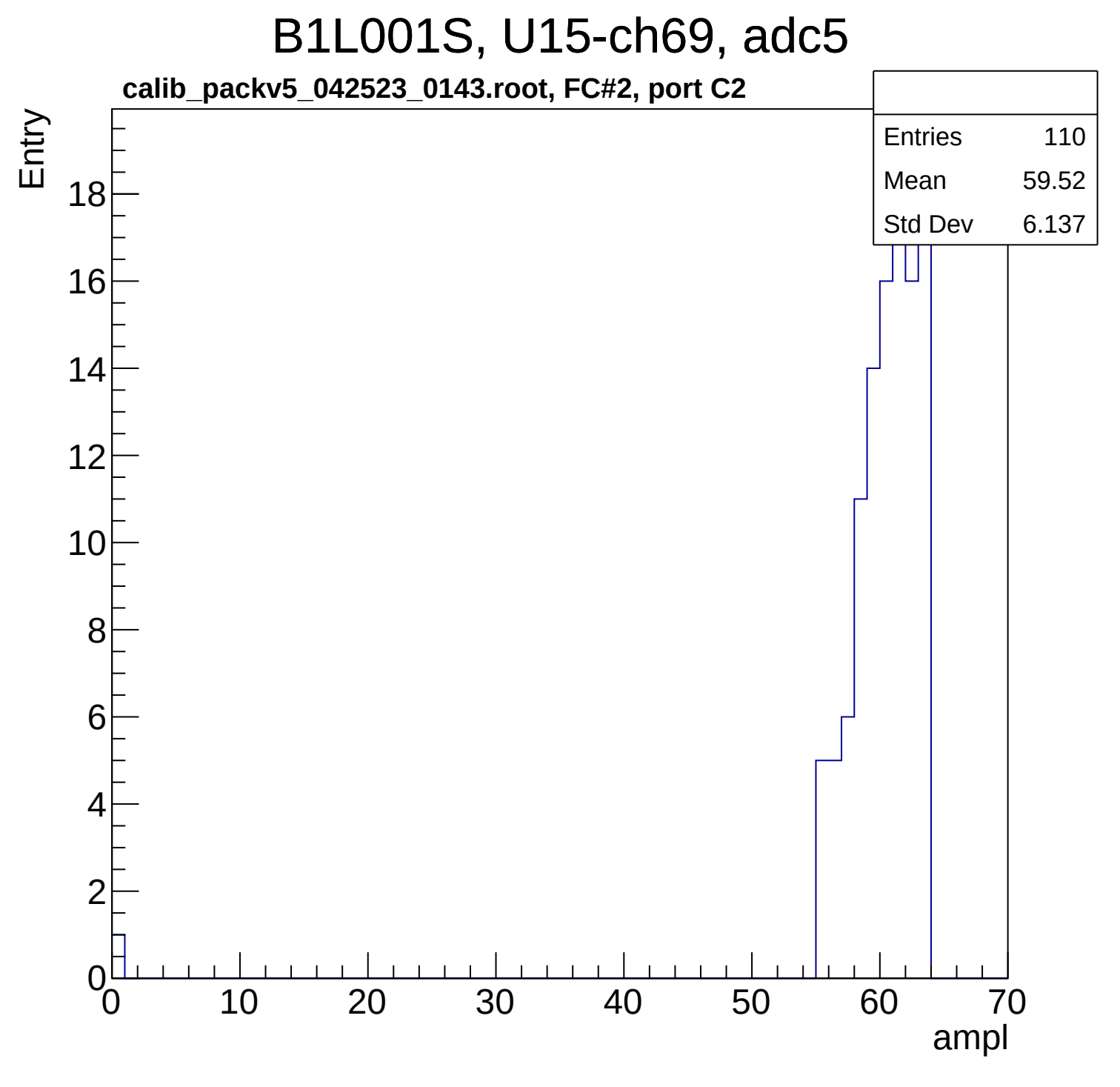
70
ampl


B1L001S, U15-ch69, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 59.52
Std Dev 6.137
Entry
ampl
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4
6
8
10
12
14
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10
20
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60
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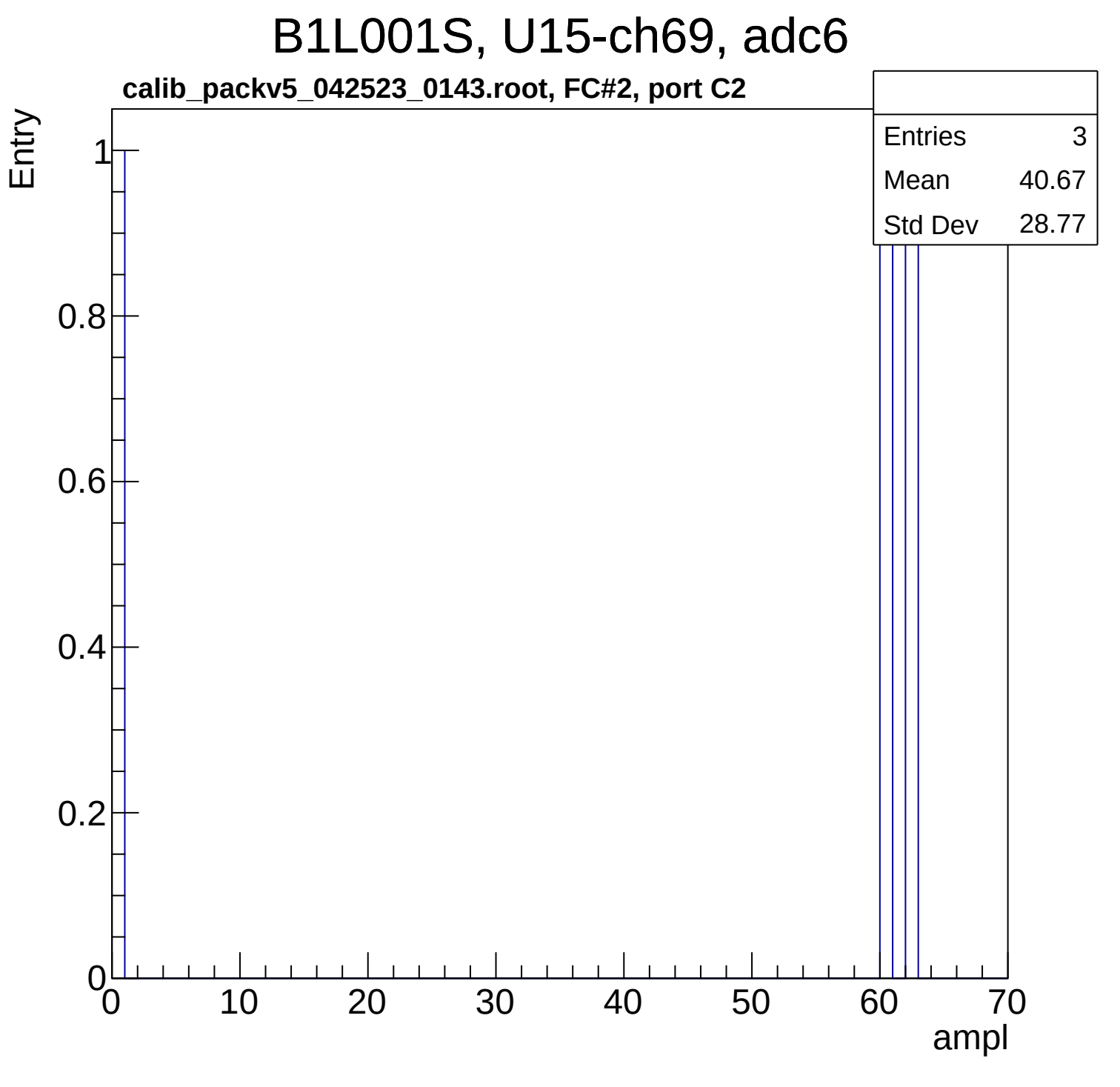
B1L001S, U15-ch69, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 40.67
Std Dev 28.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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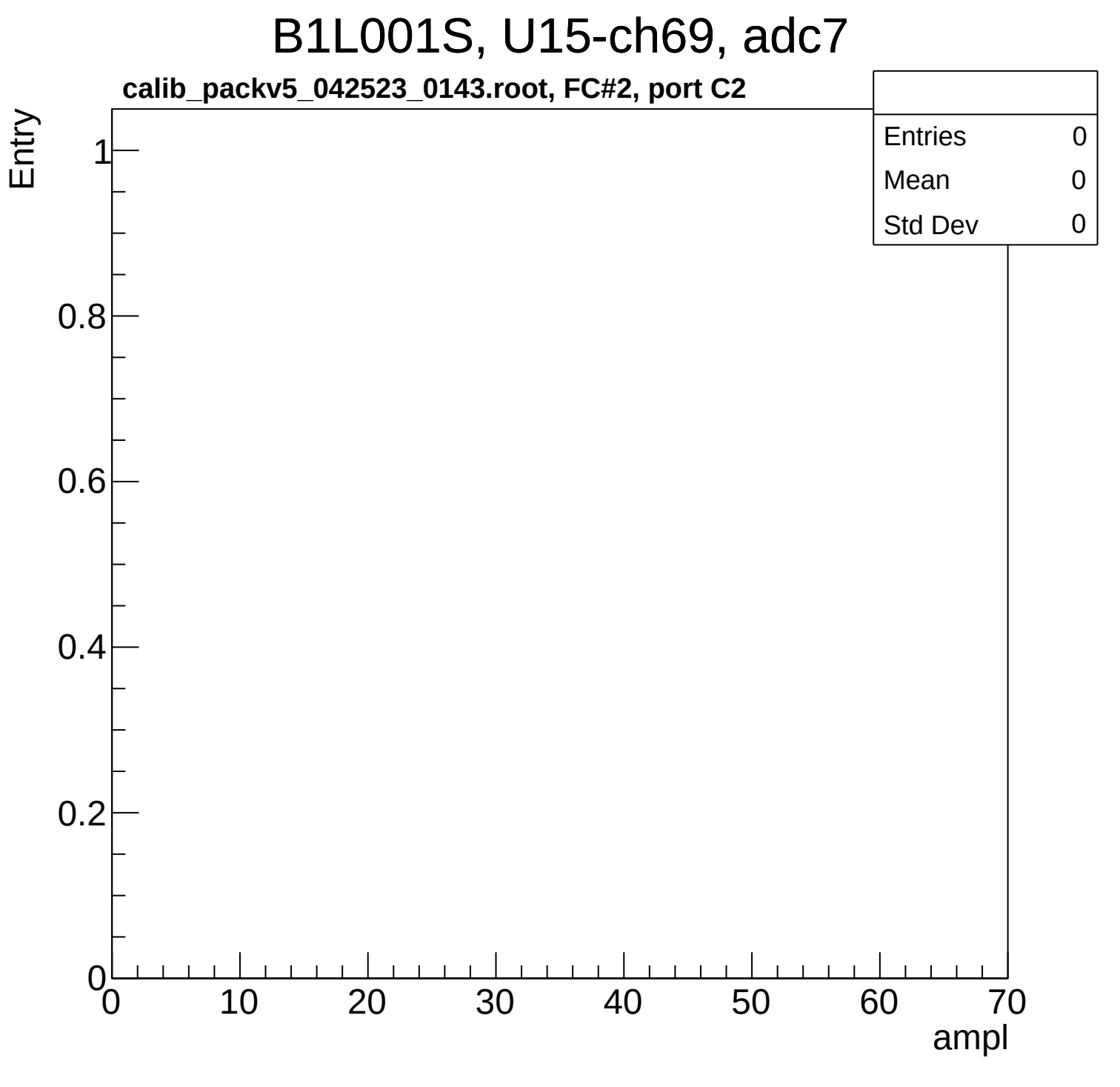
B1L001S, U15-ch69, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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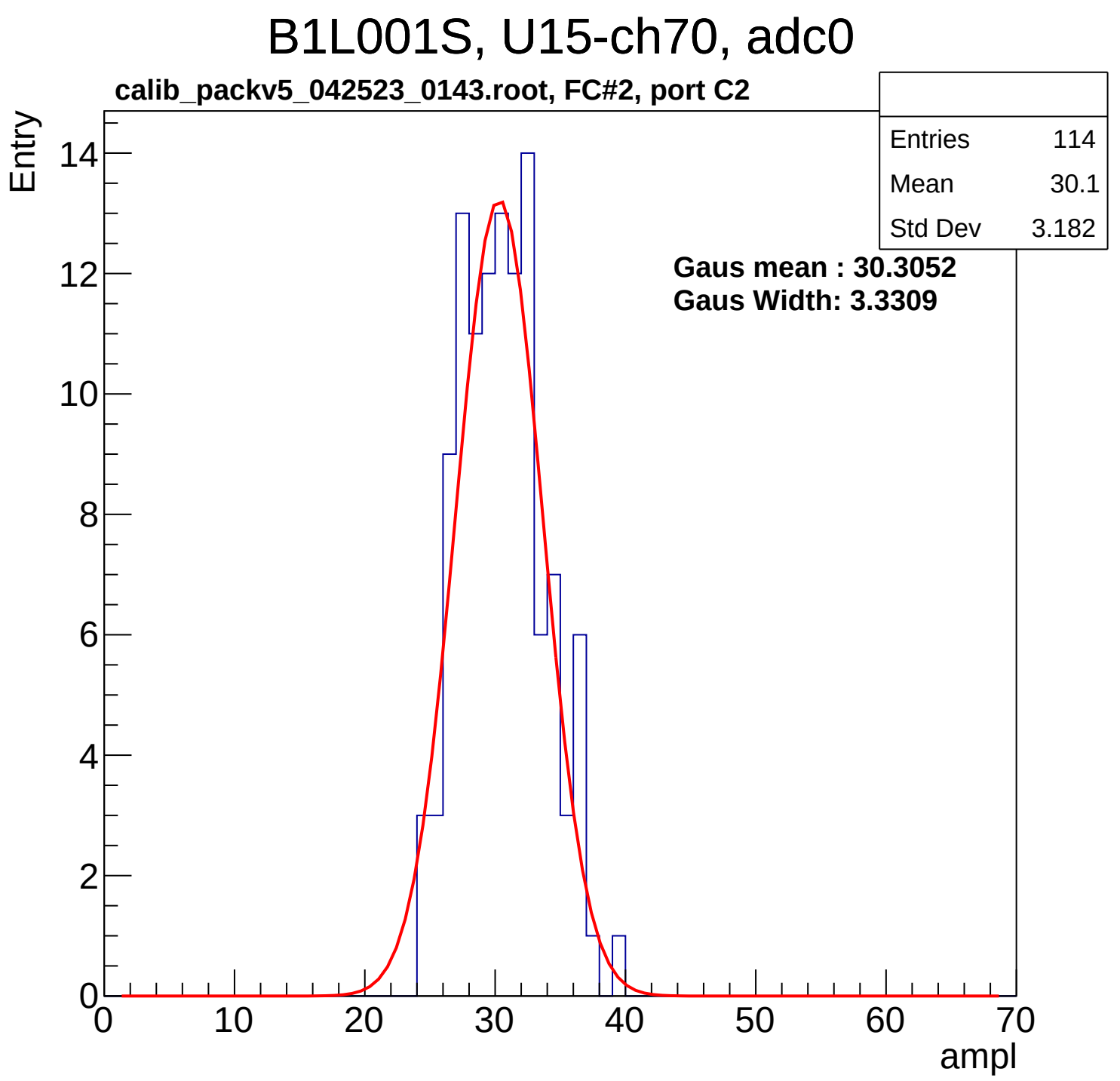
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch70, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 30.1
Std Dev 3.182
Gaus mean : 30.3052
Gaus Width: 3.3309
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
50
60
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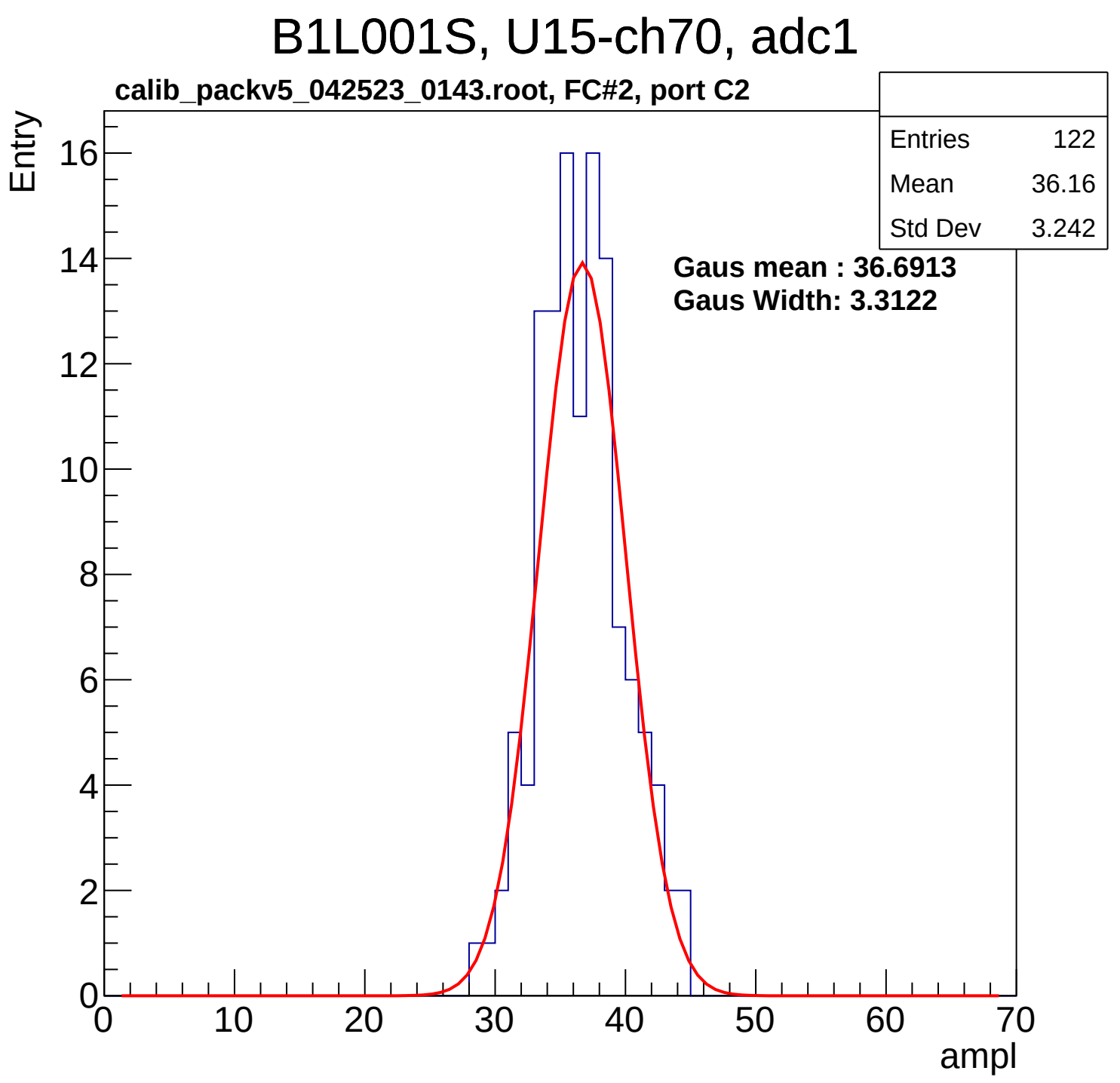
B1L001S, U15-ch70, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 36.16
Std Dev 3.242
Gaus mean : 36.6913
Gaus Width: 3.3122
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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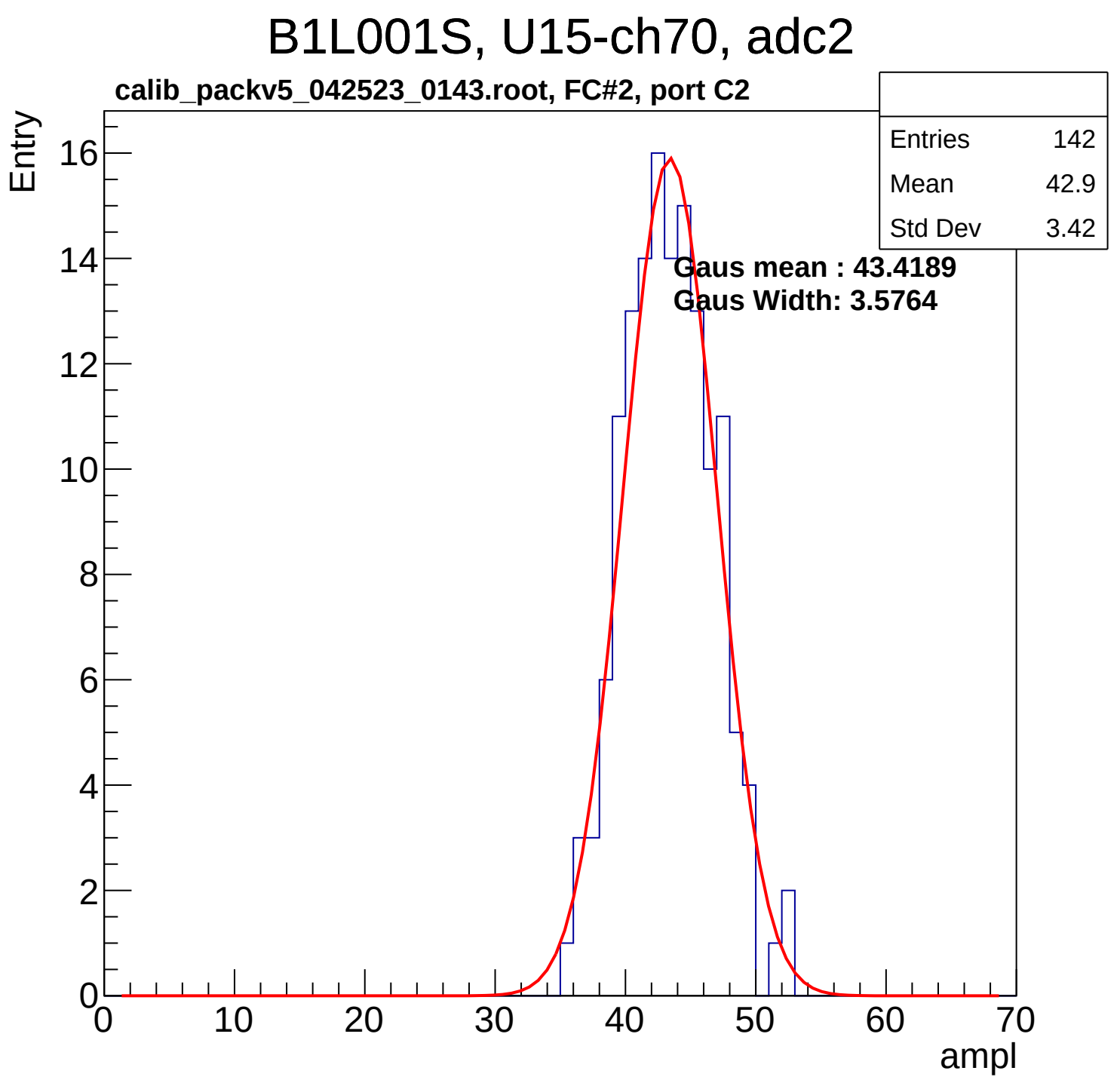
B1L001S, U15-ch70, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 42.9
Std Dev 3.42
Gaus mean : 43.4189
Gaus Width: 3.5764
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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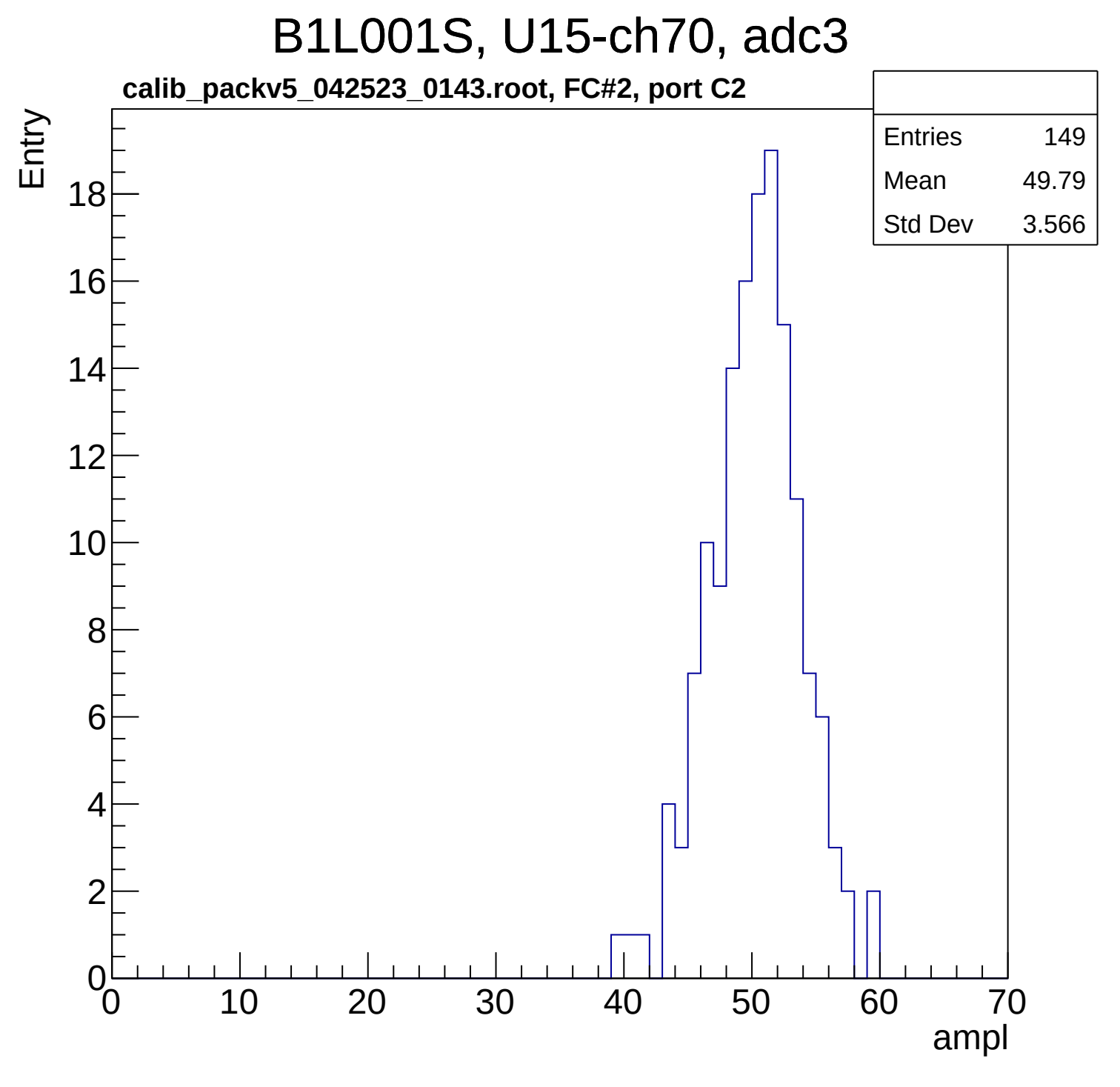
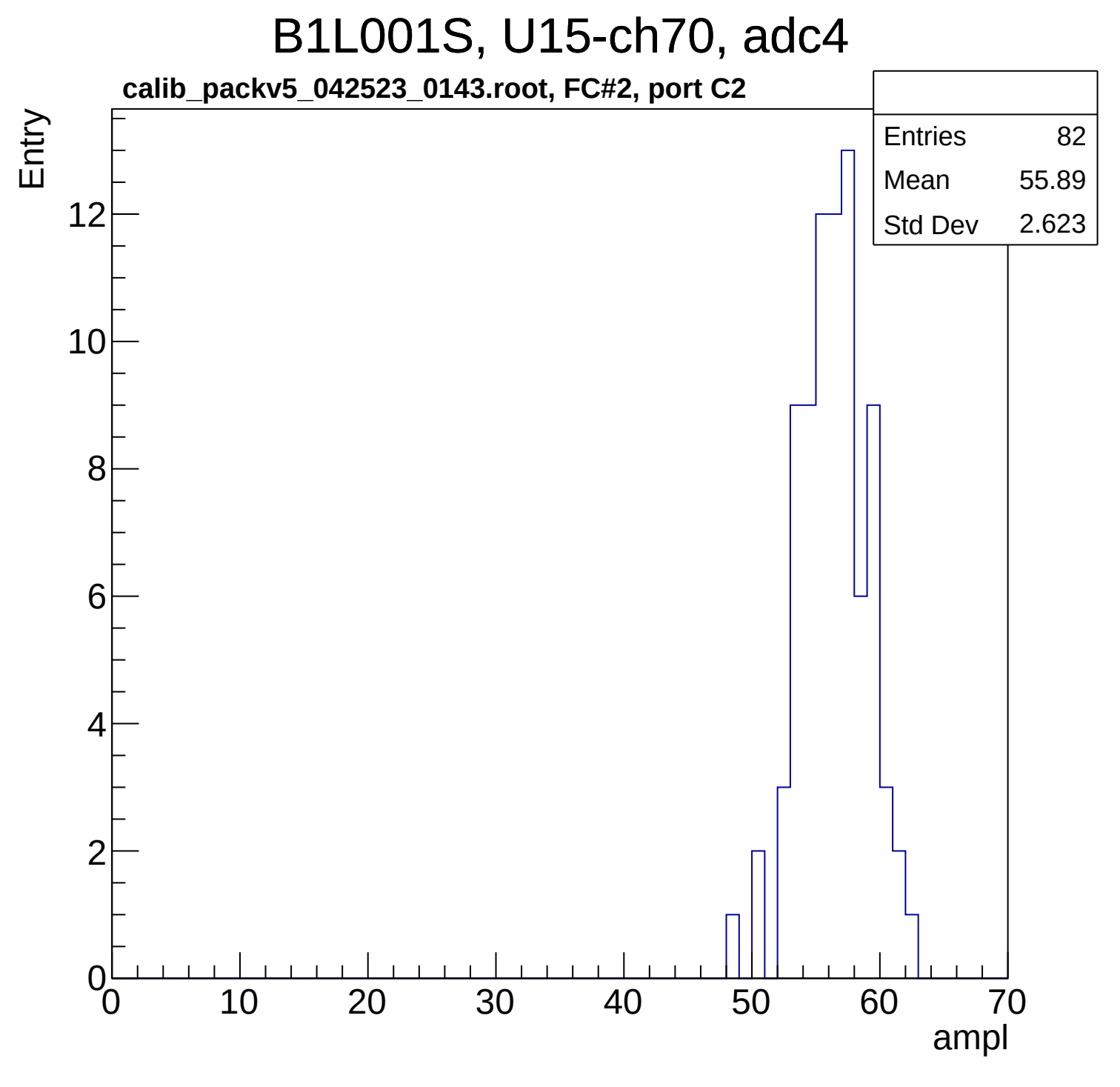
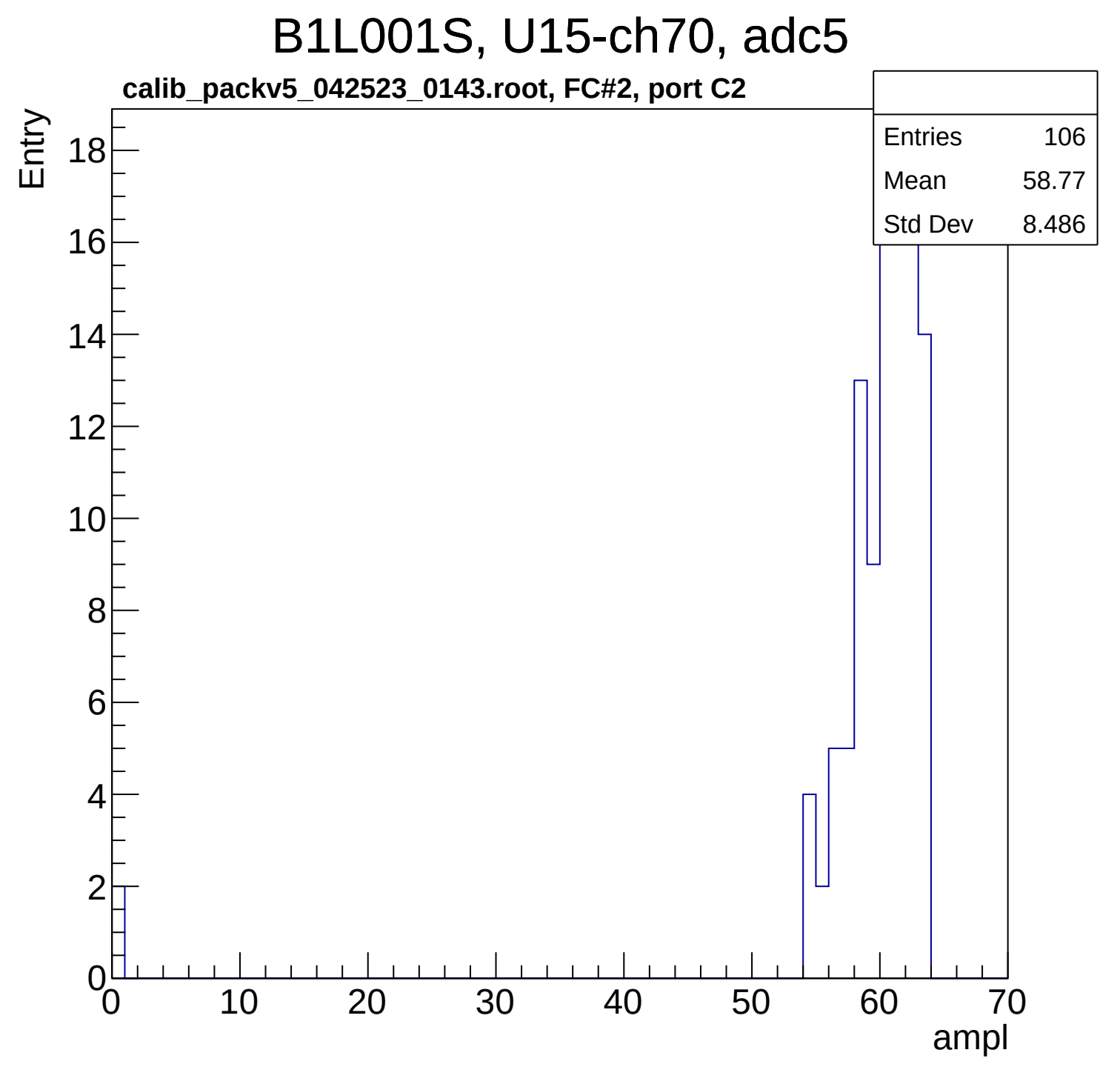

B1L001S, U15-ch70, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 49.79
Std Dev 3.566
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch70, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 55.89
Std Dev 2.623
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch70, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 58.77
Std Dev 8.486
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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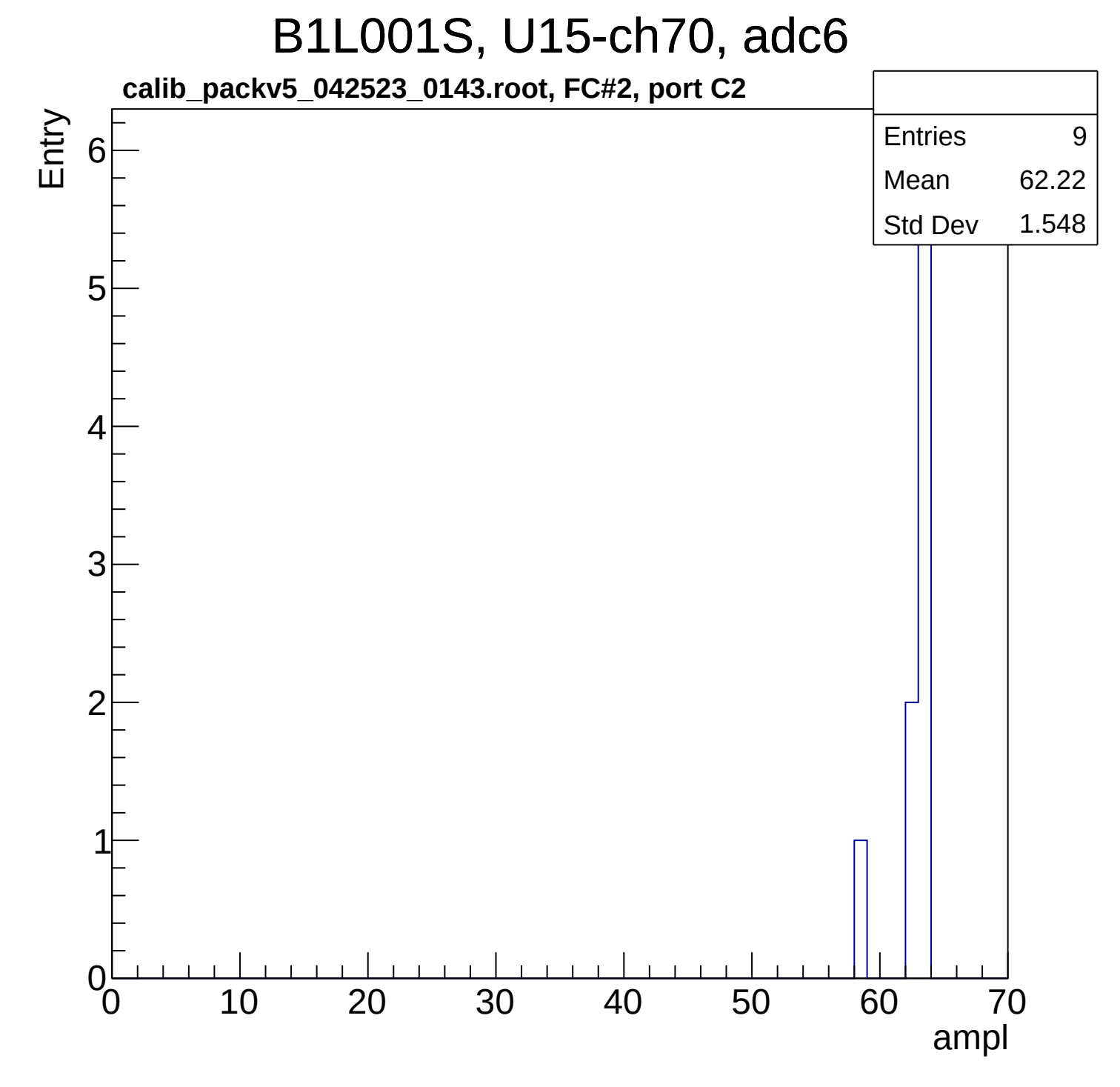
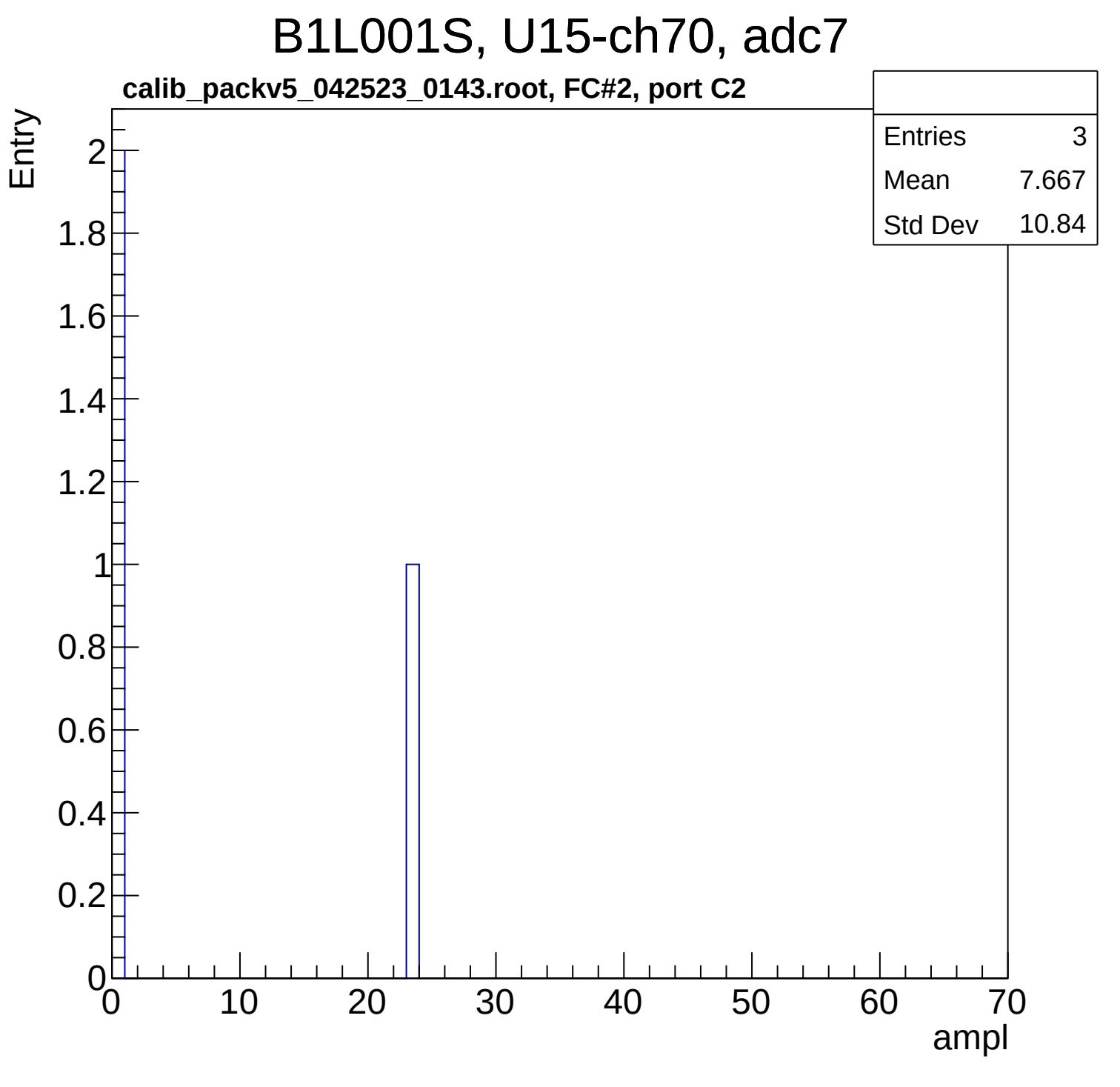

B1L001S, U15-ch70, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 62.22
Std Dev 1.548
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L001S, U15-ch70, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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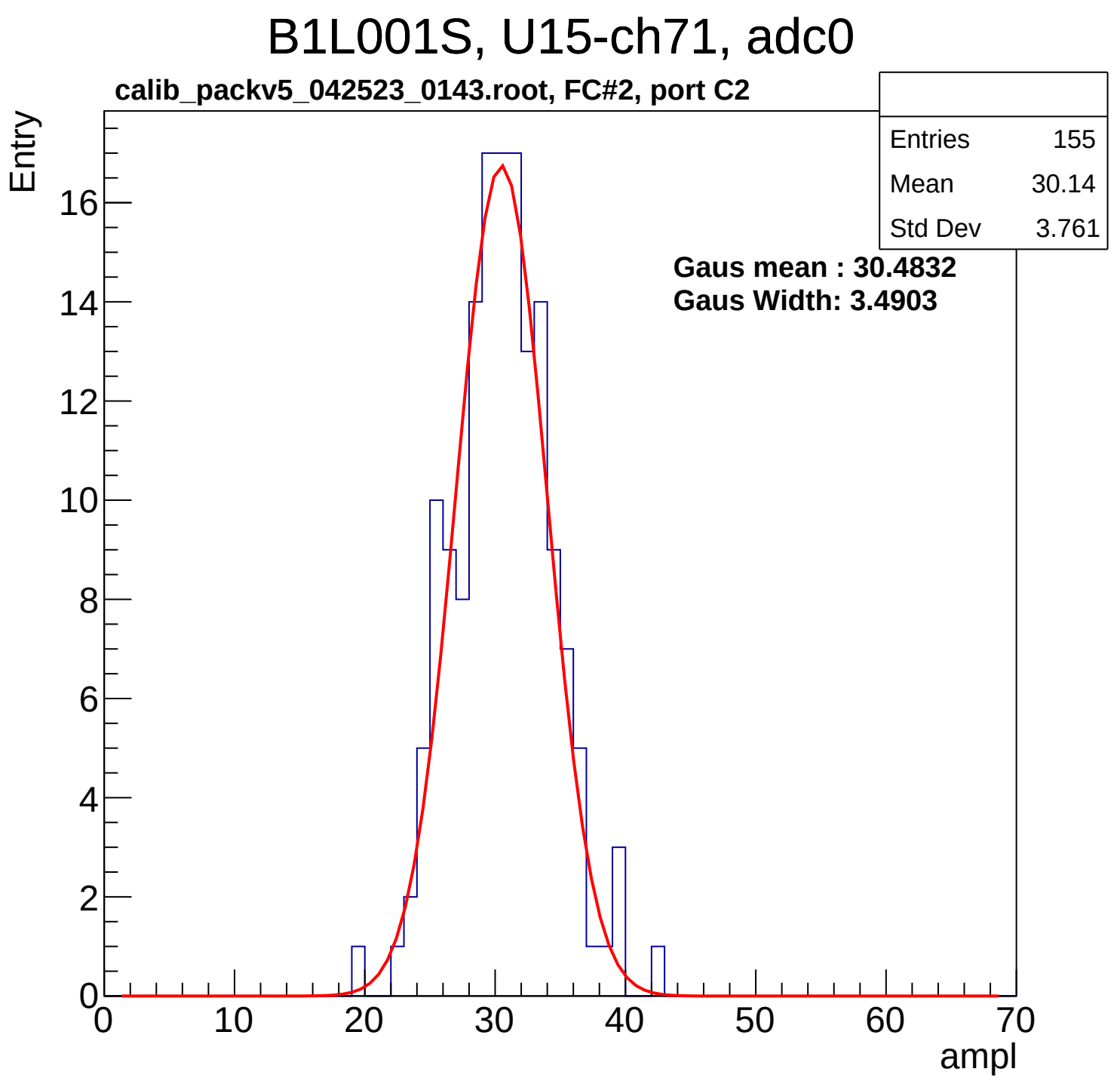
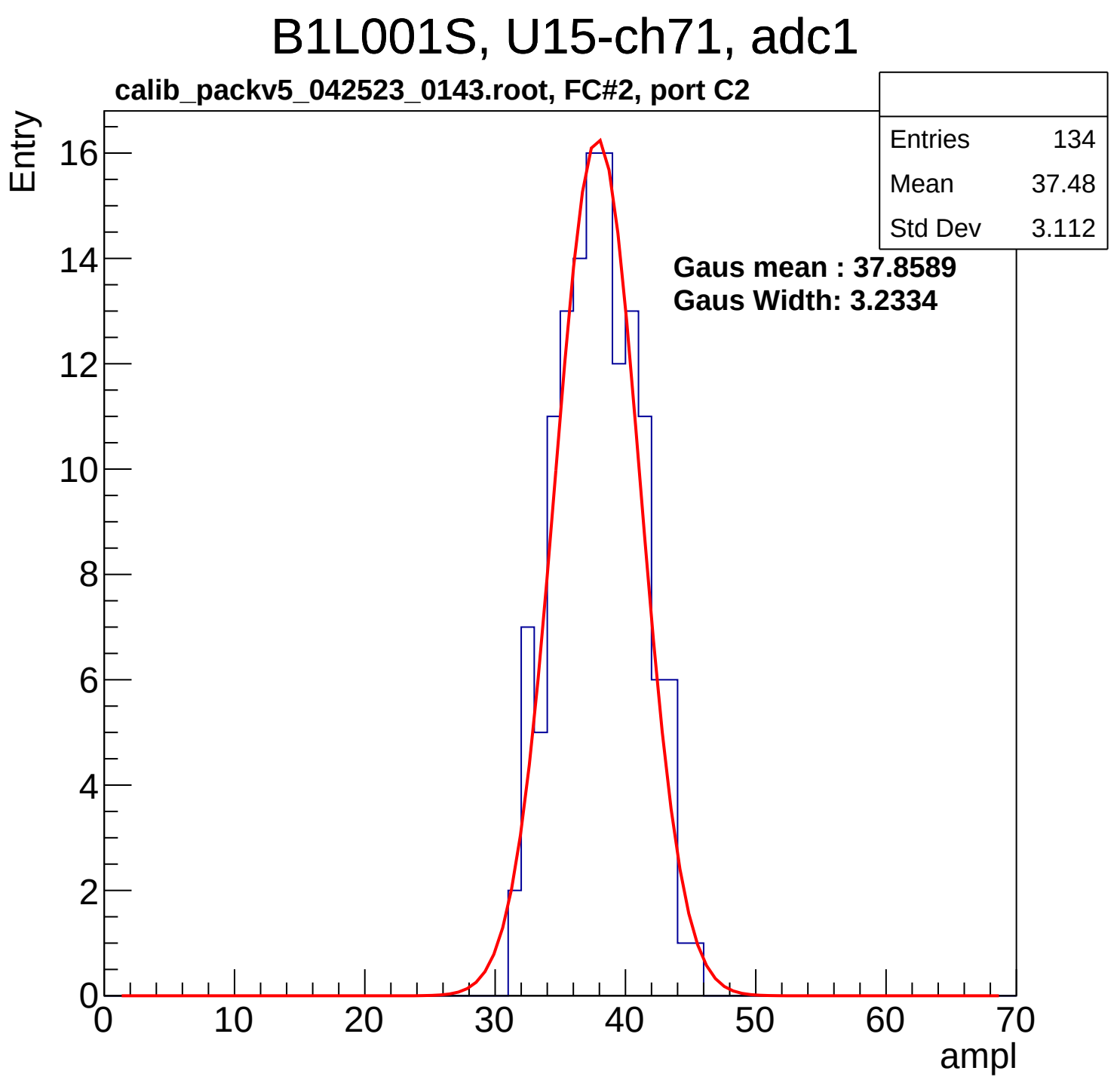

B1L001S, U15-ch71, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 30.14
Std Dev 3.761
Gaus mean : 30.4832
Gaus Width: 3.4903
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch71, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 37.48
Std Dev 3.112
Gaus mean : 37.8589
Gaus Width: 3.2334
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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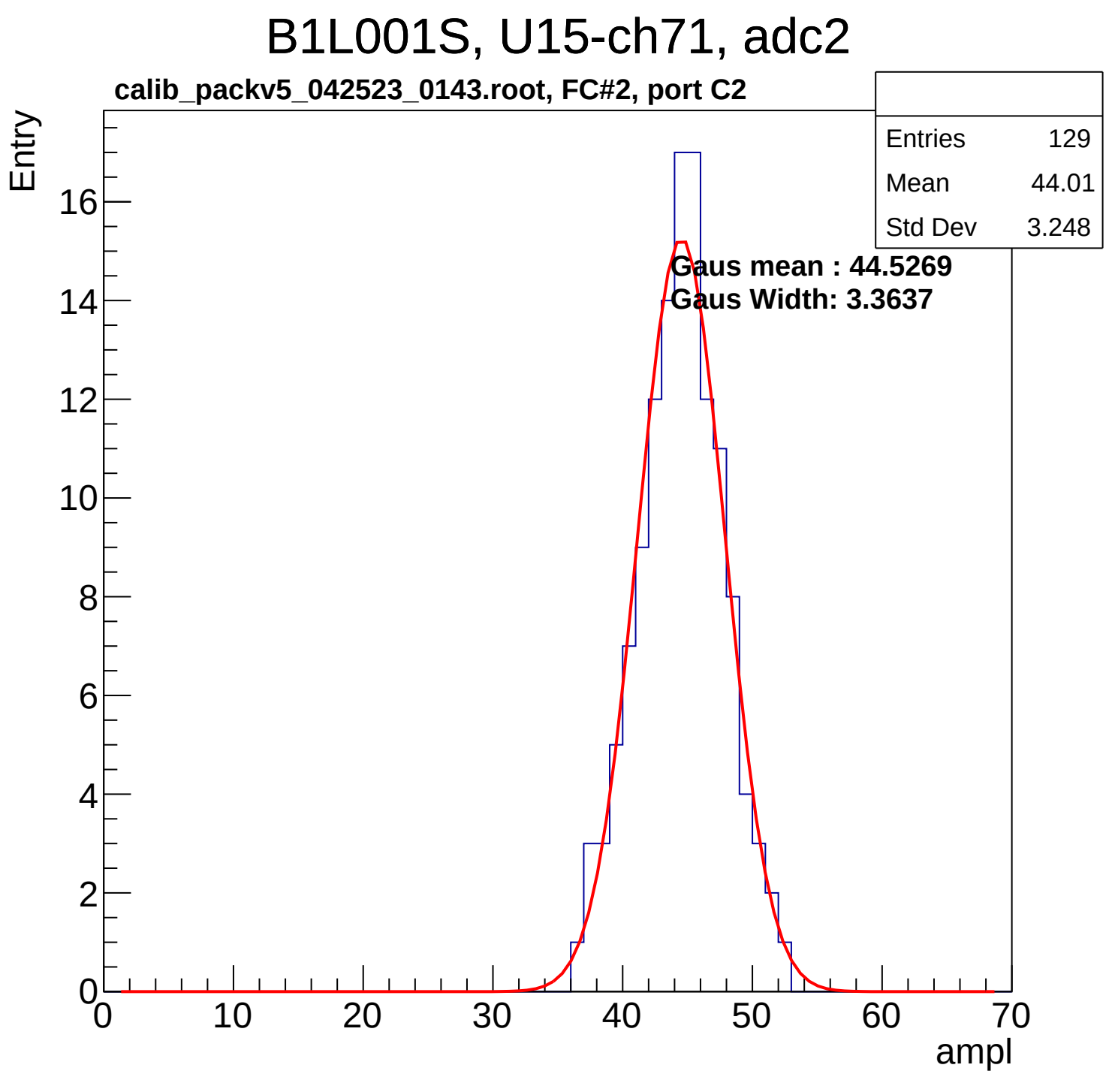
B1L001S, U15-ch71, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 44.01
Std Dev 3.248
Gaus mean : 44.5269
Gaus Width: 3.3637
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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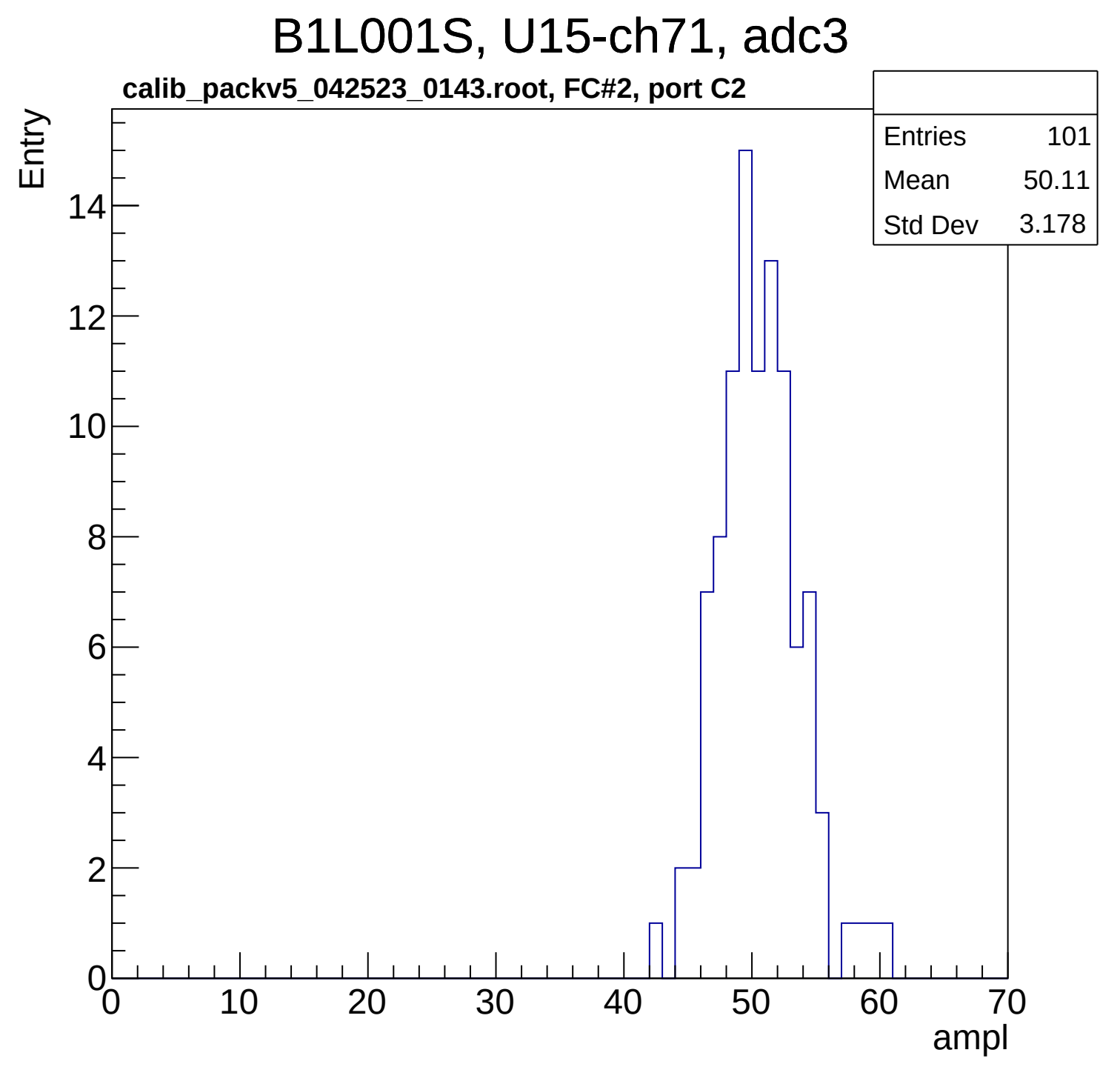
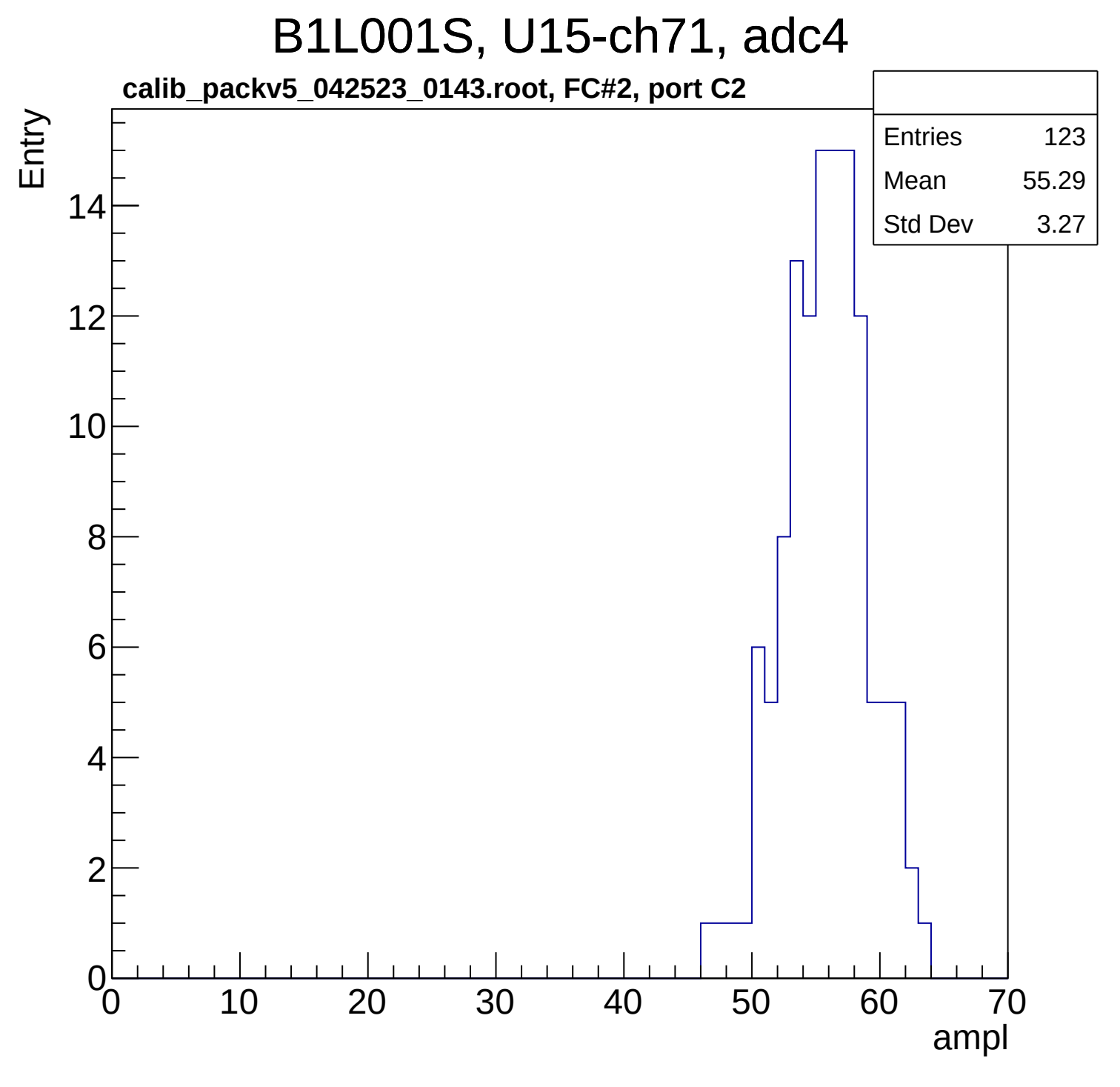

B1L001S, U15-ch71, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 50.11
Std Dev 3.178
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch71, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 55.29
Std Dev 3.27
Entry
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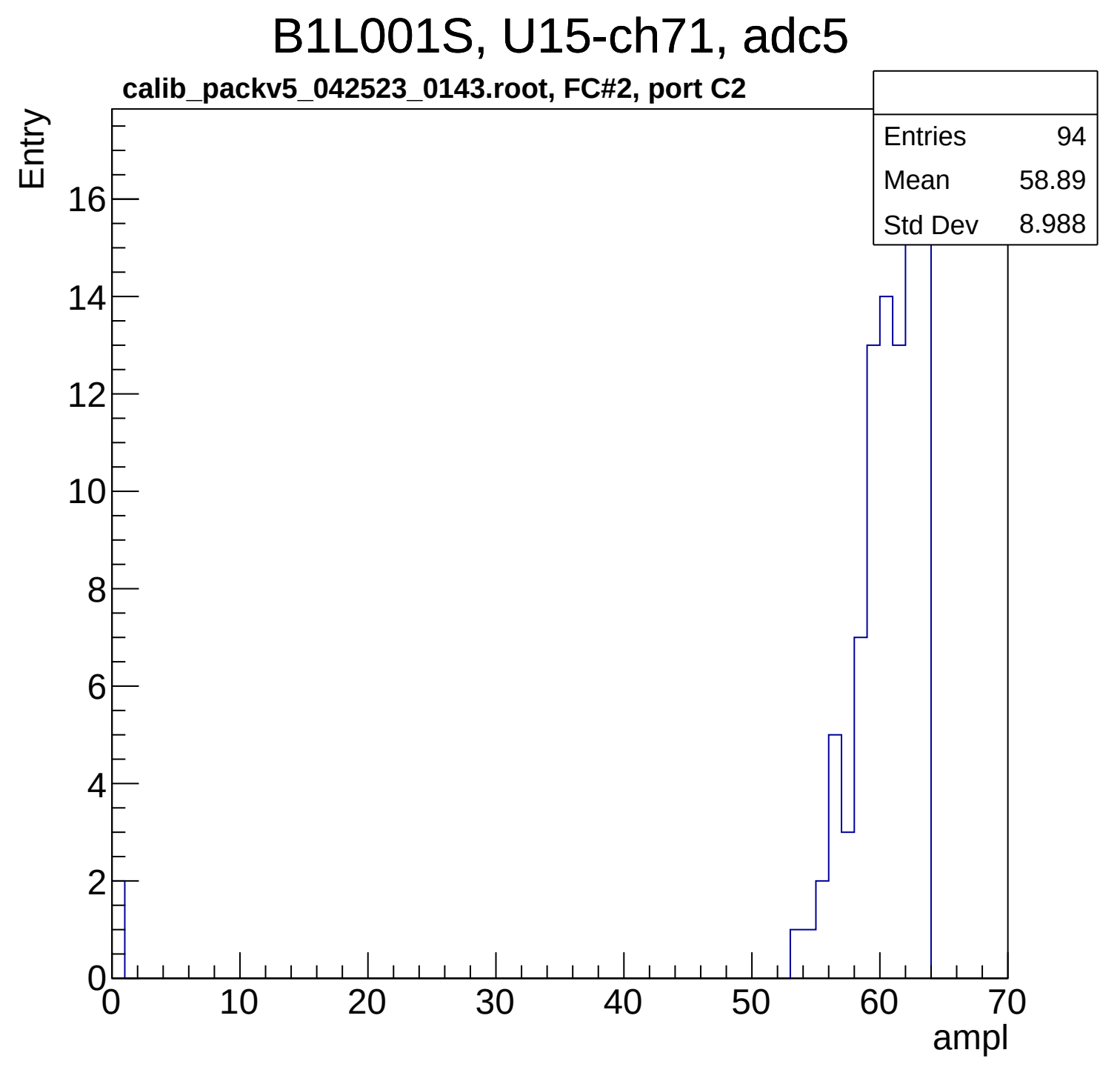
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch71, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 58.89
Std Dev 8.988
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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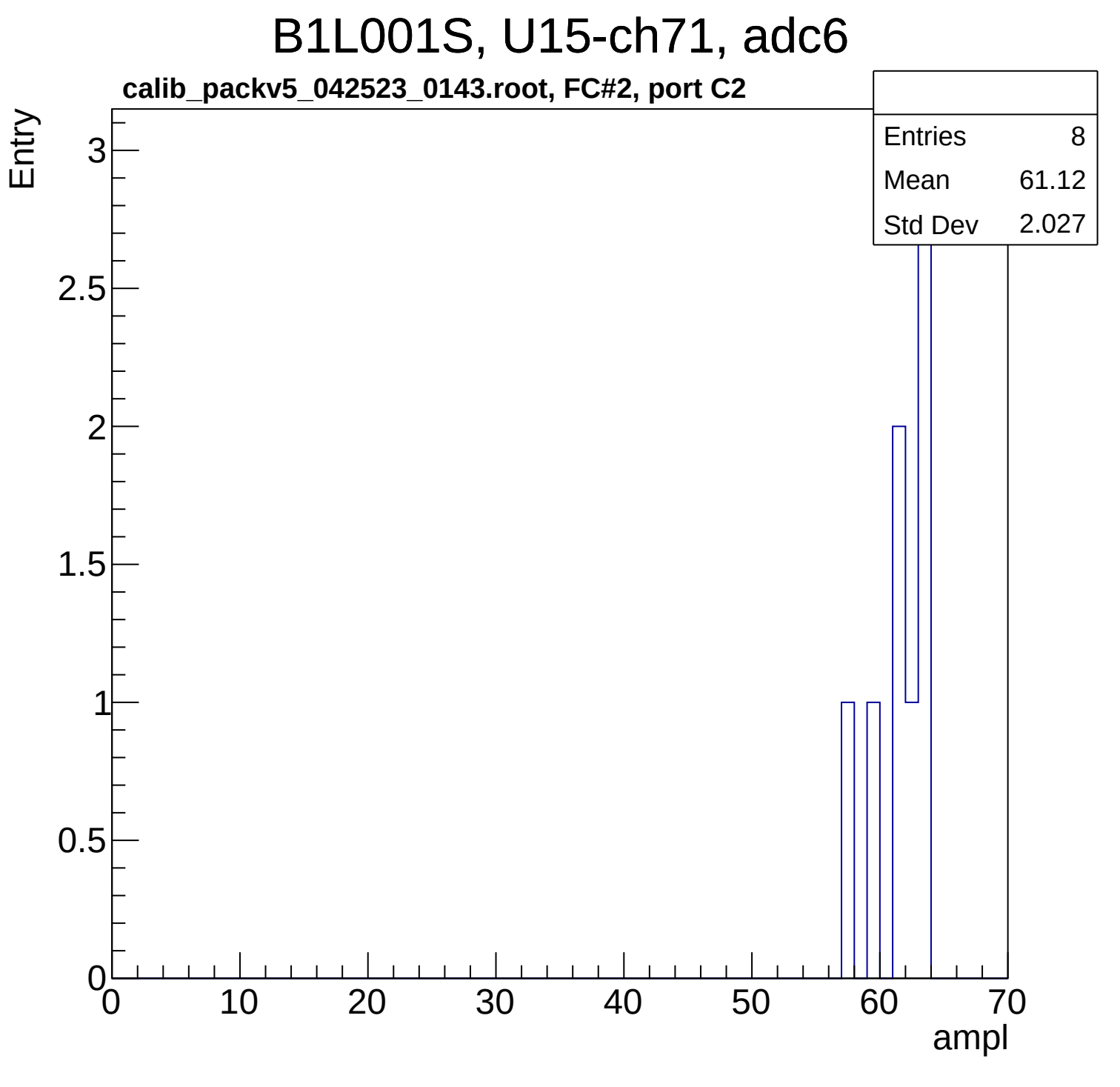
B1L001S, U15-ch71, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.12
Std Dev 2.027
Entry
ampl
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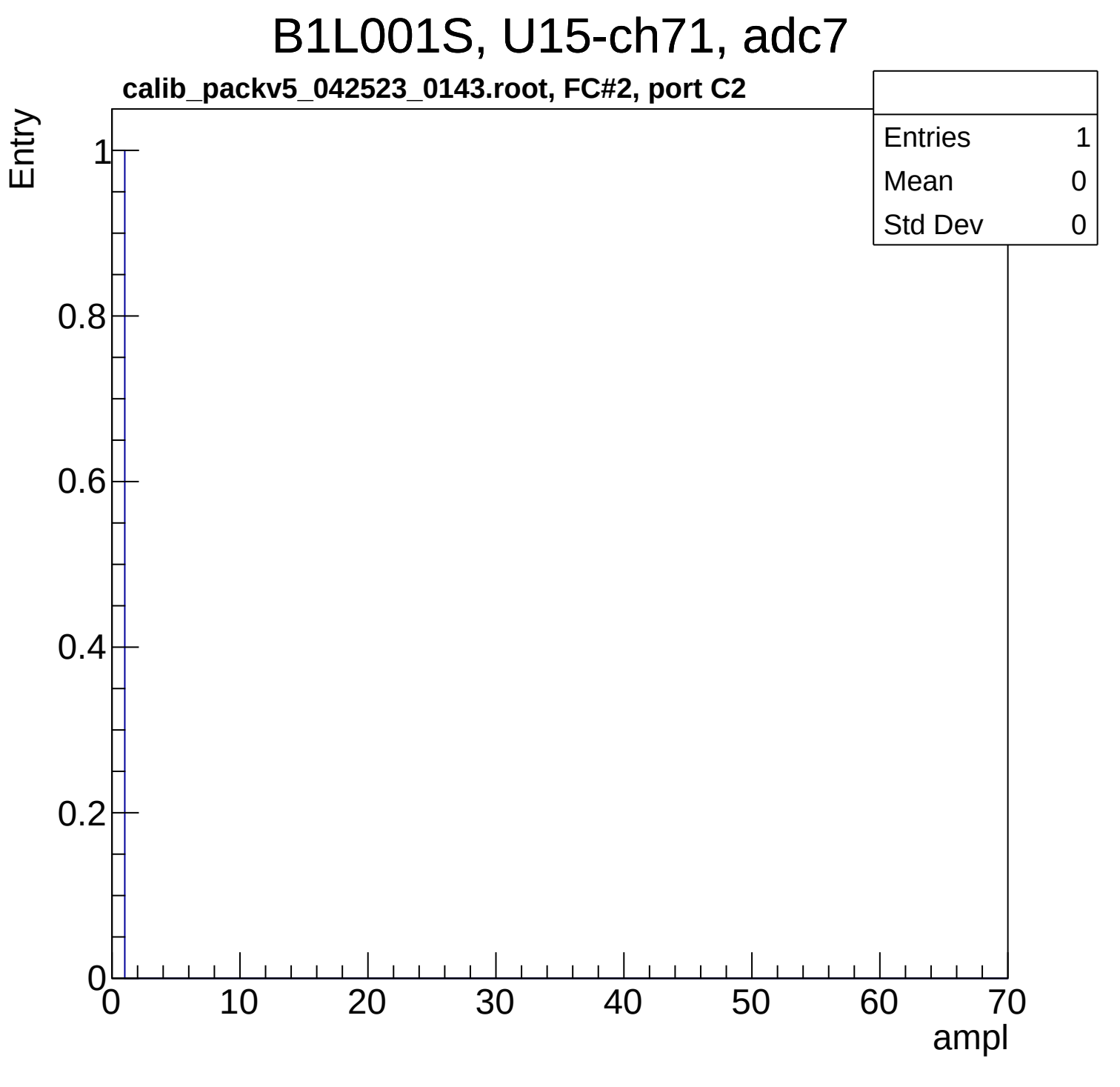
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch71, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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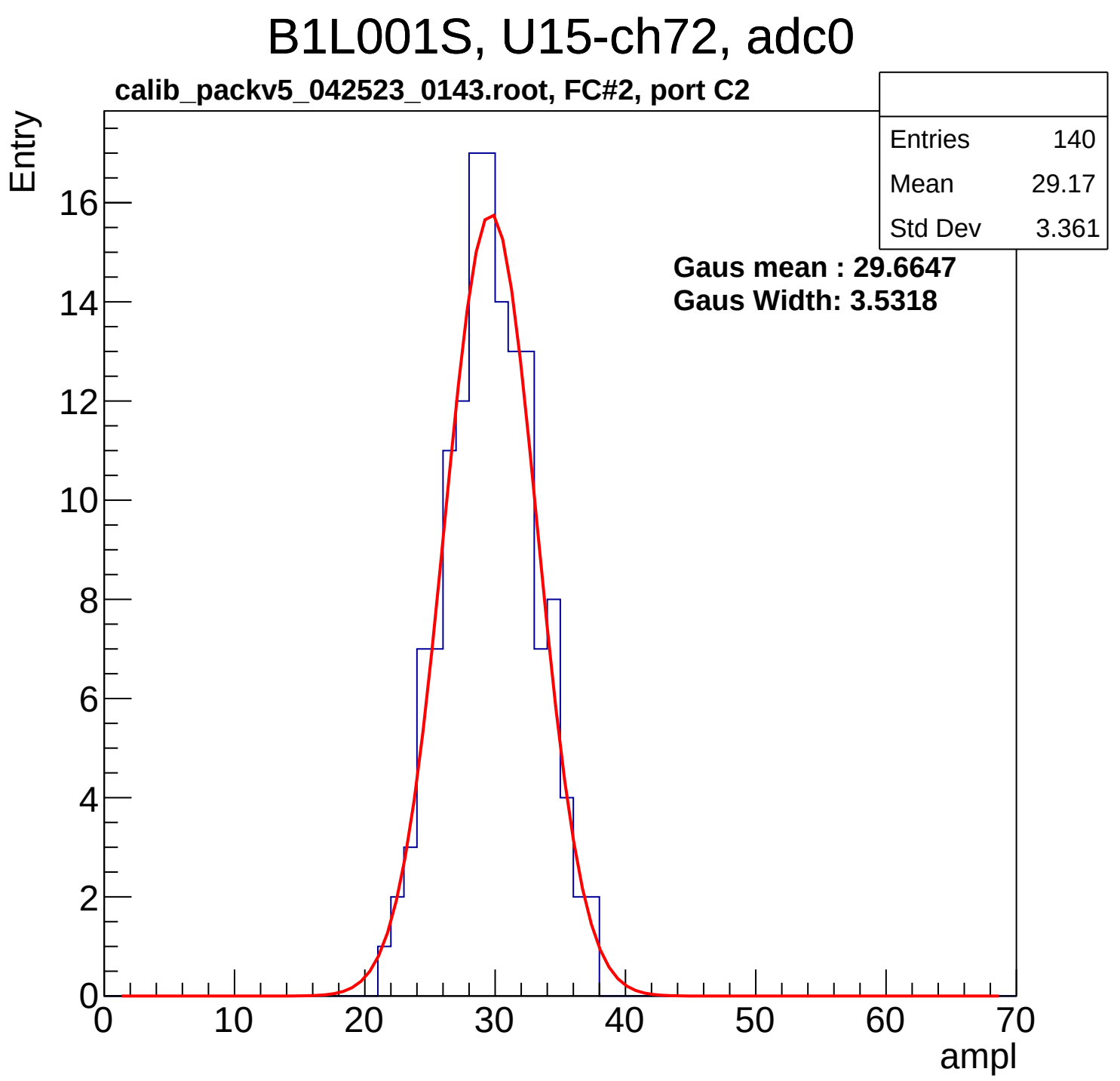
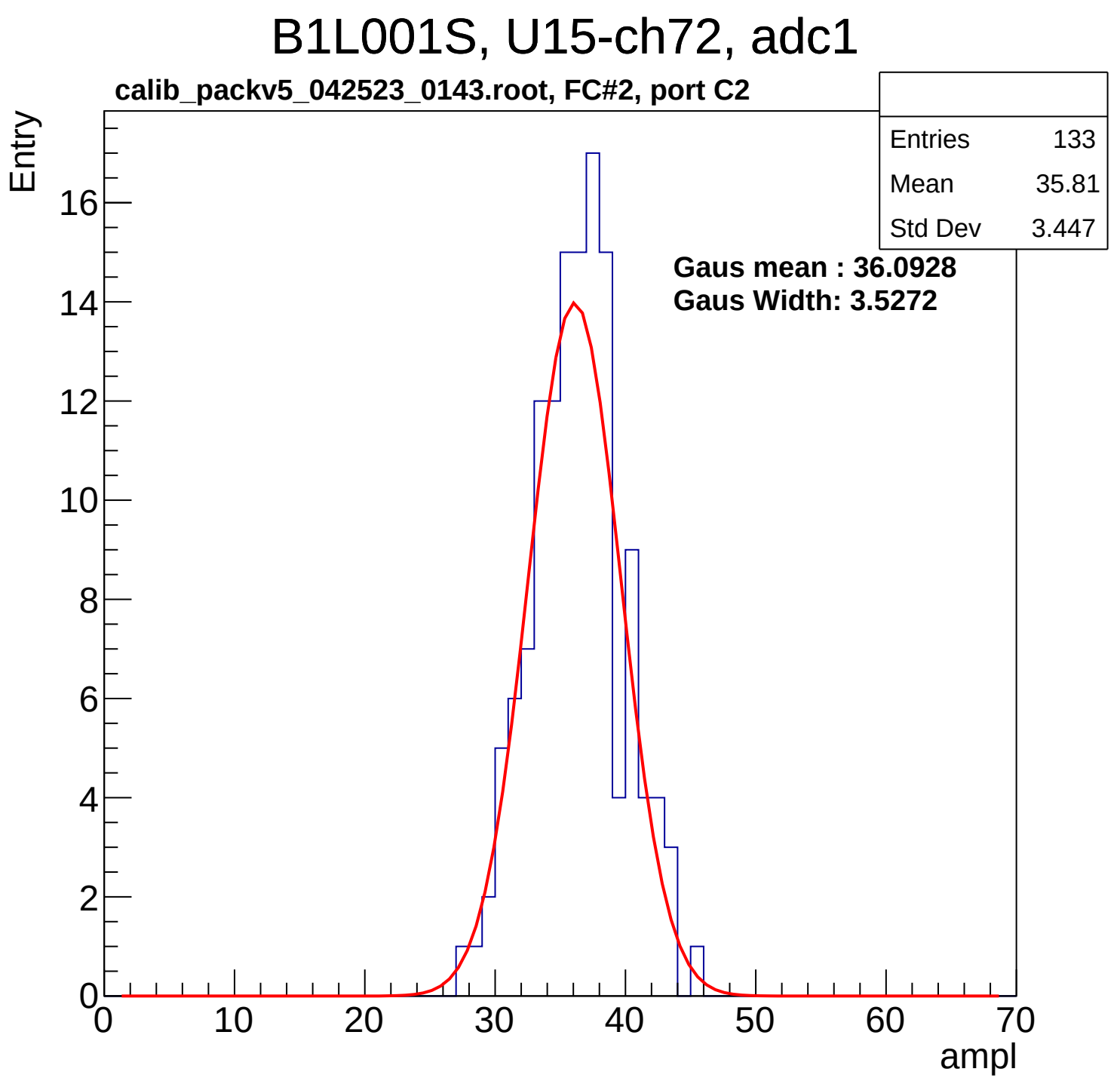
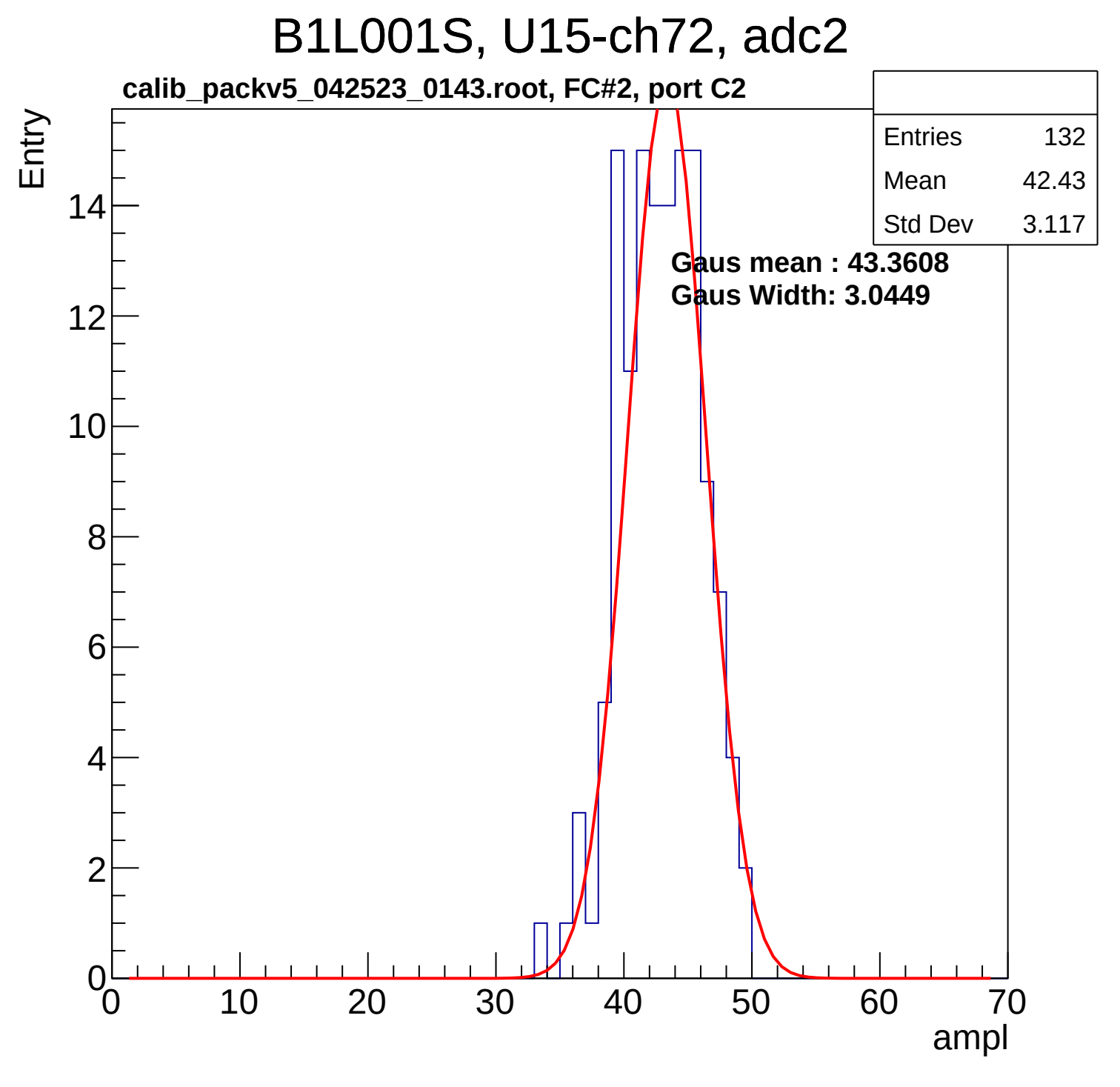
30
40
50
60
70
ampl


B1L001S, U15-ch72, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 29.17
Std Dev 3.361
Gaus mean : 29.6647
Gaus Width: 3.5318
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch72, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 35.81
Std Dev 3.447
Gaus mean : 36.0928
Gaus Width: 3.5272
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch72, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 42.43
Std Dev 3.117
Gaus mean : 43.3608
Gaus Width: 3.0449
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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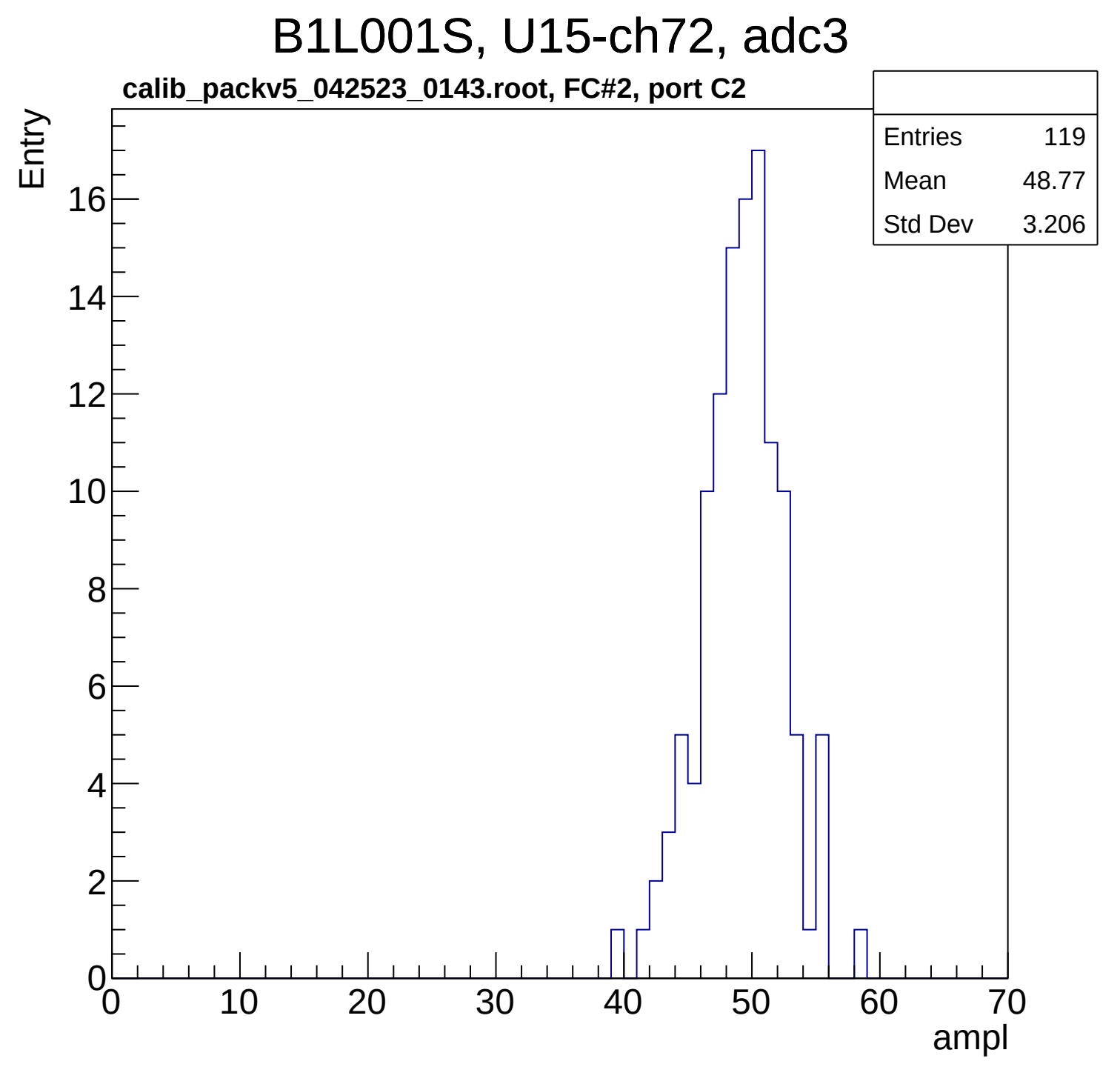
50
60
70


B1L001S, U15-ch72, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 48.77
Std Dev 3.206
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
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50
60
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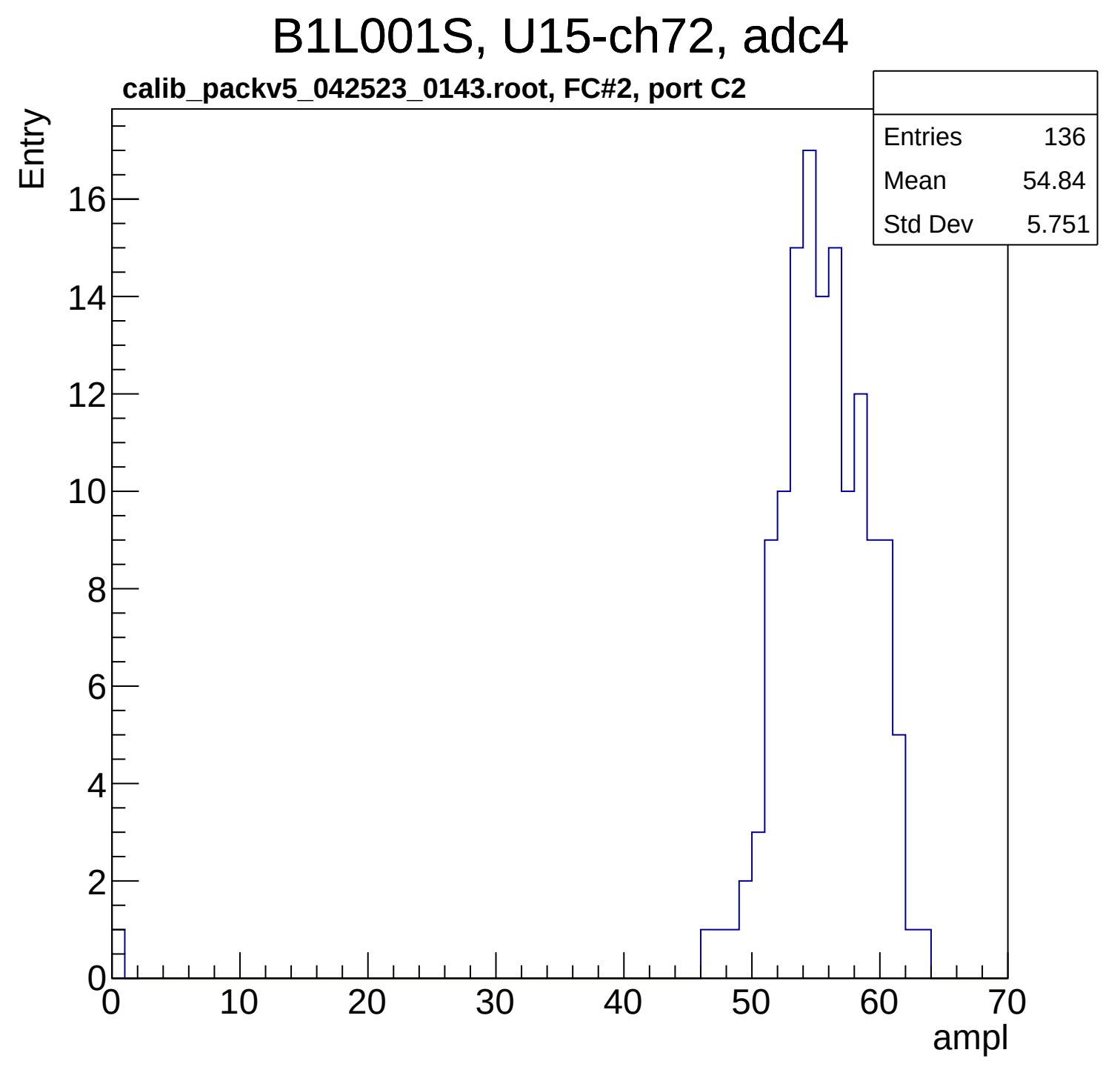
B1L001S, U15-ch72, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 54.84
Std Dev 5.751
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
50
60
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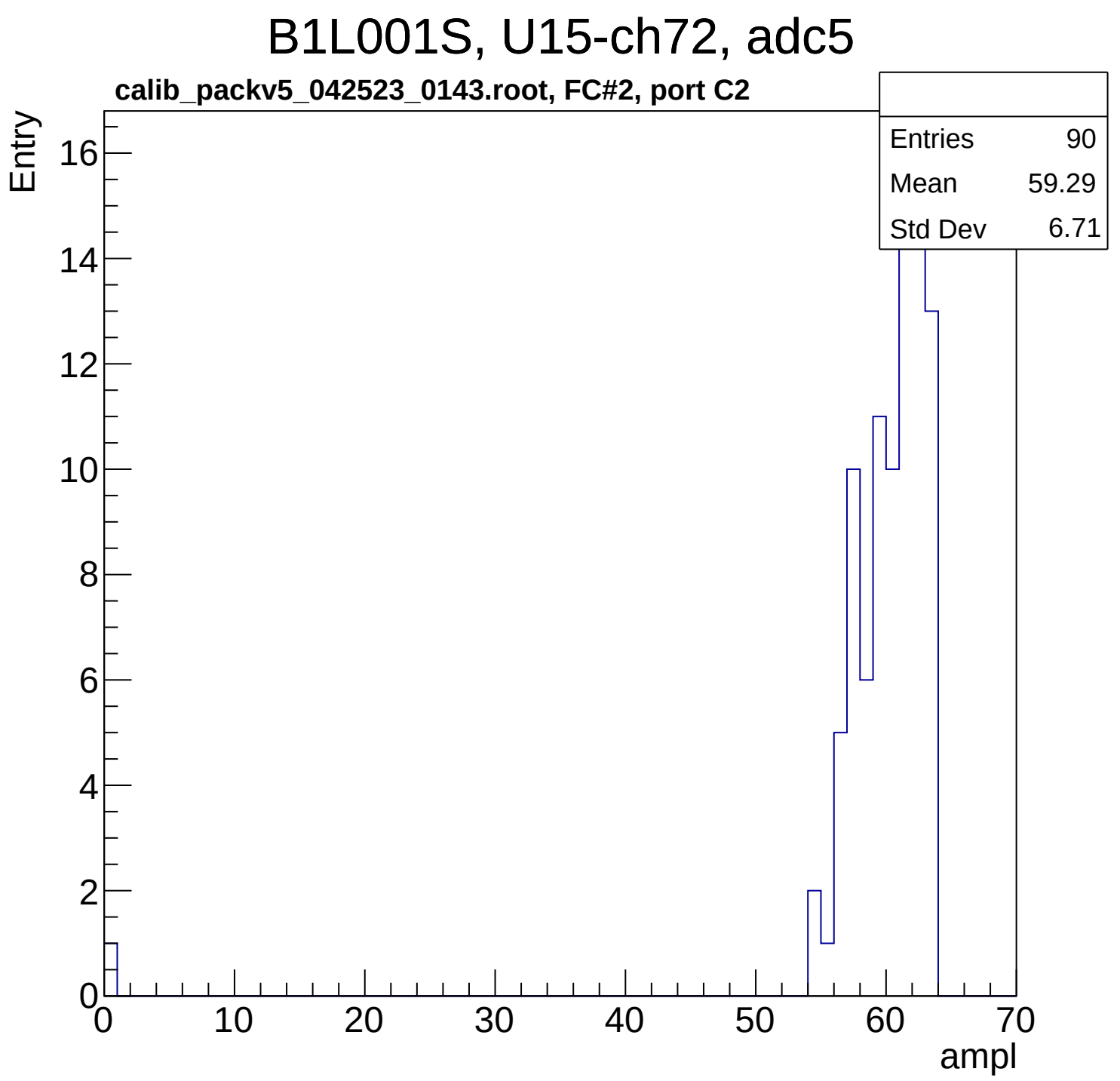
B1L001S, U15-ch72, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 59.29
Std Dev 6.71
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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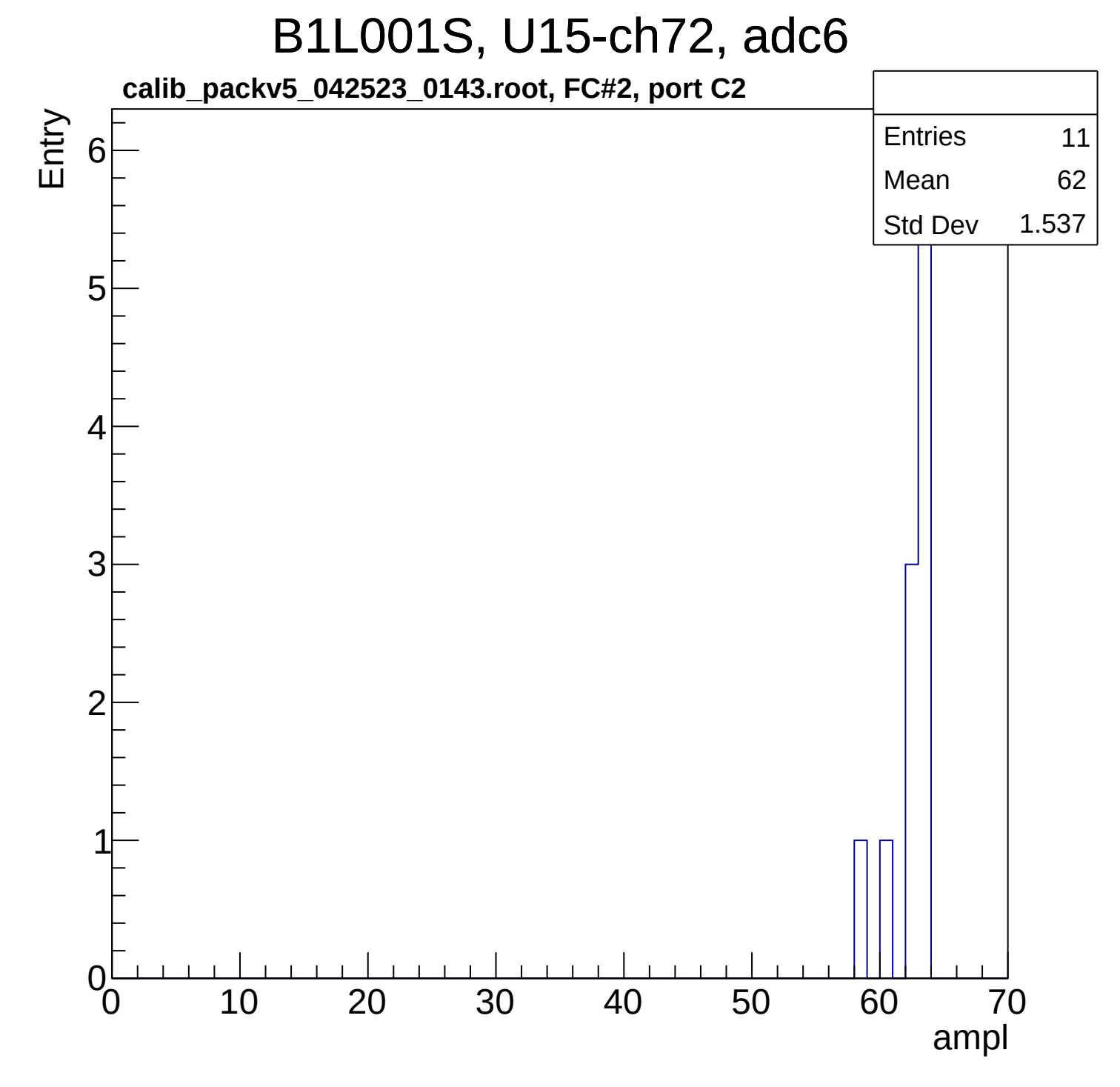
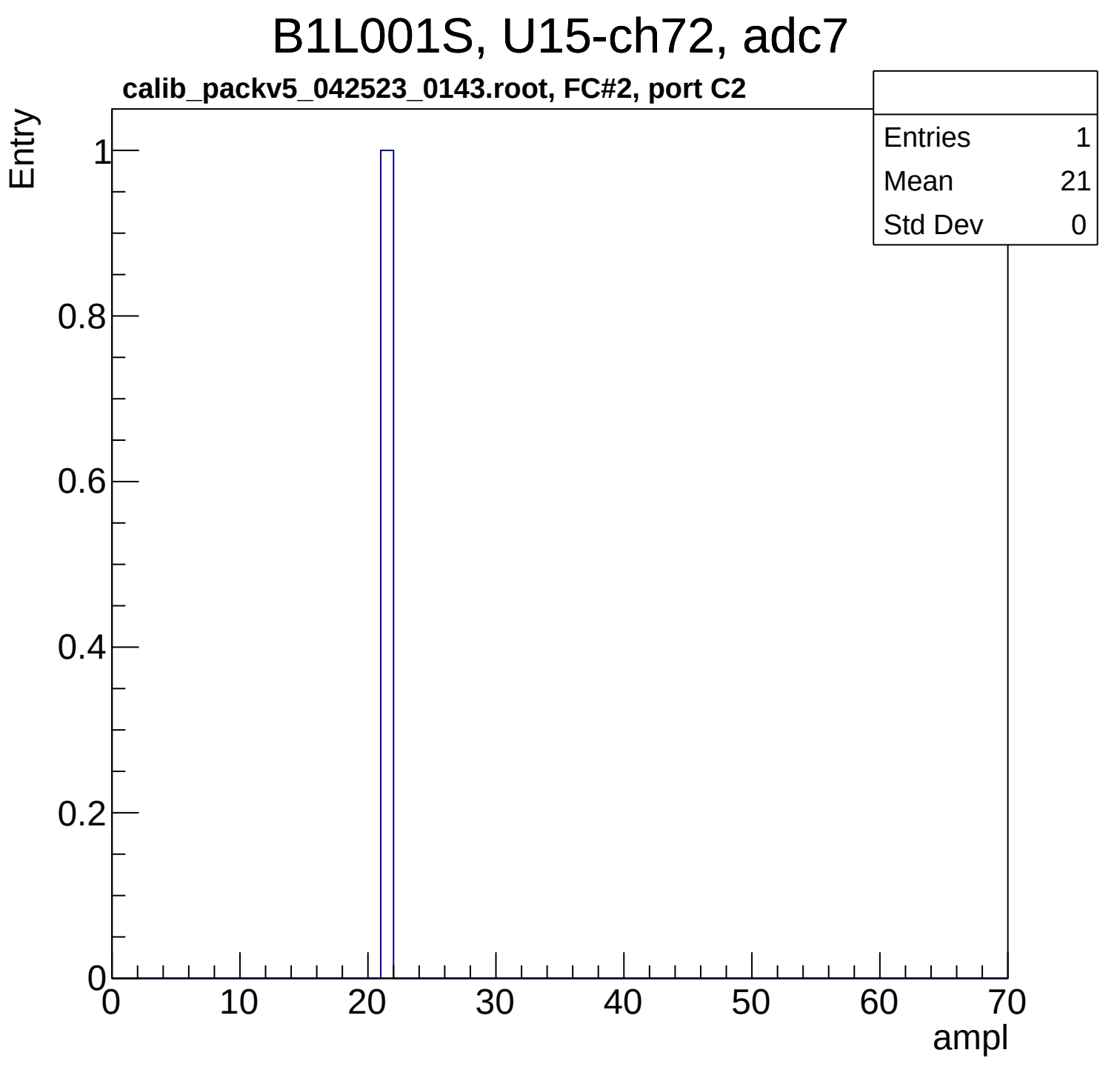

B1L001S, U15-ch72, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 62
Std Dev 1.537
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch72, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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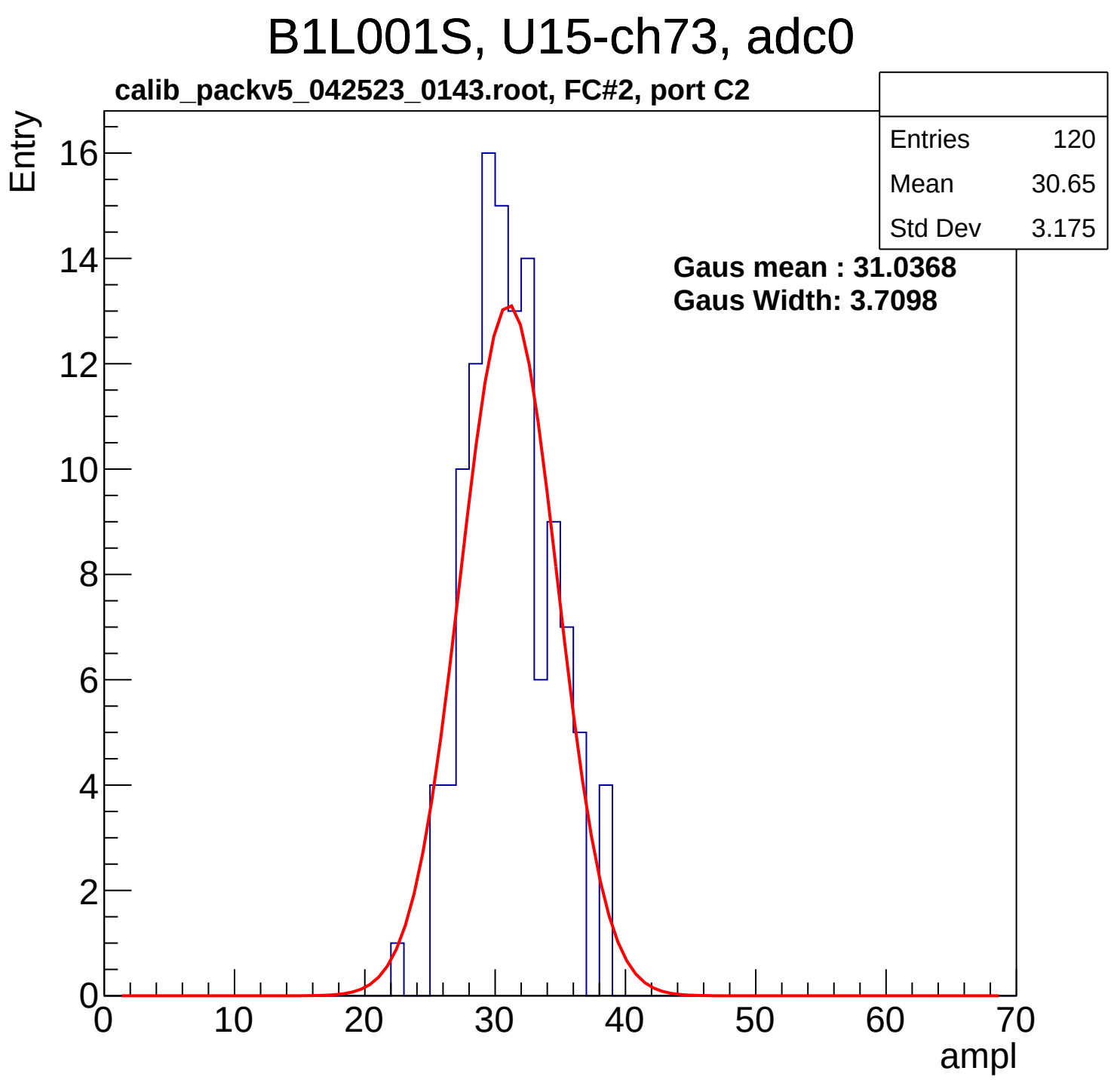
B1L001S, U15-ch73, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 30.65
Std Dev 3.175
Gaus mean : 31.0368
Gaus Width: 3.7098
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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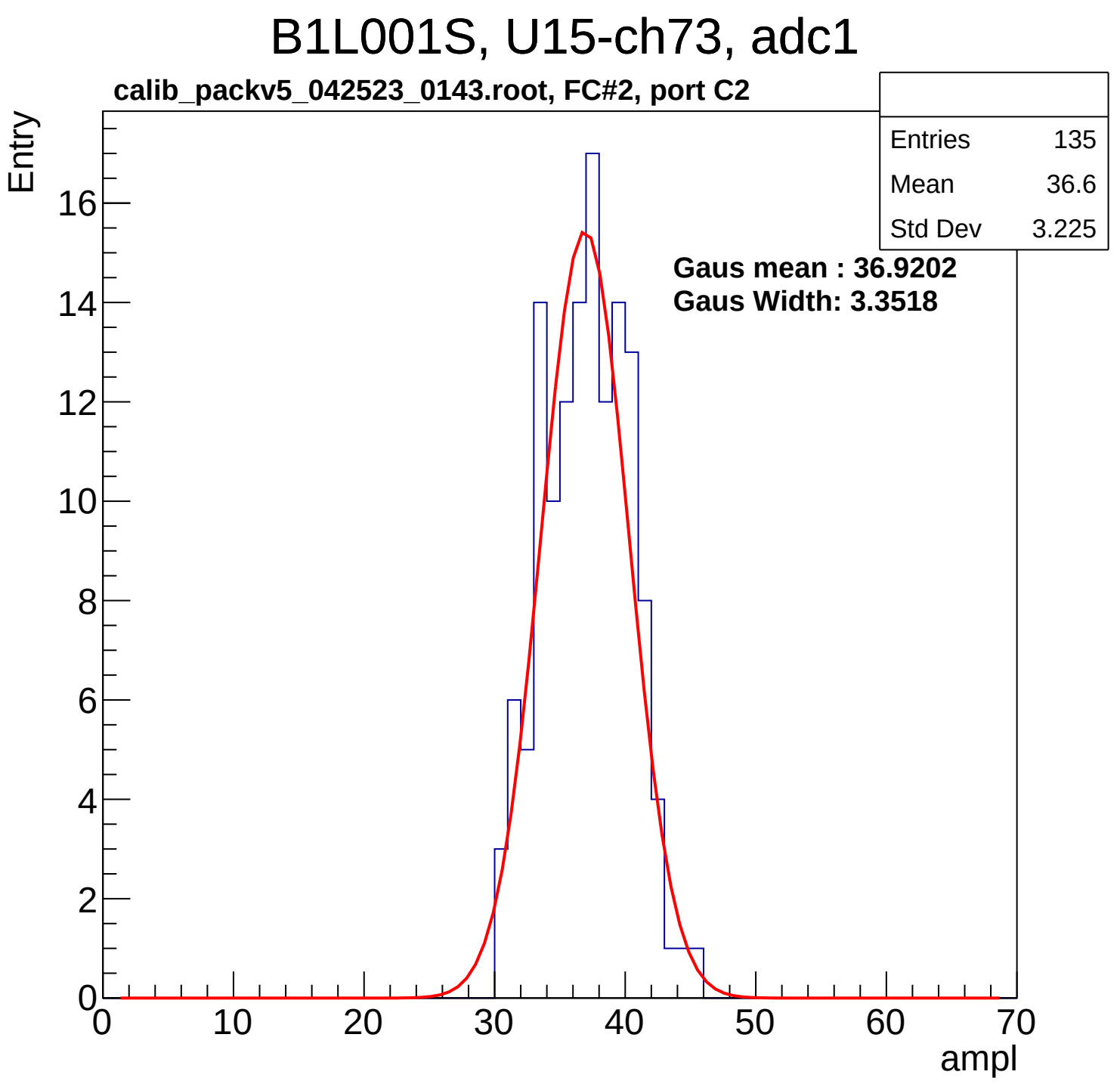
B1L001S, U15-ch73, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 36.6
Std Dev 3.225
Gaus mean : 36.9202
Gaus Width: 3.3518
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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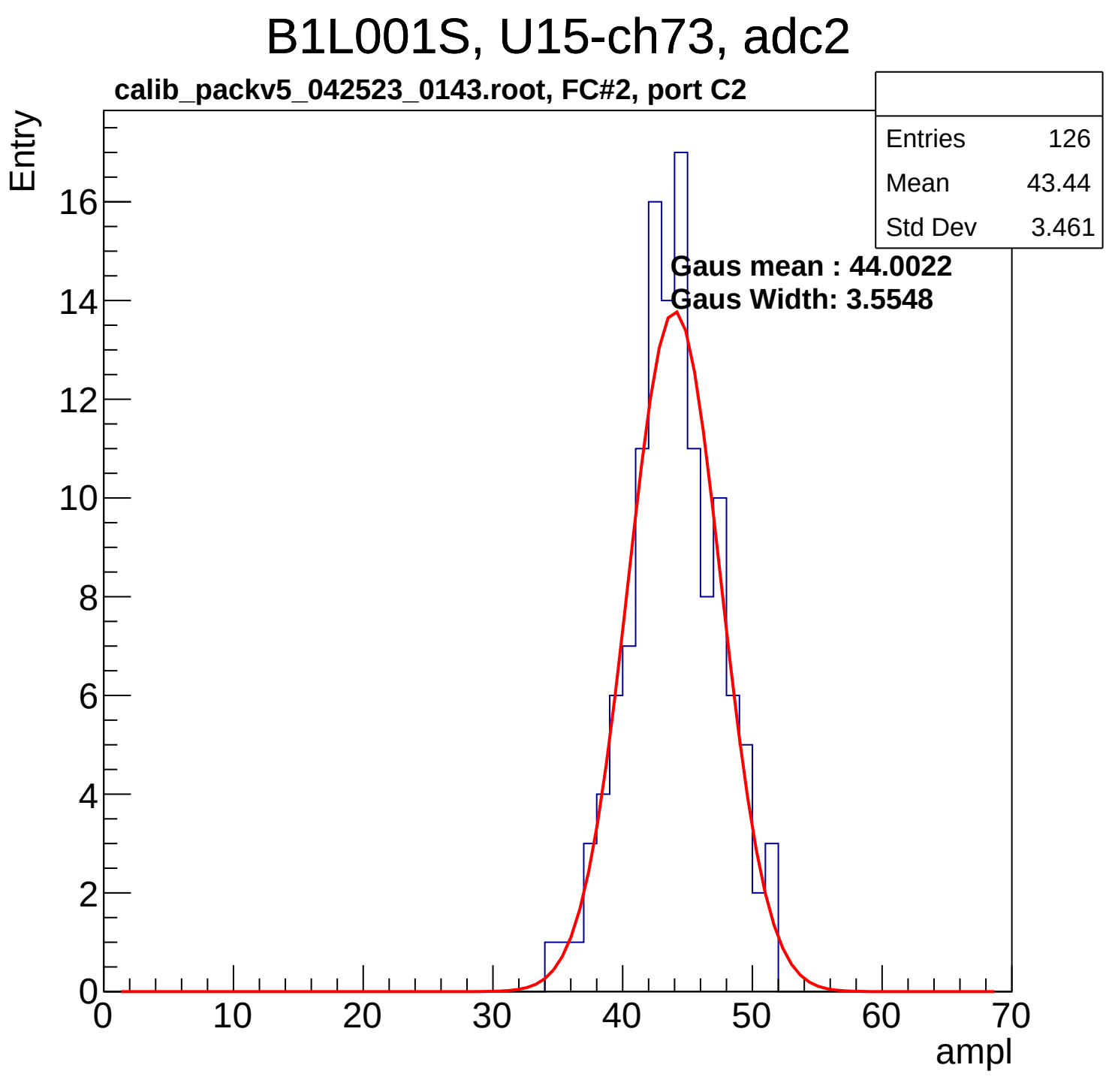
B1L001S, U15-ch73, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 43.44
Std Dev 3.461
Gaus mean : 44.0022
Gaus Width: 3.5548
Entry
ampl
0
2
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60
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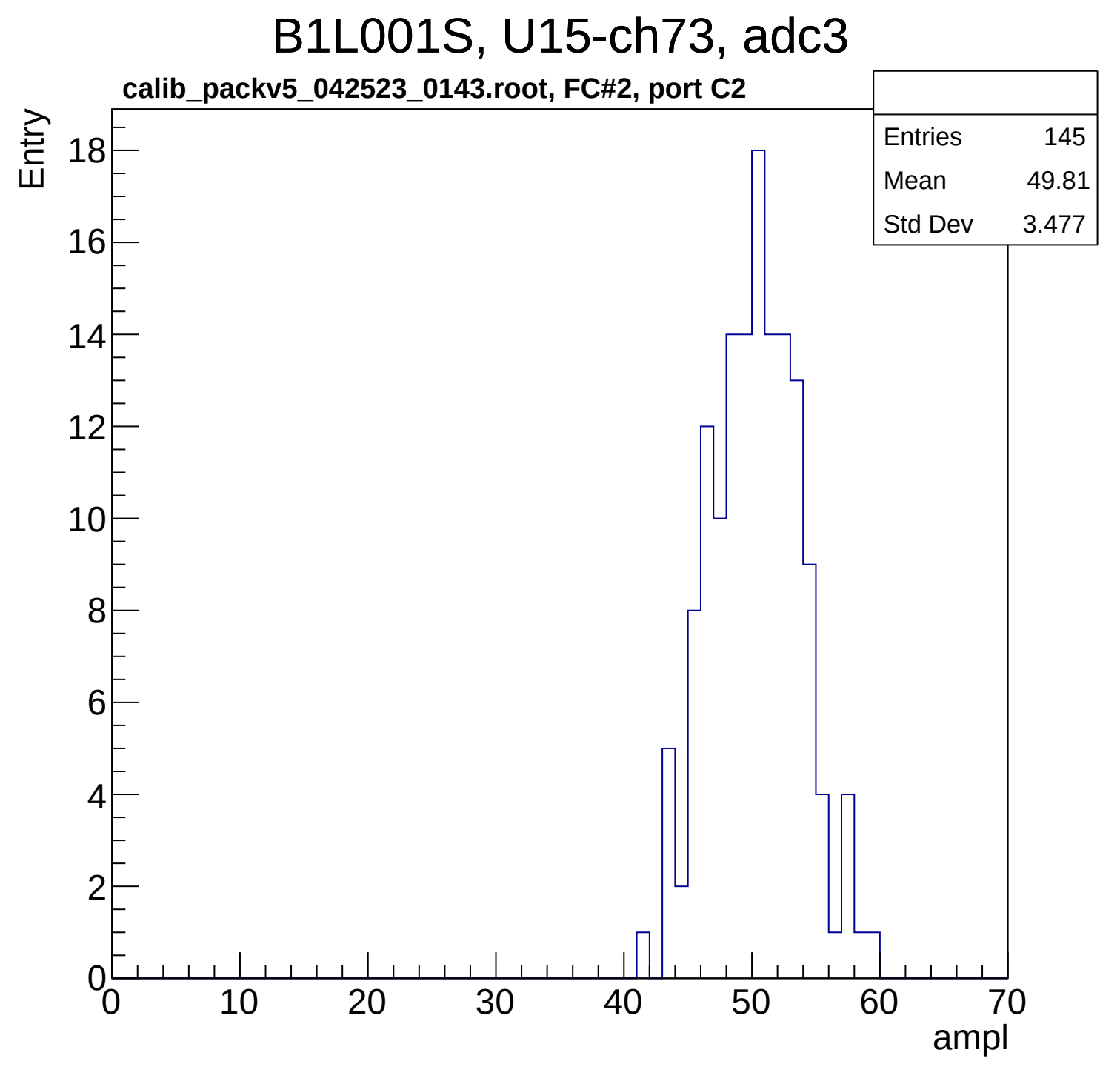
B1L001S, U15-ch73, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 49.81
Std Dev 3.477
Entry
ampl
0
2
4
6
8
10
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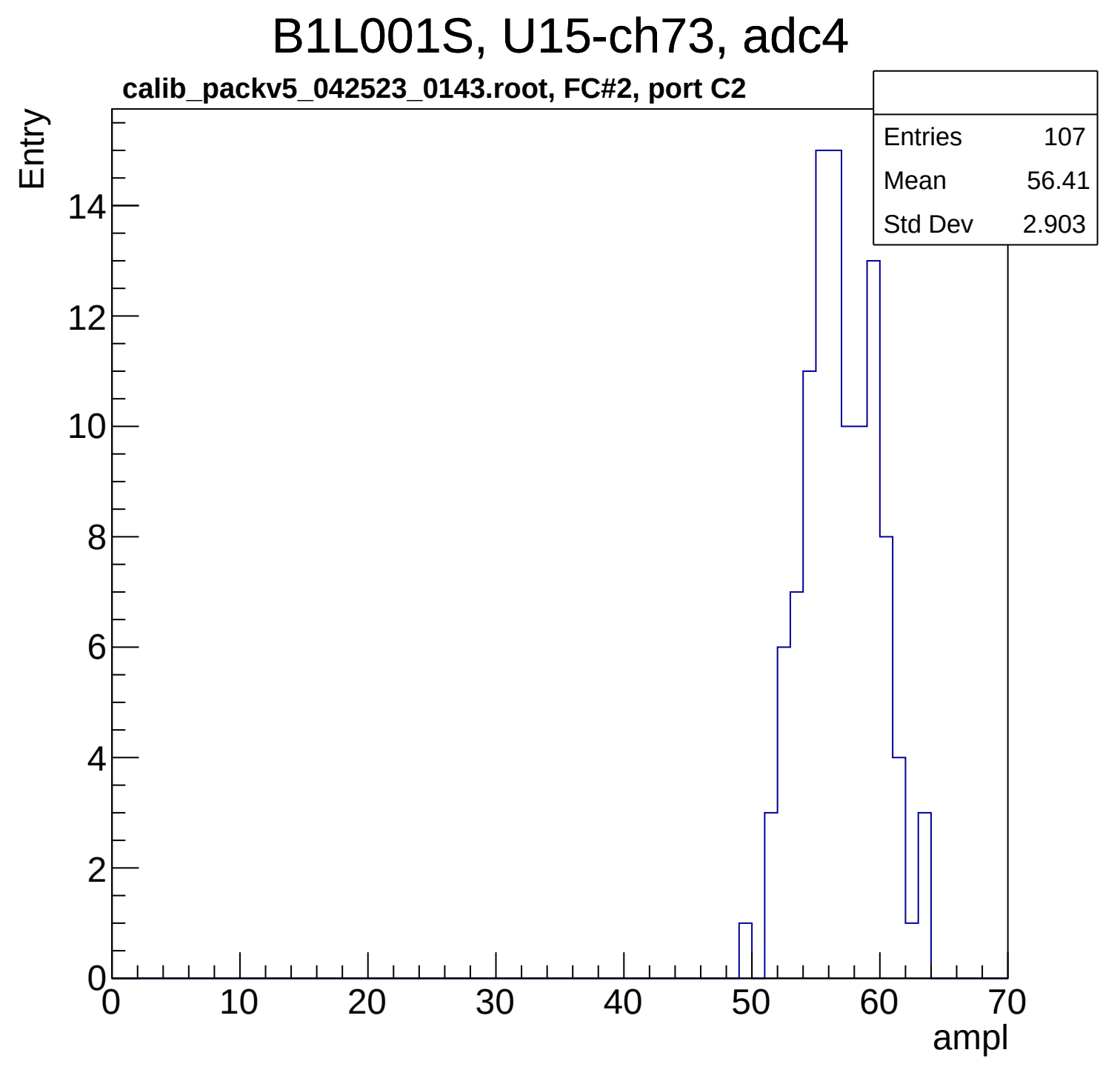
B1L001S, U15-ch73, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 56.41
Std Dev 2.903
Entry
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2
4
6
8
10
12
14
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10
20
30
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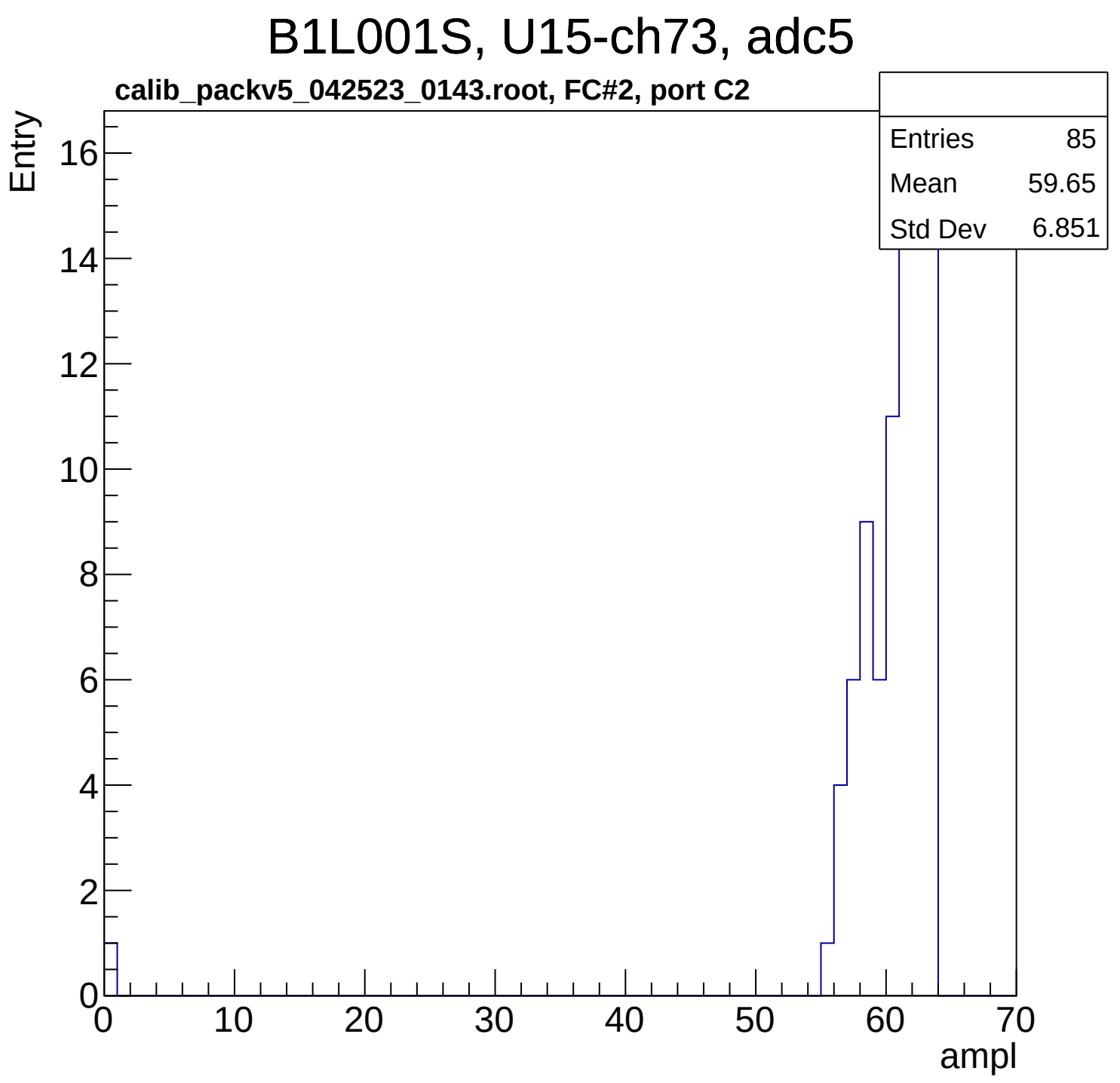
50
60
70
ampl


B1L001S, U15-ch73, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 59.65
Std Dev 6.851
Entry
ampl
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2
4
6
8
10
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0
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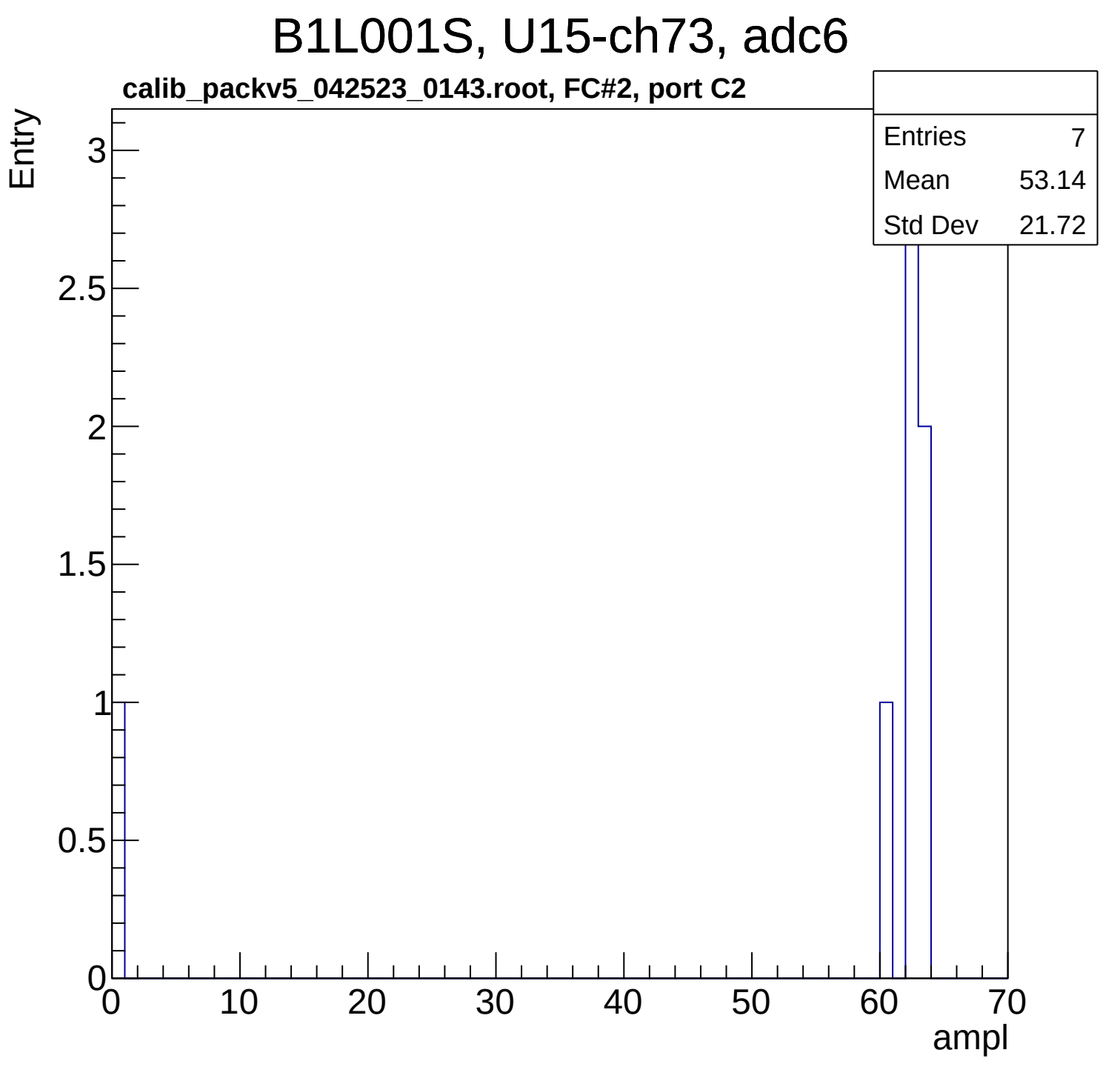
B1L001S, U15-ch73, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
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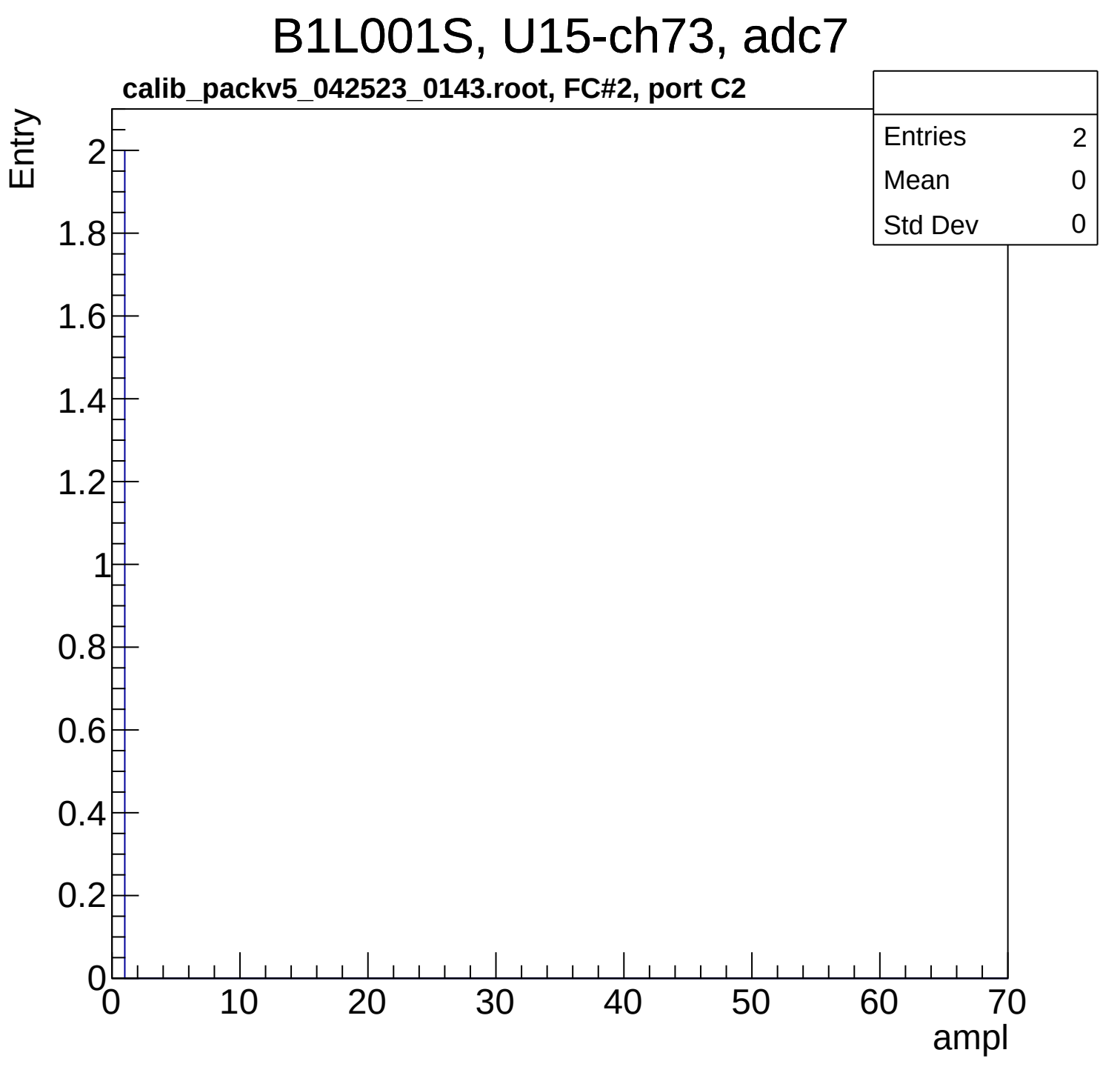
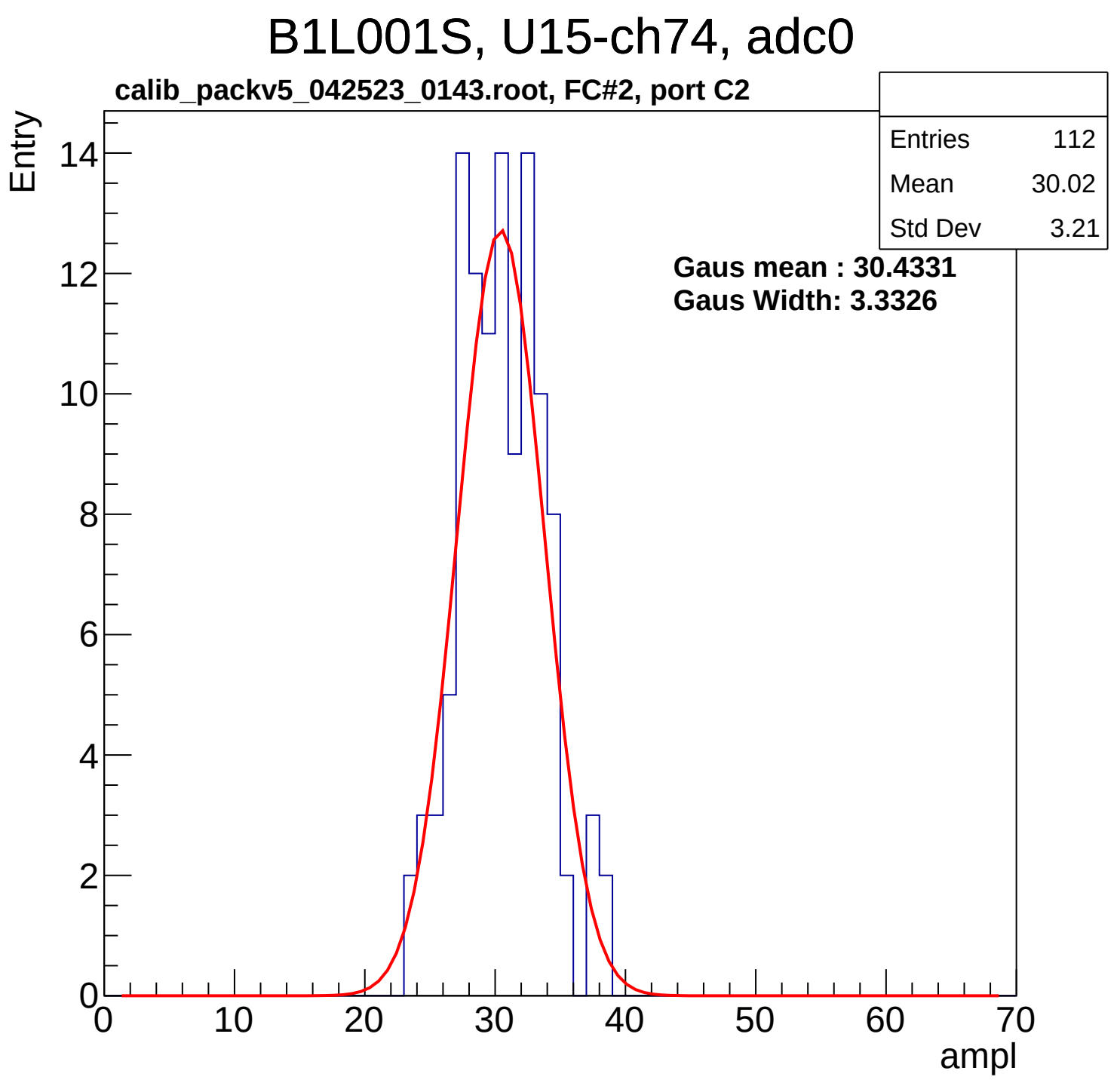
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch73, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch74, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 30.02
Std Dev 3.21
Gaus mean : 30.4331
Gaus Width: 3.3326
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
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60
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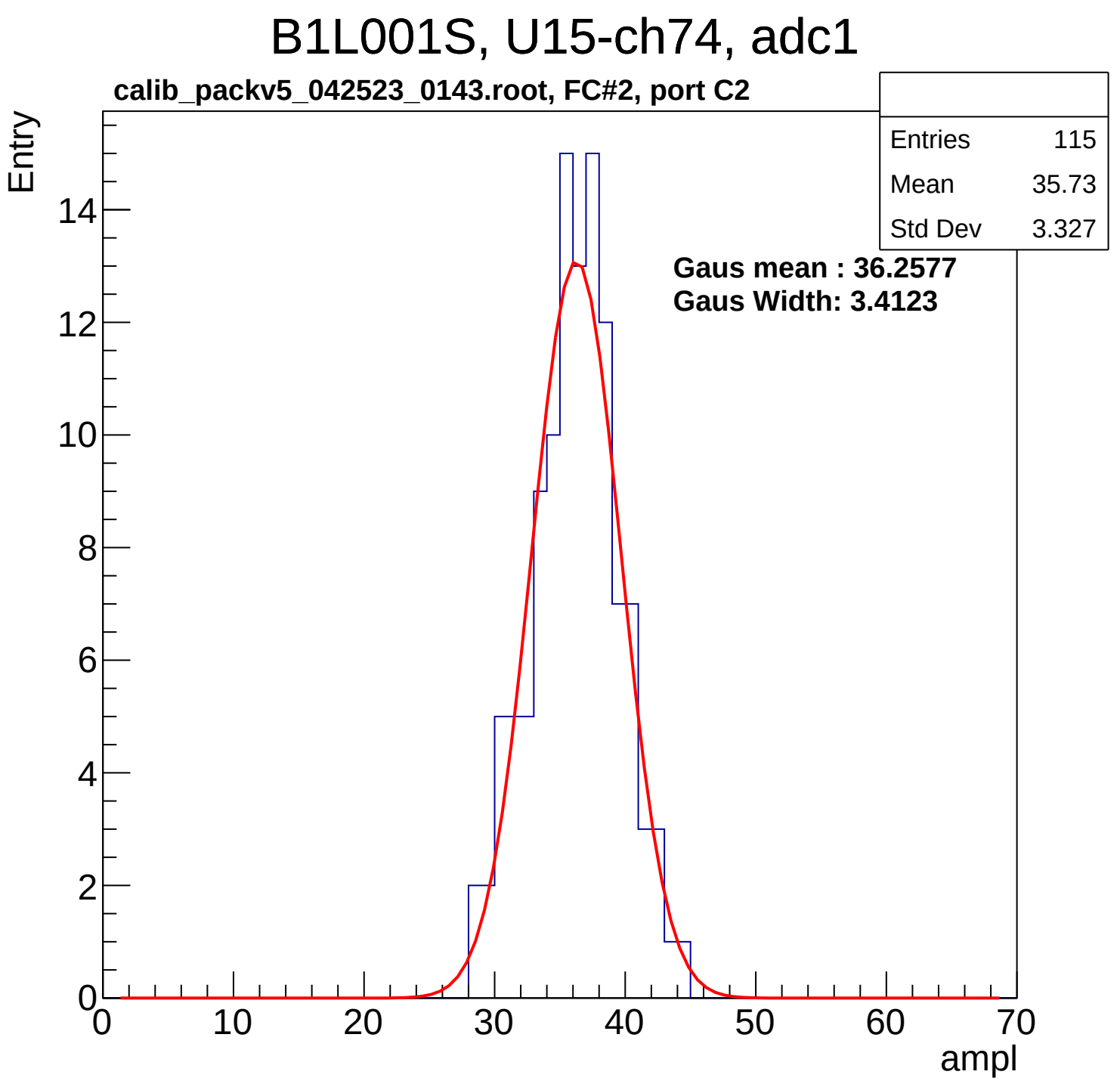
B1L001S, U15-ch74, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 35.73
Std Dev 3.327
Gaus mean : 36.2577
Gaus Width: 3.4123
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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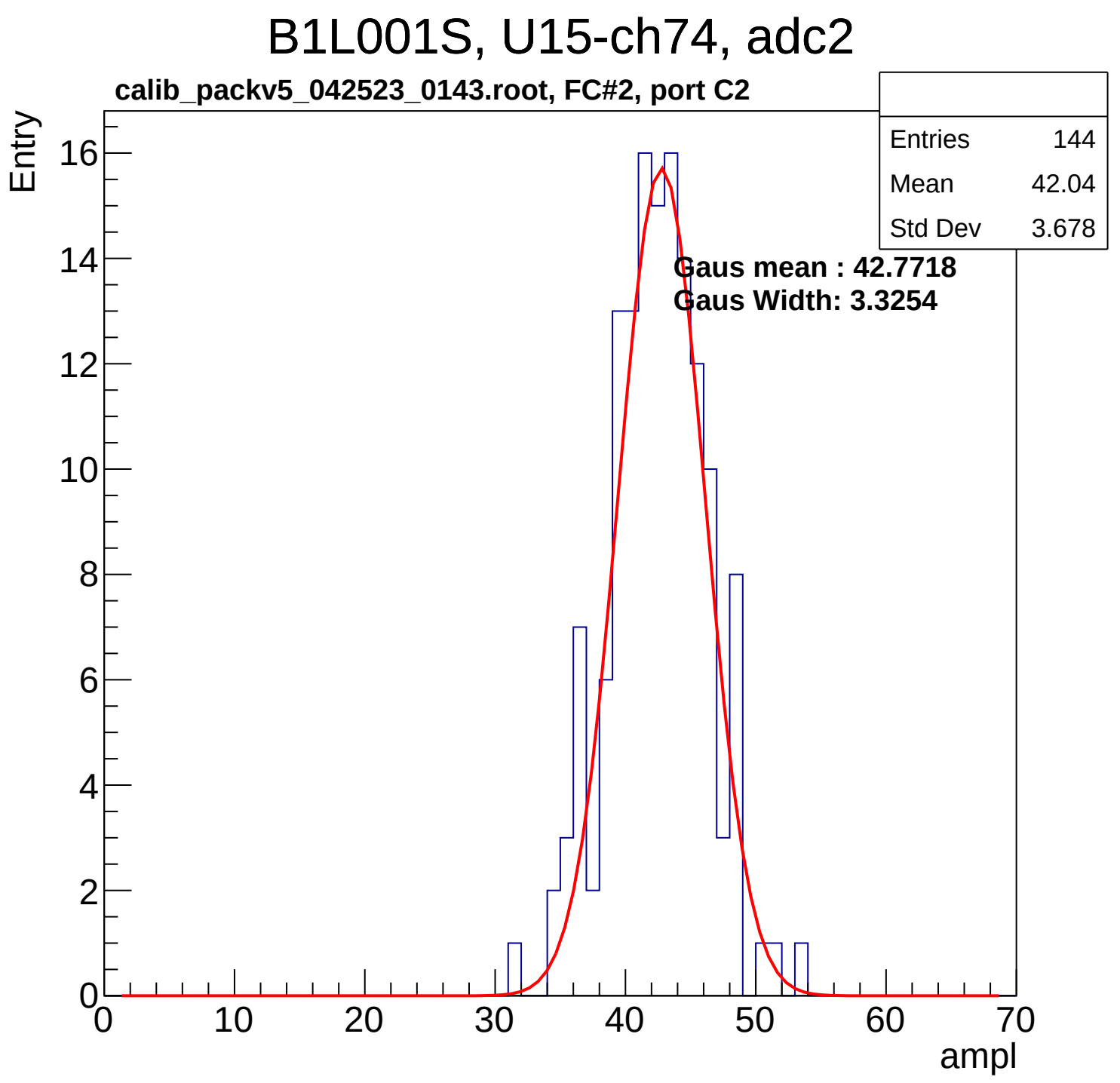
B1L001S, U15-ch74, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 42.04
Std Dev 3.678
Gaus mean : 42.7718
Gaus Width: 3.3254
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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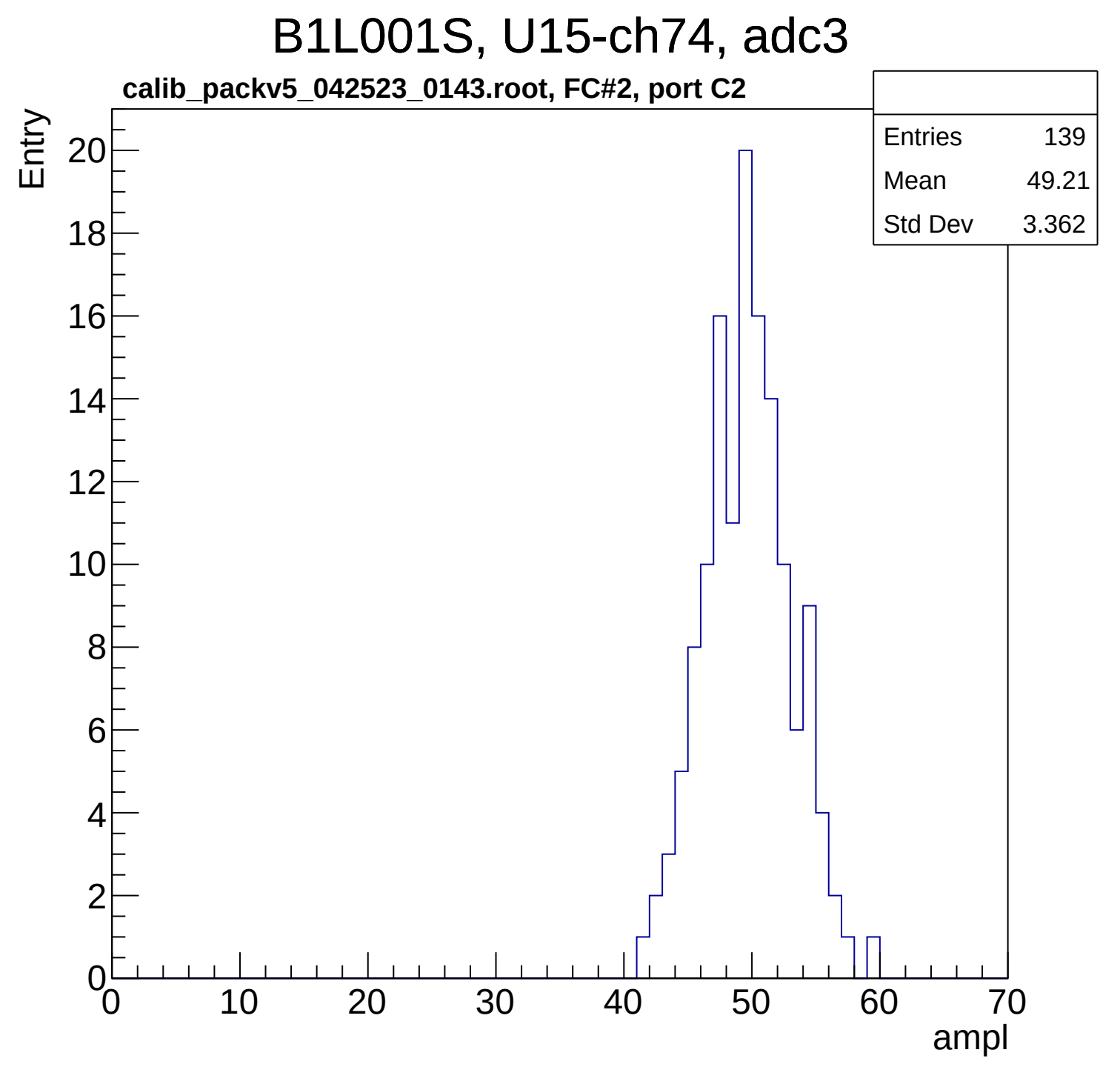
B1L001S, U15-ch74, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.21
Std Dev 3.362
Entry
ampl
0
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4
6
8
10
12
14
16
18
20
0
10
20
30
40
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60
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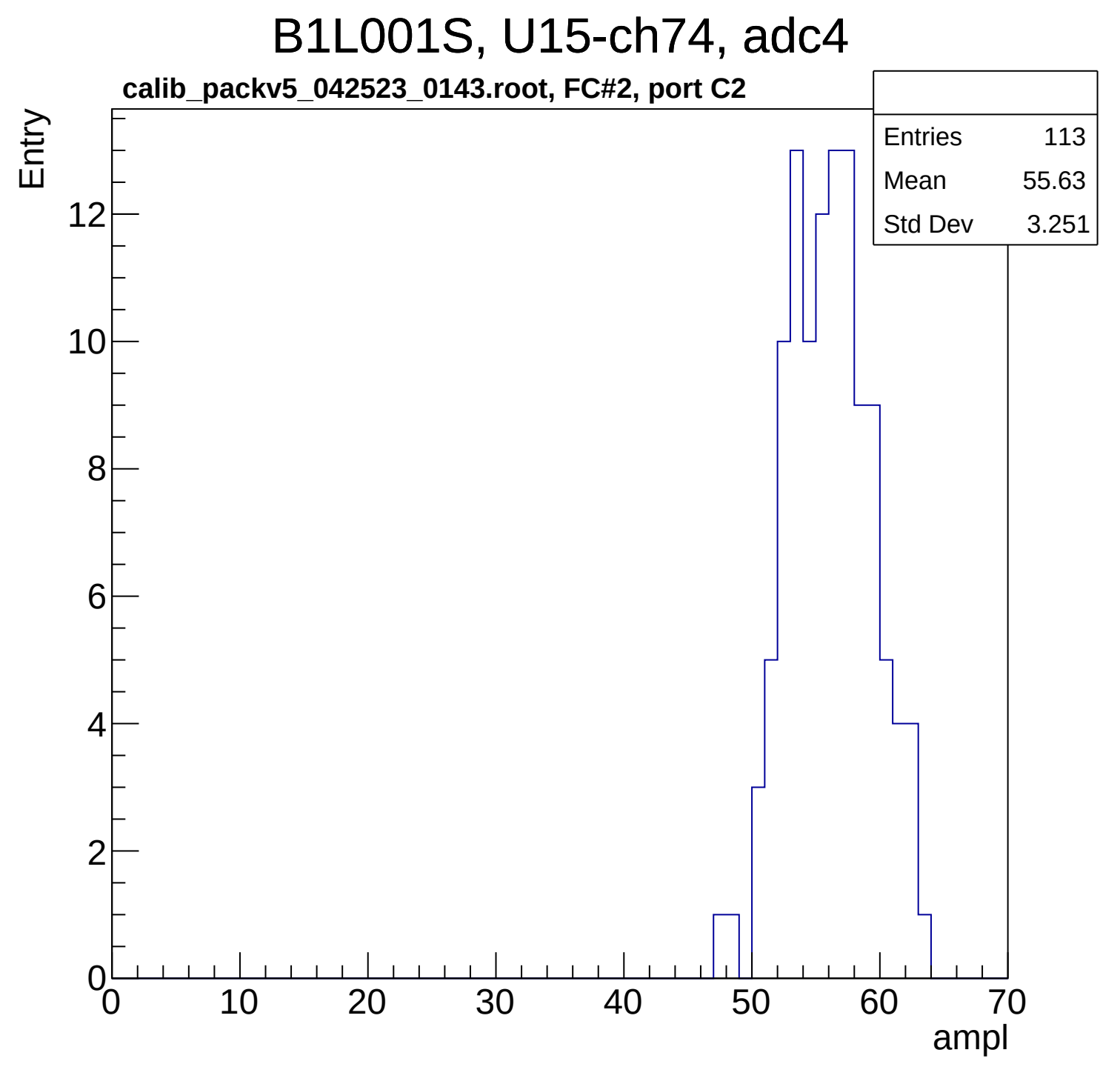
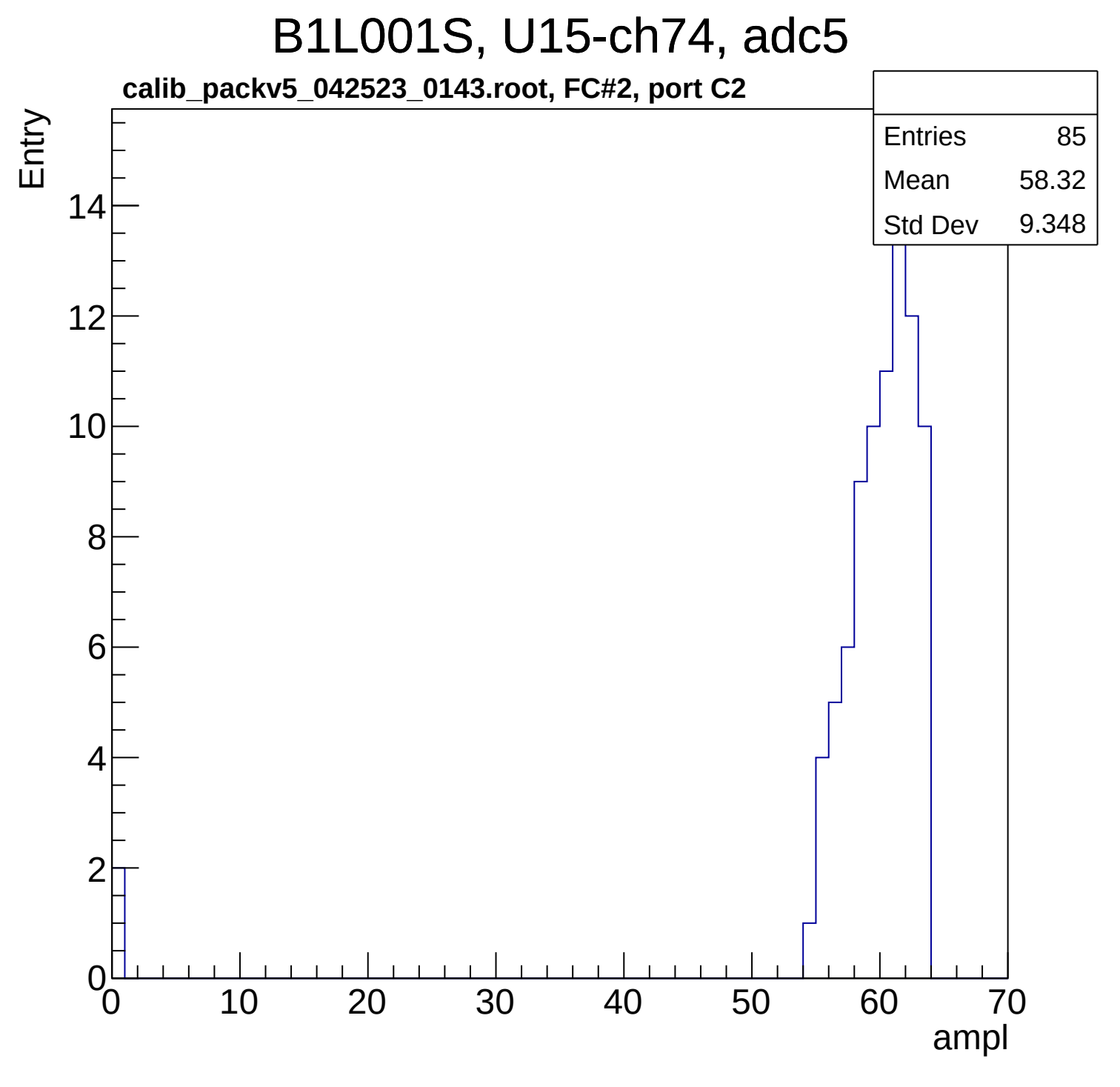

B1L001S, U15-ch74, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 55.63
Std Dev 3.251
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch74, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 58.32
Std Dev 9.348
Entry
ampl
0
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4
6
8
10
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0
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60
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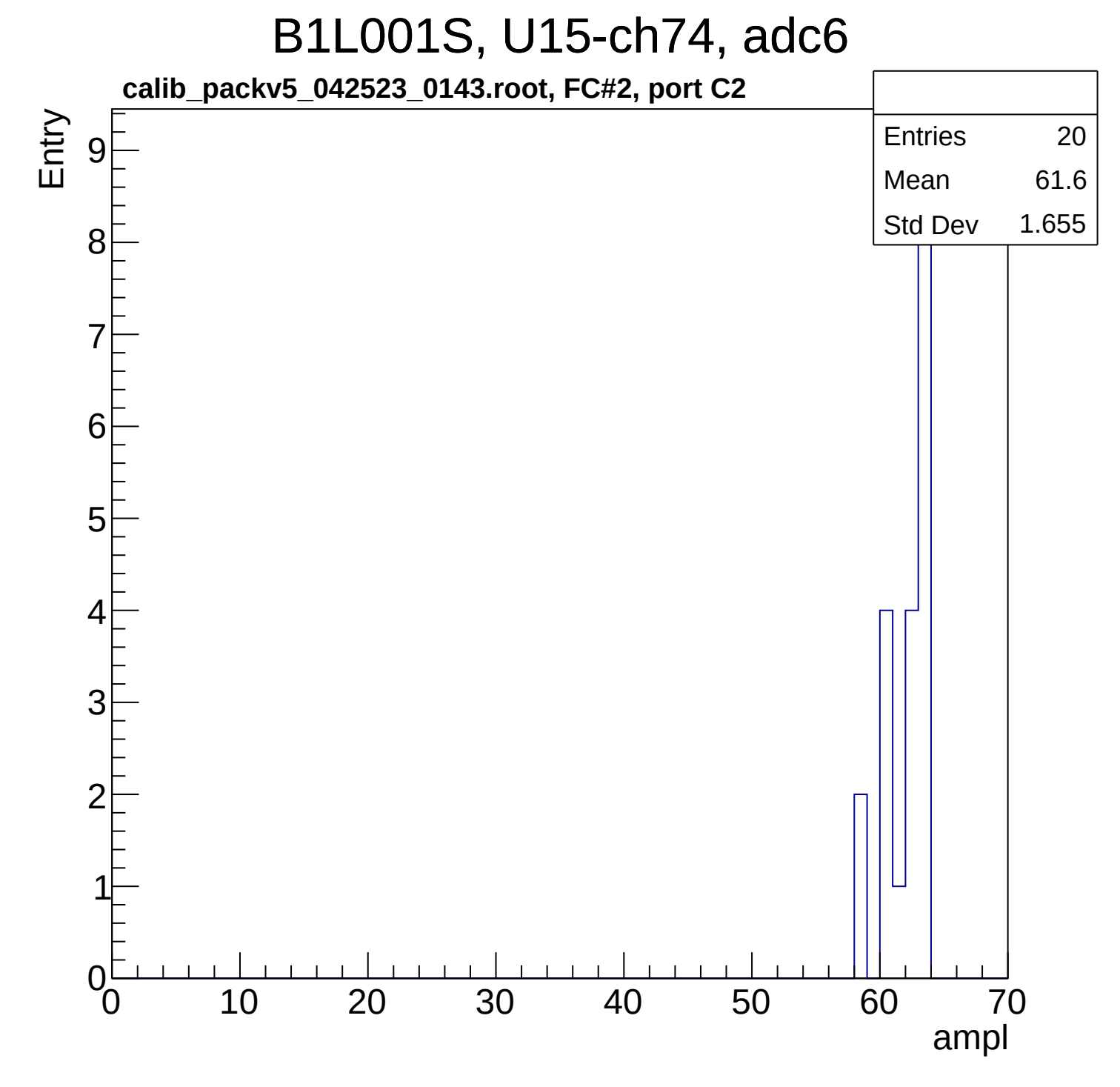
B1L001S, U15-ch74, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 20
Mean 61.6
Std Dev 1.655
Entry
ampl
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1
2
3
4
5
6
7
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9
0
10
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30
40
50
60
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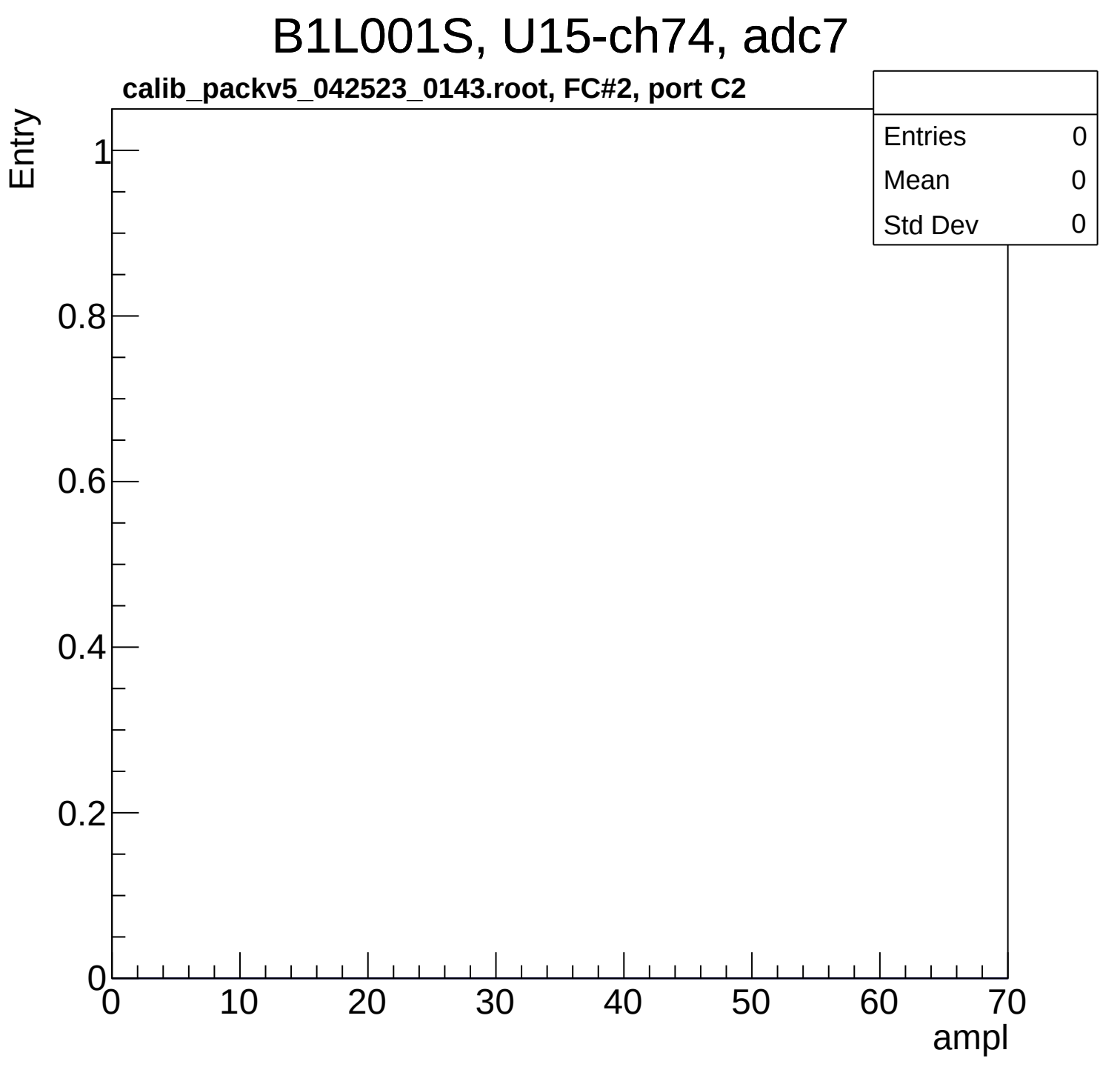
B1L001S, U15-ch74, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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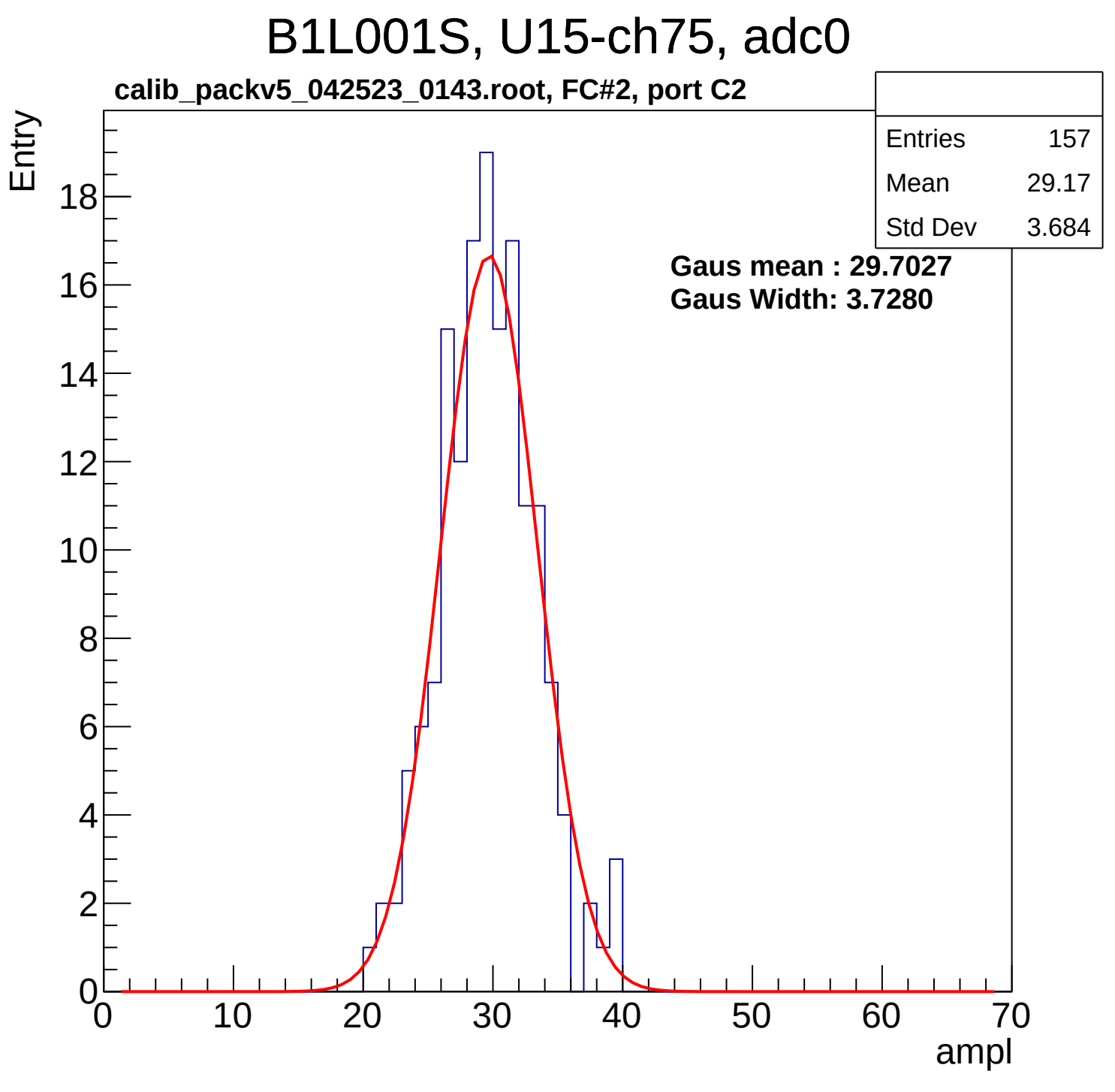
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch75, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 29.17
Std Dev 3.684
Gaus mean : 29.7027
Gaus Width: 3.7280
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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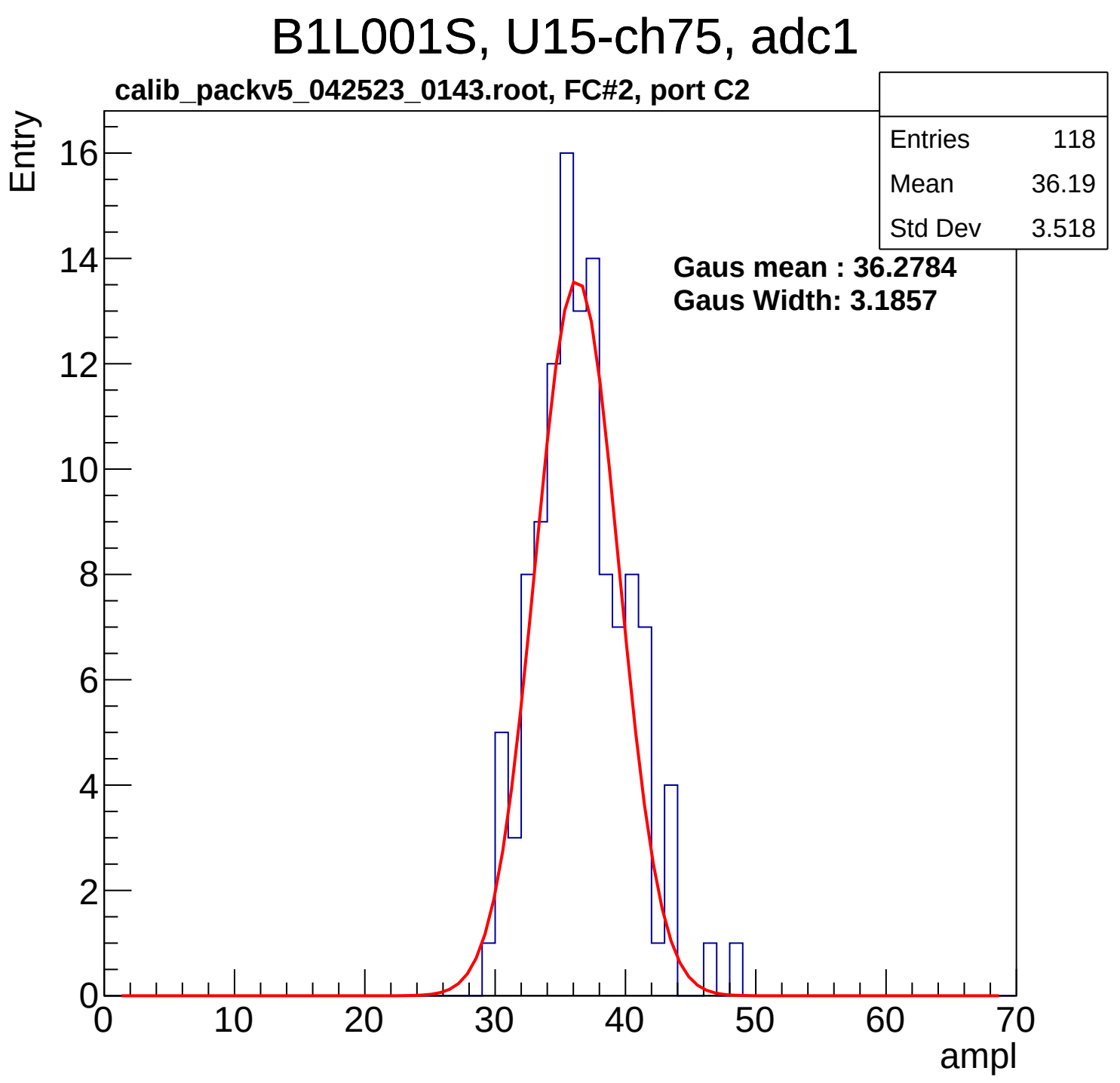
60
70


B1L001S, U15-ch75, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 36.19
Std Dev 3.518
Gaus mean : 36.2784
Gaus Width: 3.1857
Entry
ampl
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2
4
6
8
10
12
14
16
0
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60
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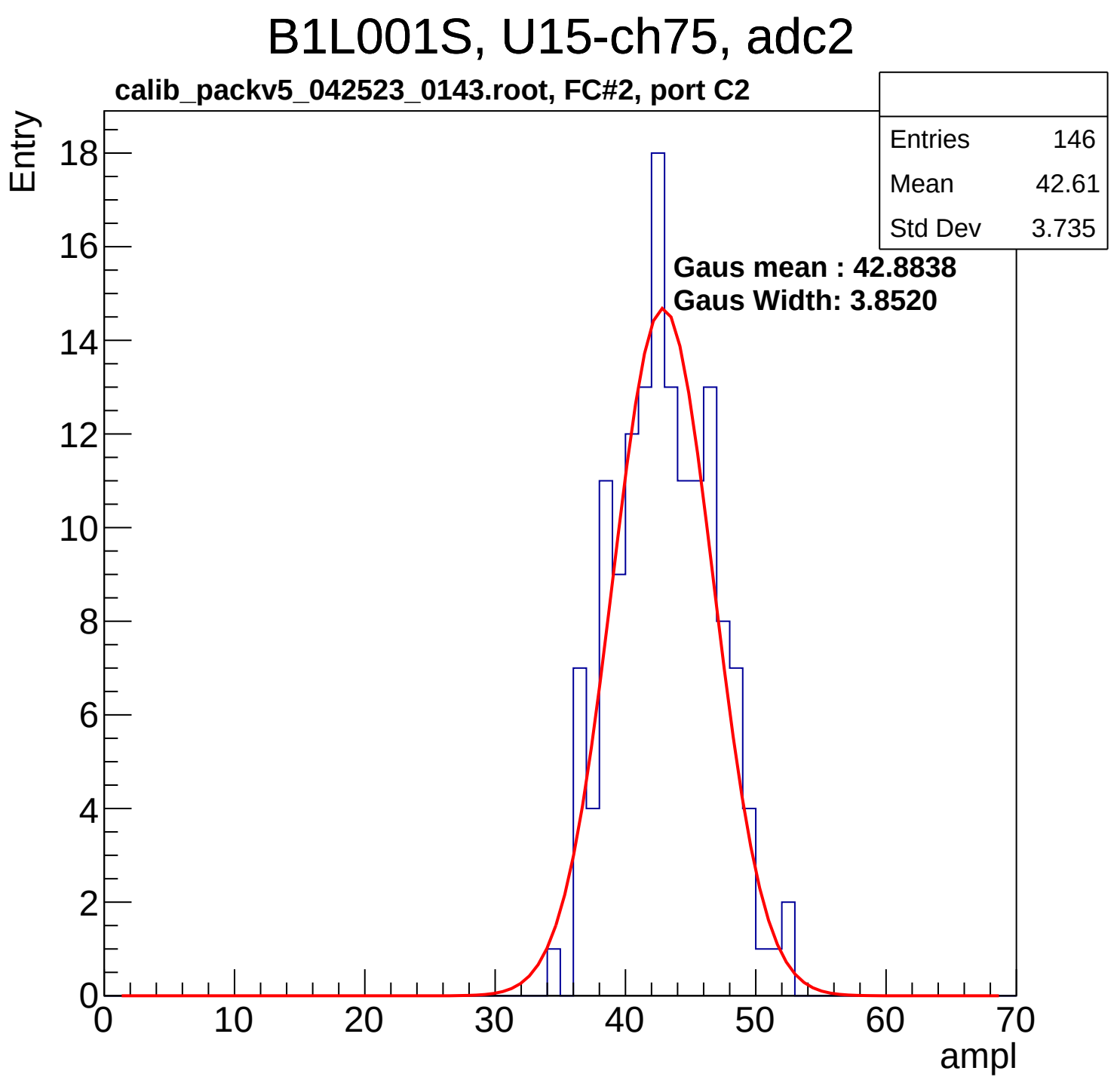
B1L001S, U15-ch75, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 42.61
Std Dev 3.735
Gaus mean : 42.8838
Gaus Width: 3.8520
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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20
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60
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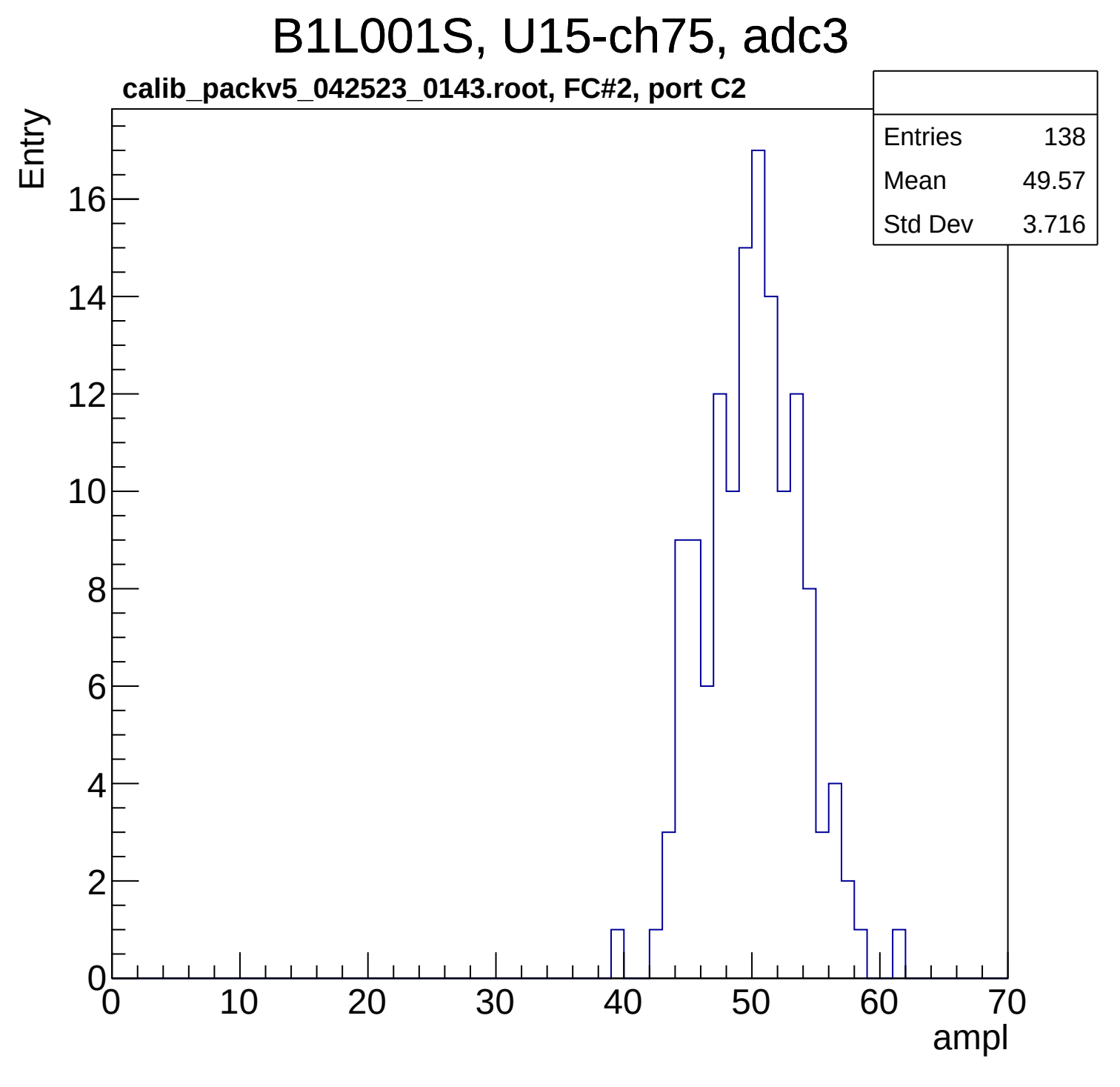
B1L001S, U15-ch75, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 49.57
Std Dev 3.716
Entry
ampl
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2
4
6
8
10
12
14
16
0
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60
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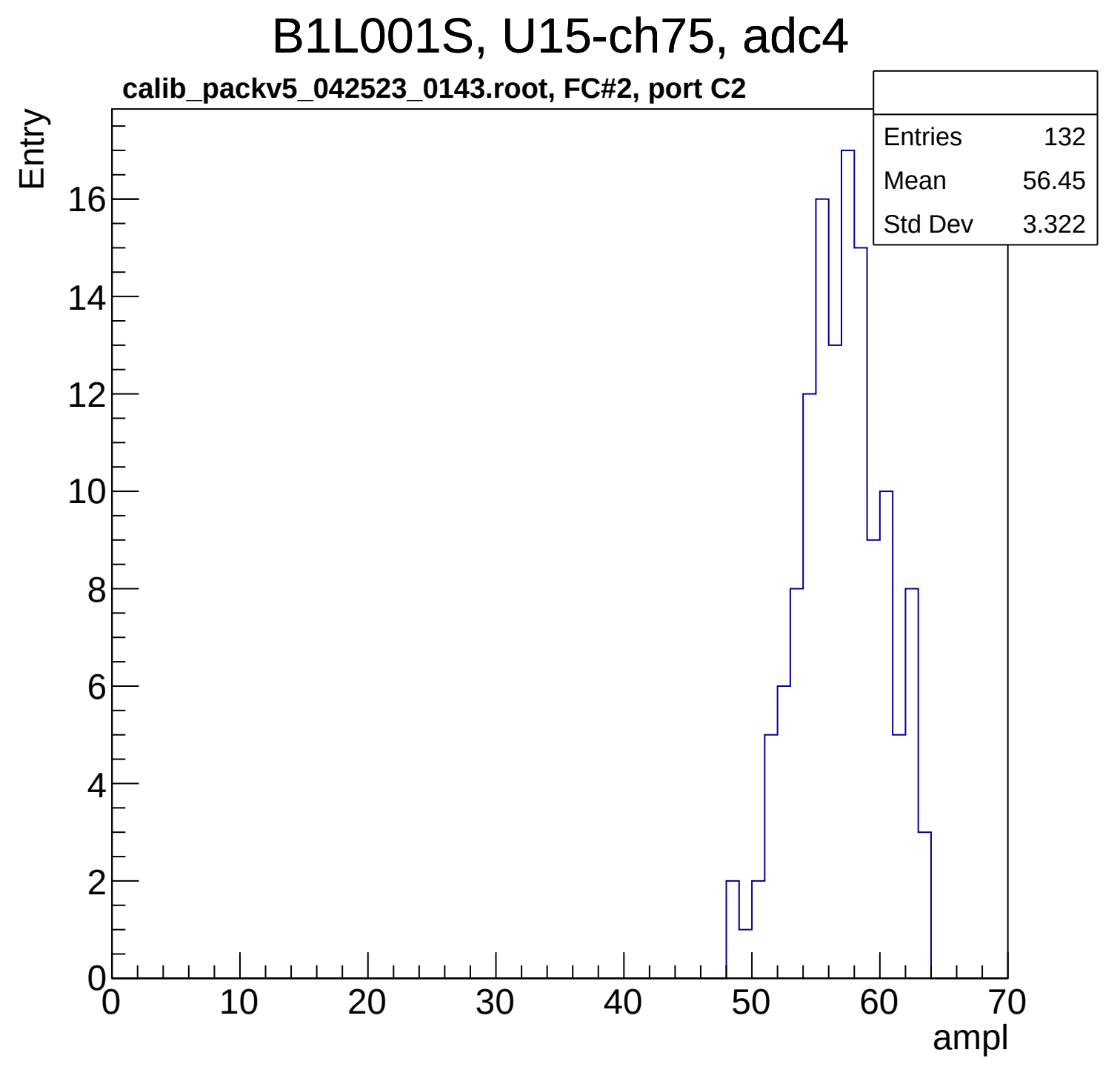
B1L001S, U15-ch75, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 56.45
Std Dev 3.322
Entry
ampl
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4
6
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10
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0
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60
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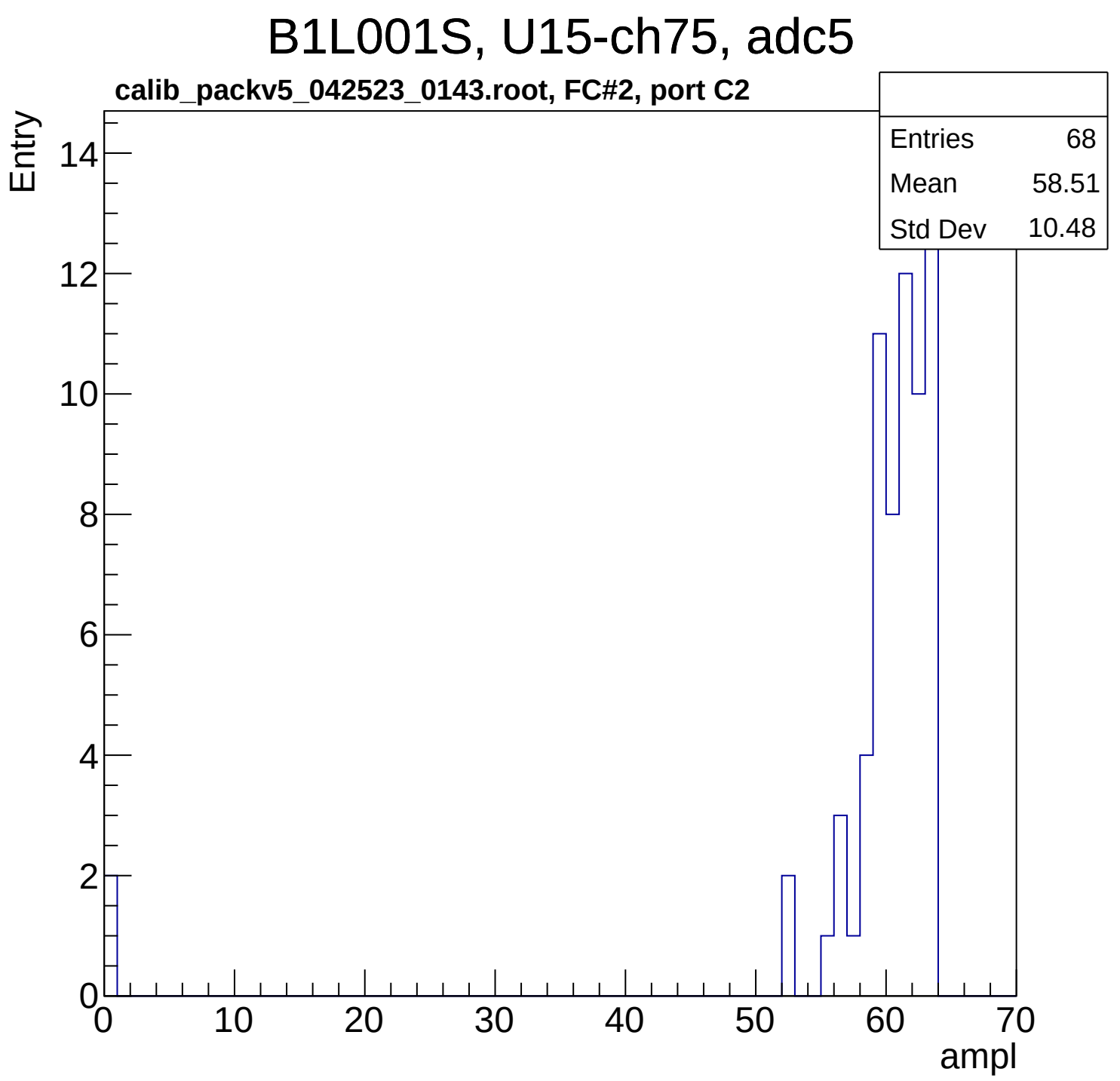
B1L001S, U15-ch75, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 68
Mean 58.51
Std Dev 10.48
Entry
ampl
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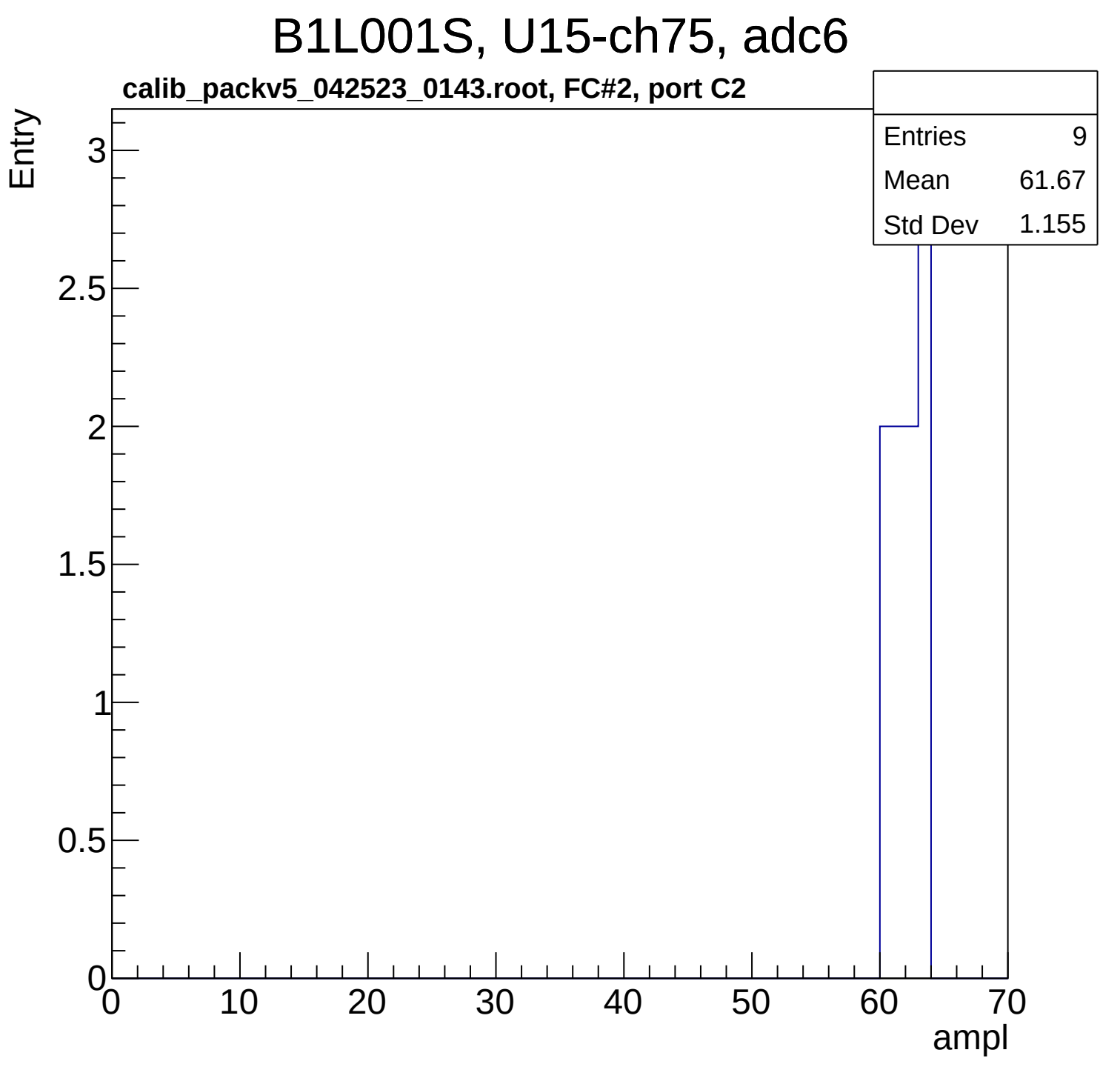
B1L001S, U15-ch75, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 61.67
Std Dev 1.155
Entry
ampl
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1
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2
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0
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60
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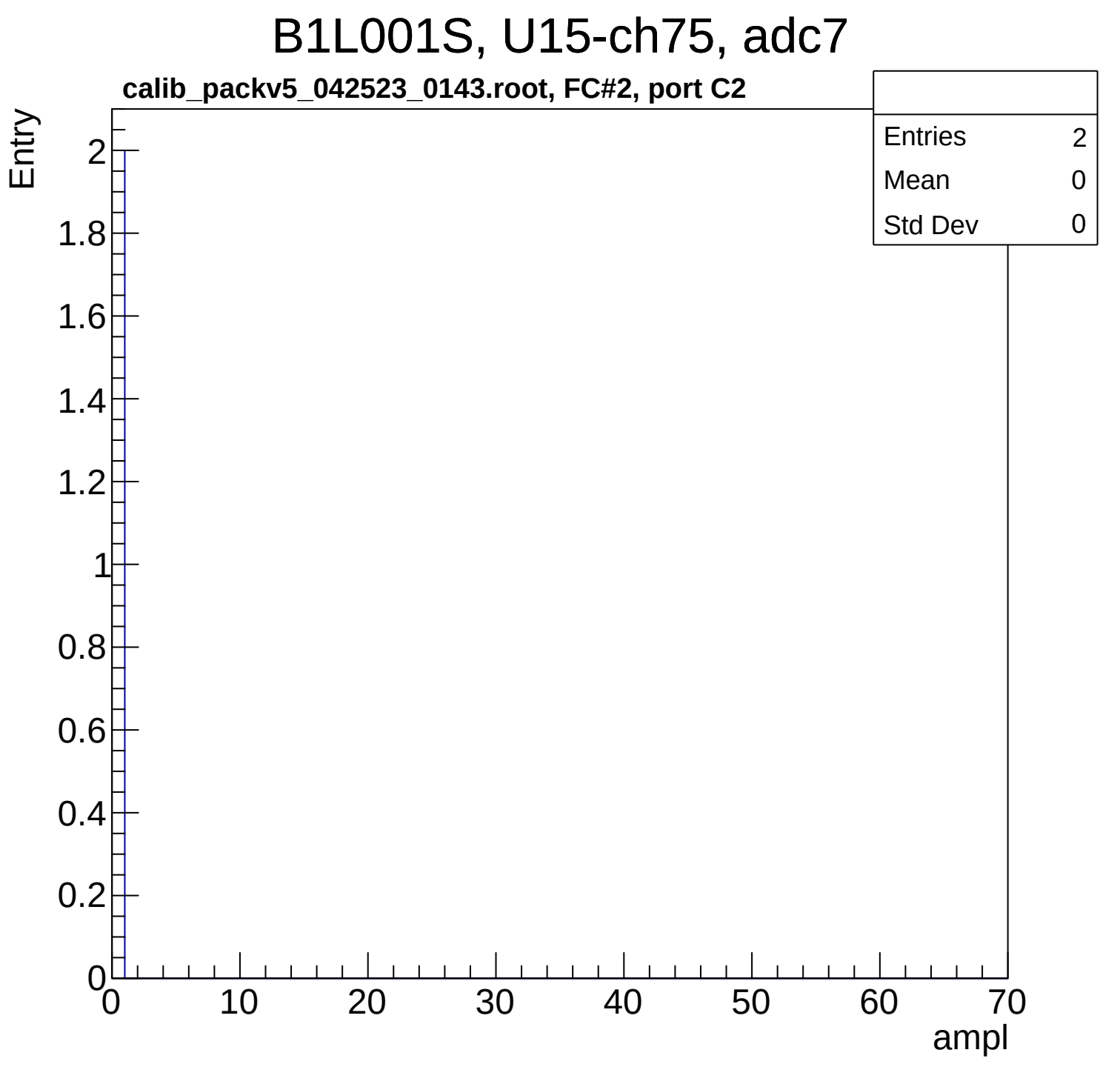
B1L001S, U15-ch75, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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30
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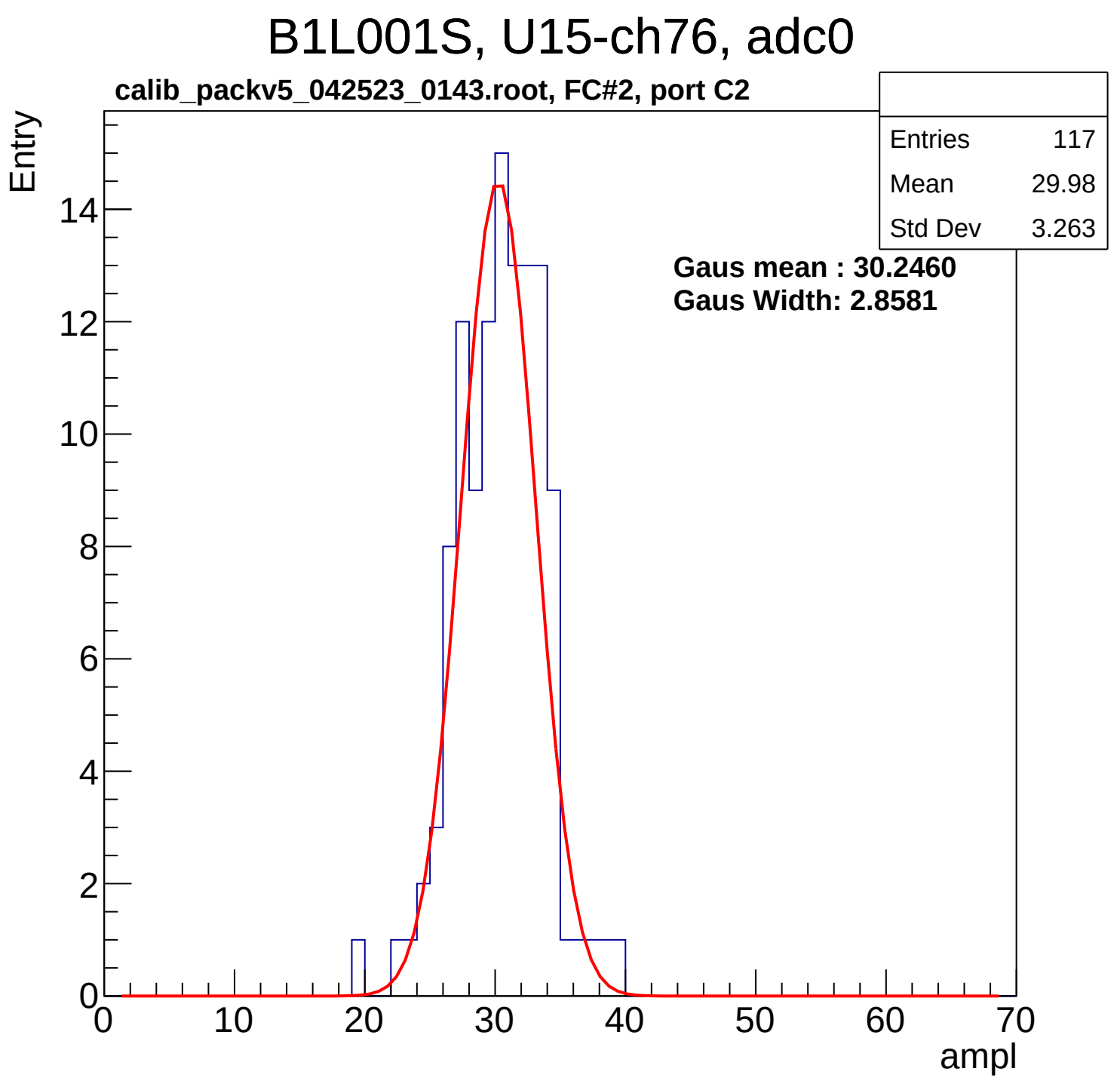
50
60
70
ampl


B1L001S, U15-ch76, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 29.98
Std Dev 3.263
Gaus mean : 30.2460
Gaus Width: 2.8581
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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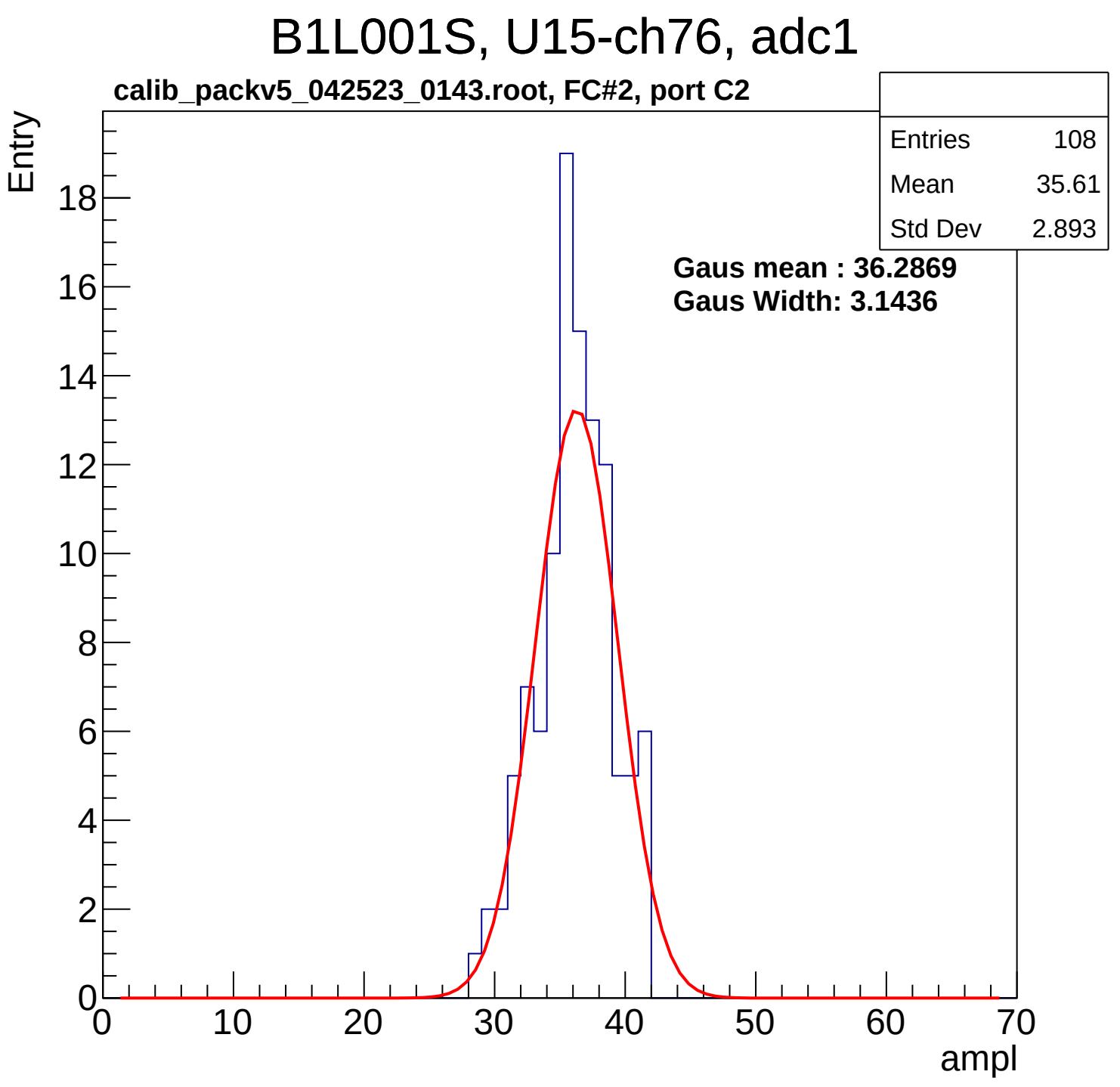
B1L001S, U15-ch76, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 35.61
Std Dev 2.893
Gaus mean : 36.2869
Gaus Width: 3.1436
Entry
ampl
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4
6
8
10
12
14
16
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0
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60
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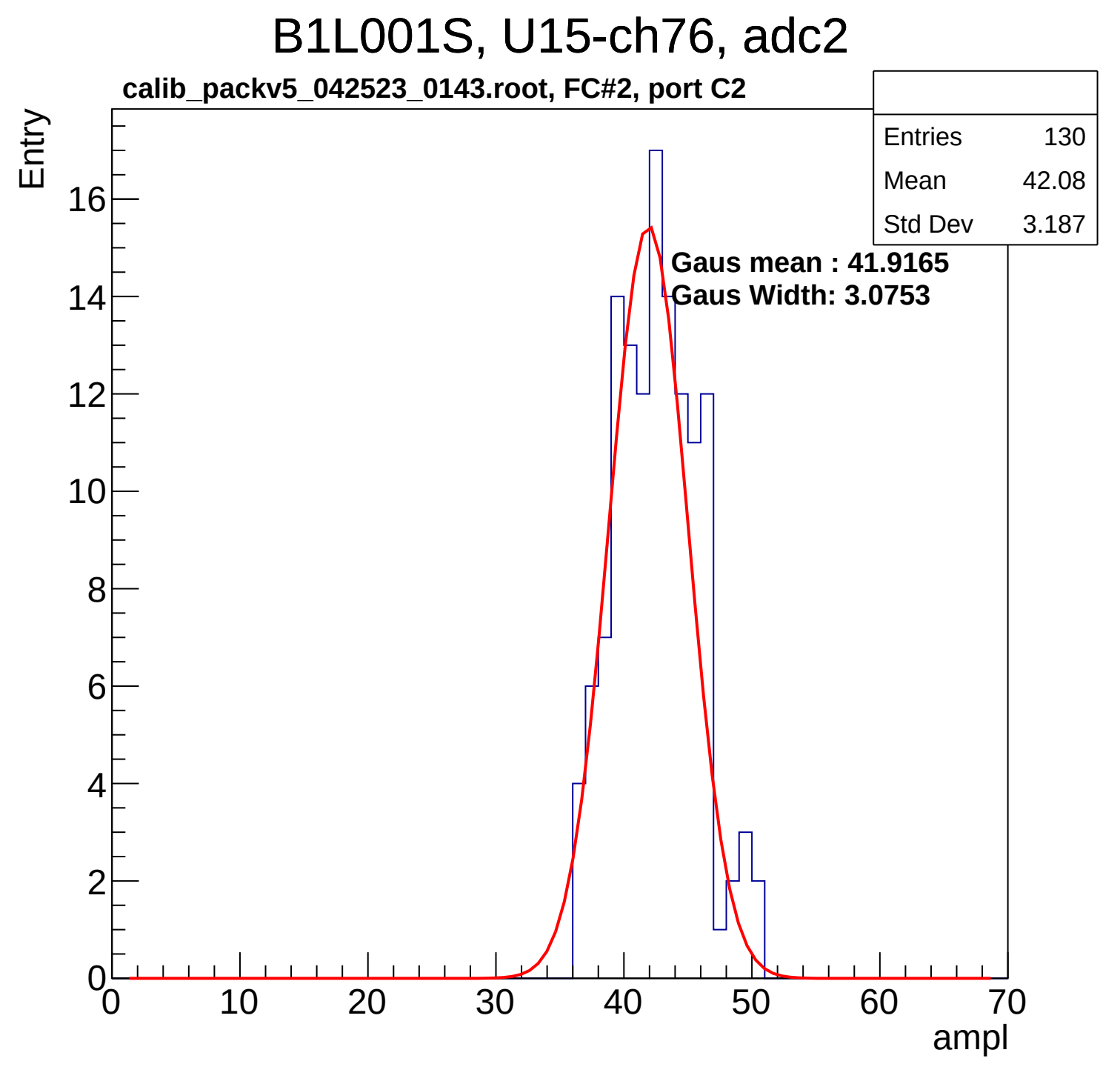
B1L001S, U15-ch76, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.08
Std Dev 3.187
Gaus mean : 41.9165
Gaus Width: 3.0753
Entry
ampl
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2
4
6
8
10
12
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16
0
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60
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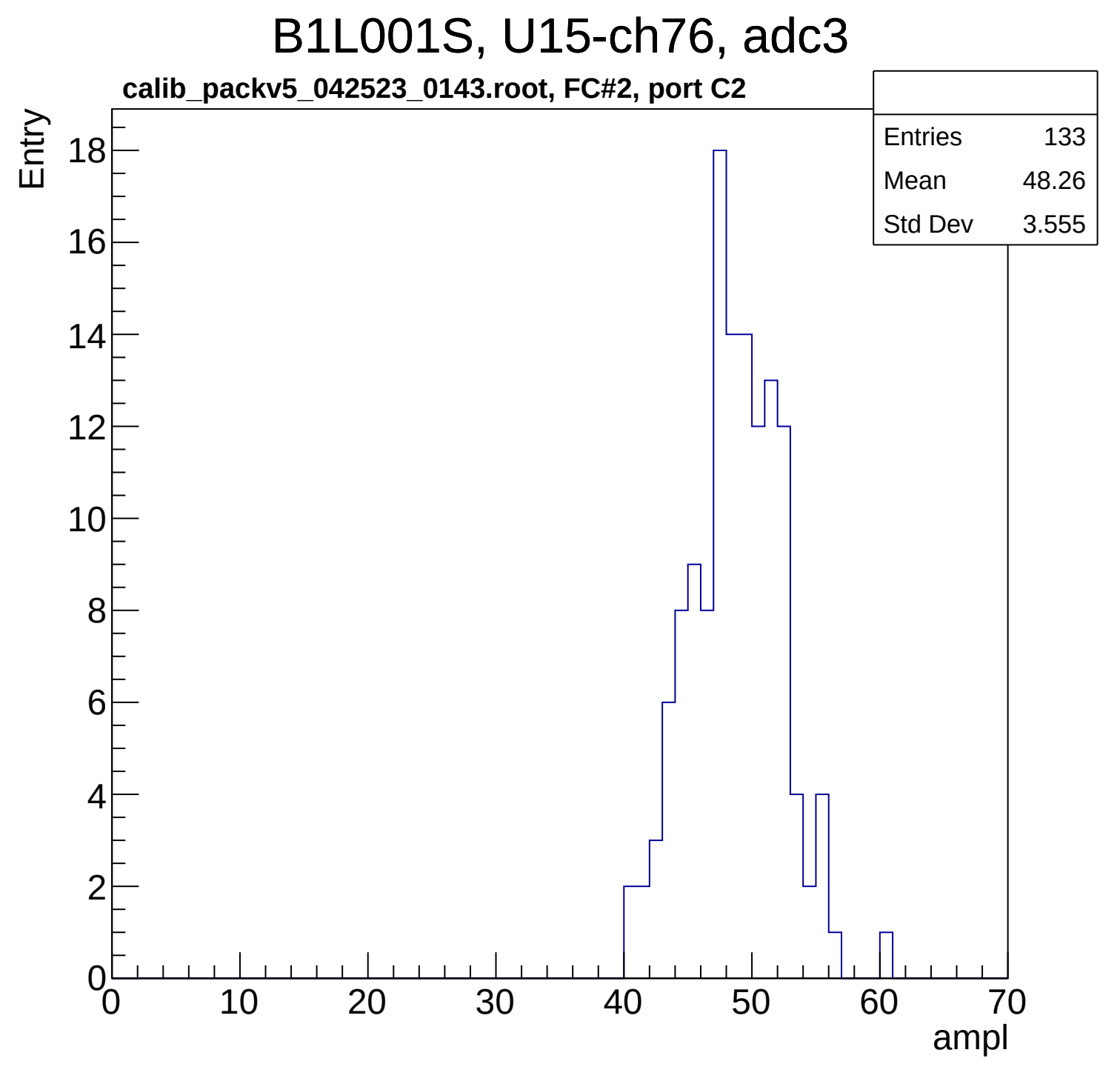
B1L001S, U15-ch76, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 48.26
Std Dev 3.555
Entry
ampl
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6
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10
12
14
16
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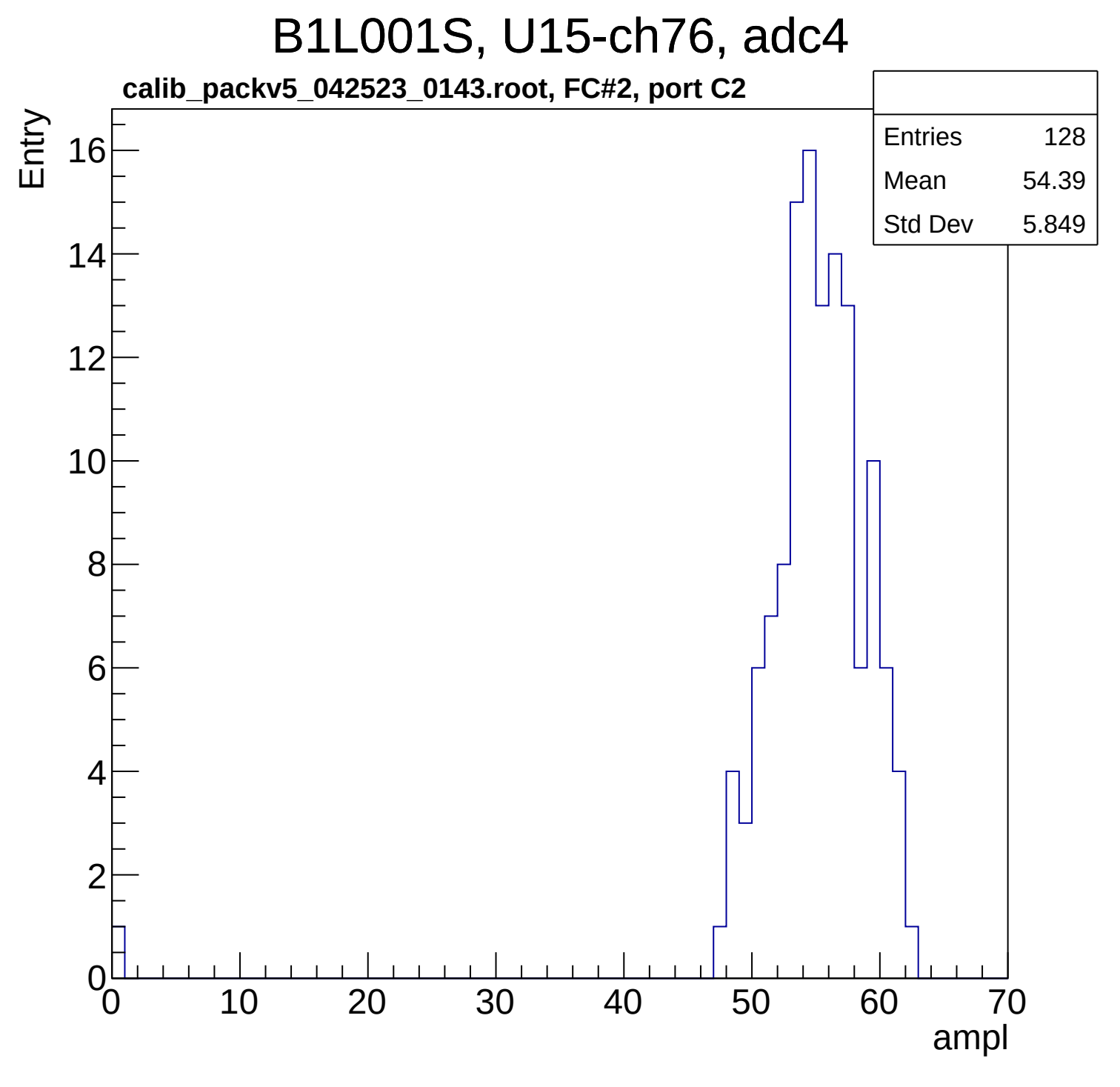
B1L001S, U15-ch76, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 54.39
Std Dev 5.849
Entry
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4
6
8
10
12
14
16
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10
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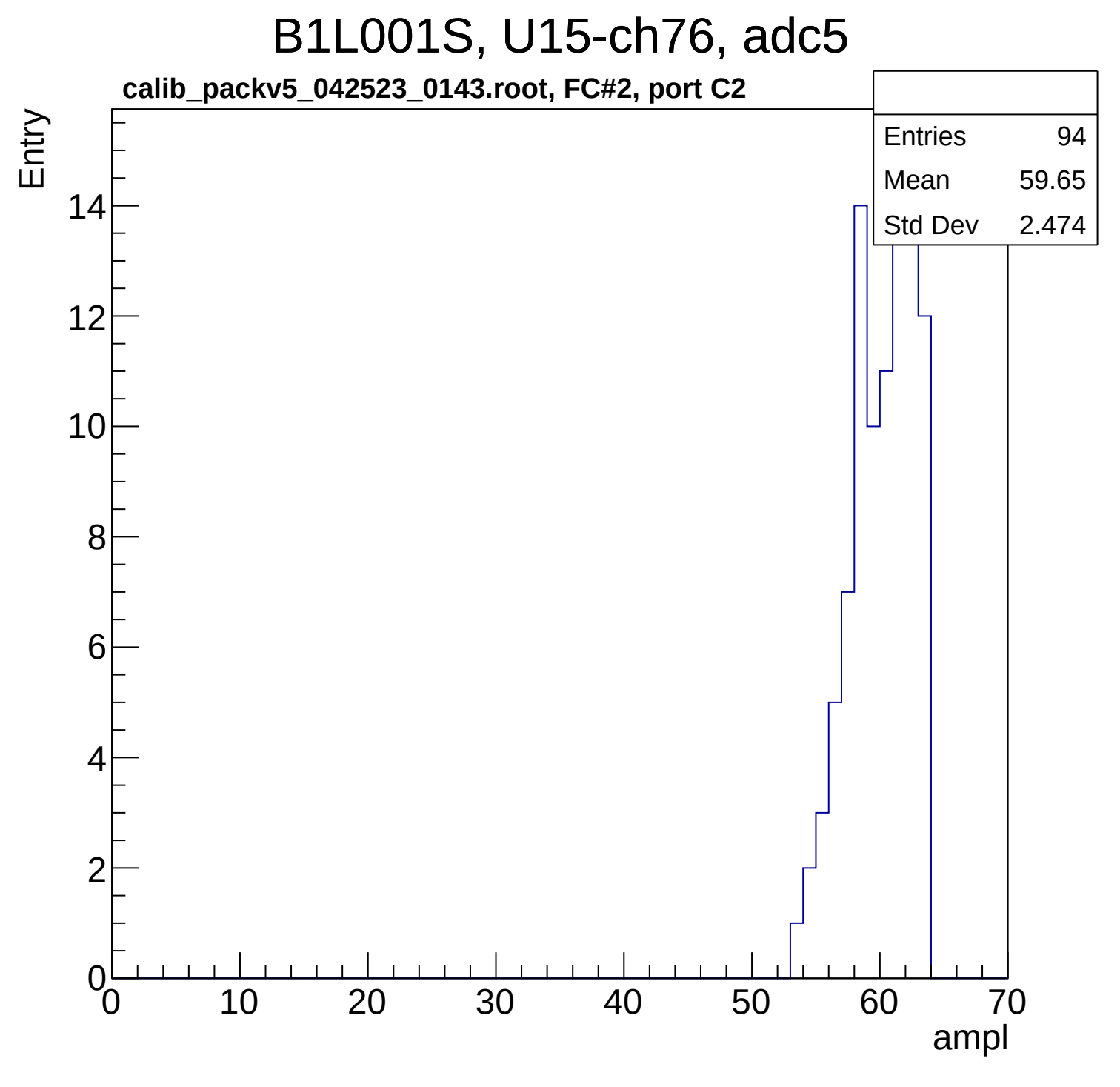
50
60
70
ampl


B1L001S, U15-ch76, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.65
Std Dev 2.474
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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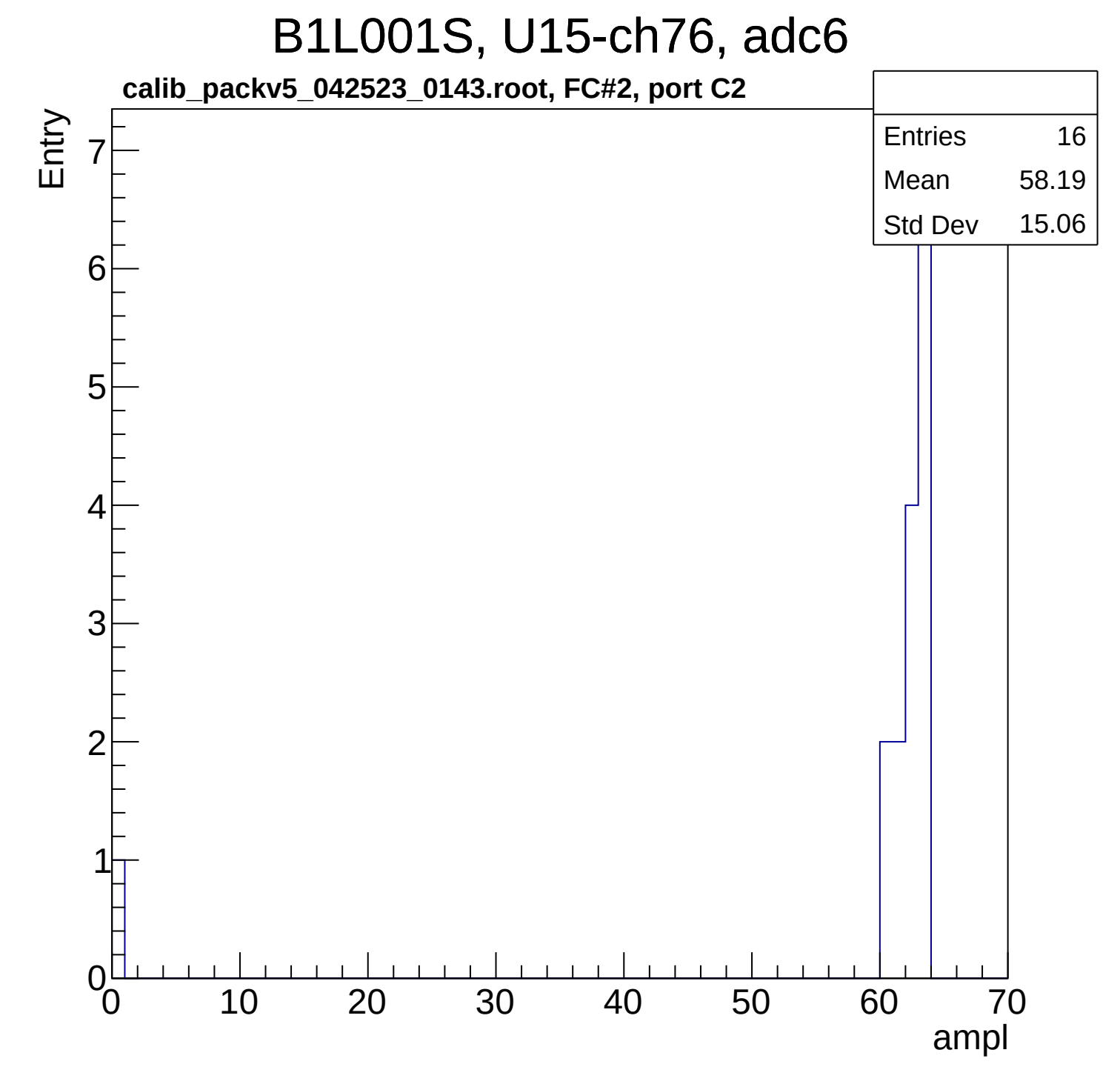
B1L001S, U15-ch76, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 58.19
Std Dev 15.06
Entry
ampl
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1
2
3
4
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6
7
0
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60
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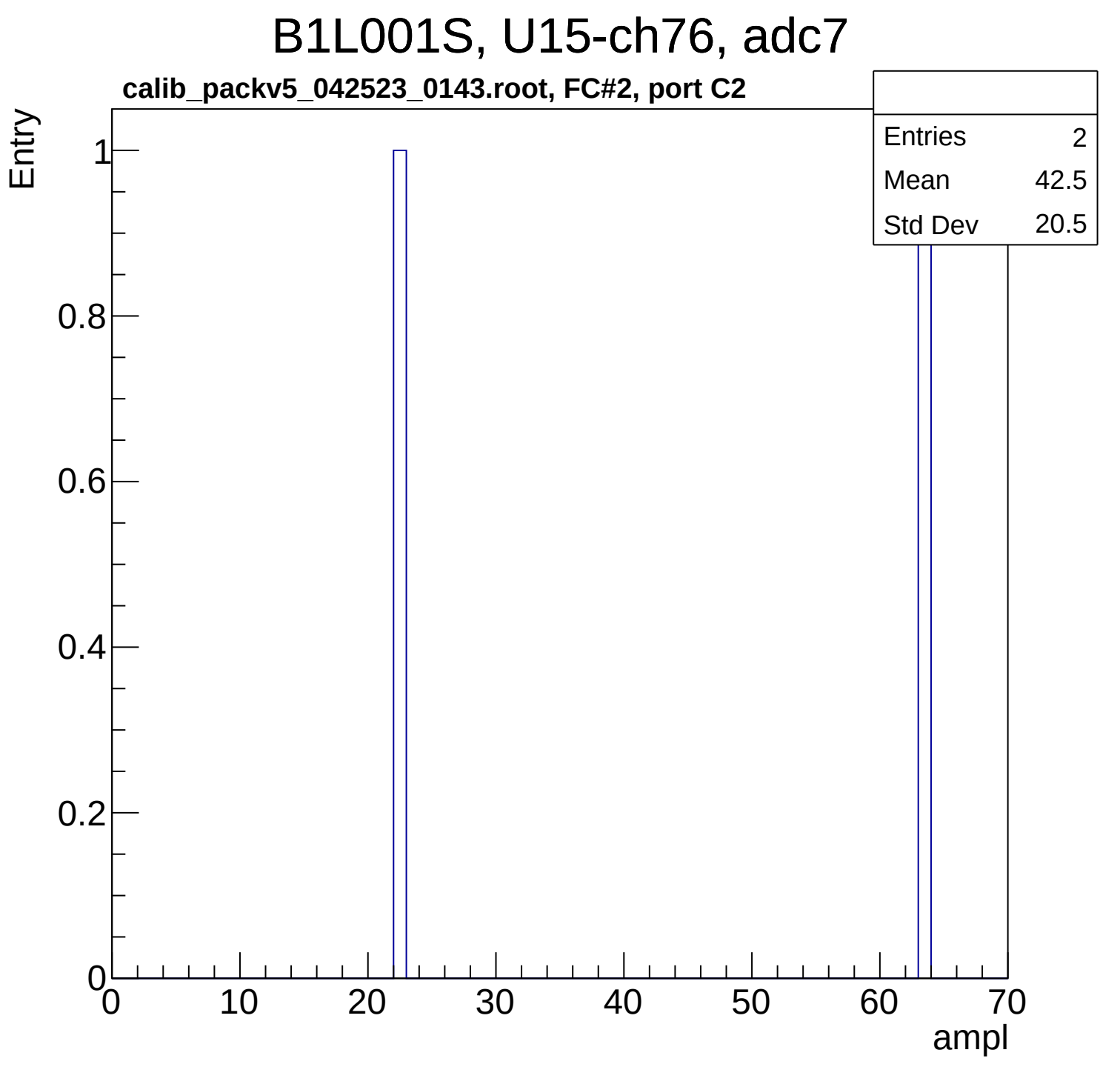
B1L001S, U15-ch76, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
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0.6
0.4
0.2
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0
10
20
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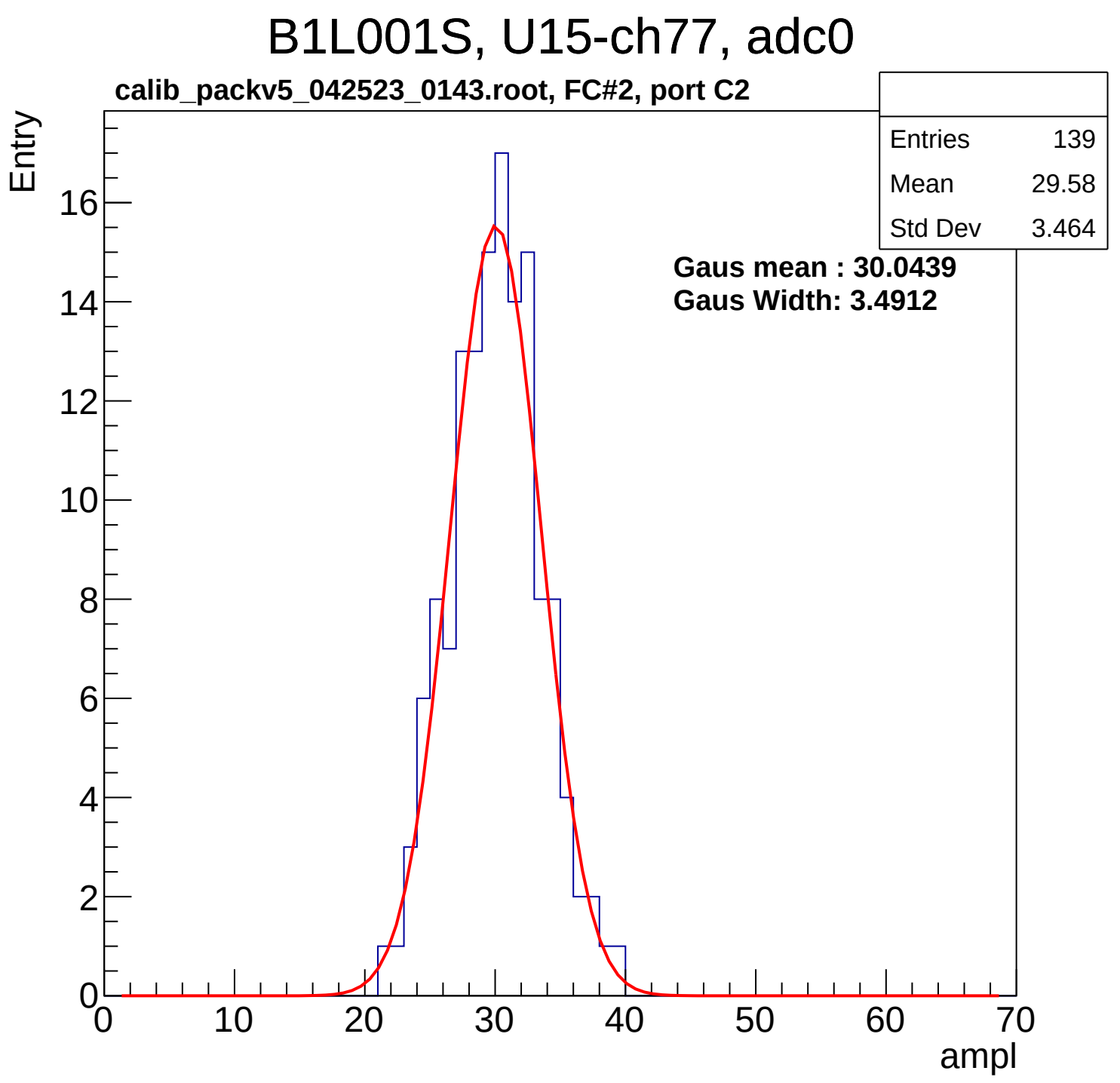
40
50
60
70
ampl


B1L001S, U15-ch77, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 29.58
Std Dev 3.464
Gaus mean : 30.0439
Gaus Width: 3.4912
Entry
ampl
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2
4
6
8
10
12
14
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40
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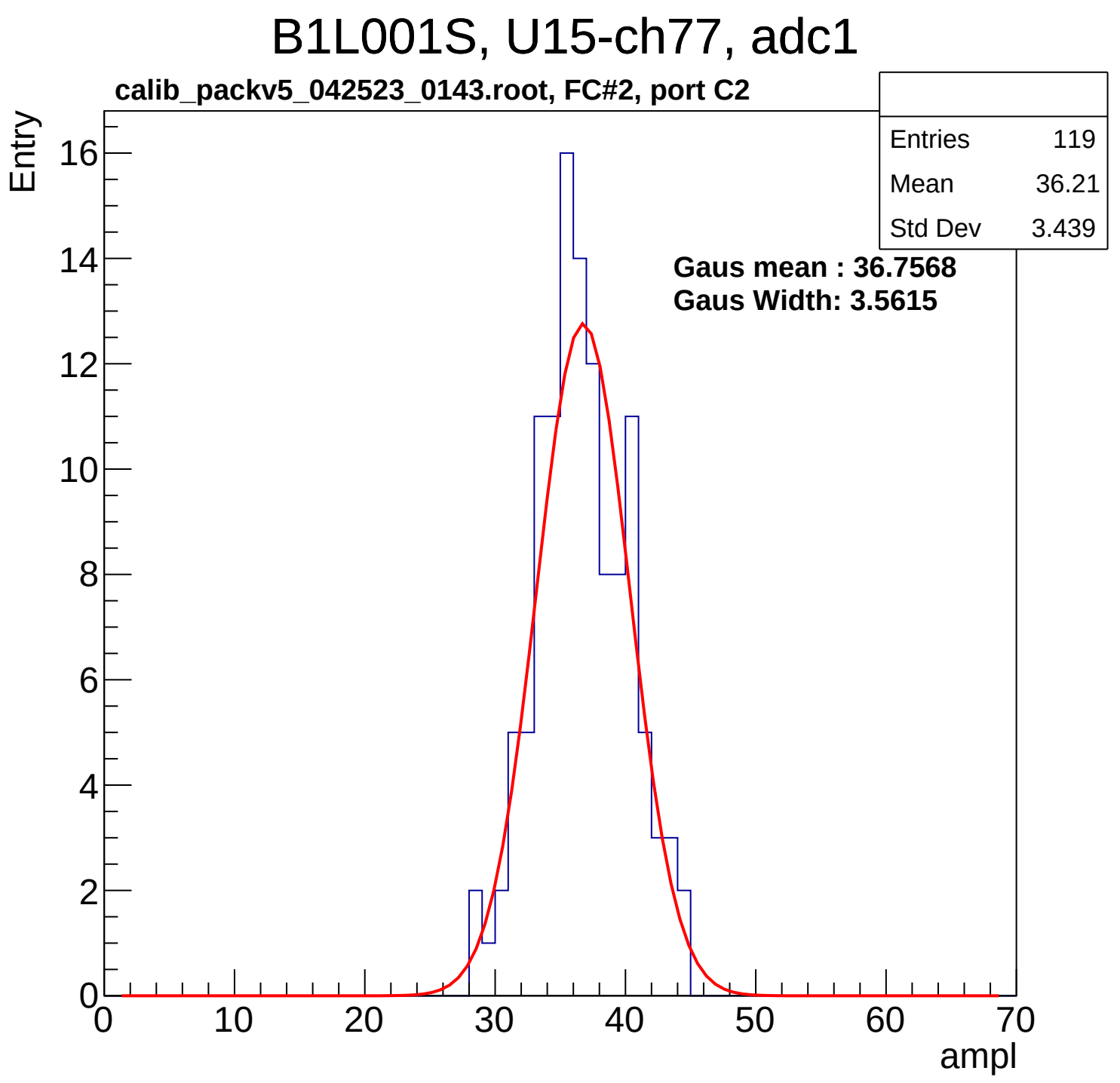
B1L001S, U15-ch77, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 36.21
Std Dev 3.439
Gaus mean : 36.7568
Gaus Width: 3.5615
Entry
ampl
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2
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10
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0
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40
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60
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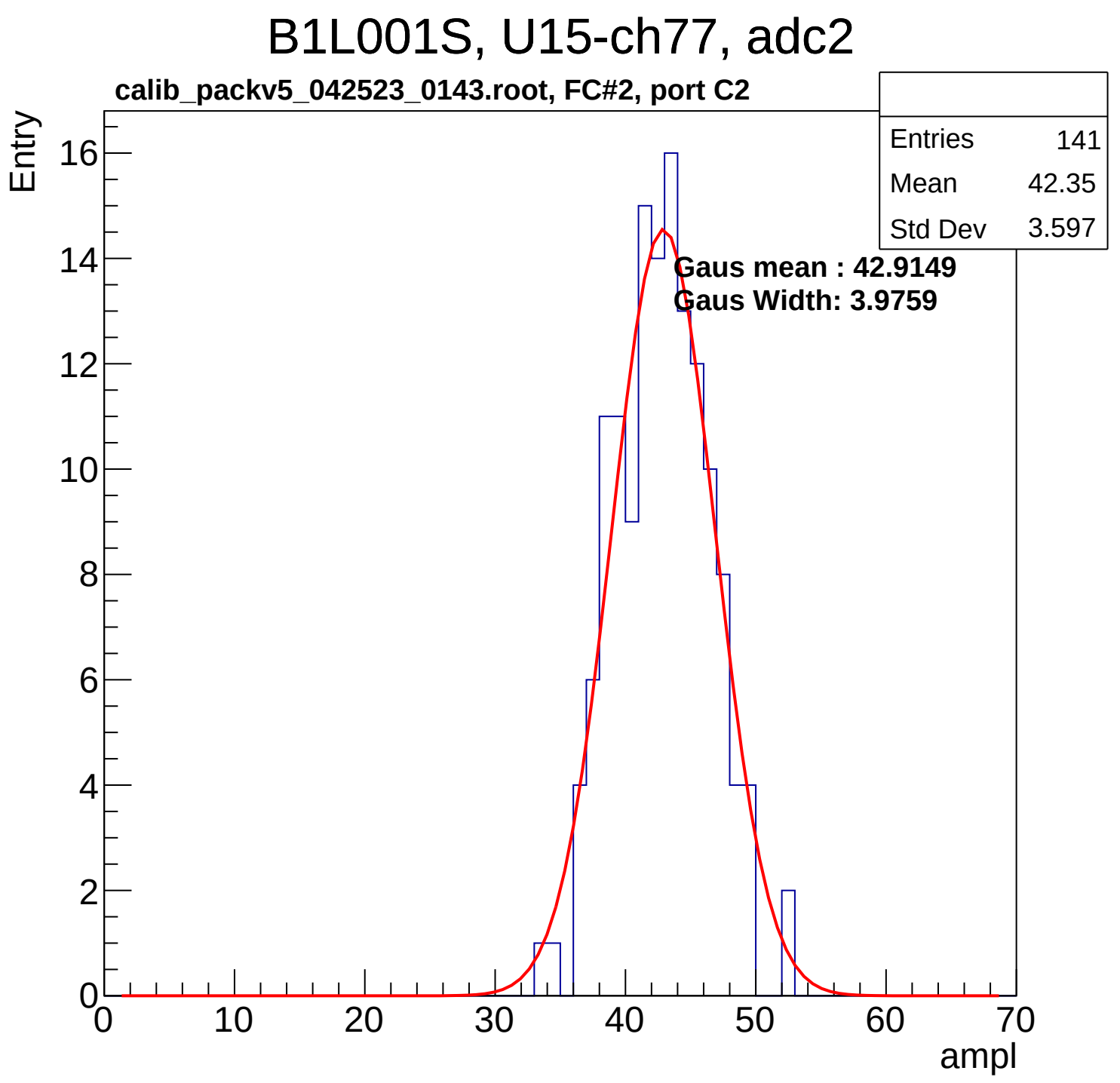
B1L001S, U15-ch77, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 42.35
Std Dev 3.597
Gaus mean : 42.9149
Gaus Width: 3.9759
Entry
ampl
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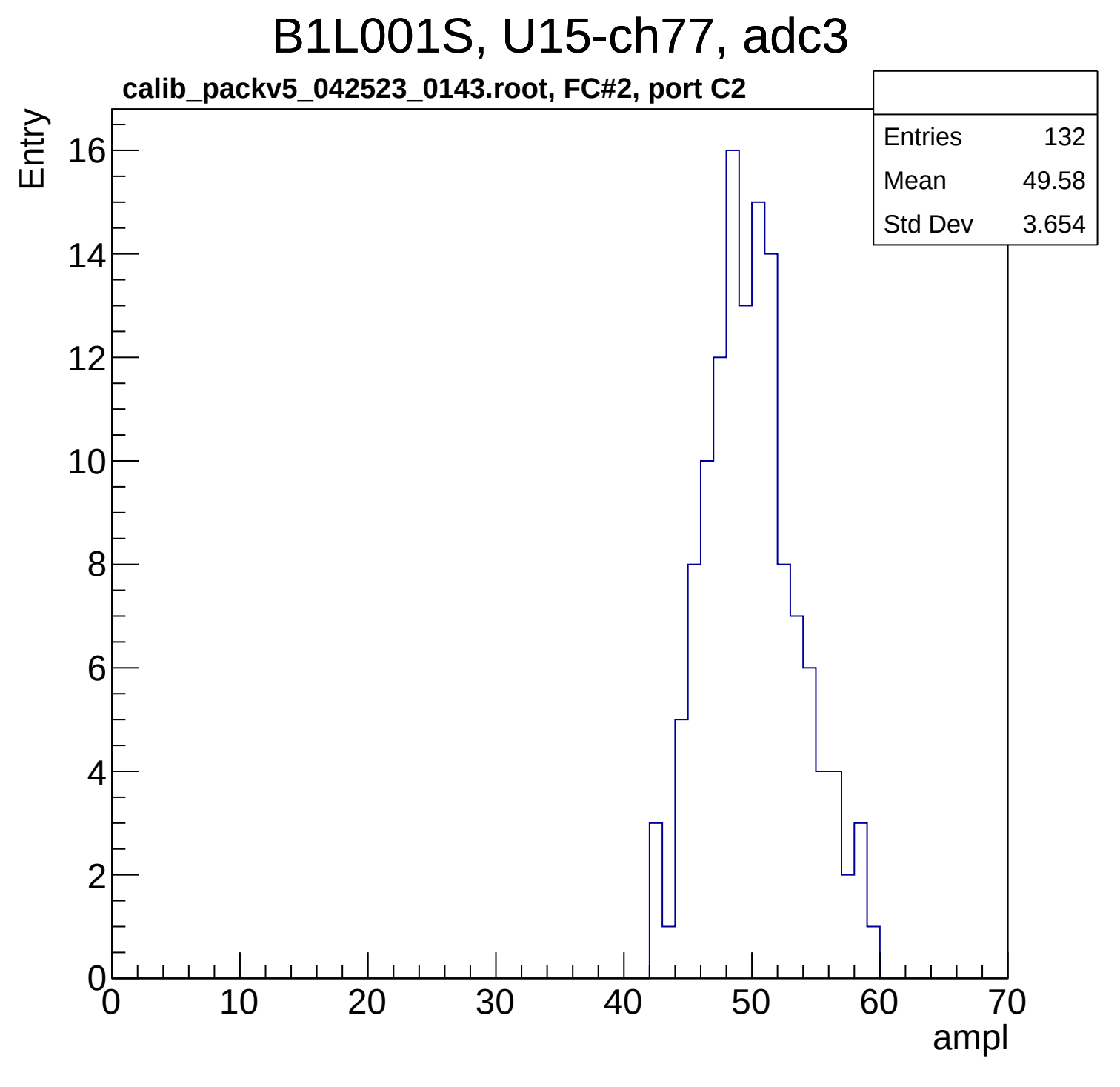
B1L001S, U15-ch77, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 49.58
Std Dev 3.654
Entry
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4
6
8
10
12
14
16
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10
20
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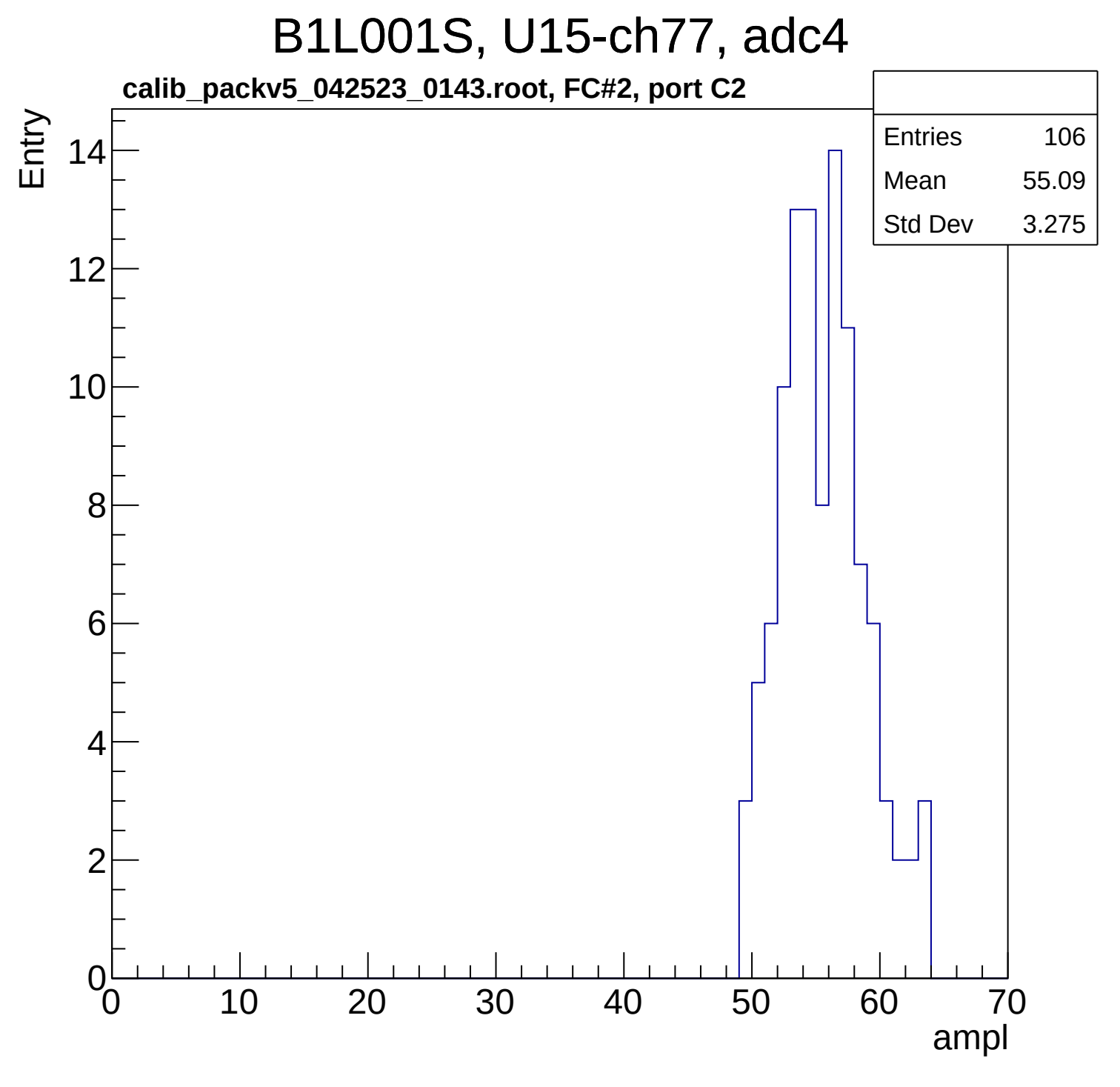
40
50
60
70
ampl


B1L001S, U15-ch77, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 55.09
Std Dev 3.275
Entry
ampl
0
2
4
6
8
10
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14
0
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20
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40
50
60
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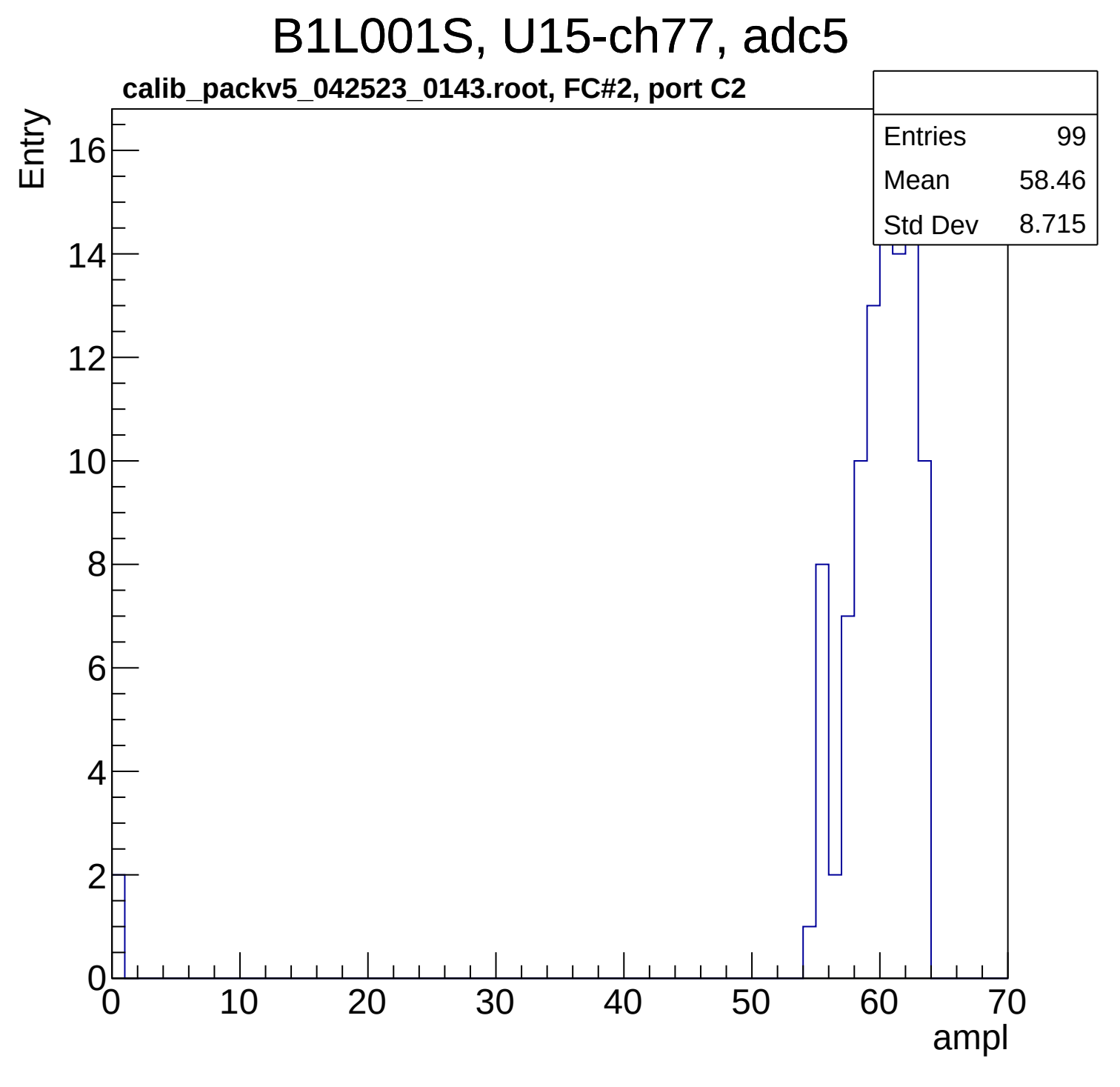
B1L001S, U15-ch77, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 58.46
Std Dev 8.715
Entry
ampl
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2
4
6
8
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60
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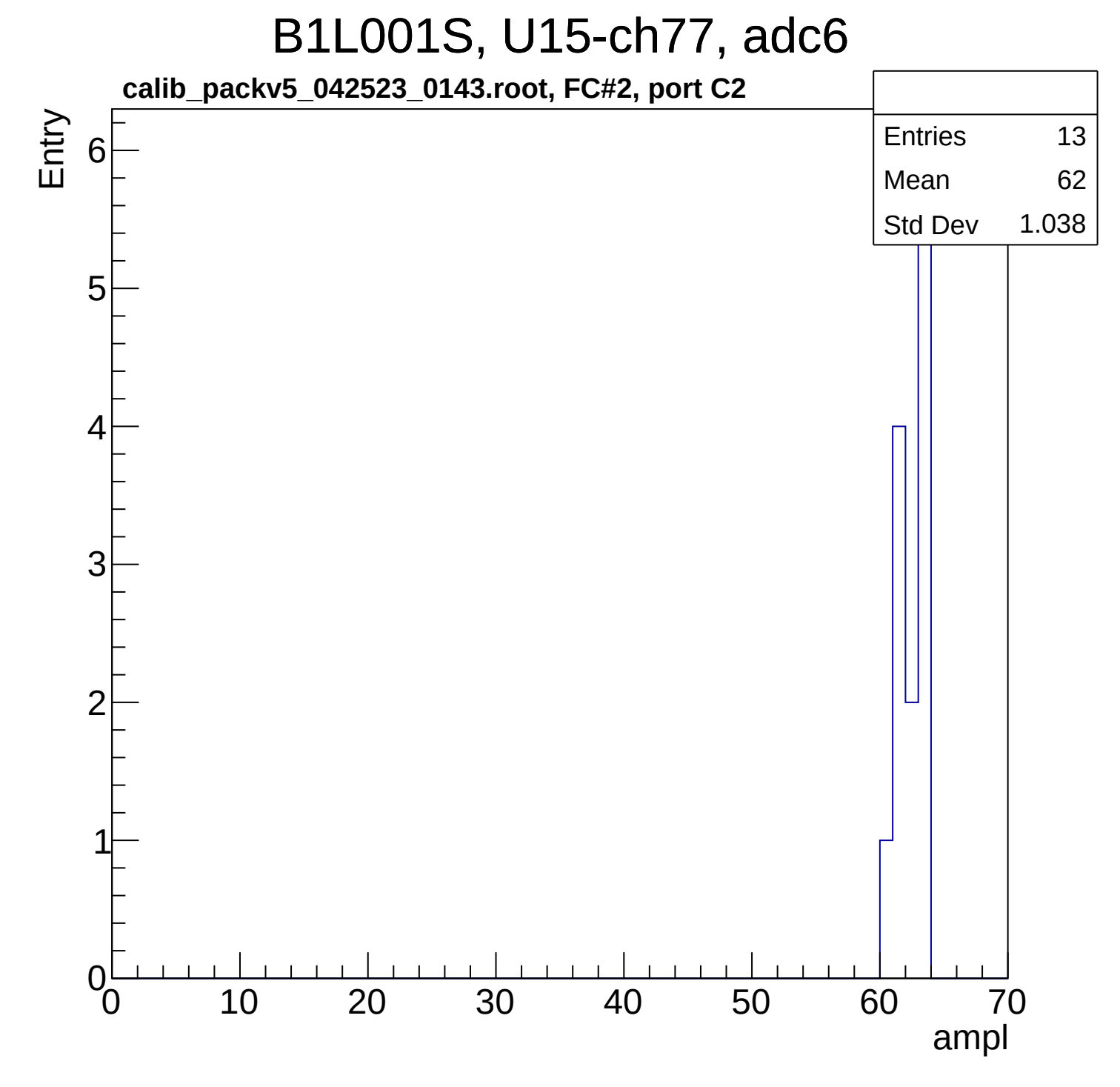
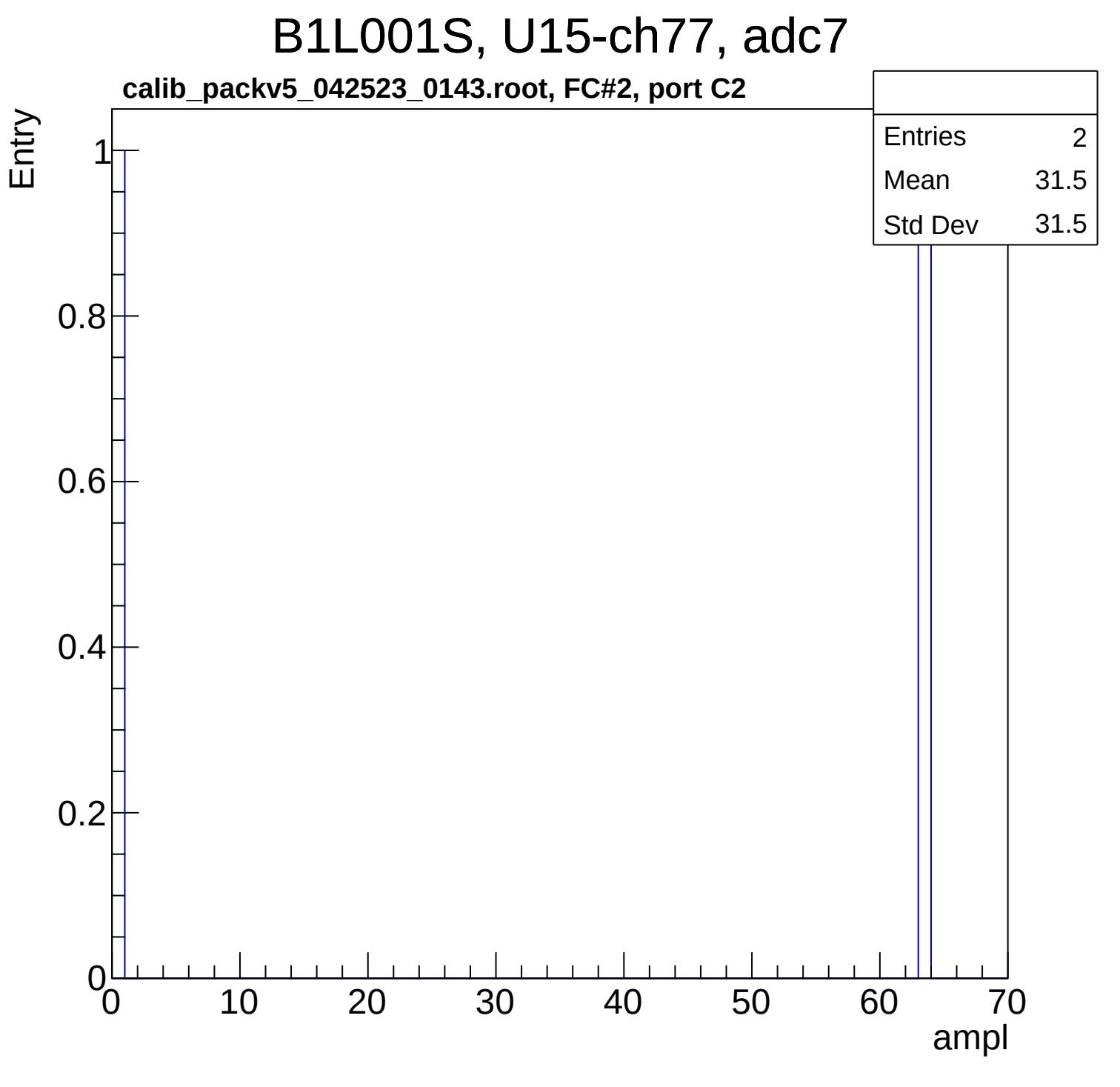
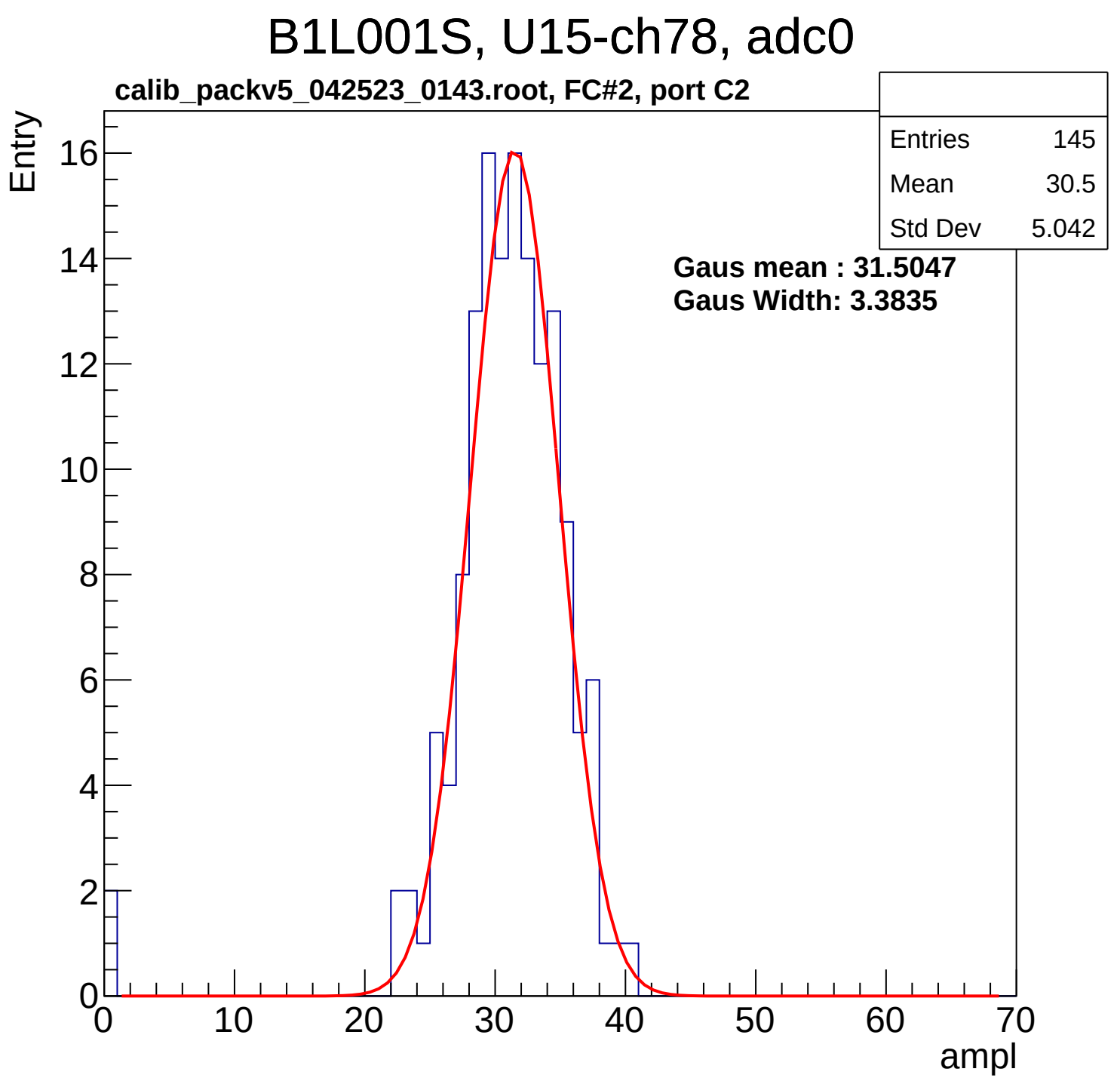

B1L001S, U15-ch77, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62
Std Dev 1.038
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch77, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch78, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 30.5
Std Dev 5.042
Gaus mean : 31.5047
Gaus Width: 3.3835
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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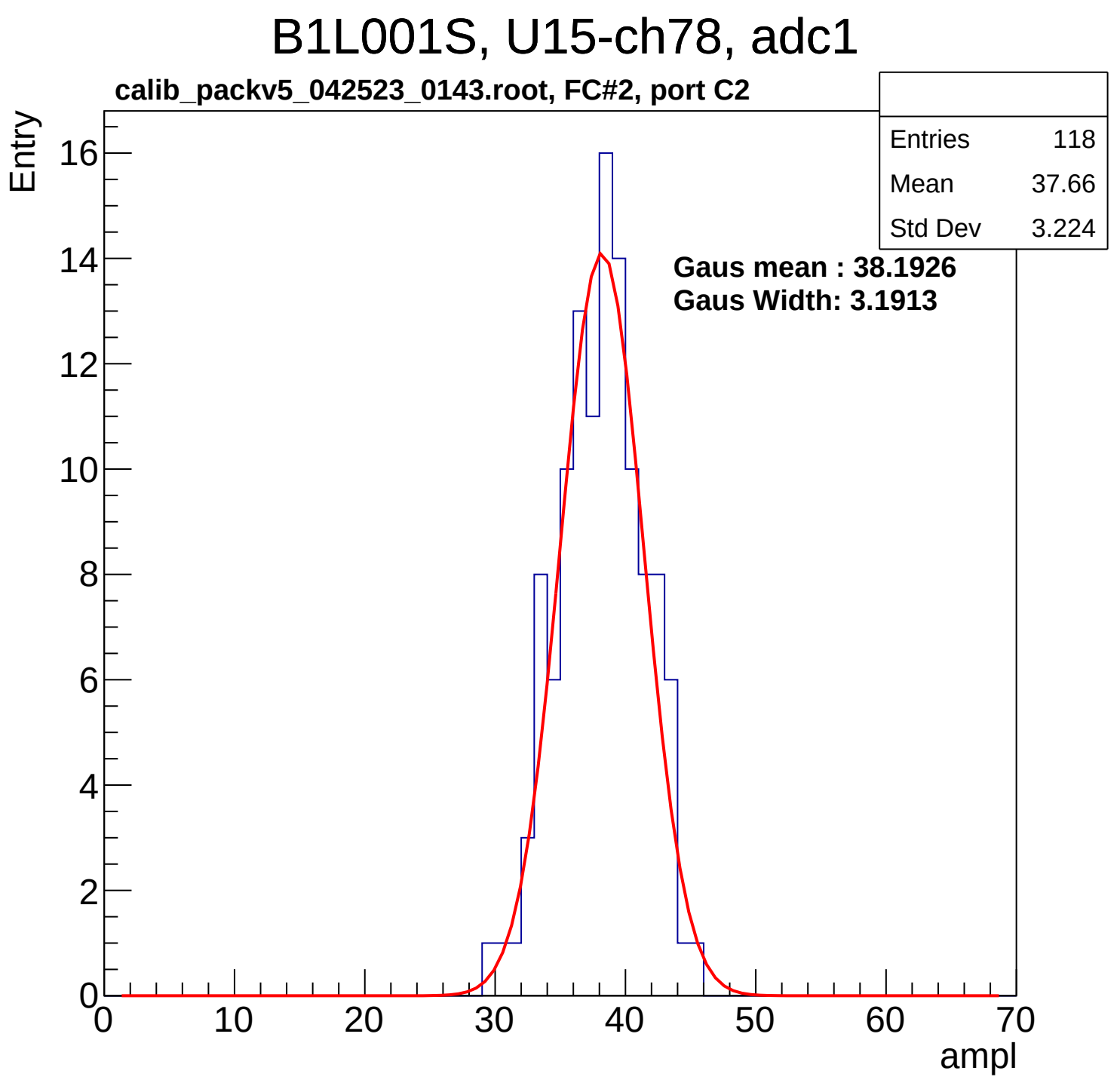
B1L001S, U15-ch78, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 37.66
Std Dev 3.224
Gaus mean : 38.1926
Gaus Width: 3.1913
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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60
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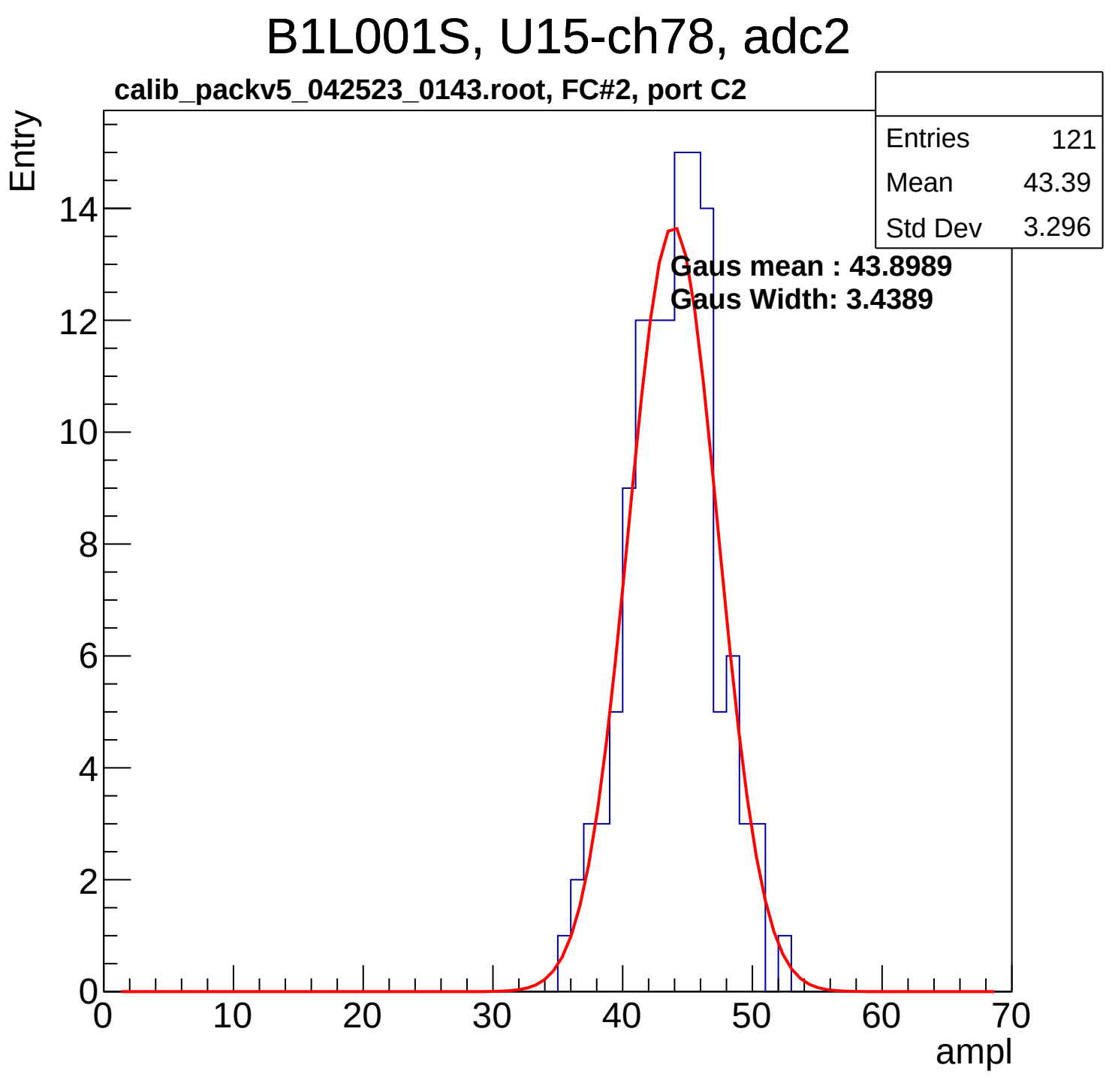
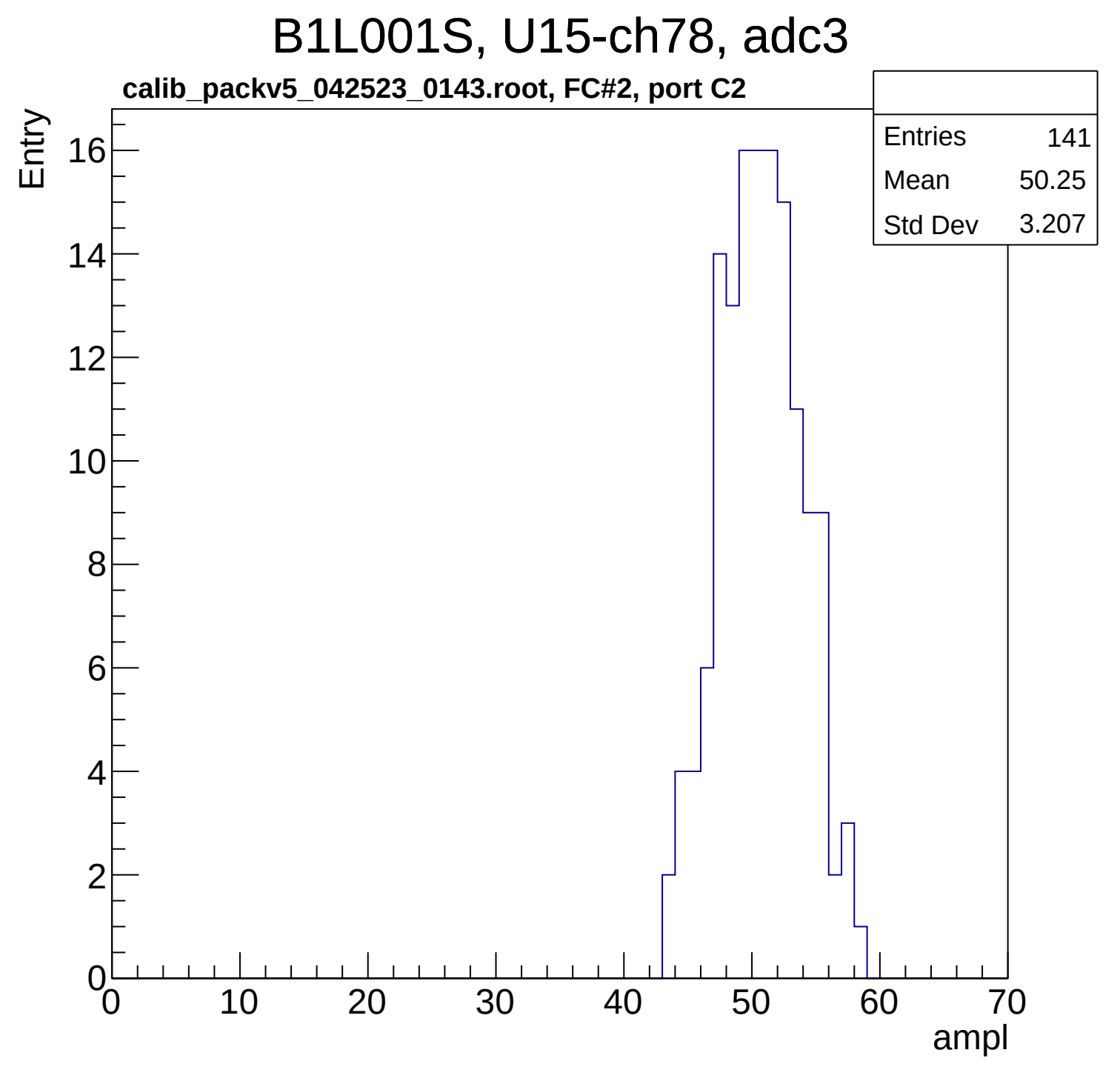
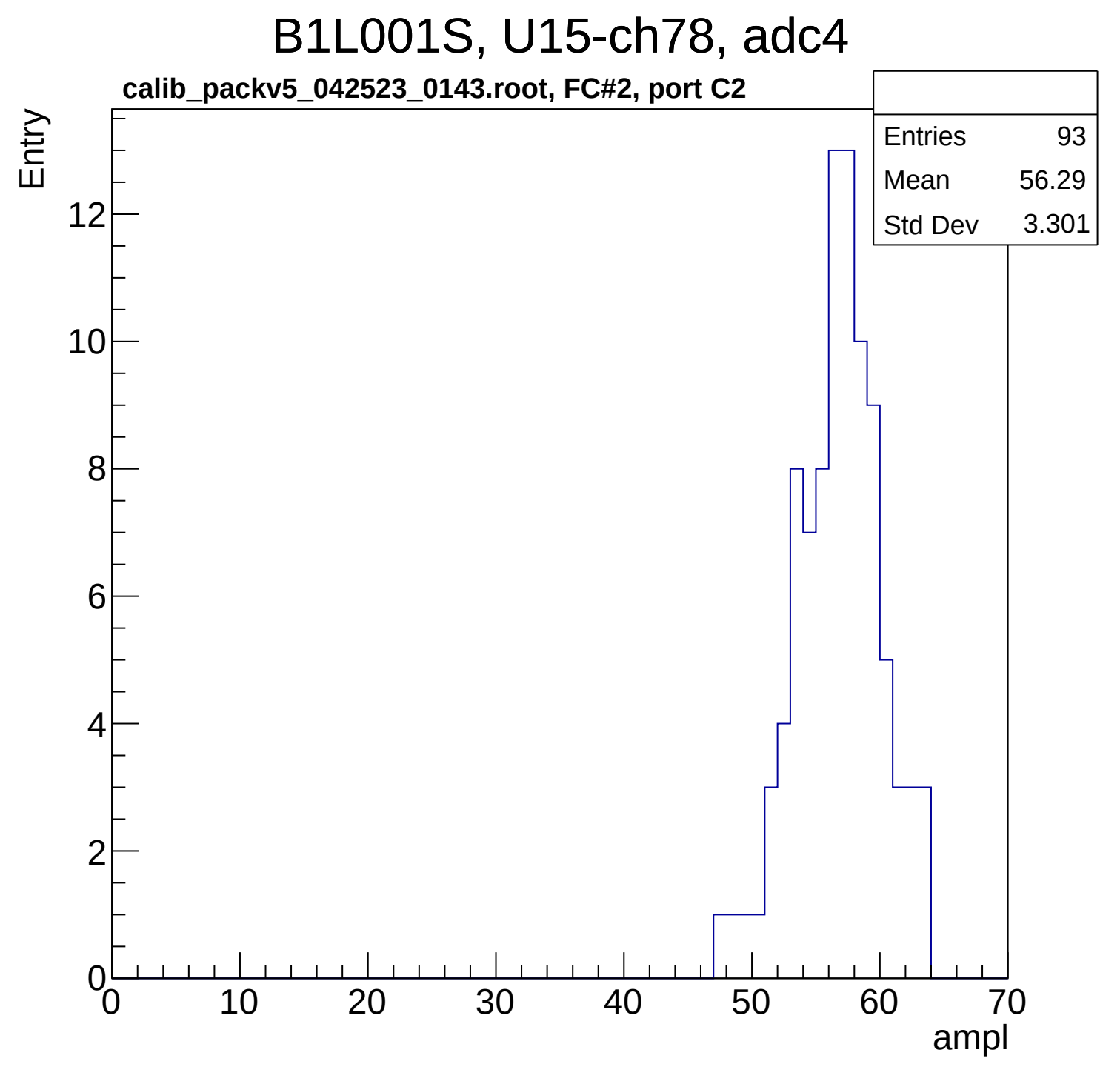
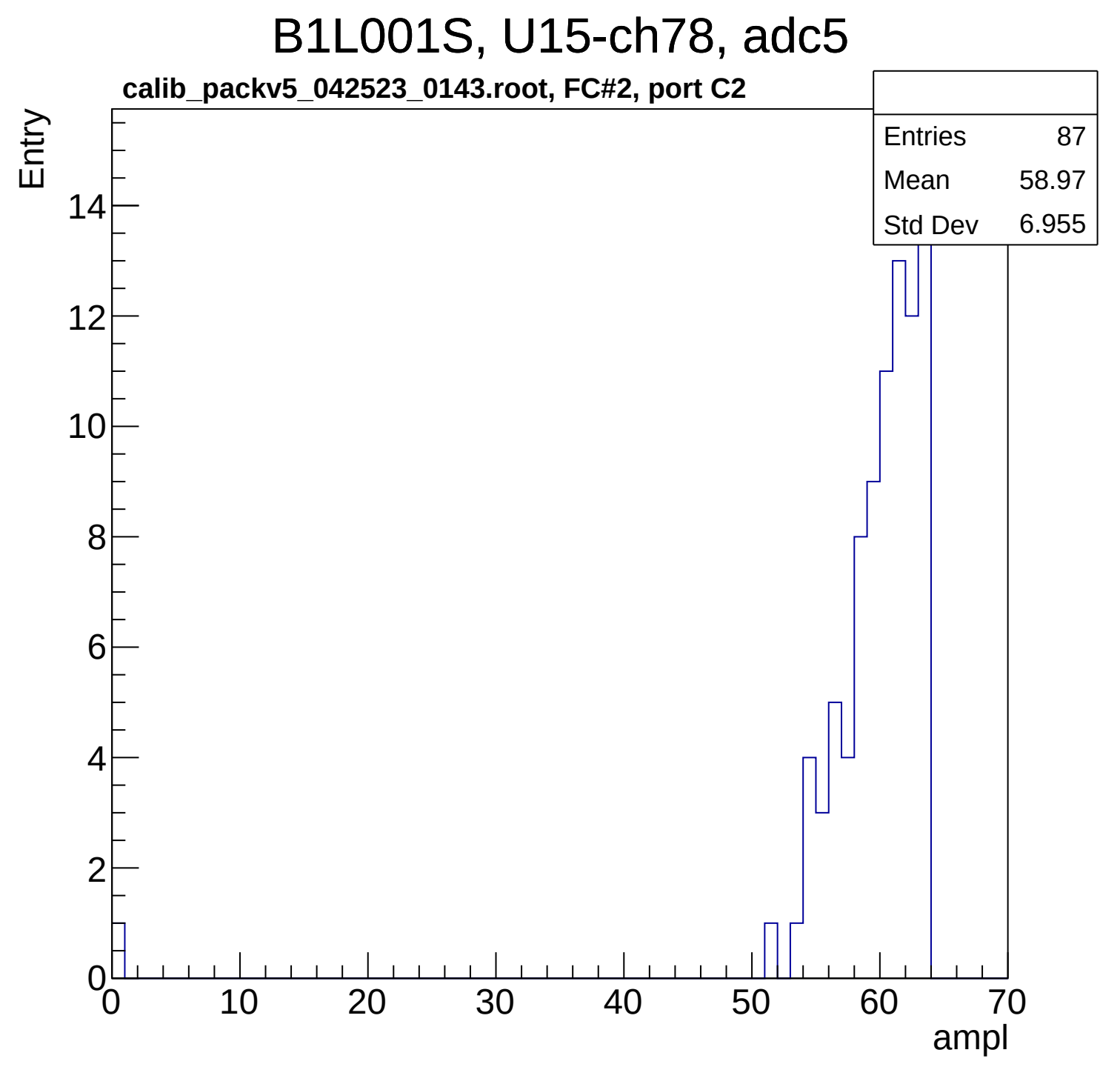

B1L001S, U15-ch78, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 43.39
Std Dev 3.296
Gaus mean : 43.8989
Gaus Width: 3.4389
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch78, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 50.25
Std Dev 3.207
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch78, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 56.29
Std Dev 3.301
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch78, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 58.97
Std Dev 6.955
Entry
ampl
0
2
4
6
8
10
12
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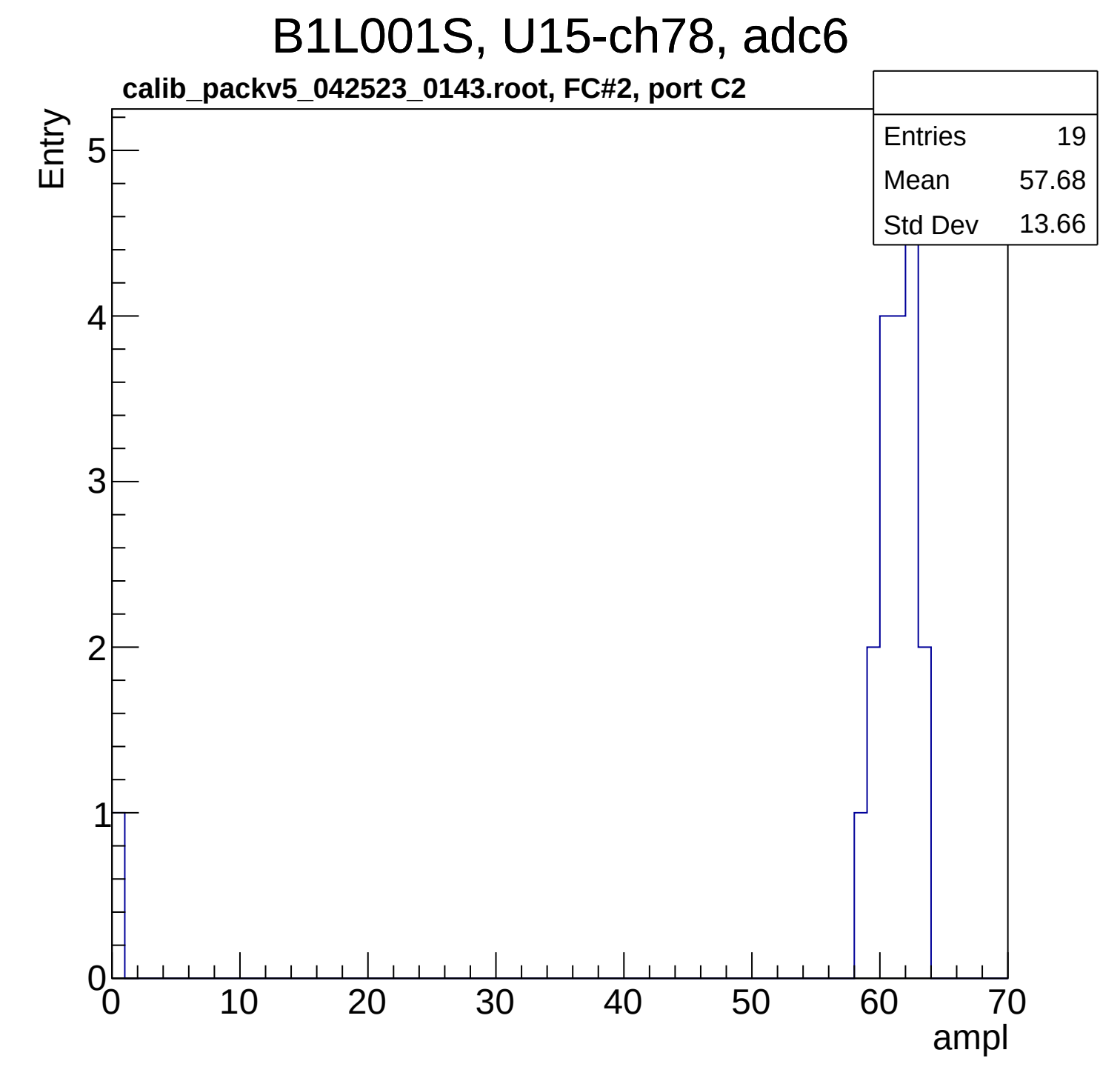
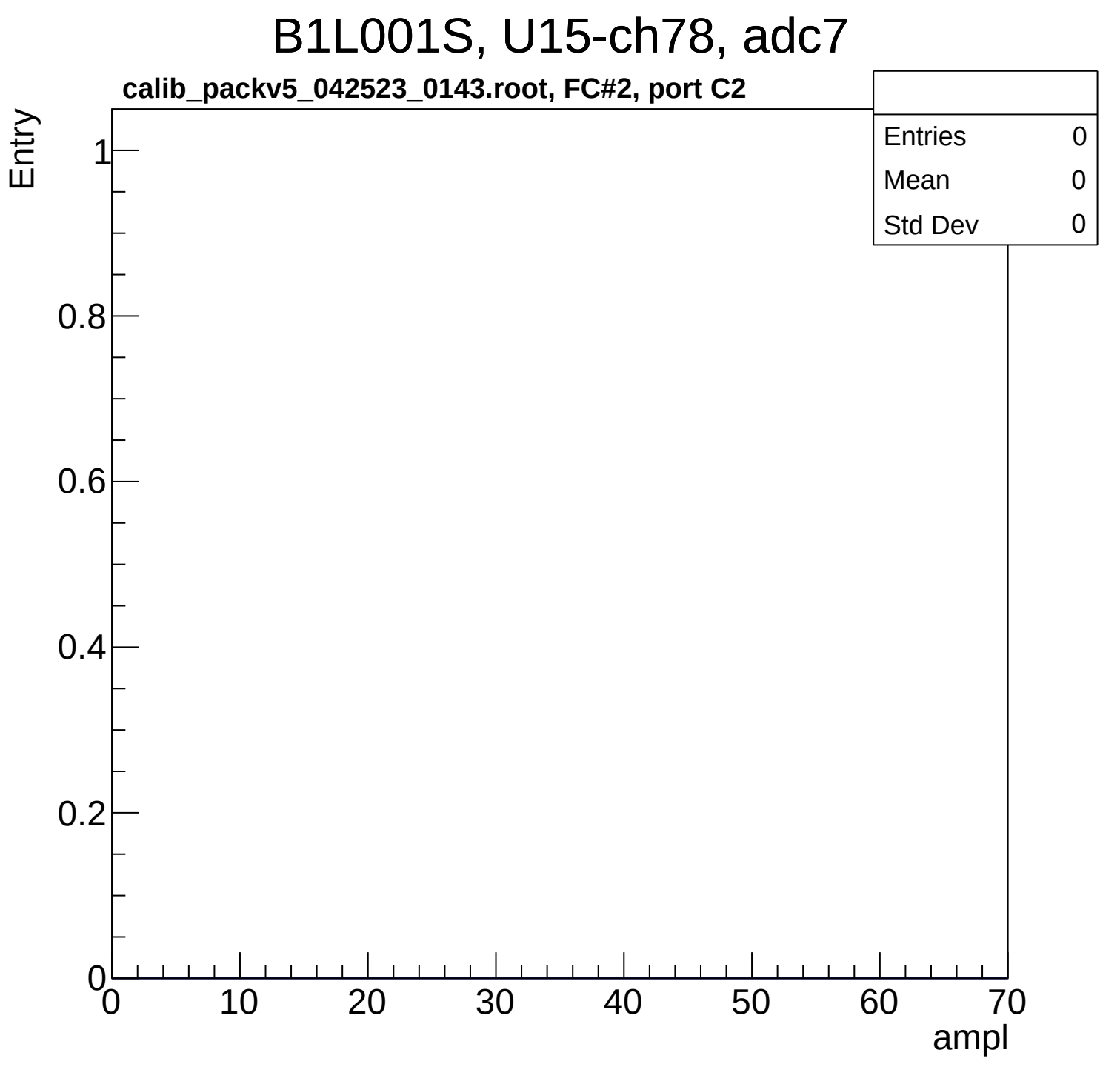
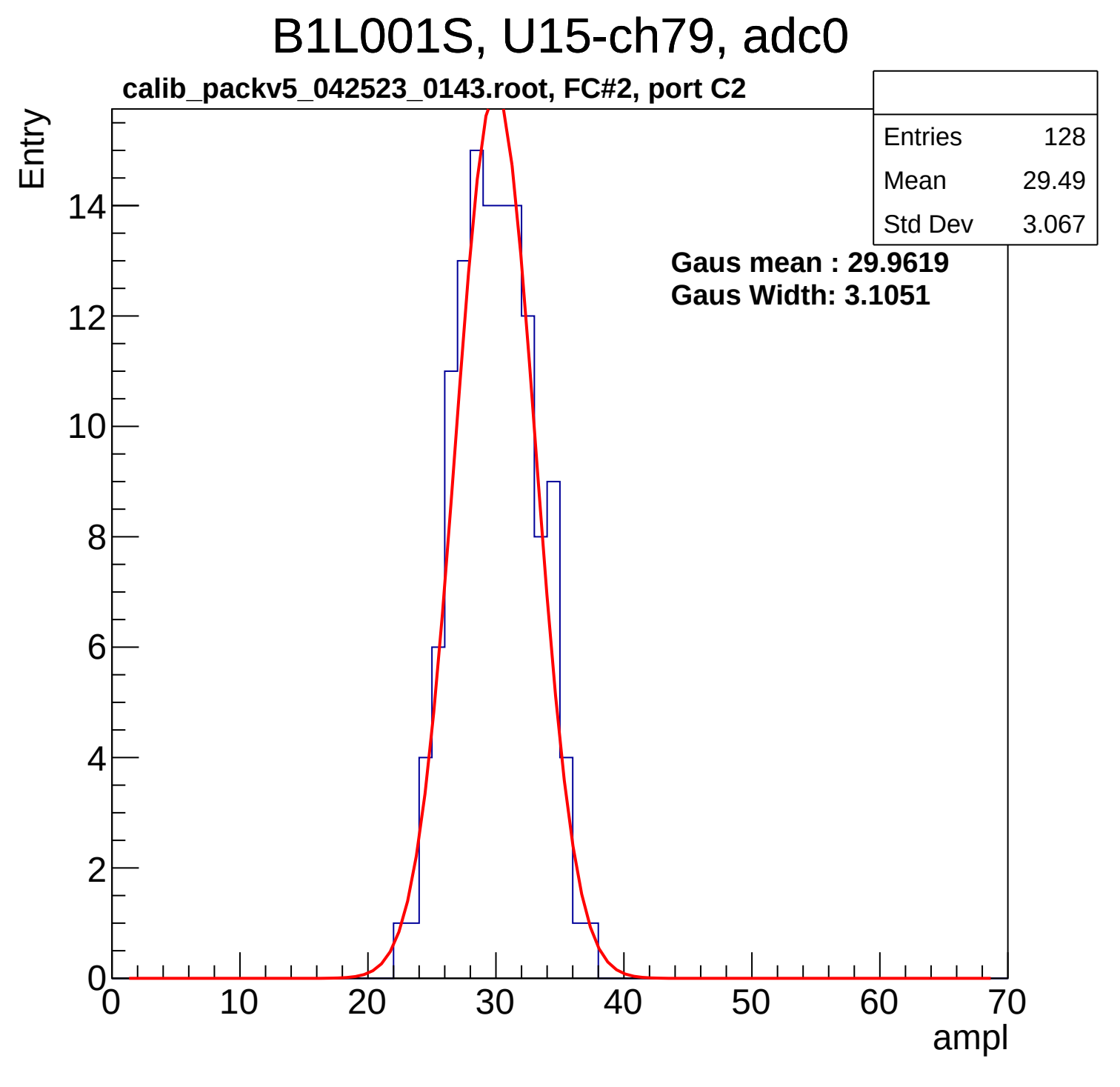
0
10
20
30
40
50
60
70


B1L001S, U15-ch78, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 19
Mean 57.68
Std Dev 13.66
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch78, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch79, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 29.49
Std Dev 3.067
Gaus mean : 29.9619
Gaus Width: 3.1051
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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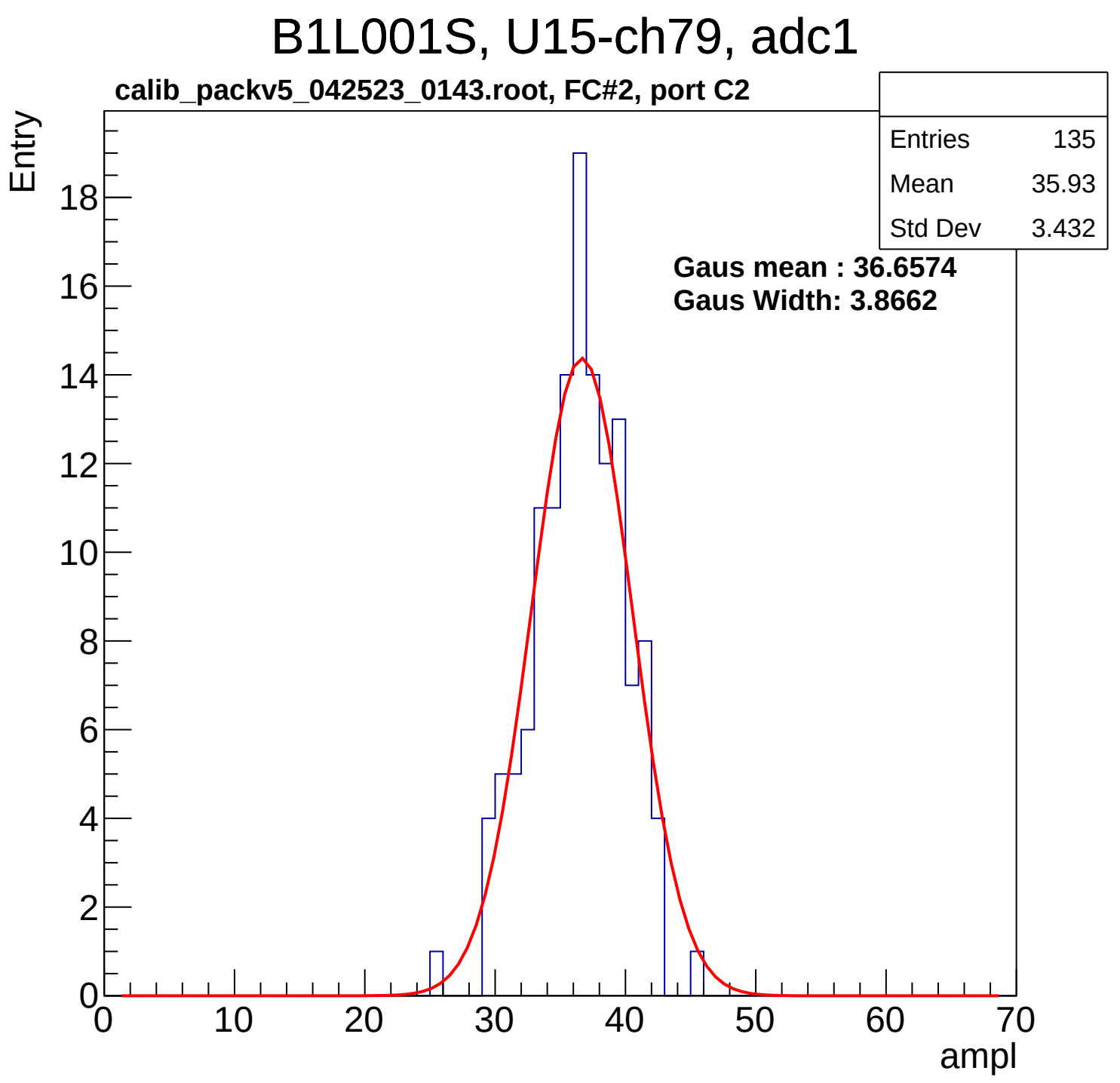
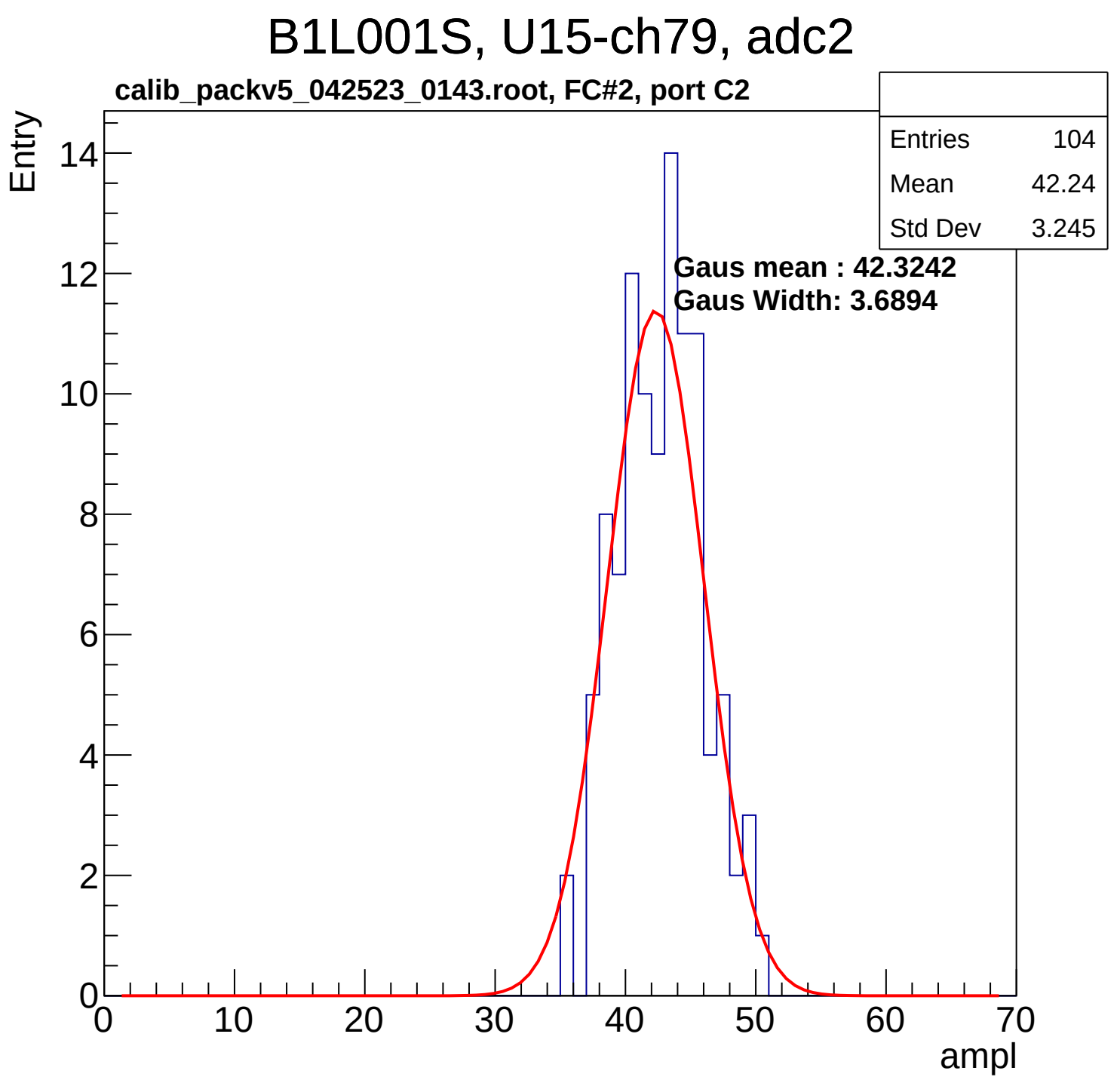

B1L001S, U15-ch79, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 35.93
Std Dev 3.432
Gaus mean : 36.6574
Gaus Width: 3.8662
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch79, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 42.24
Std Dev 3.245
Gaus mean : 42.3242
Gaus Width: 3.6894
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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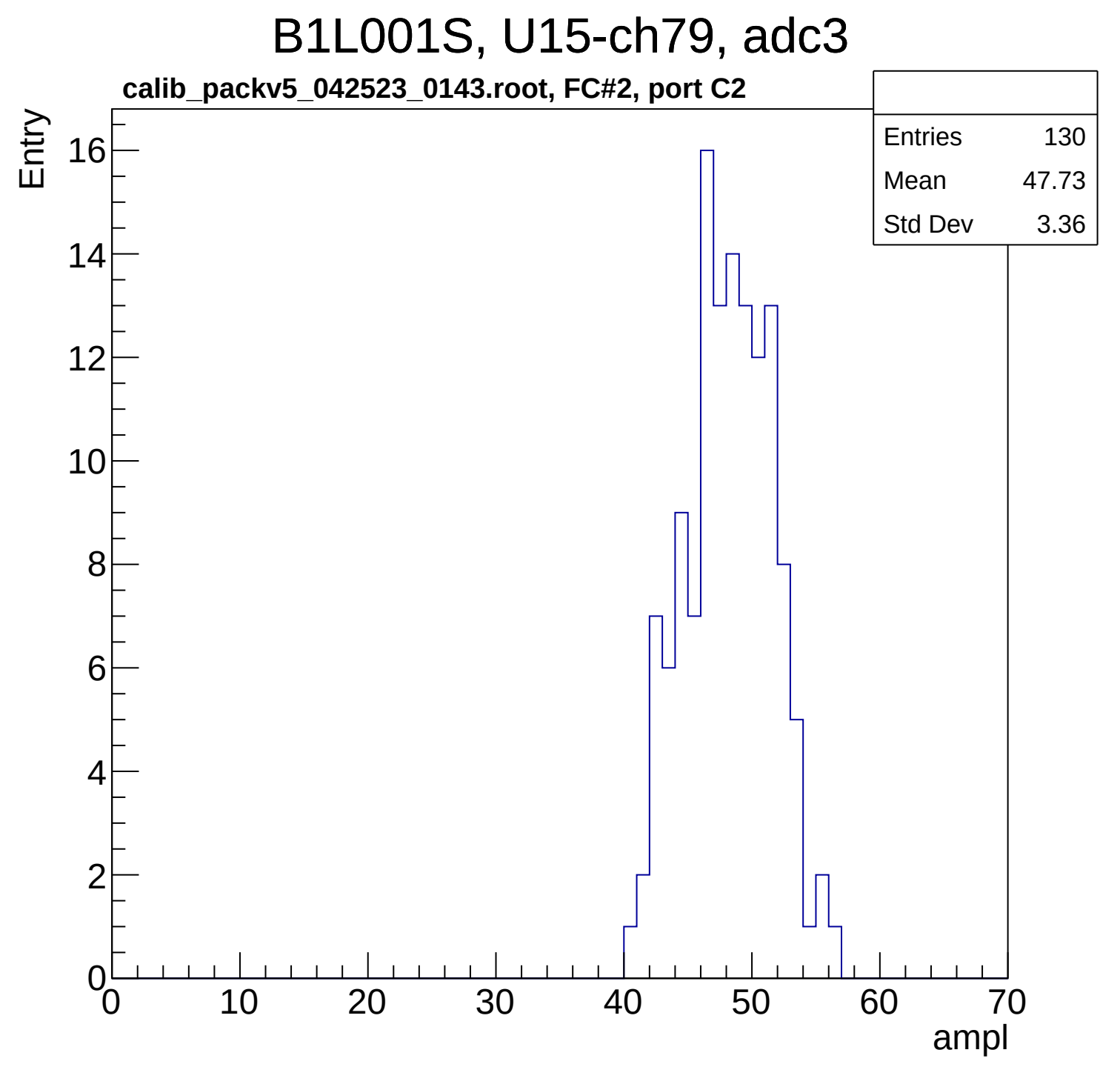
B1L001S, U15-ch79, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 47.73
Std Dev 3.36
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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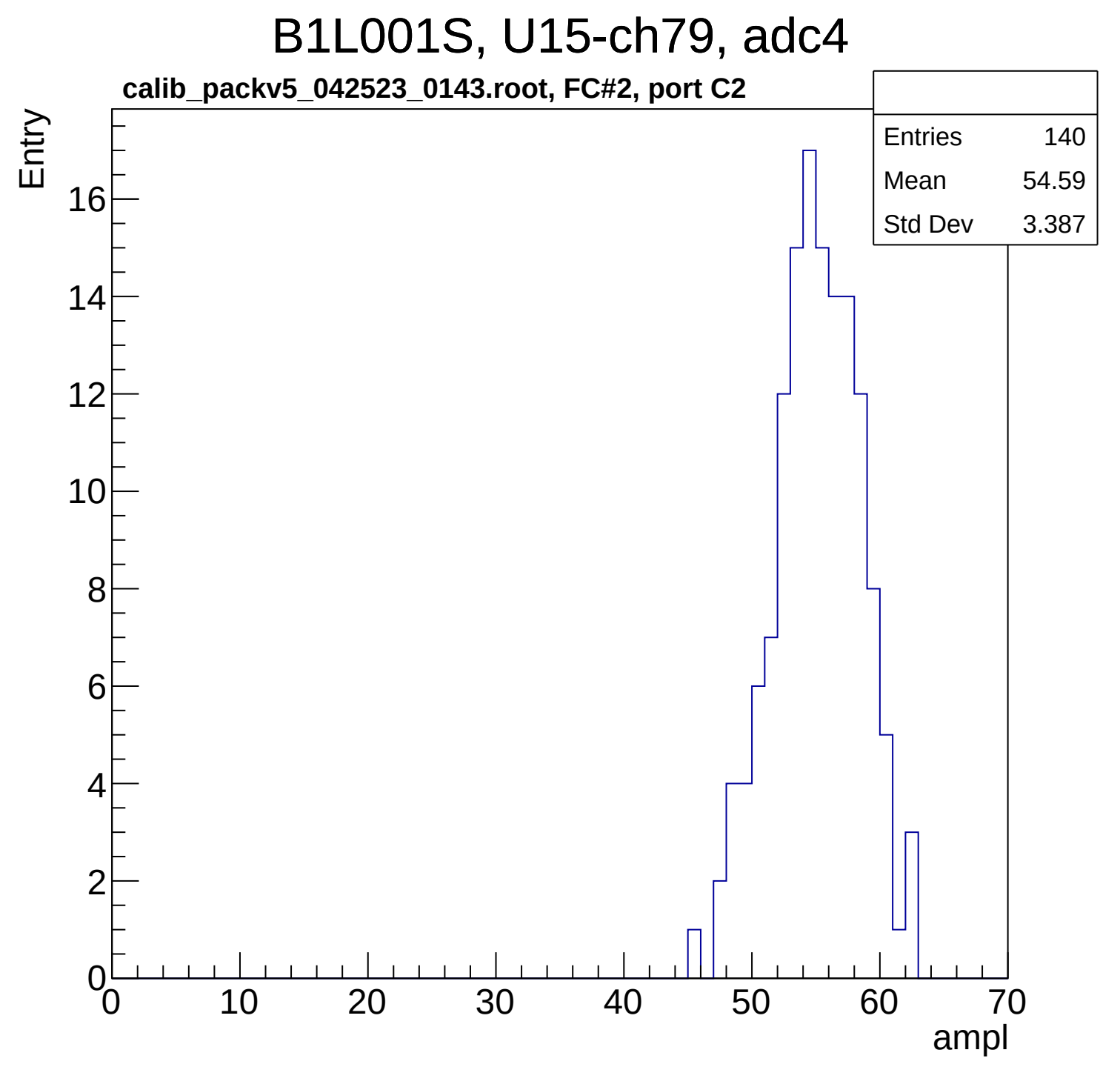
B1L001S, U15-ch79, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 54.59
Std Dev 3.387
Entry
ampl
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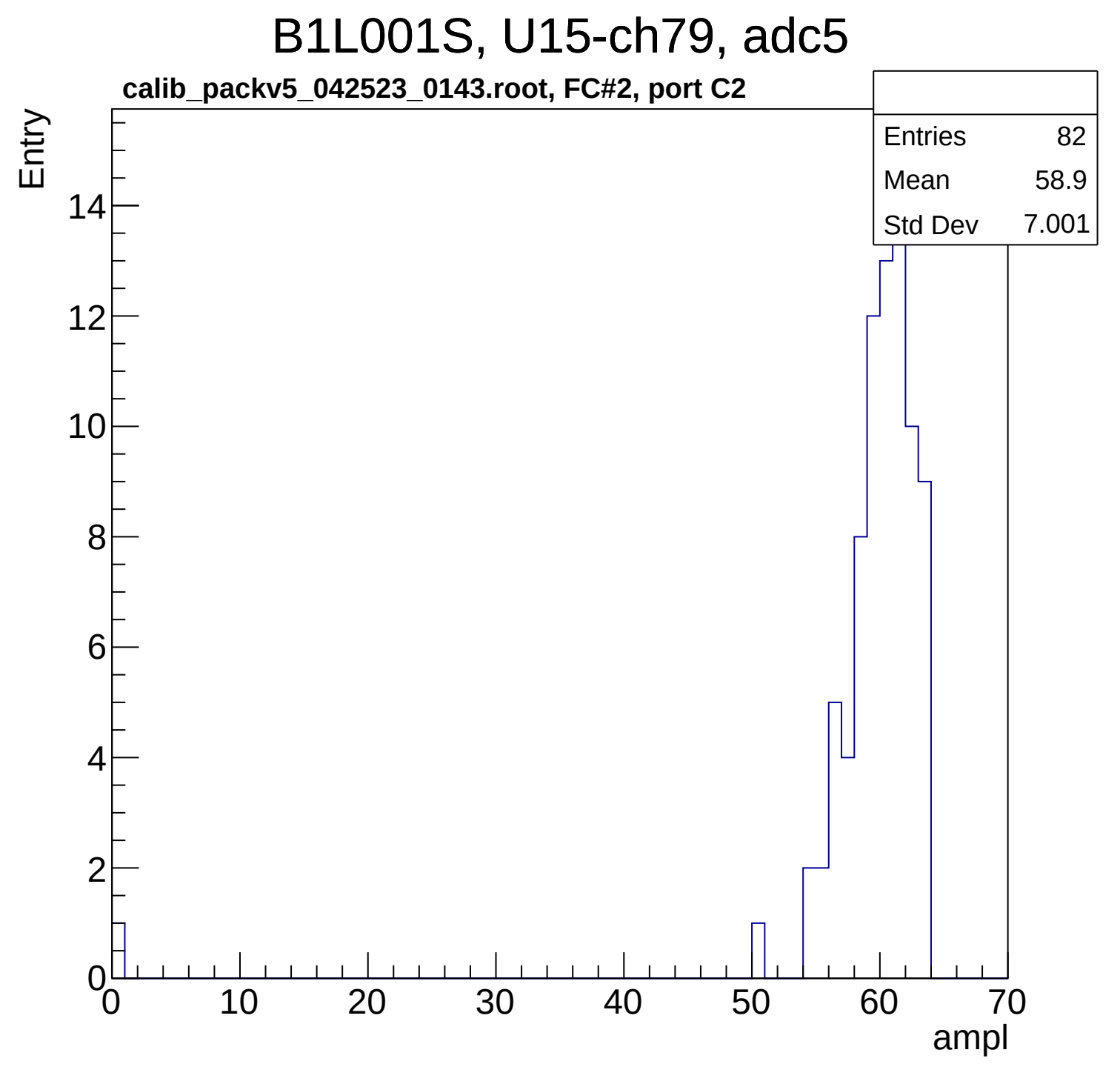
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch79, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 58.9
Std Dev 7.001
Entry
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2
4
6
8
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10
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50
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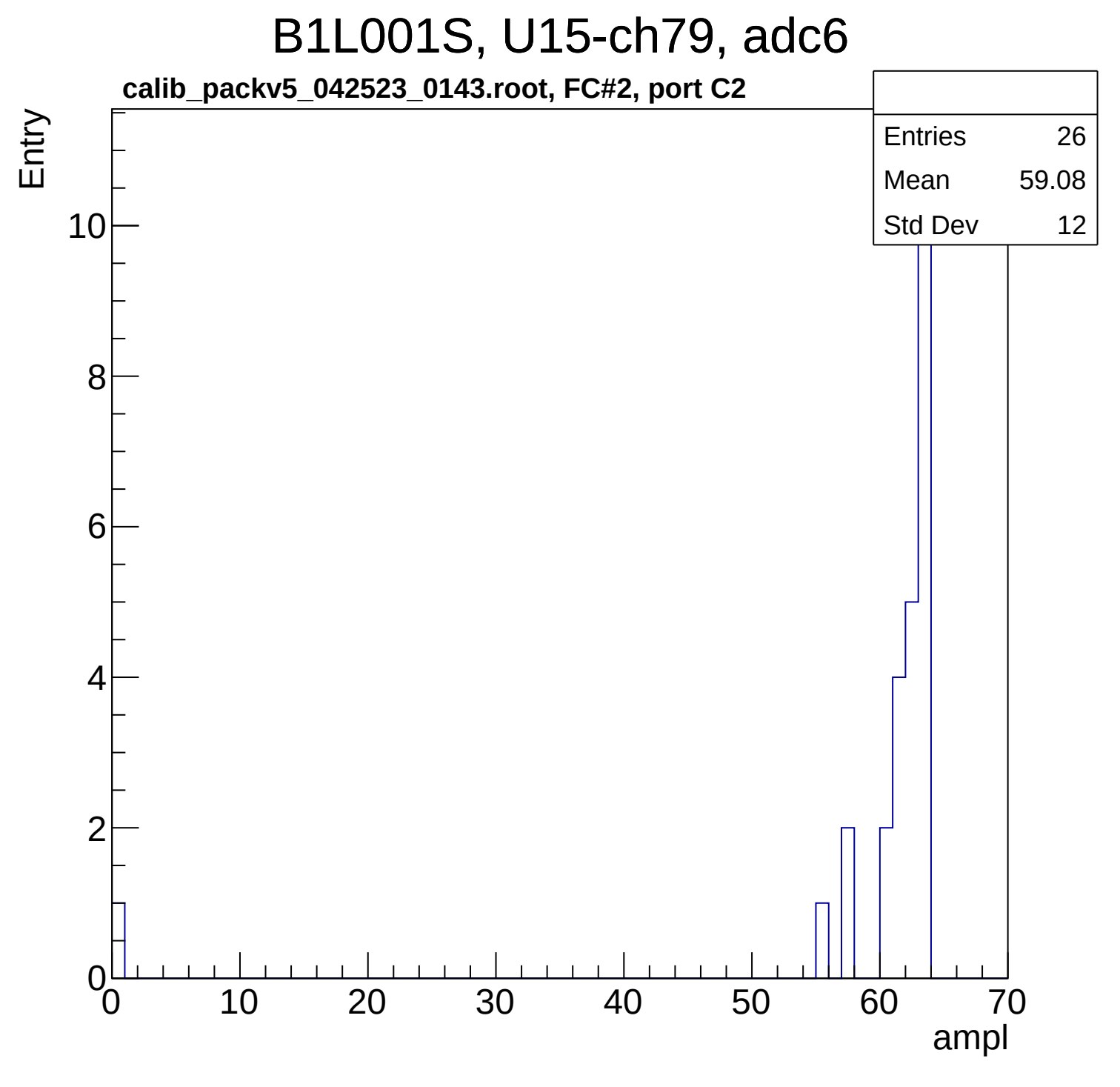
70
ampl


B1L001S, U15-ch79, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 59.08
Std Dev 12
Entry
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2
4
6
8
10
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10
20
30
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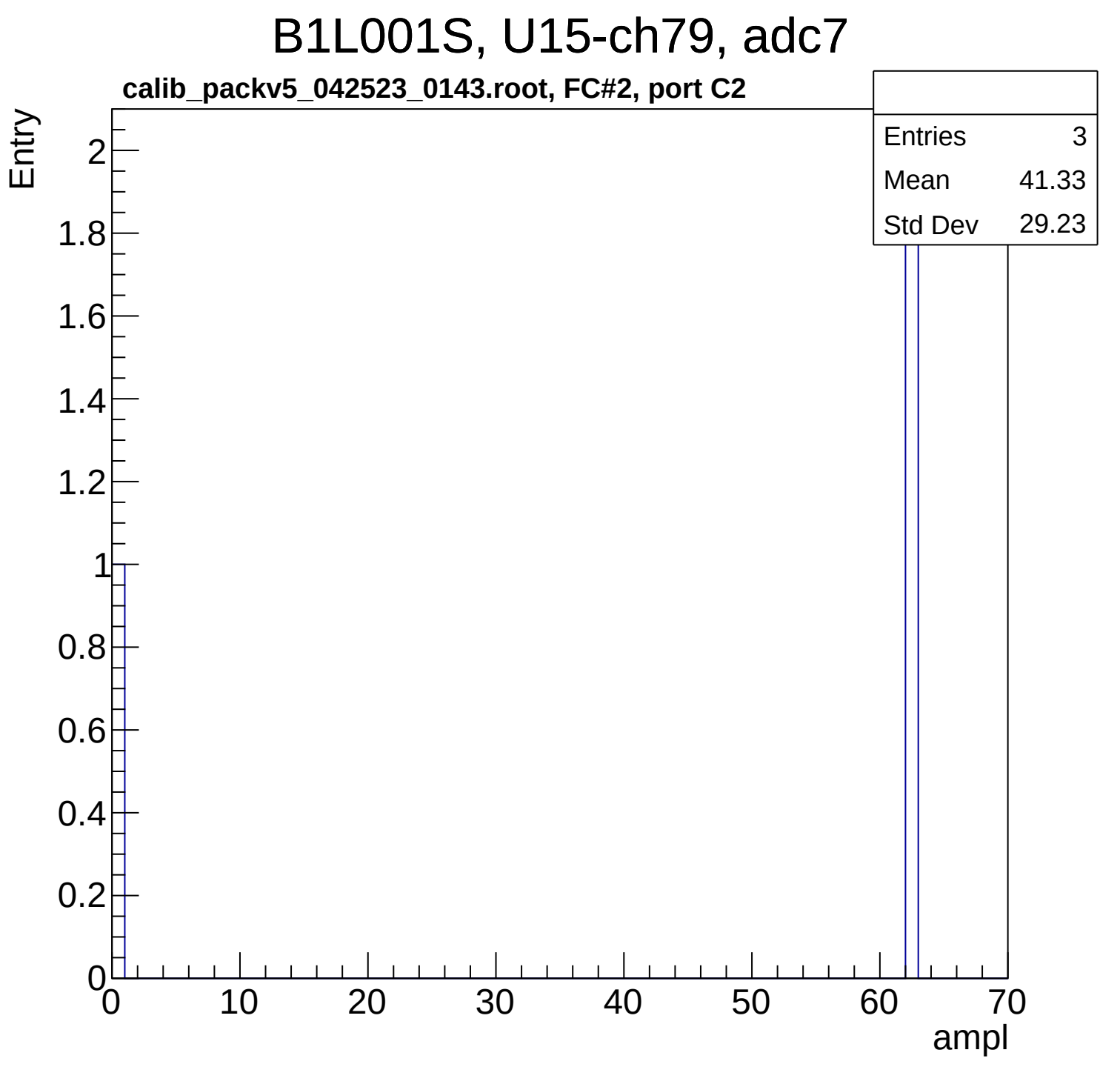
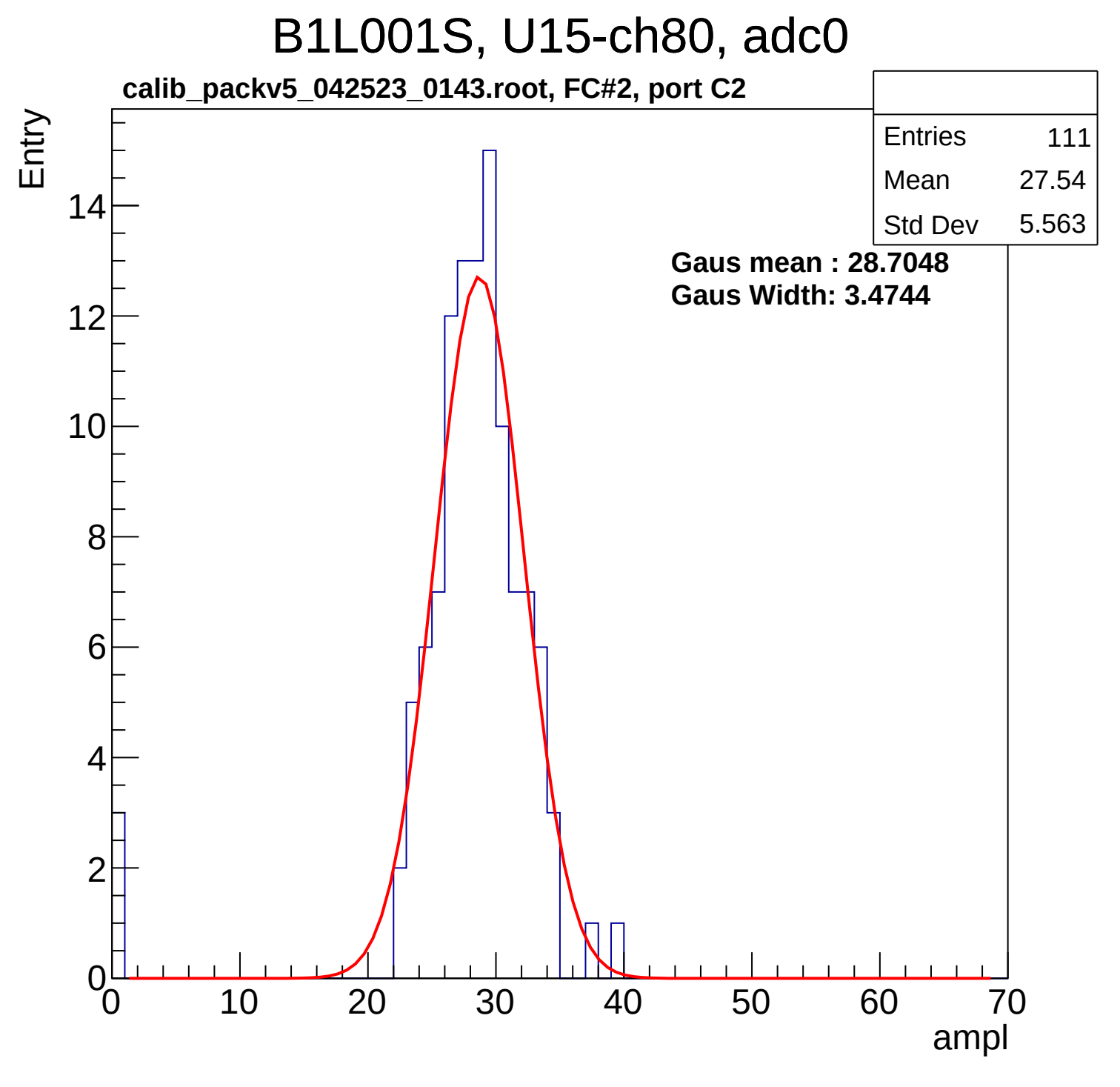
50
60
70
ampl


B1L001S, U15-ch79, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch80, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 27.54
Std Dev 5.563
Gaus mean : 28.7048
Gaus Width: 3.4744
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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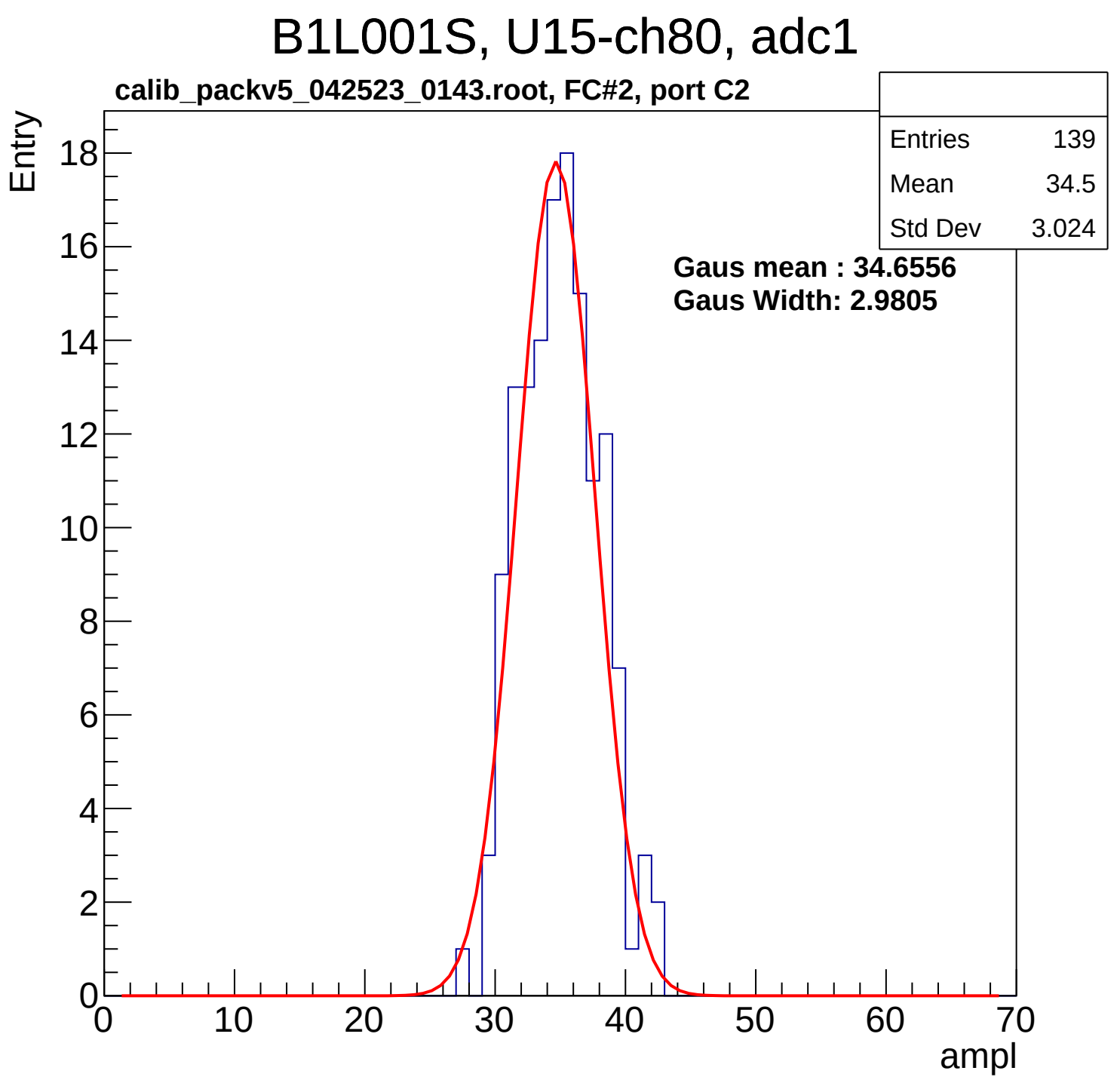
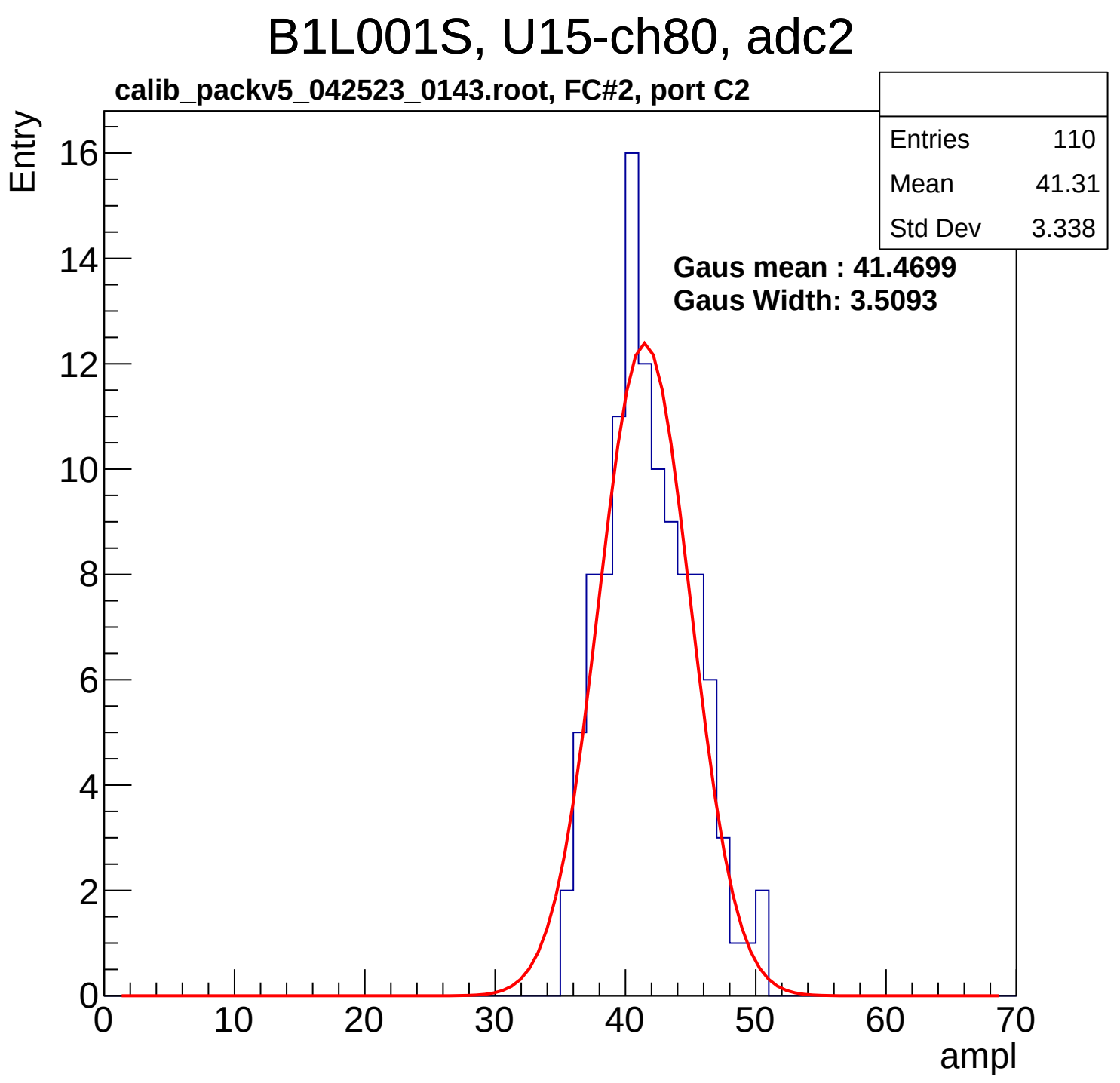
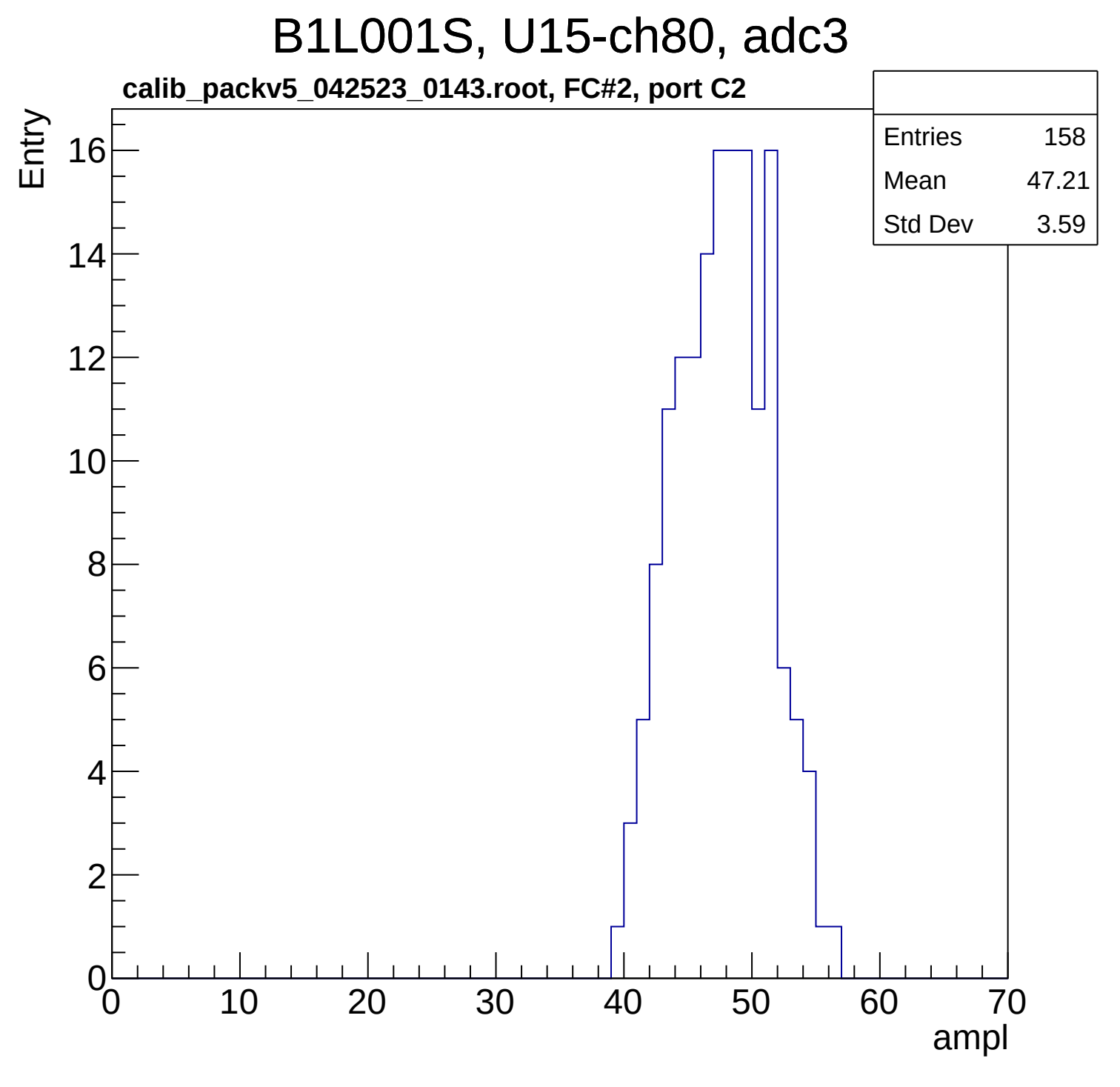

B1L001S, U15-ch80, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 34.5
Std Dev 3.024
Gaus mean : 34.6556
Gaus Width: 2.9805
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch80, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 41.31
Std Dev 3.338
Gaus mean : 41.4699
Gaus Width: 3.5093
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch80, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 47.21
Std Dev 3.59
Entry
0
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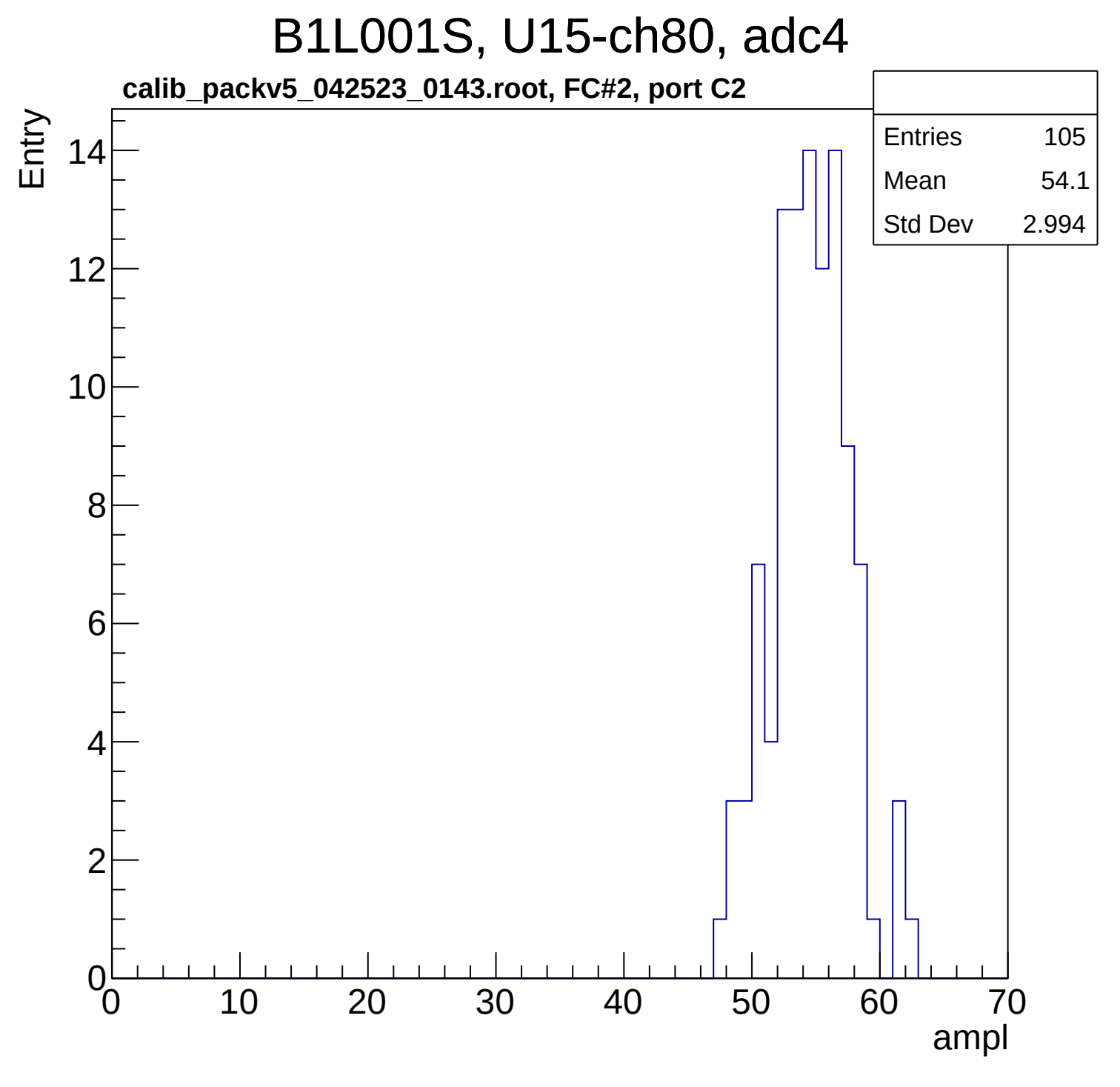
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch80, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 54.1
Std Dev 2.994
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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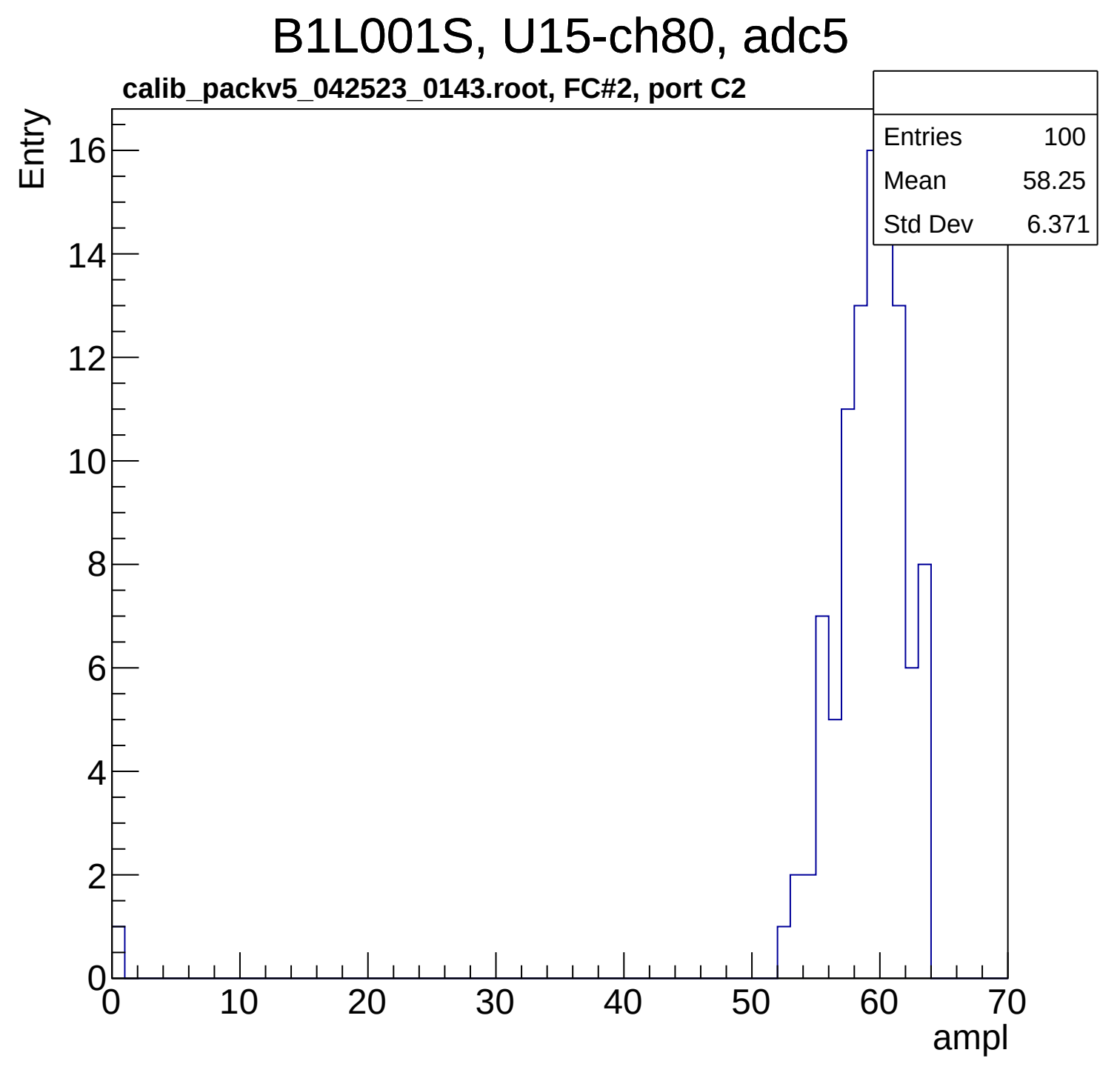
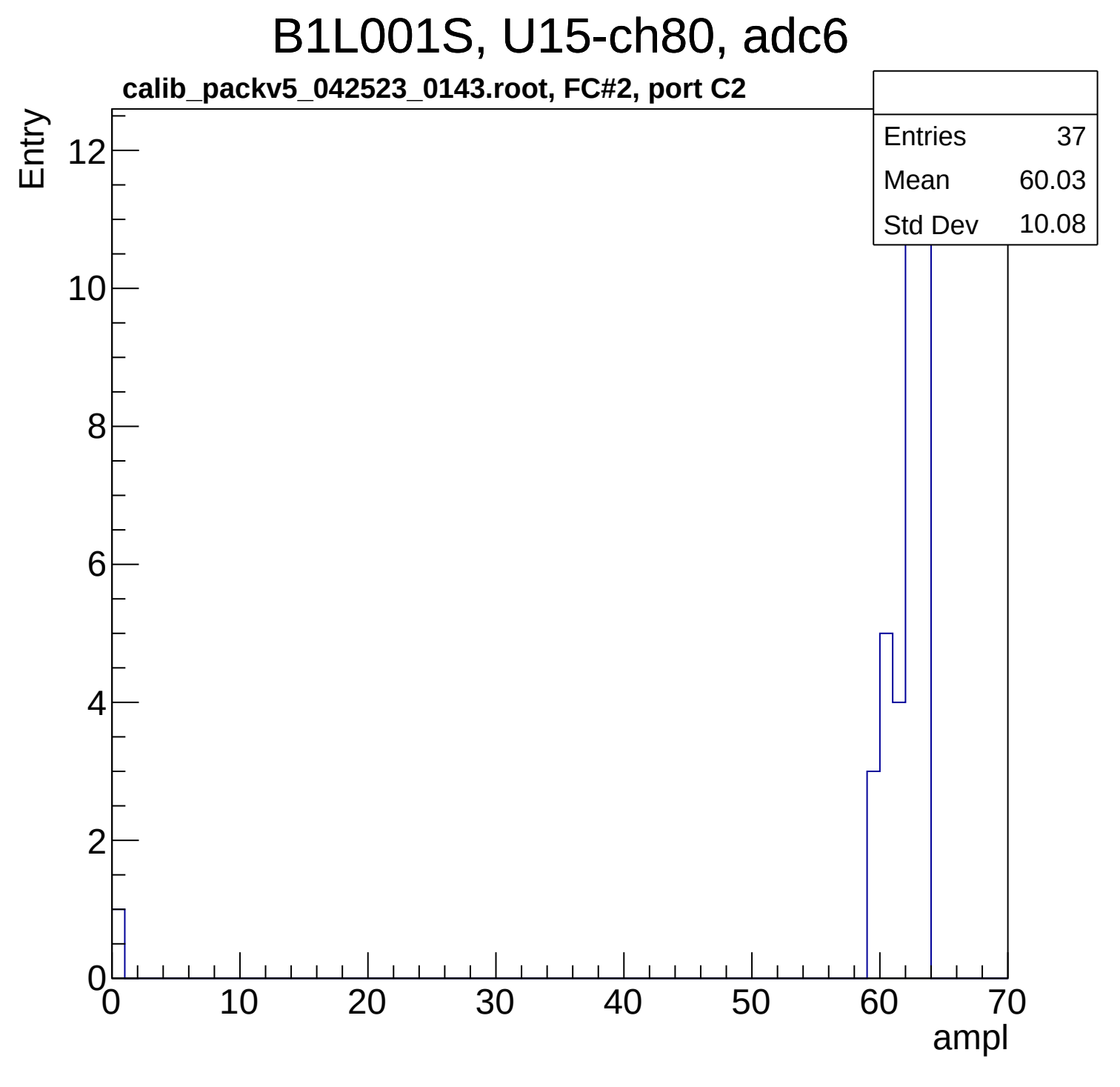
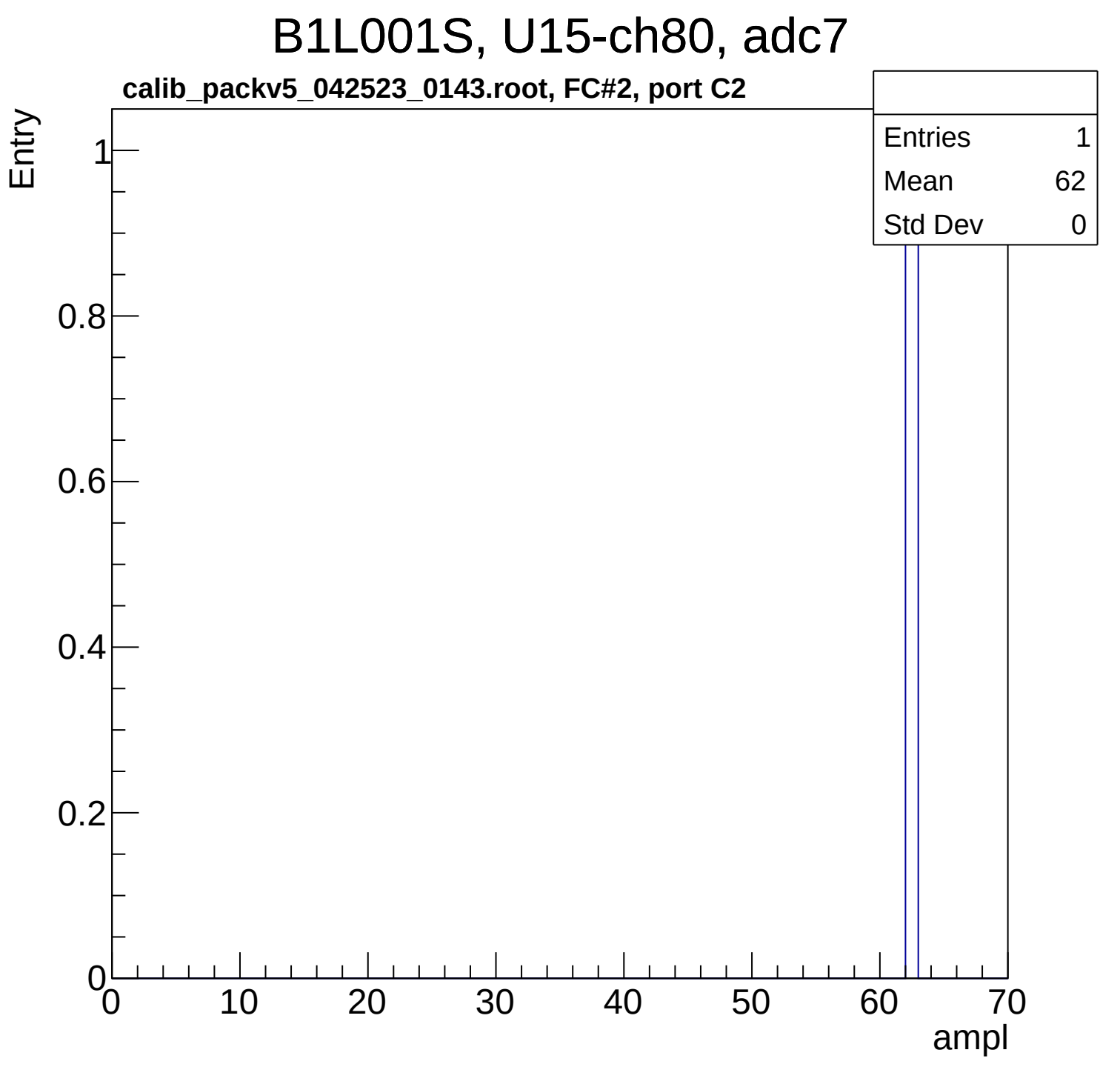
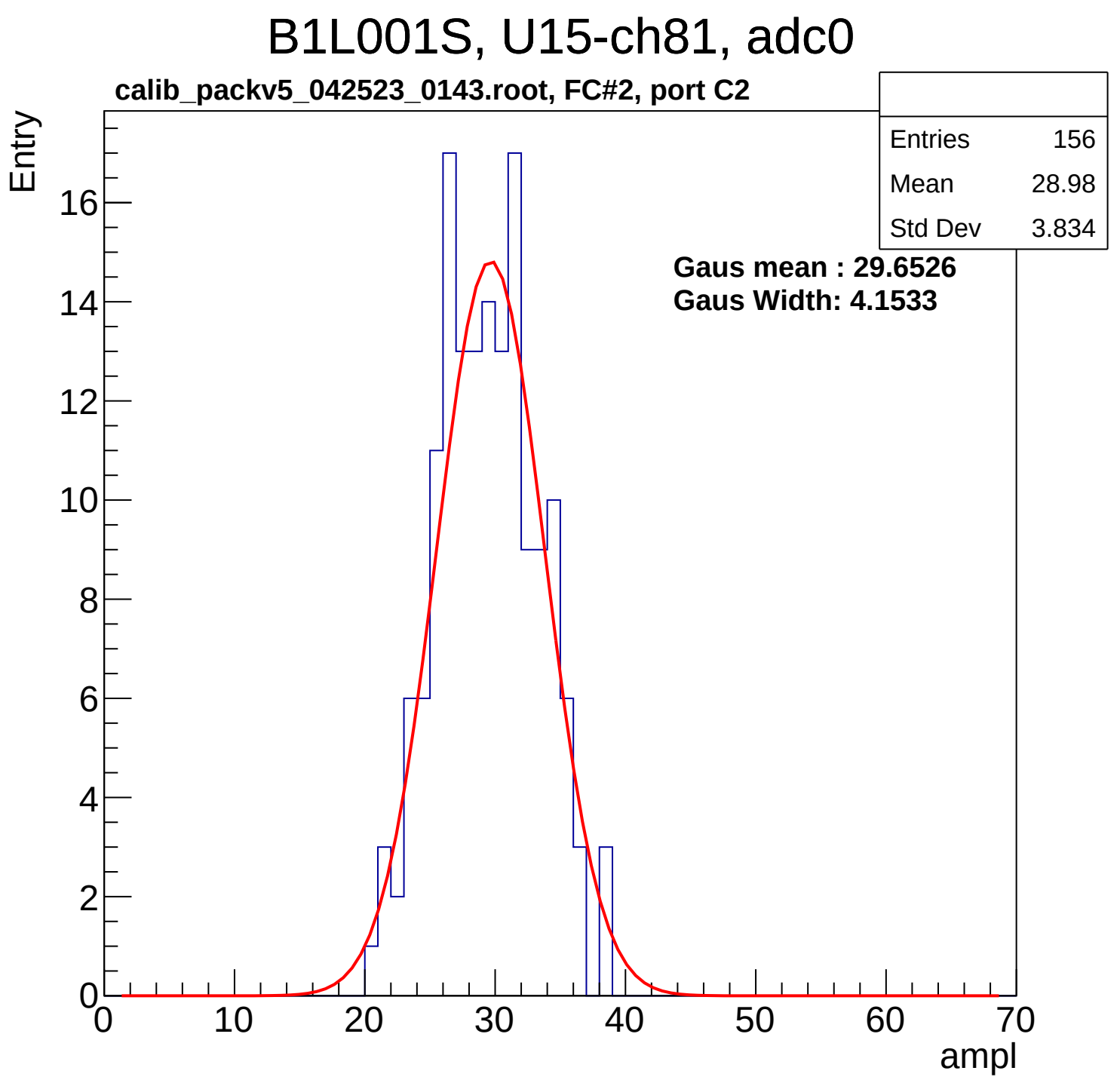
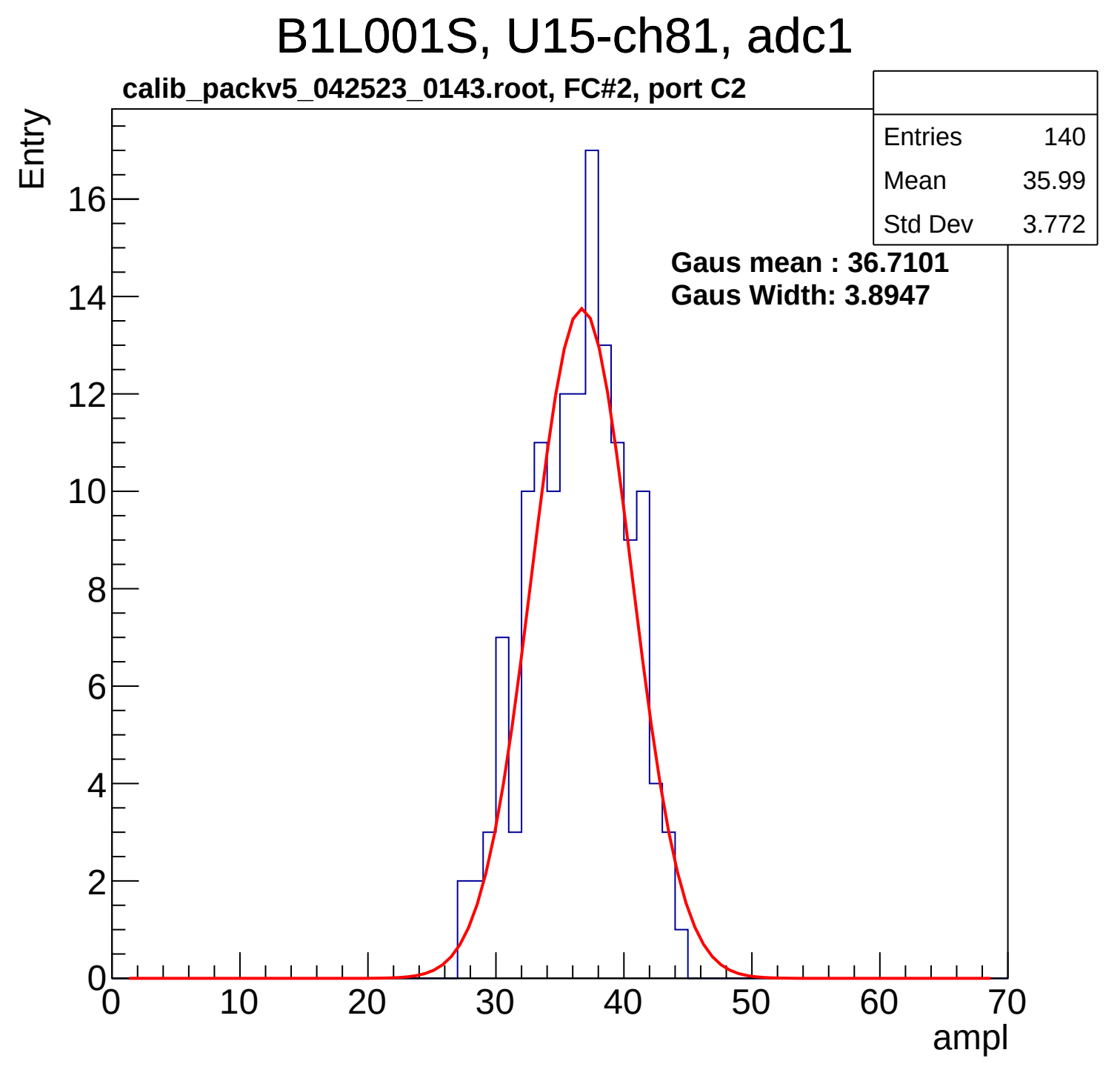

B1L001S, U15-ch80, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 58.25
Std Dev 6.371
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch80, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 37
Mean 60.03
Std Dev 10.08
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch80, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch81, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 28.98
Std Dev 3.834
Gaus mean : 29.6526
Gaus Width: 4.1533
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch81, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 35.99
Std Dev 3.772
Gaus mean : 36.7101
Gaus Width: 3.8947
Entry
ampl
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2
4
6
8
10
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14
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0
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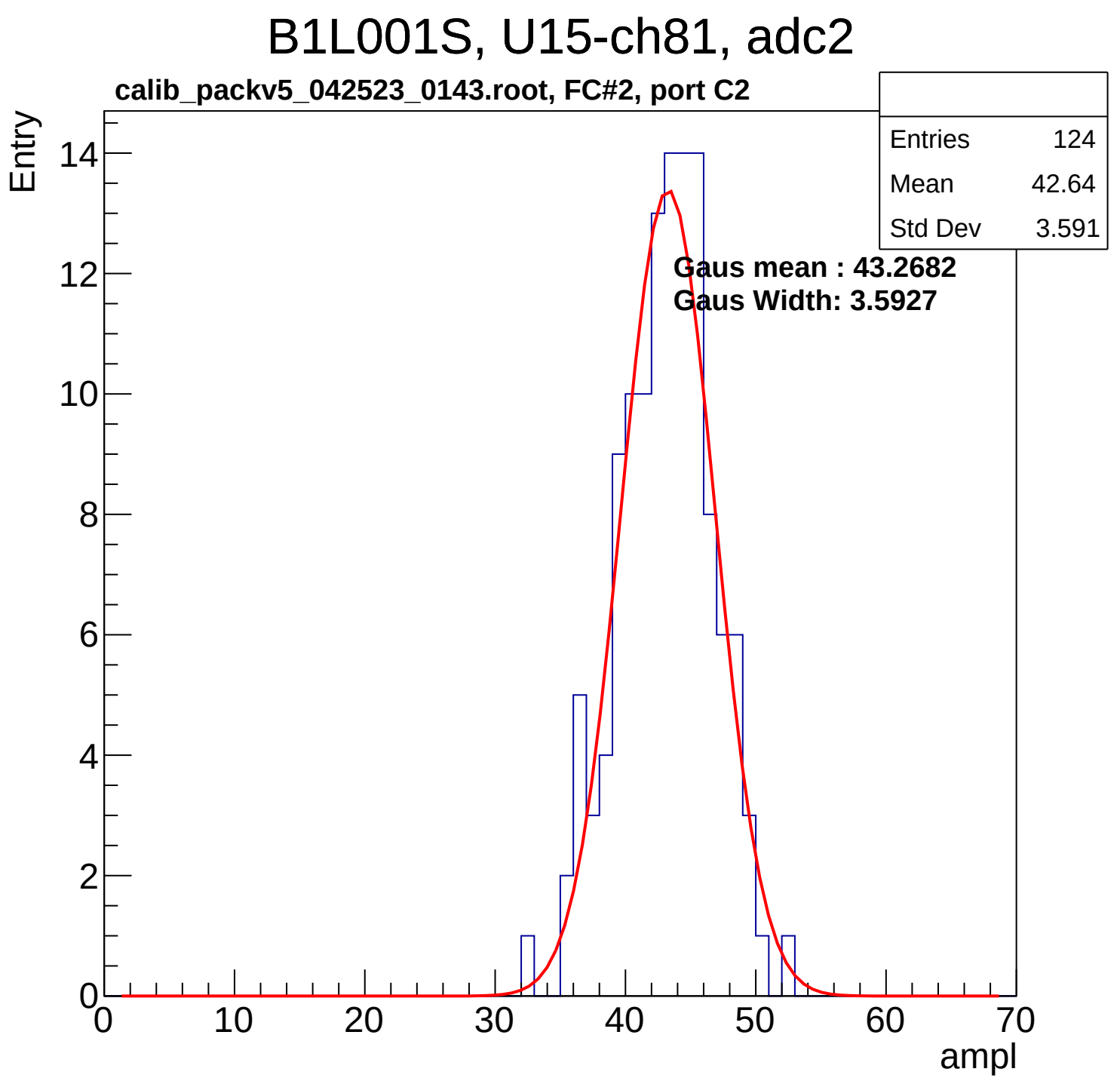
B1L001S, U15-ch81, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 42.64
Std Dev 3.591
Gaus mean : 43.2682
Gaus Width: 3.5927
Entry
ampl
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4
6
8
10
12
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0
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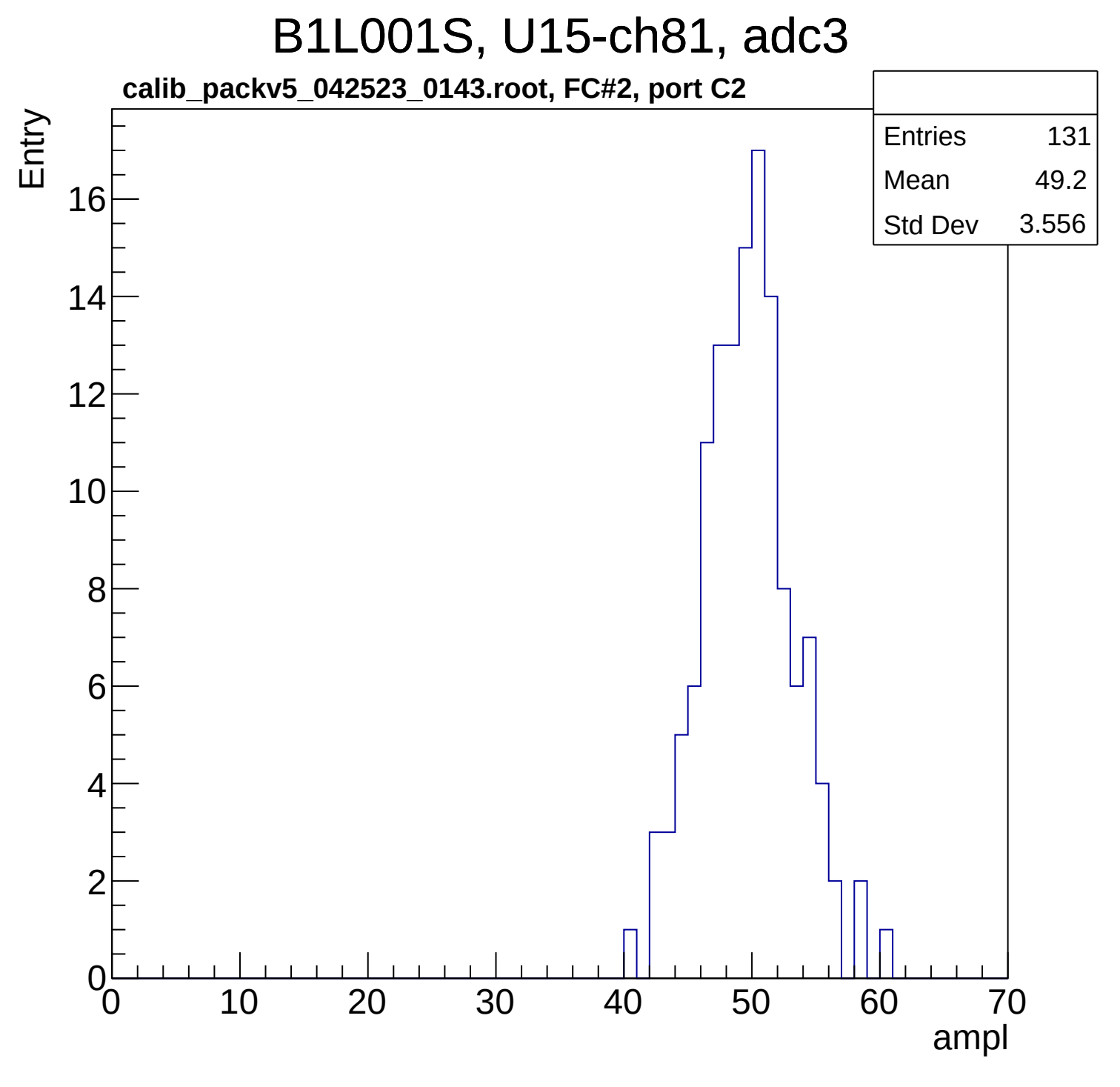
B1L001S, U15-ch81, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 49.2
Std Dev 3.556
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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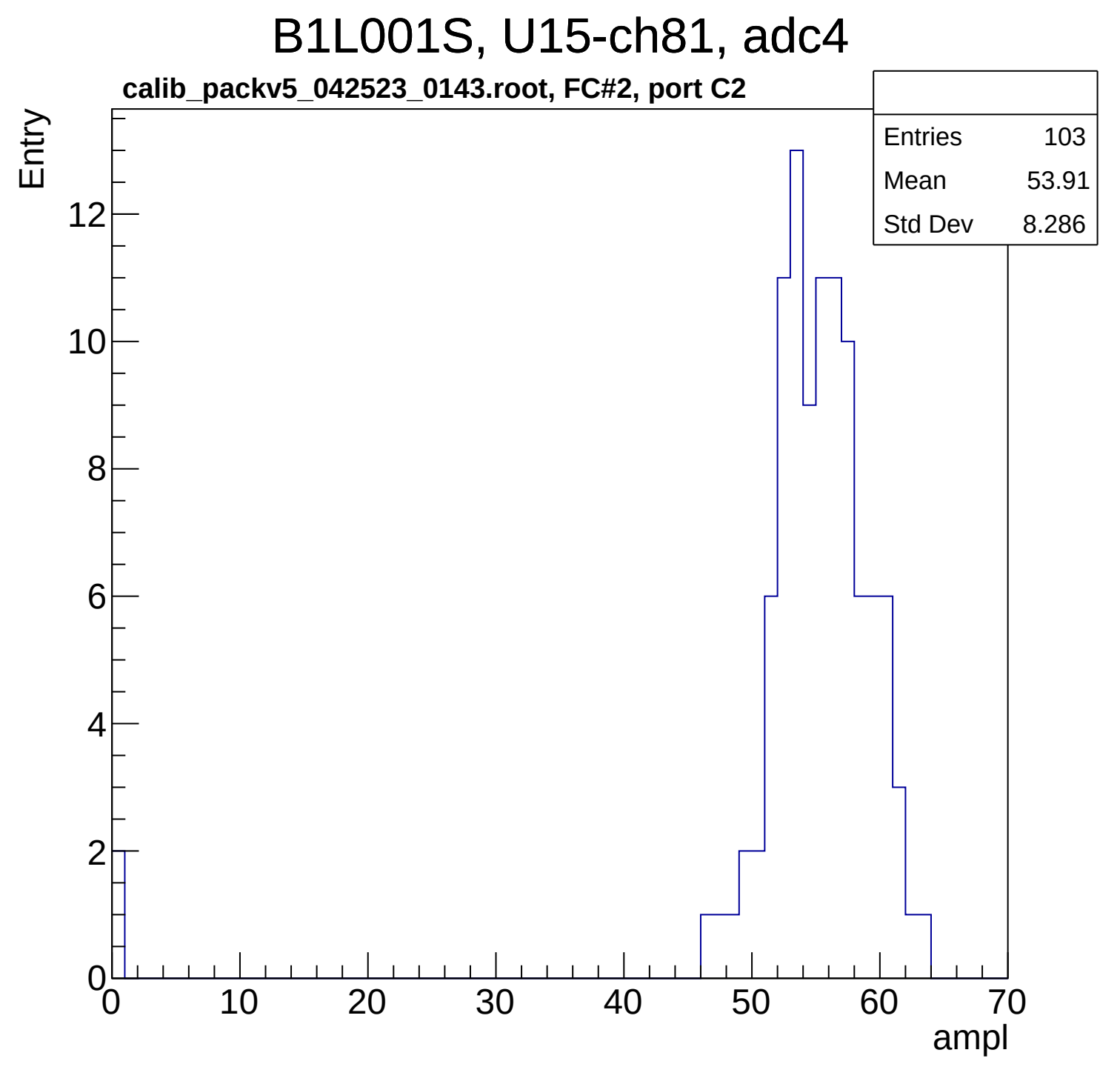
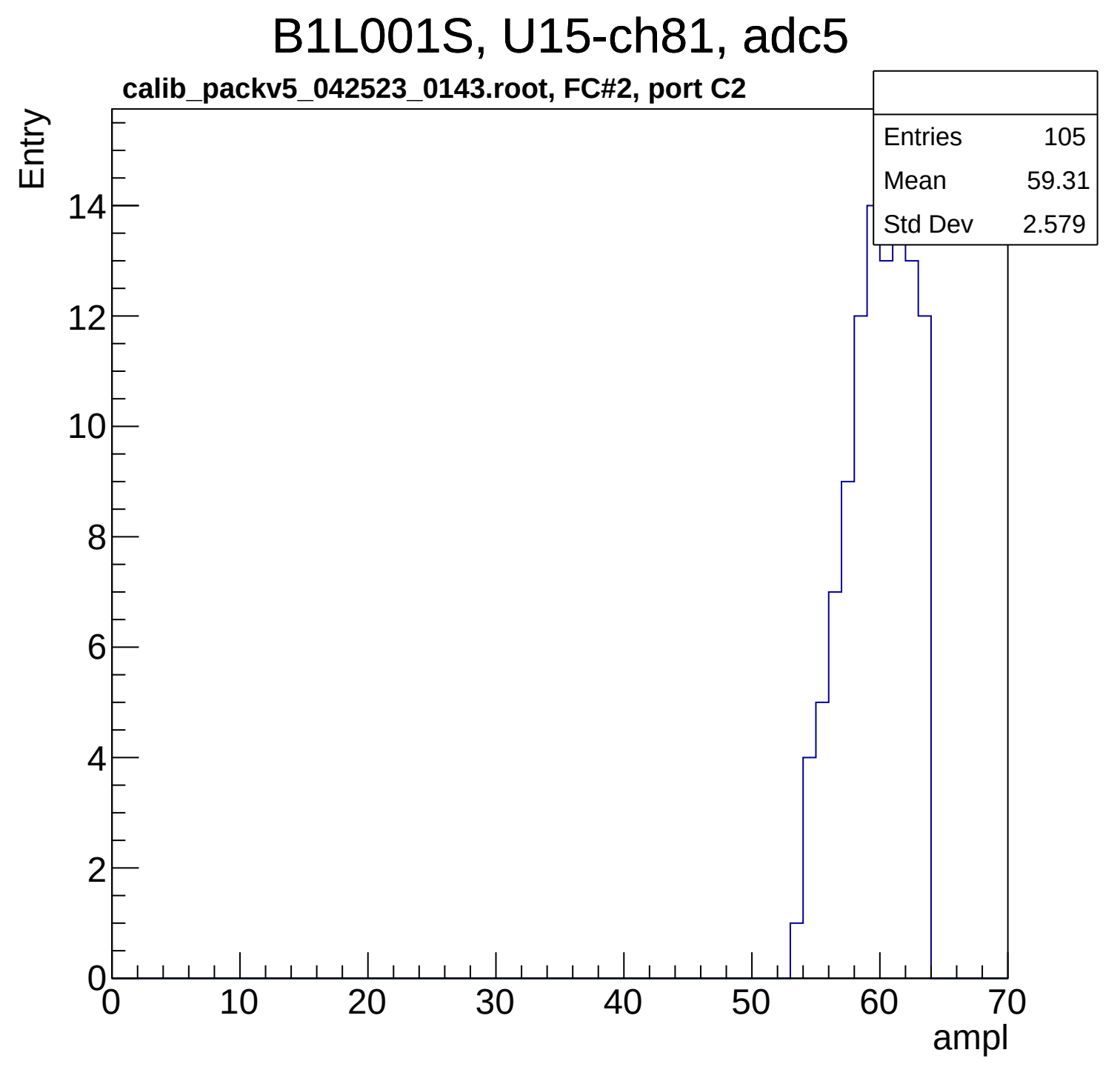

B1L001S, U15-ch81, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 53.91
Std Dev 8.286
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch81, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 59.31
Std Dev 2.579
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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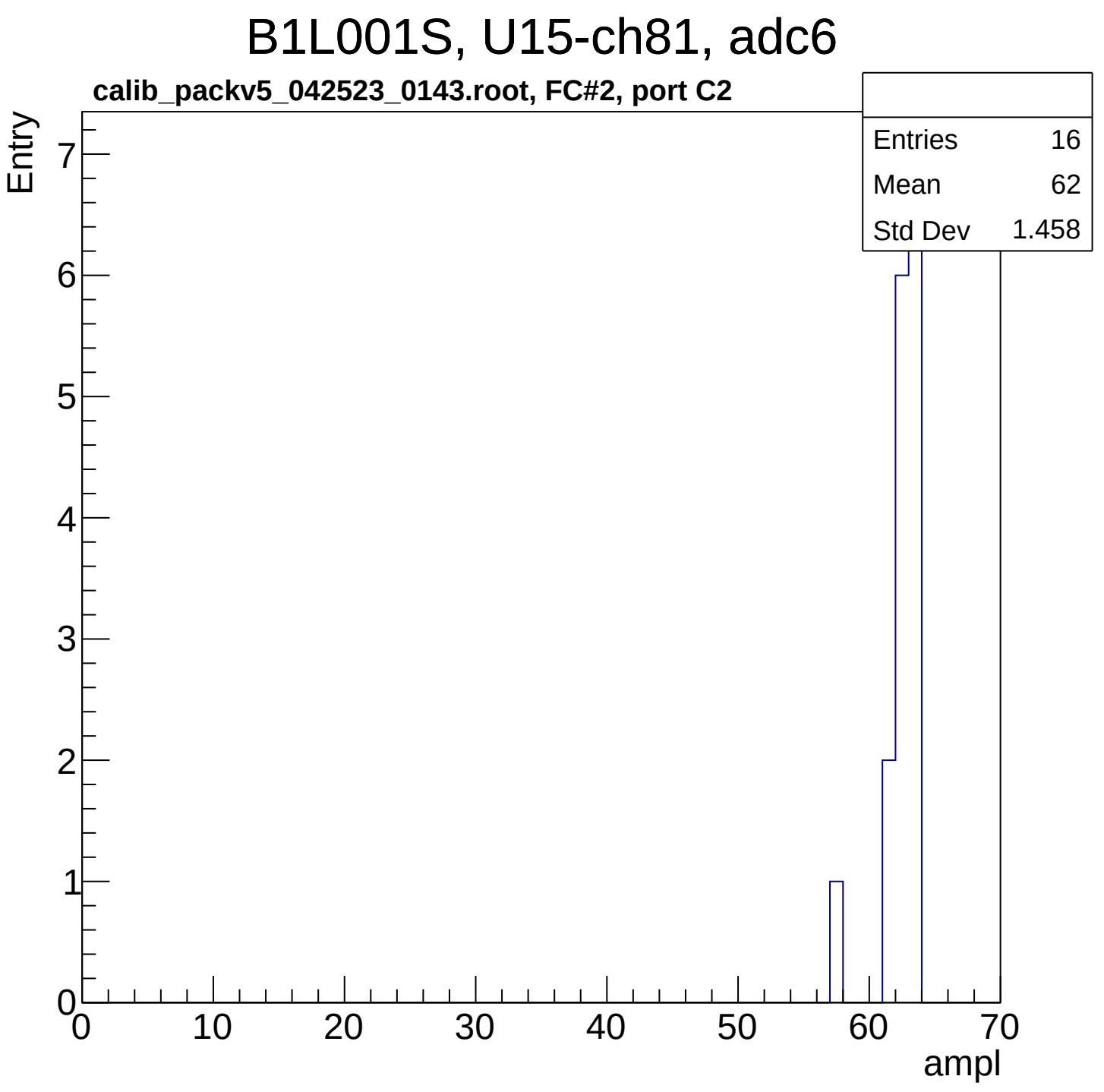
B1L001S, U15-ch81, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 62
Std Dev 1.458
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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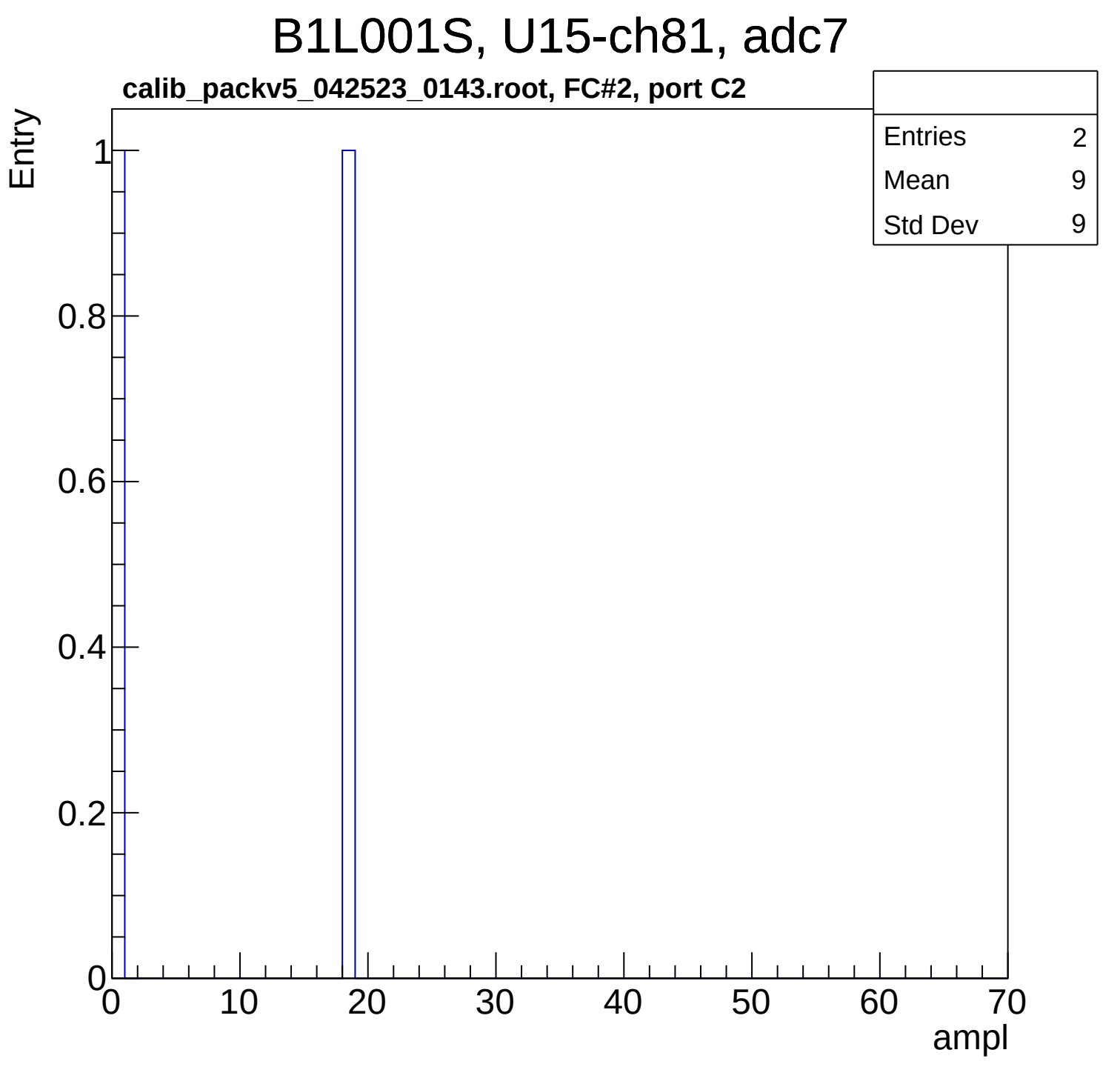
B1L001S, U15-ch81, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 9
Std Dev 9
Entry
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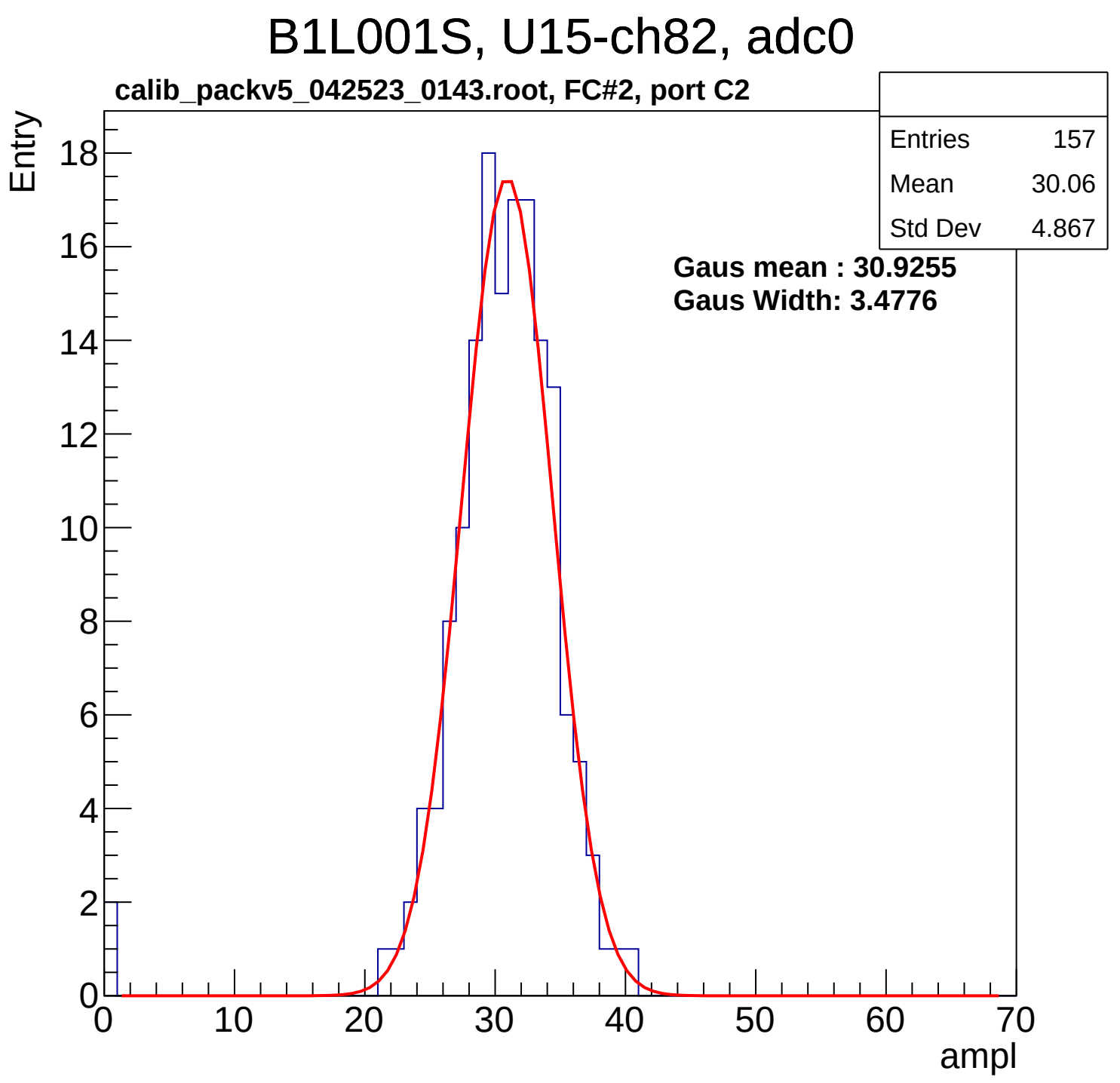
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch82, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 30.06
Std Dev 4.867
Gaus mean : 30.9255
Gaus Width: 3.4776
Entry
ampl
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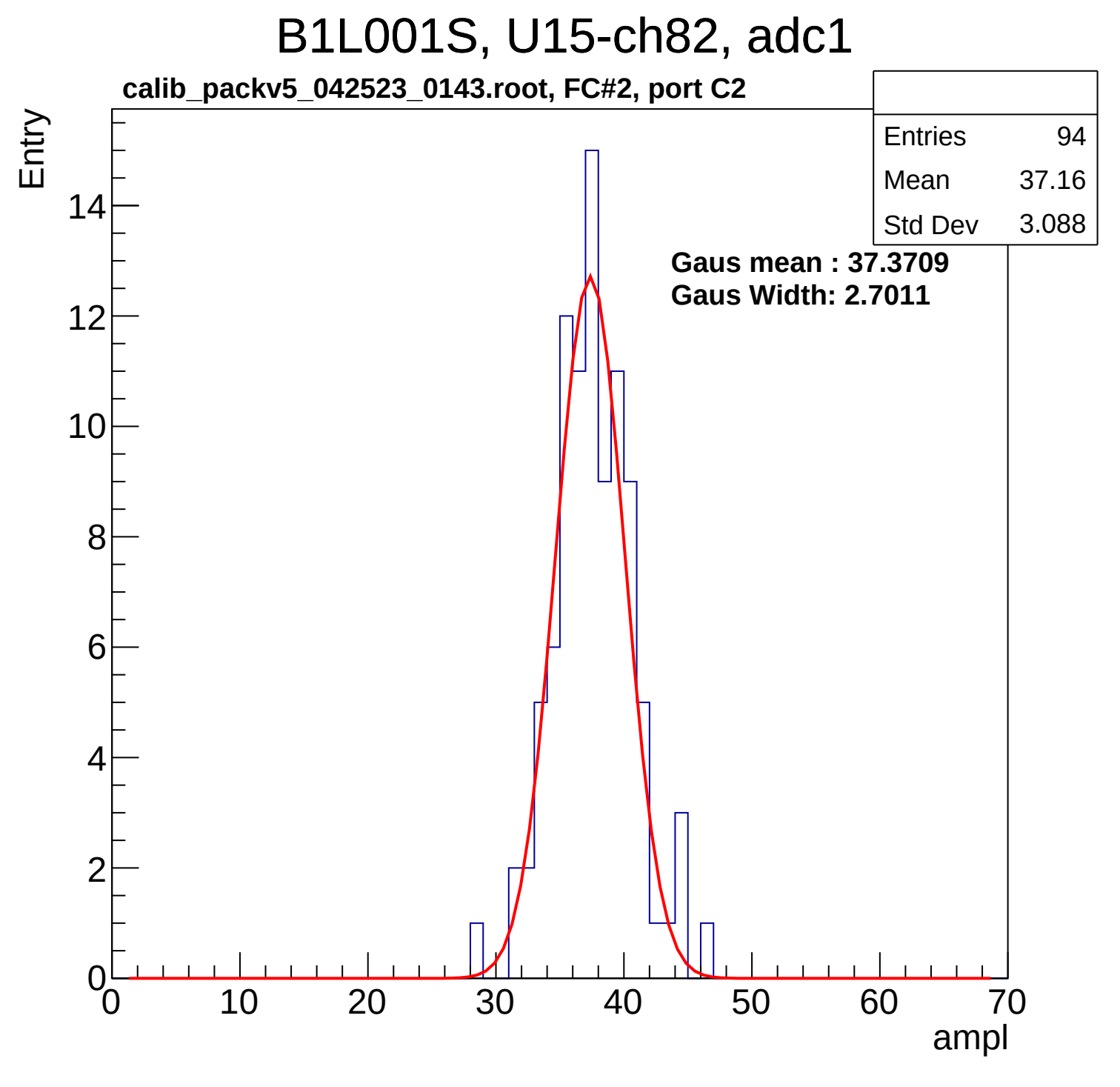
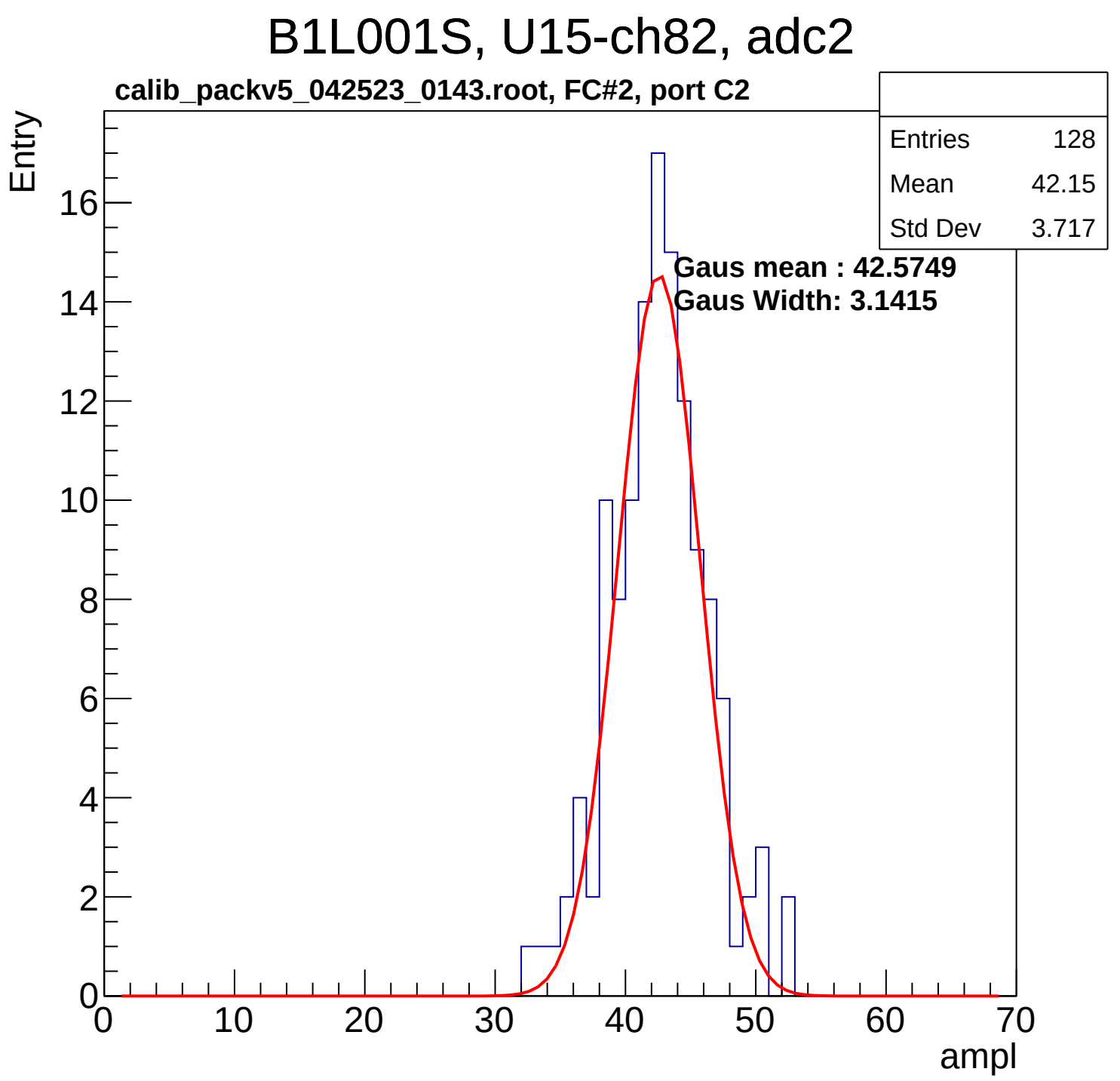
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch82, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 37.16
Std Dev 3.088
Gaus mean : 37.3709
Gaus Width: 2.7011
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch82, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 42.15
Std Dev 3.717
Gaus mean : 42.5749
Gaus Width: 3.1415
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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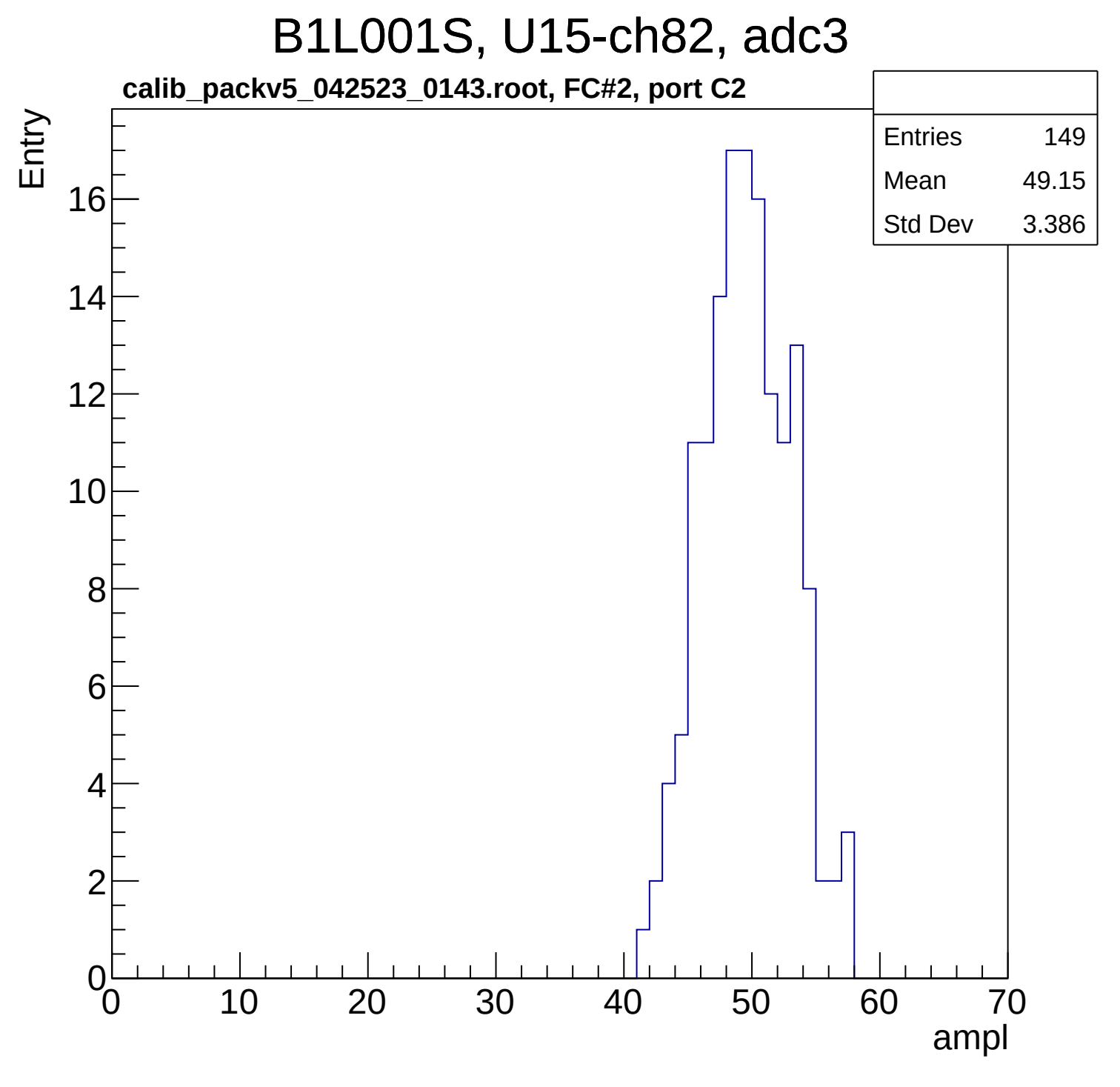
B1L001S, U15-ch82, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 49.15
Std Dev 3.386
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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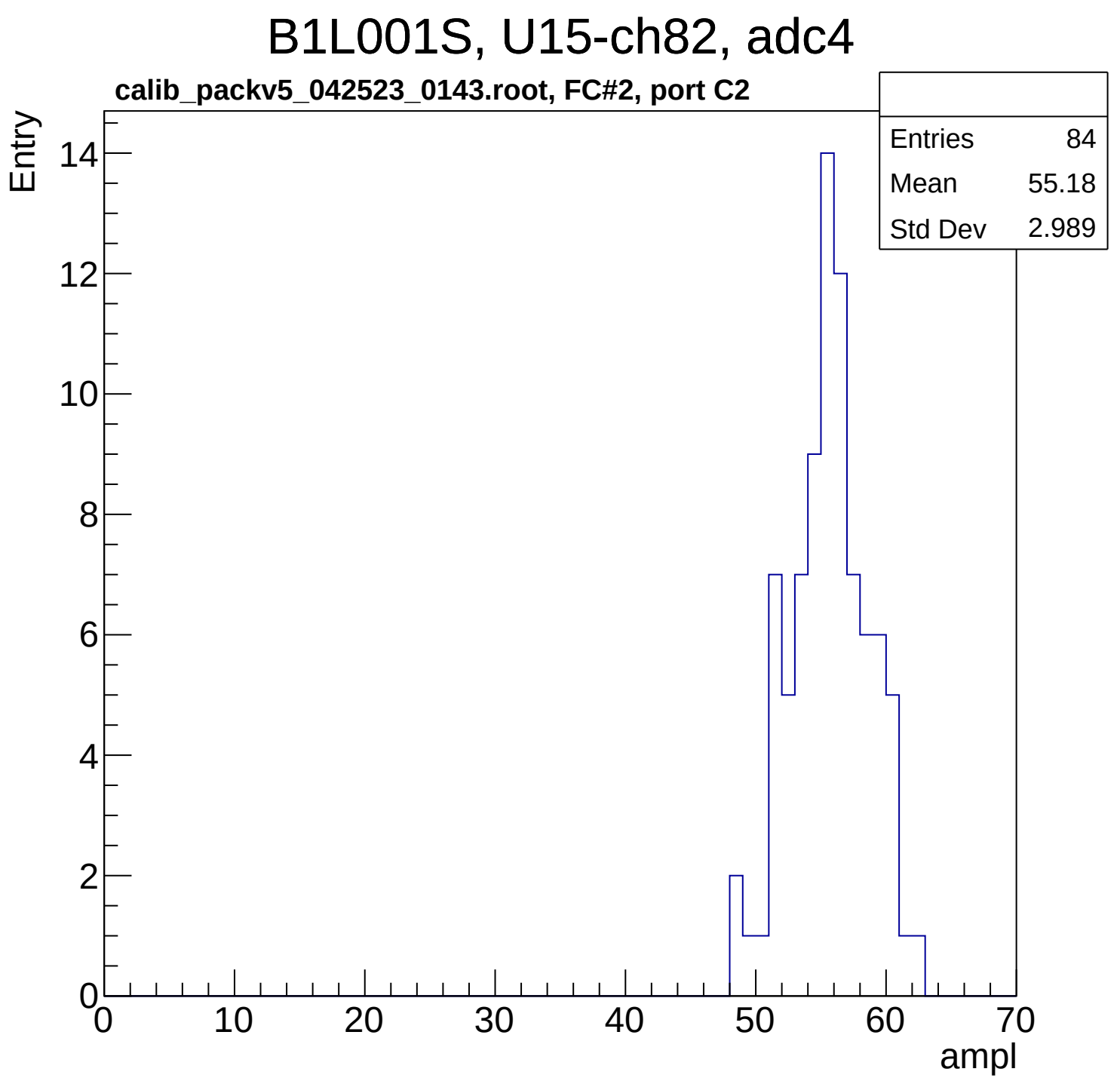
B1L001S, U15-ch82, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 55.18
Std Dev 2.989
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
50
60
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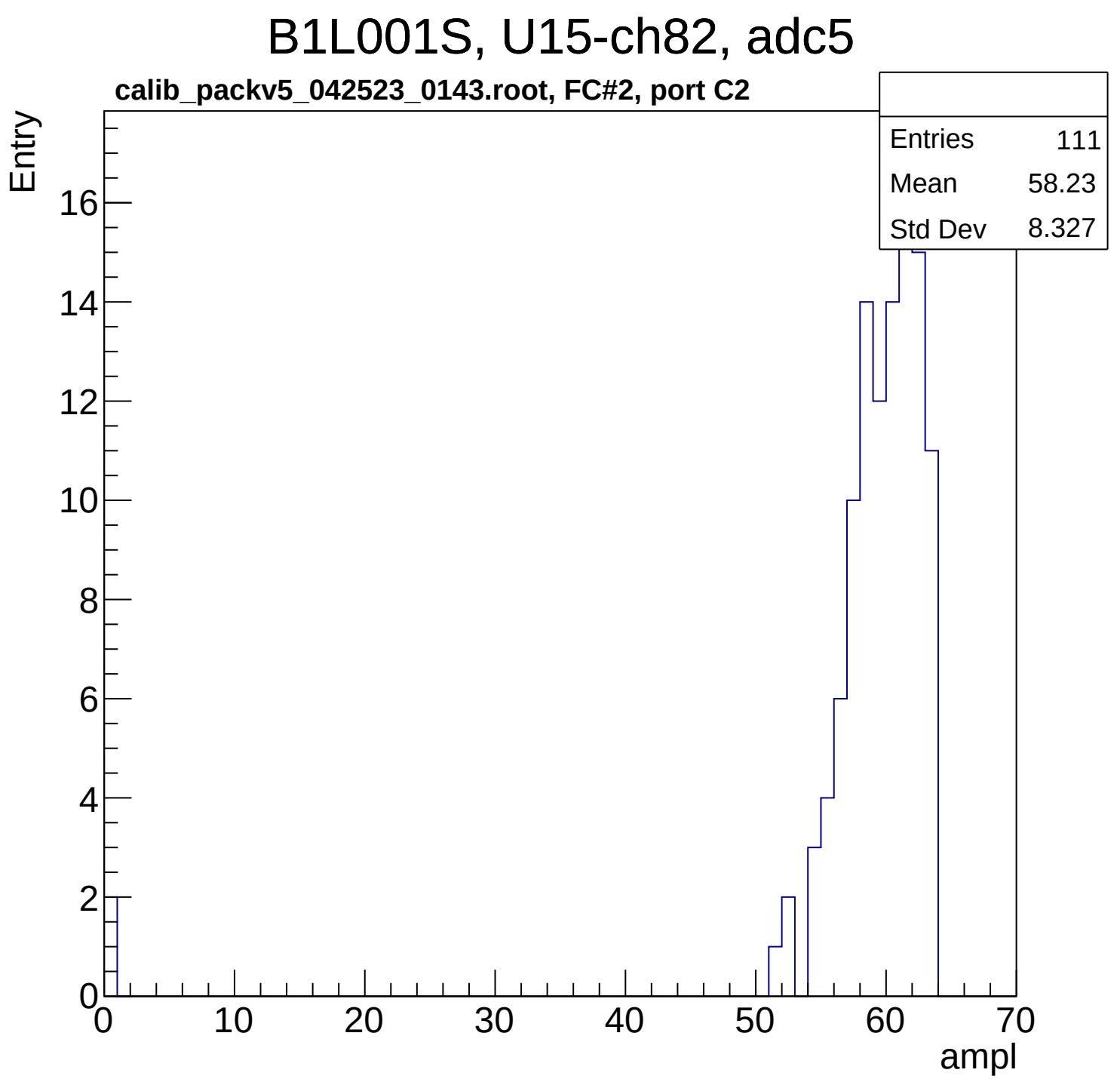
B1L001S, U15-ch82, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 58.23
Std Dev 8.327
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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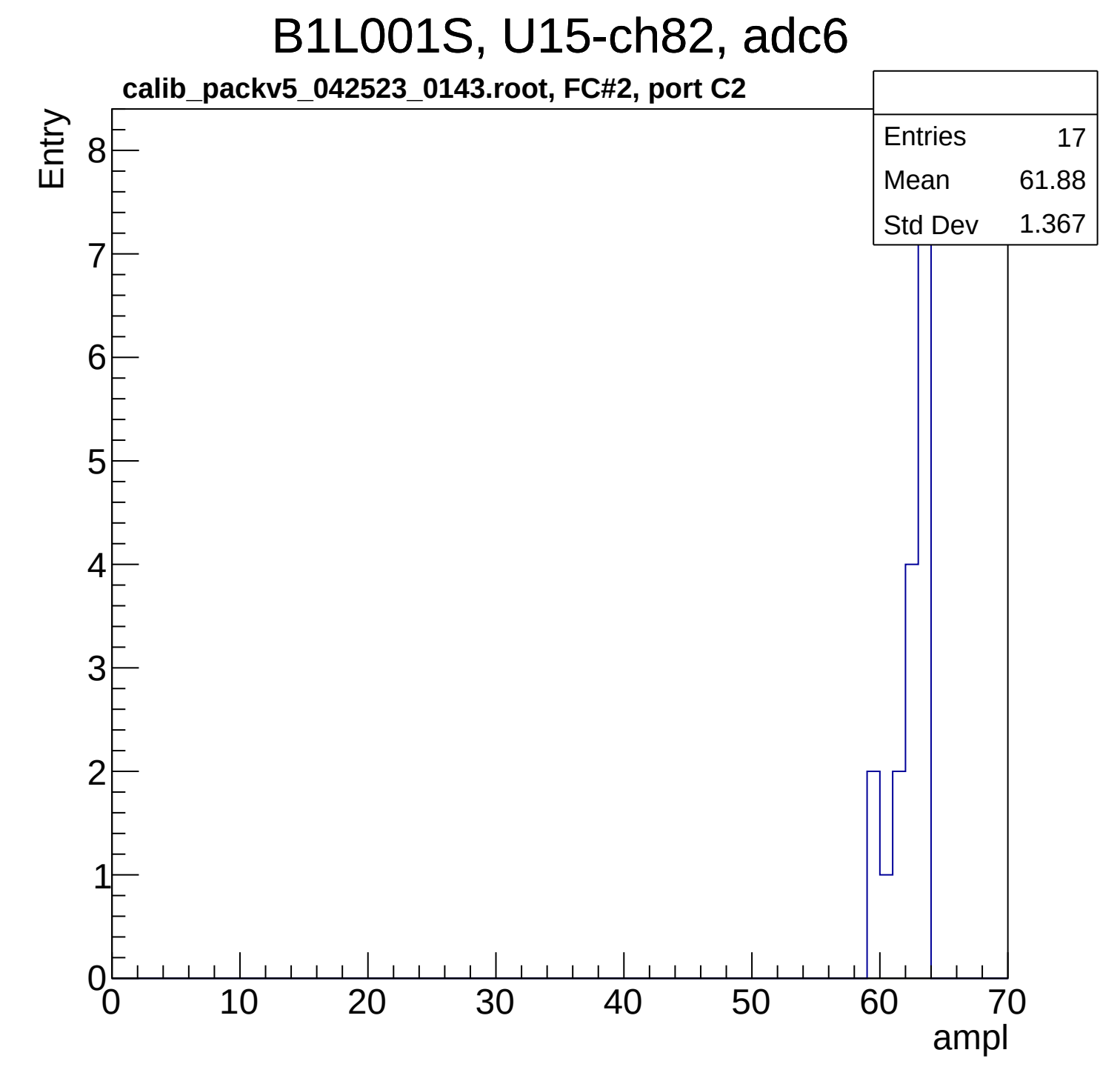
B1L001S, U15-ch82, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 17
Mean 61.88
Std Dev 1.367
Entry
ampl
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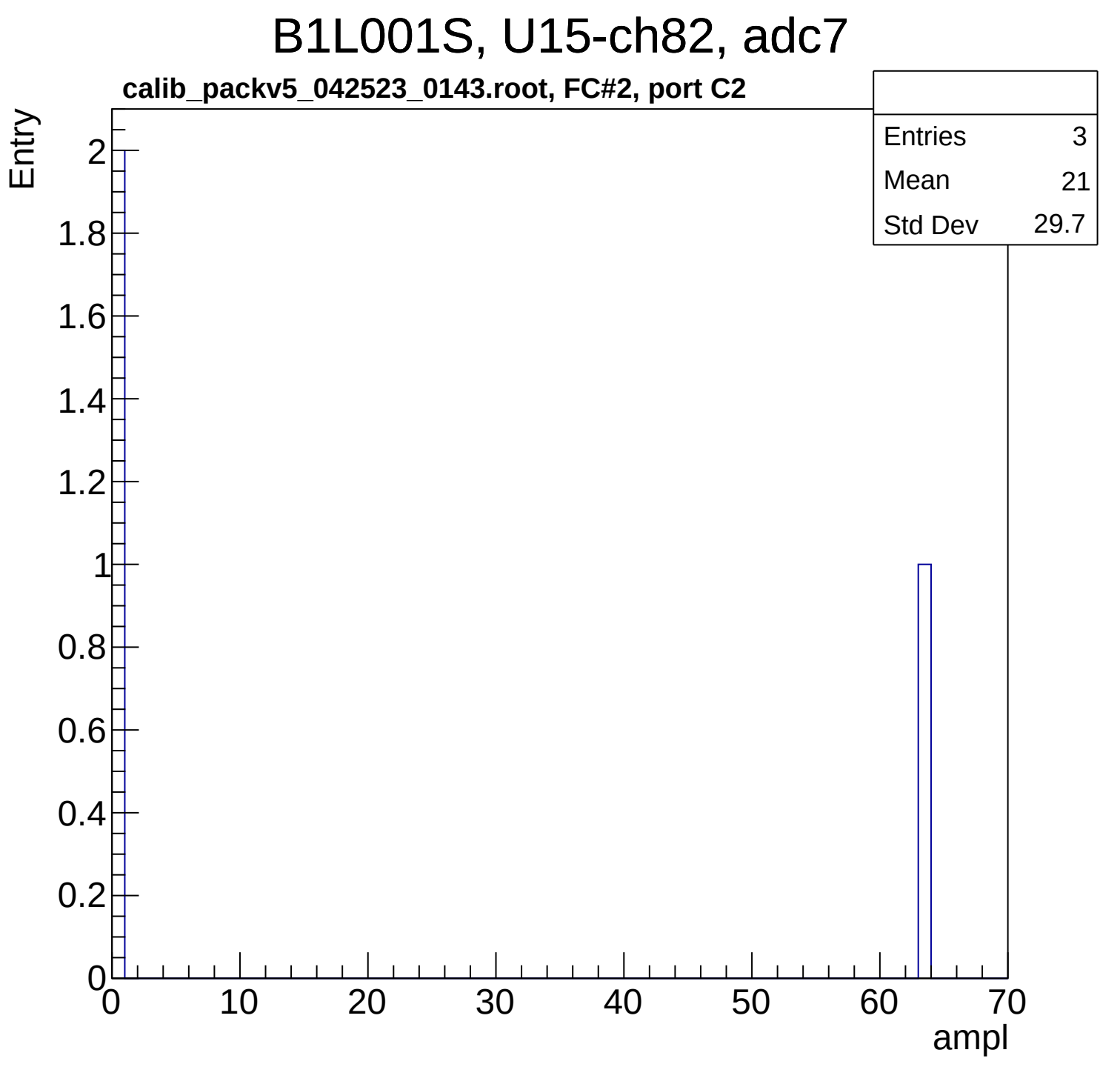
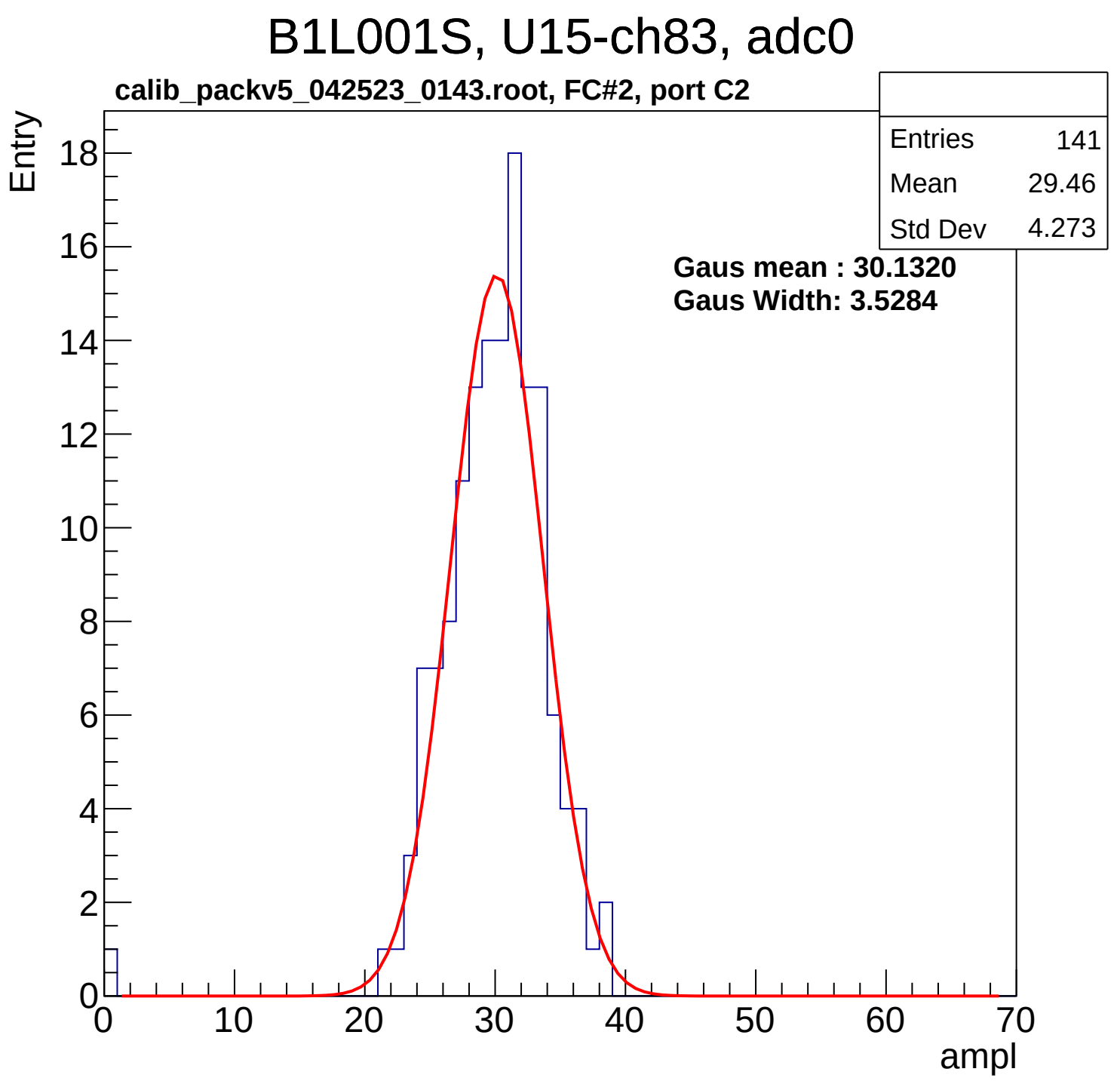
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch82, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch83, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 29.46
Std Dev 4.273
Gaus mean : 30.1320
Gaus Width: 3.5284
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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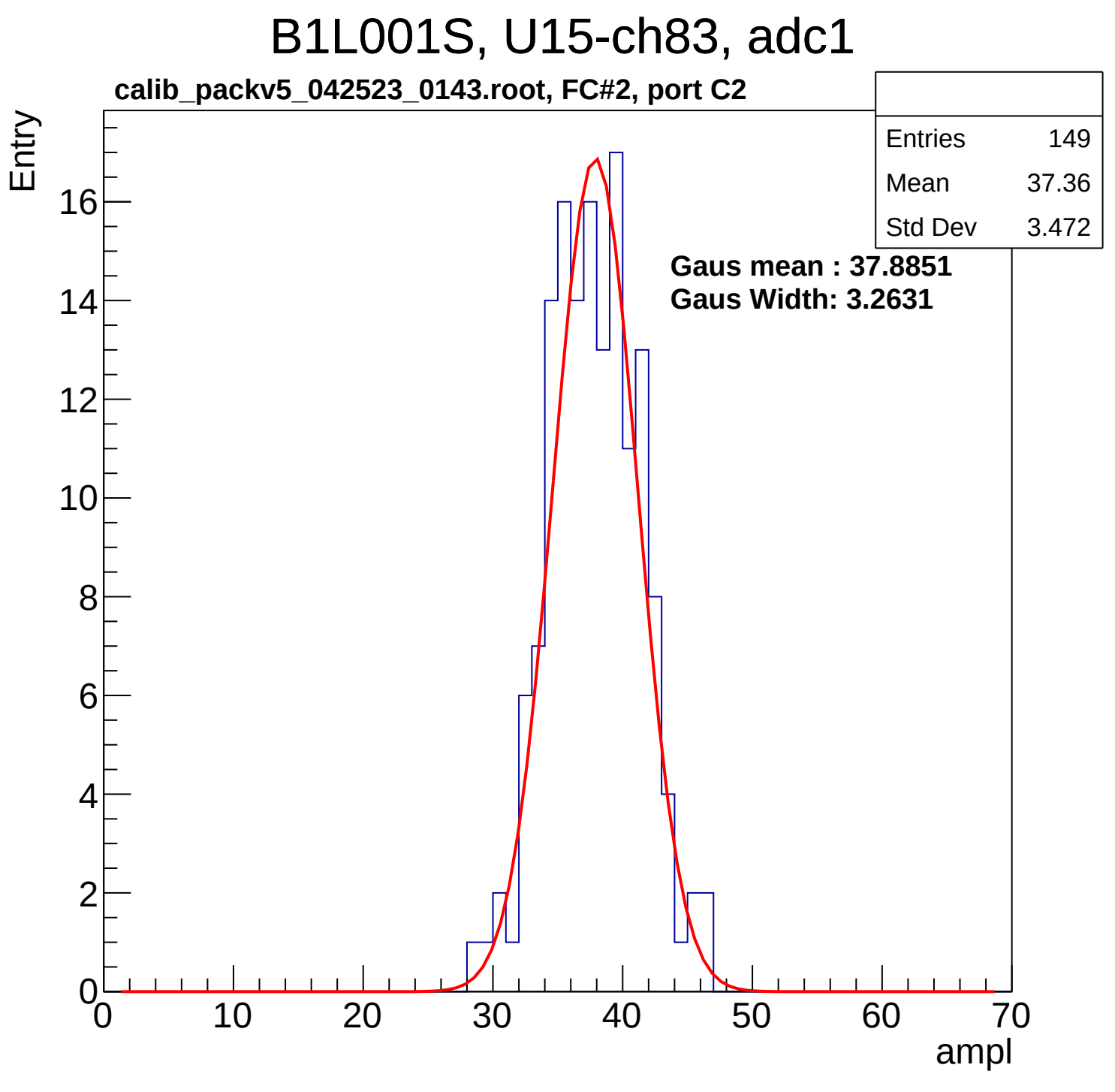
B1L001S, U15-ch83, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 37.36
Std Dev 3.472
Gaus mean : 37.8851
Gaus Width: 3.2631
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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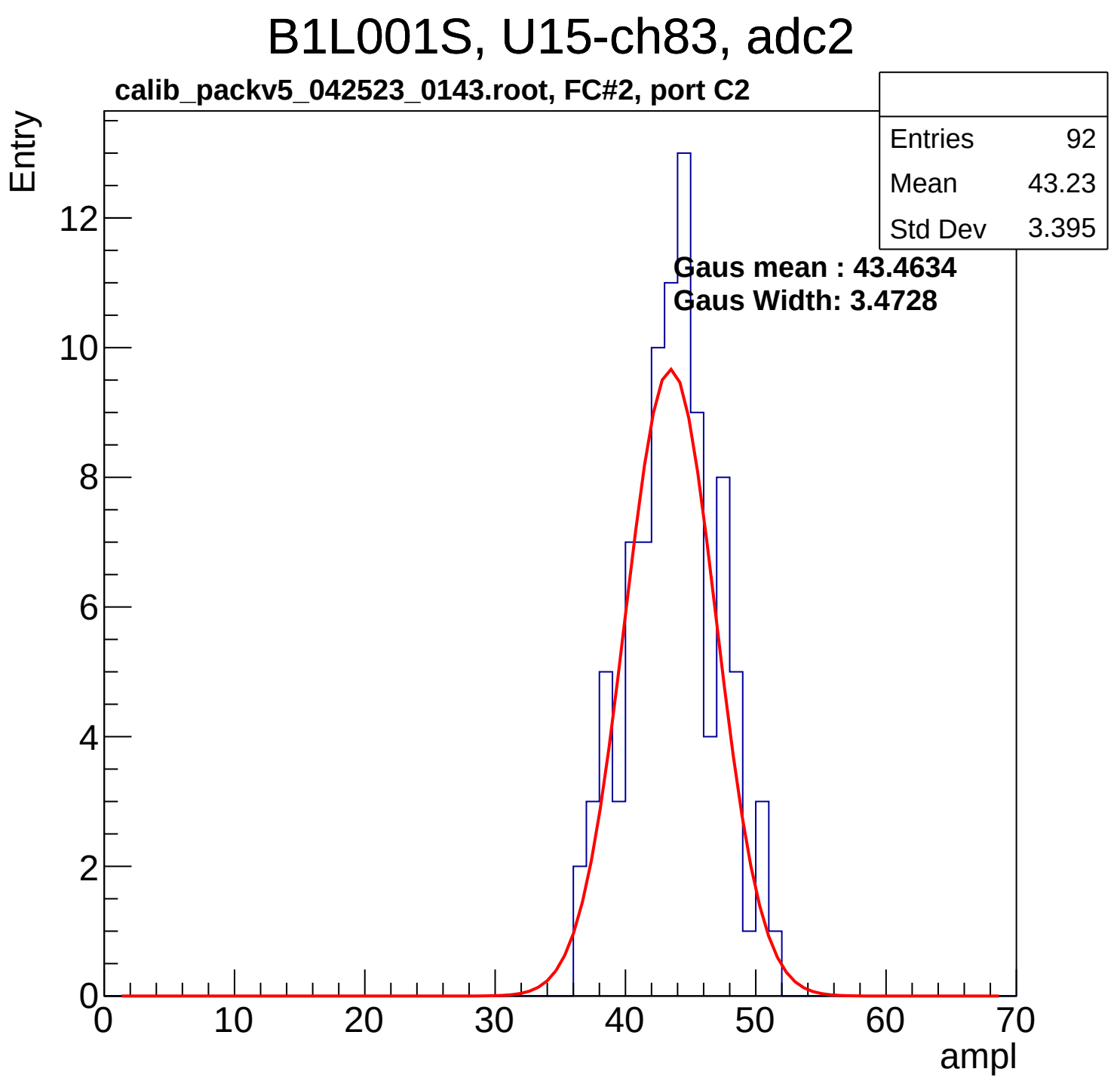
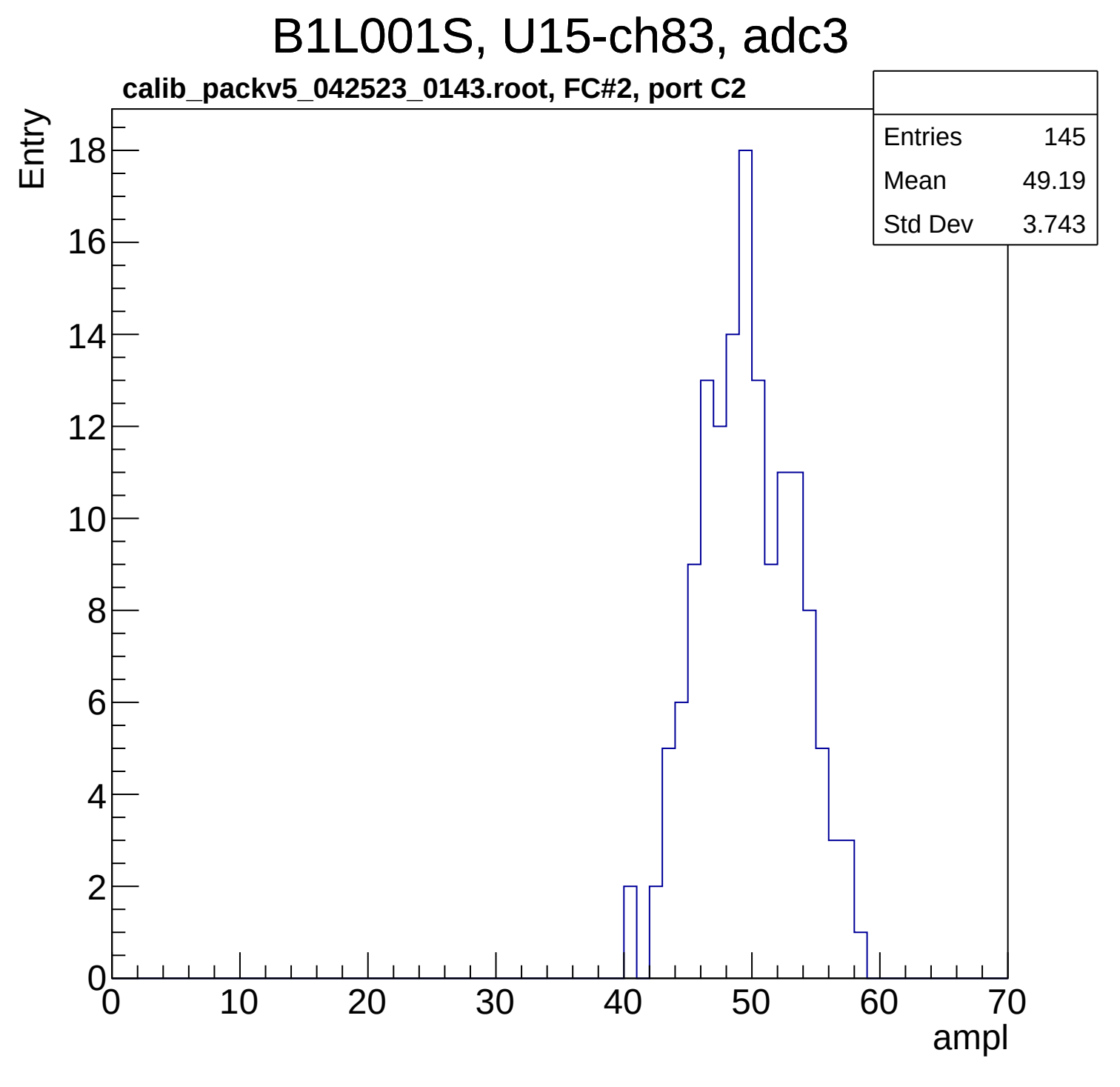

B1L001S, U15-ch83, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 92
Mean 43.23
Std Dev 3.395
Gaus mean : 43.4634
Gaus Width: 3.4728
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch83, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 49.19
Std Dev 3.743
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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60
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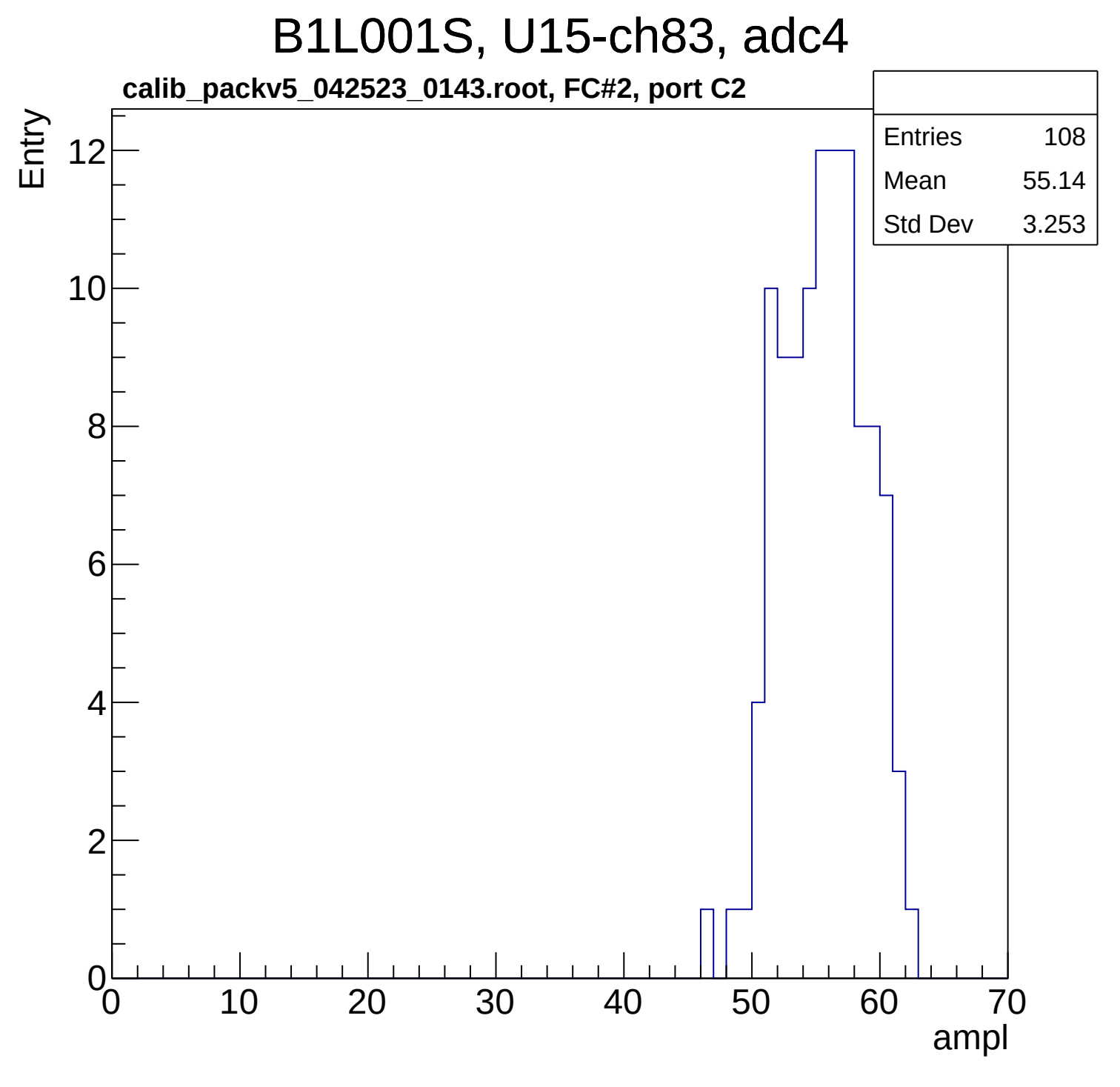
B1L001S, U15-ch83, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 55.14
Std Dev 3.253
Entry
ampl
0
2
4
6
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10
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0
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20
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40
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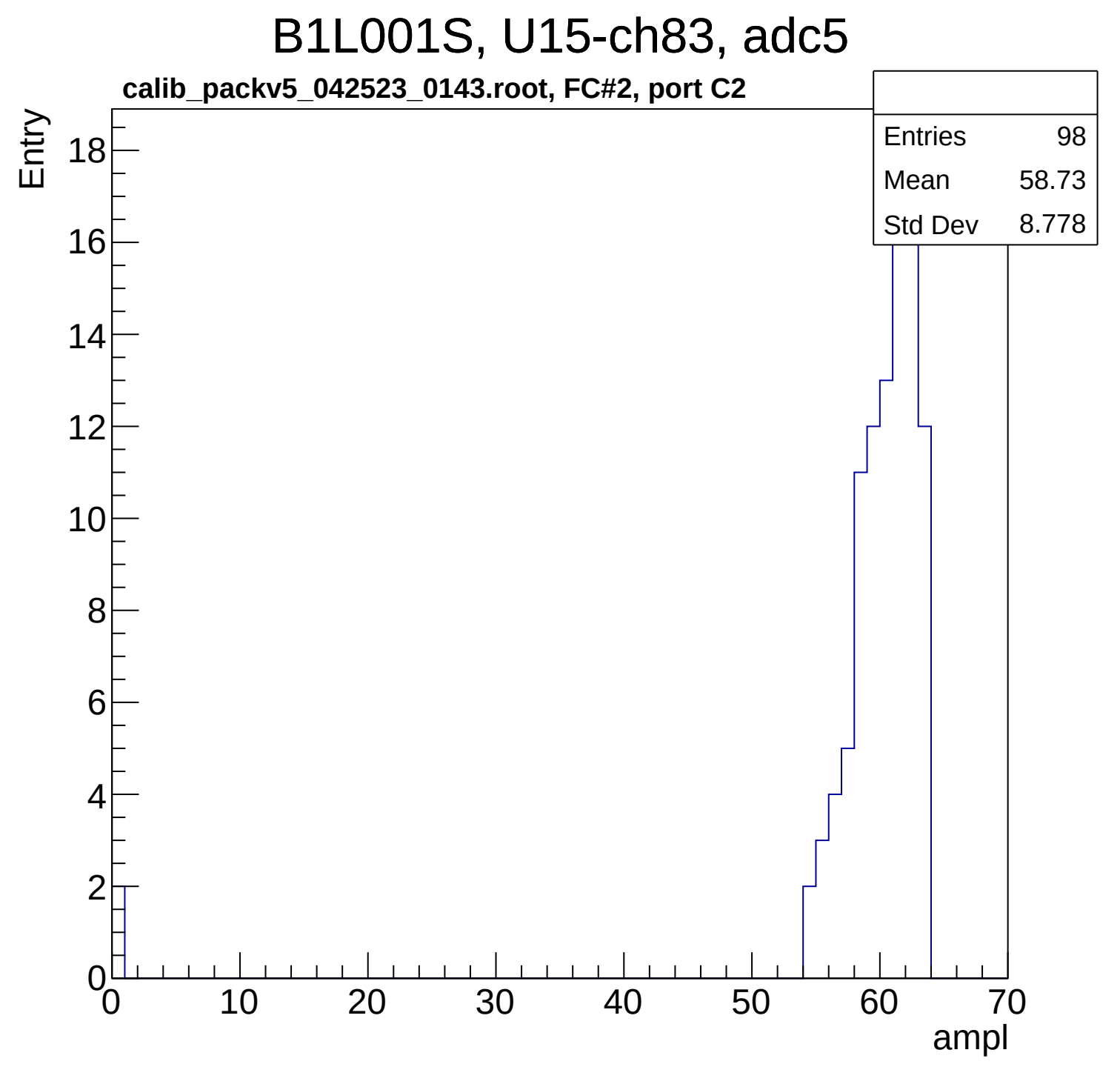
B1L001S, U15-ch83, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 58.73
Std Dev 8.778
Entry
ampl
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6
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14
16
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0
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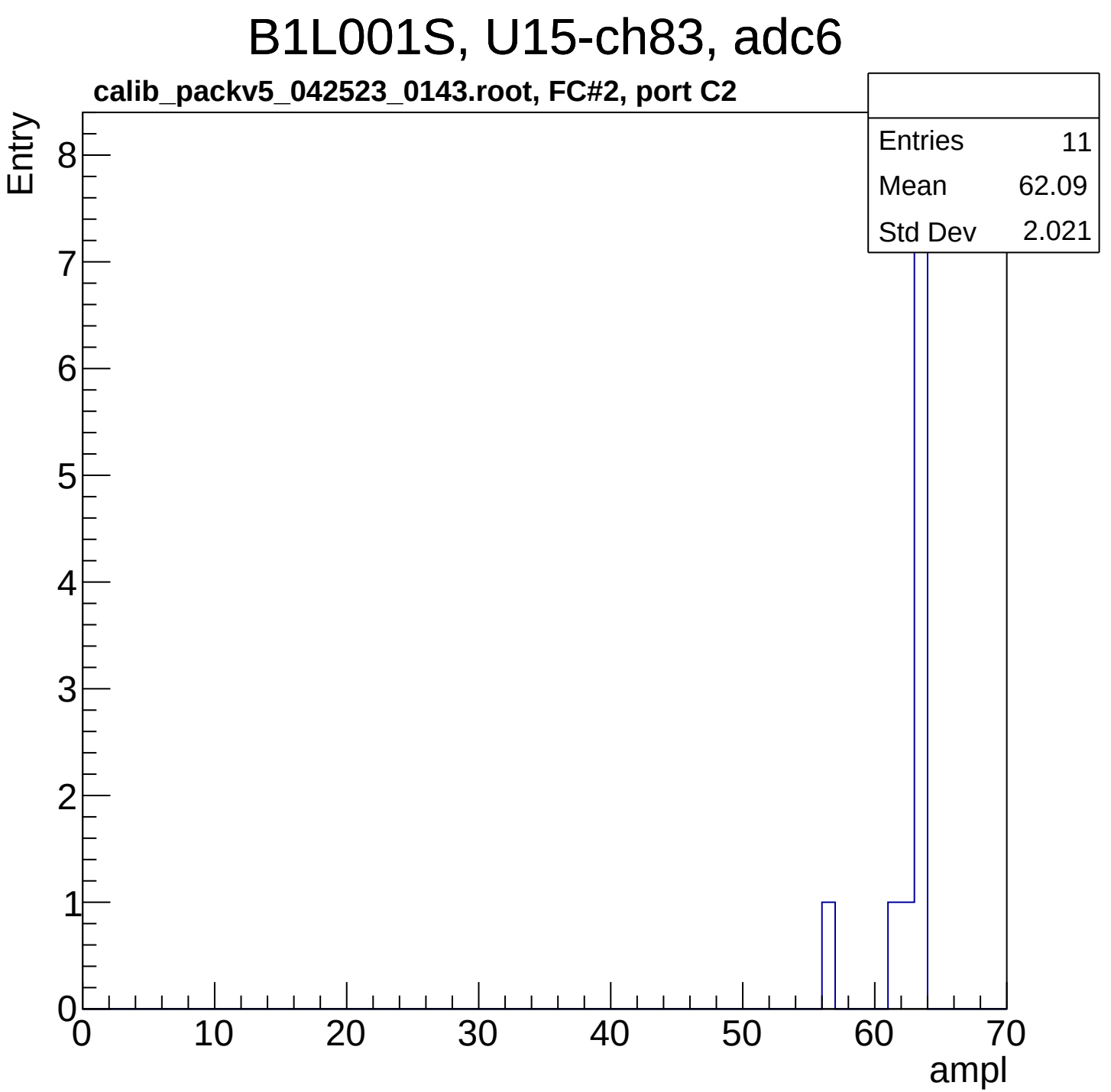
B1L001S, U15-ch83, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 62.09
Std Dev 2.021
Entry
ampl
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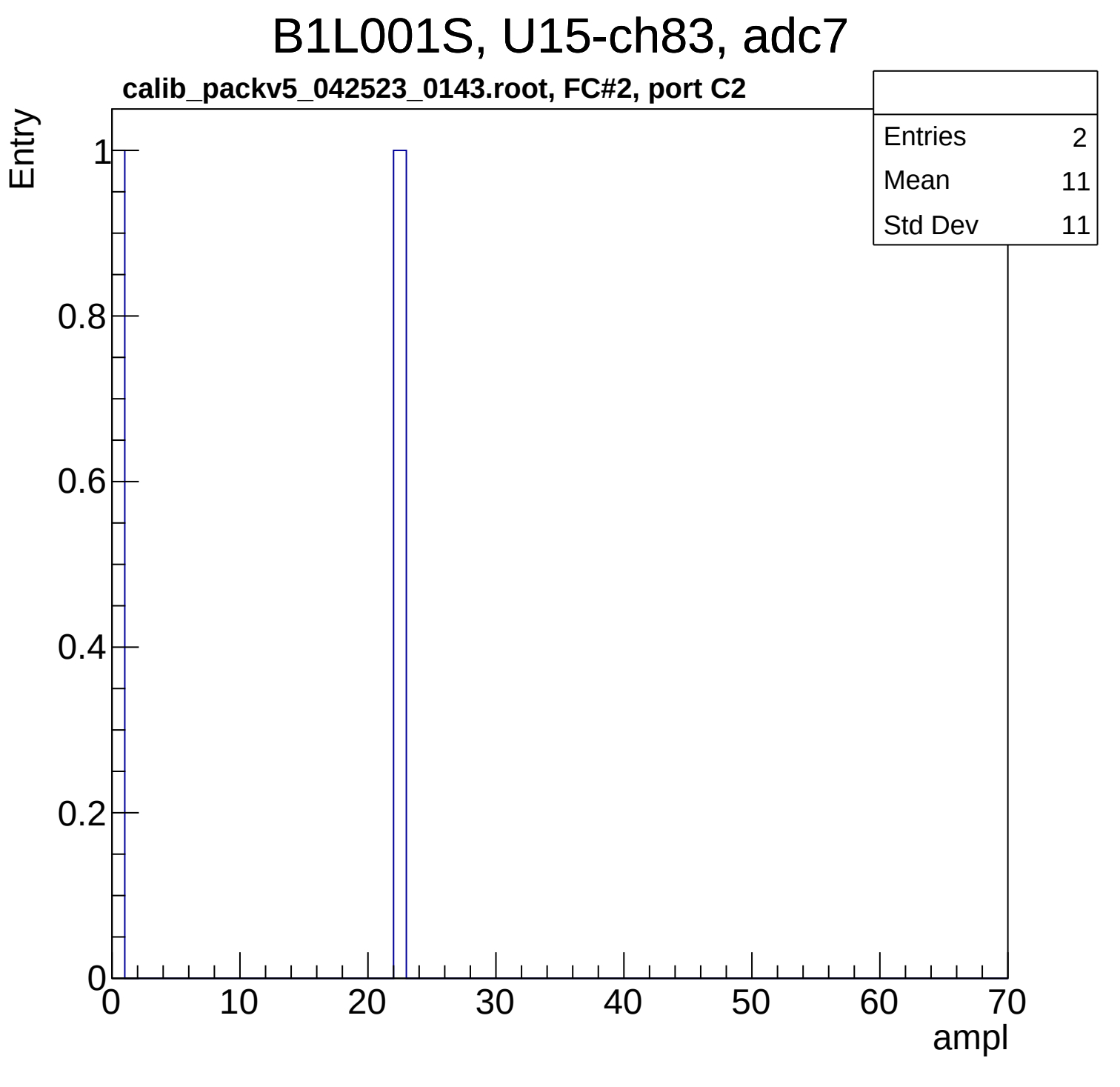
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch83, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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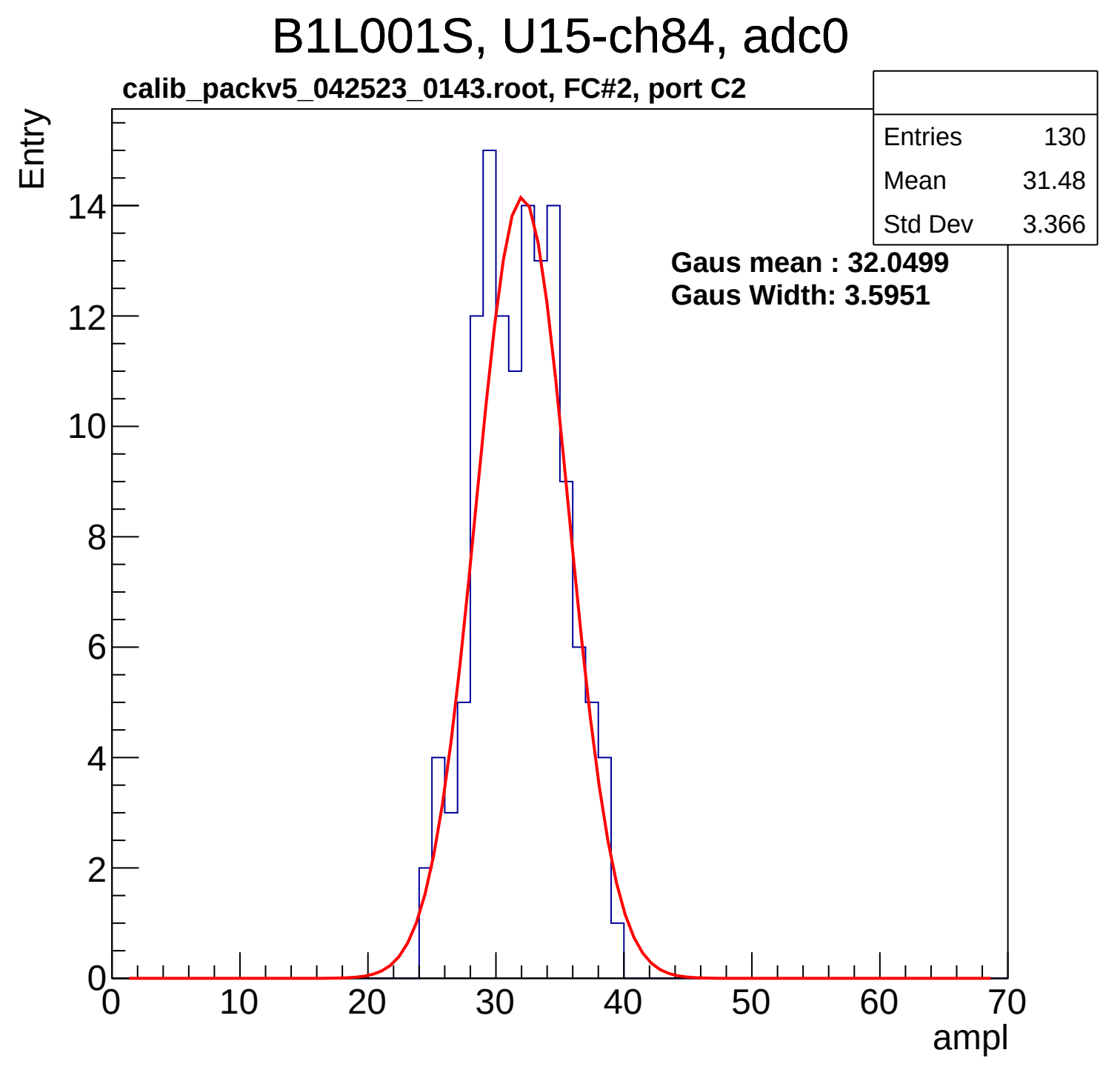
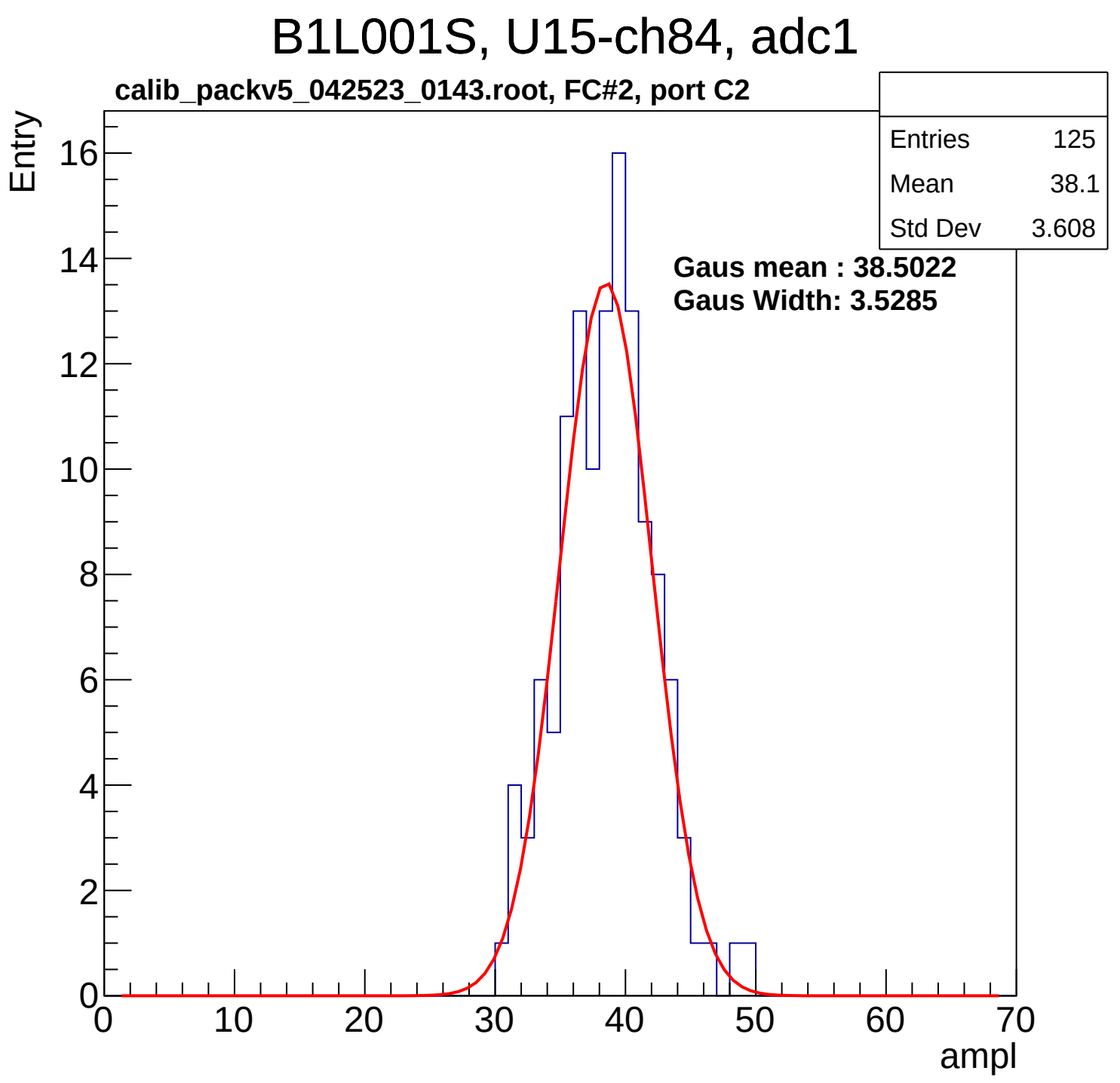
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch84, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 31.48
Std Dev 3.366
Gaus mean : 32.0499
Gaus Width: 3.5951
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch84, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 38.1
Std Dev 3.608
Gaus mean : 38.5022
Gaus Width: 3.5285
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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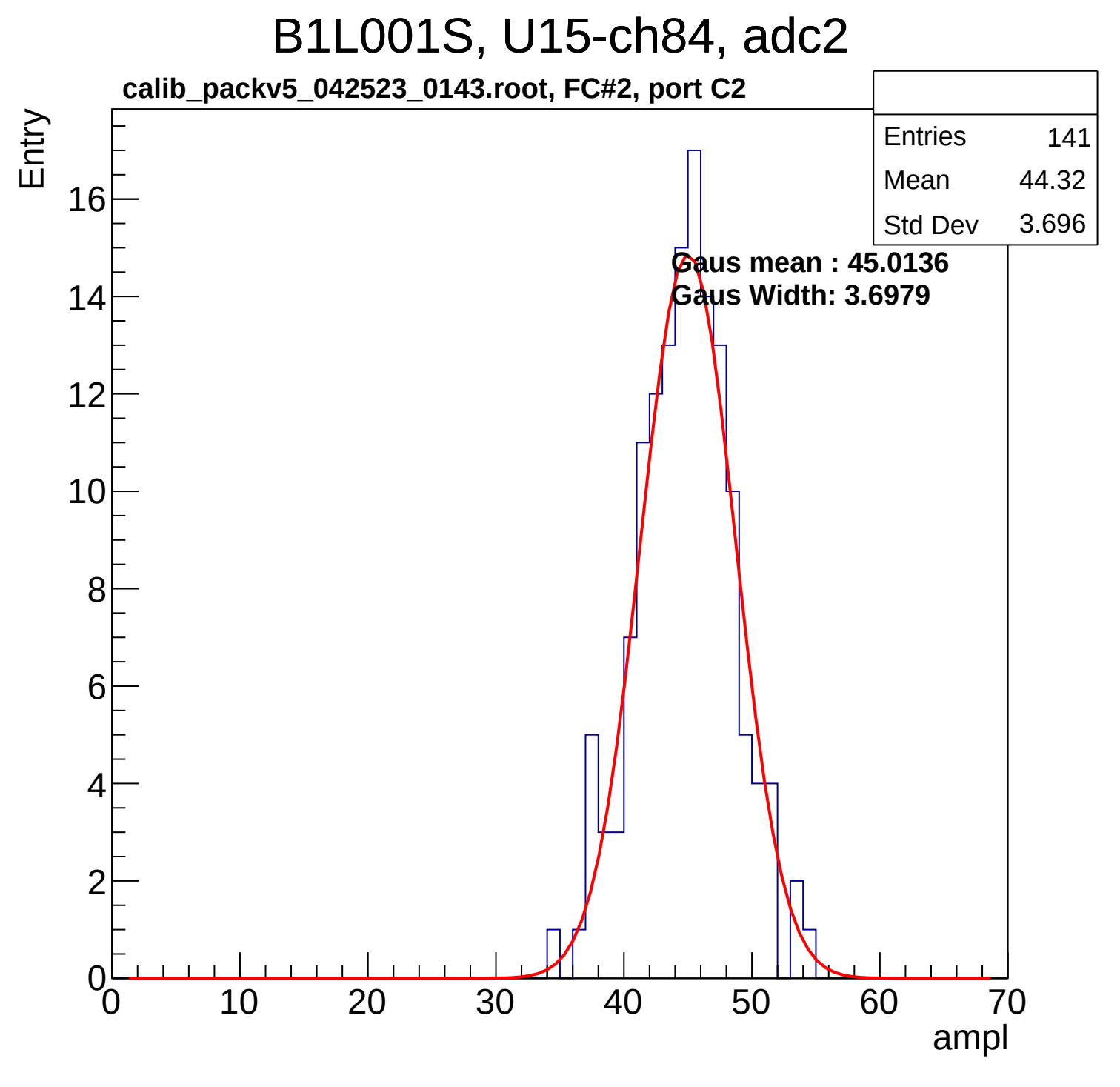
B1L001S, U15-ch84, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 44.32
Std Dev 3.696
Gaus mean : 45.0136
Gaus Width: 3.6979
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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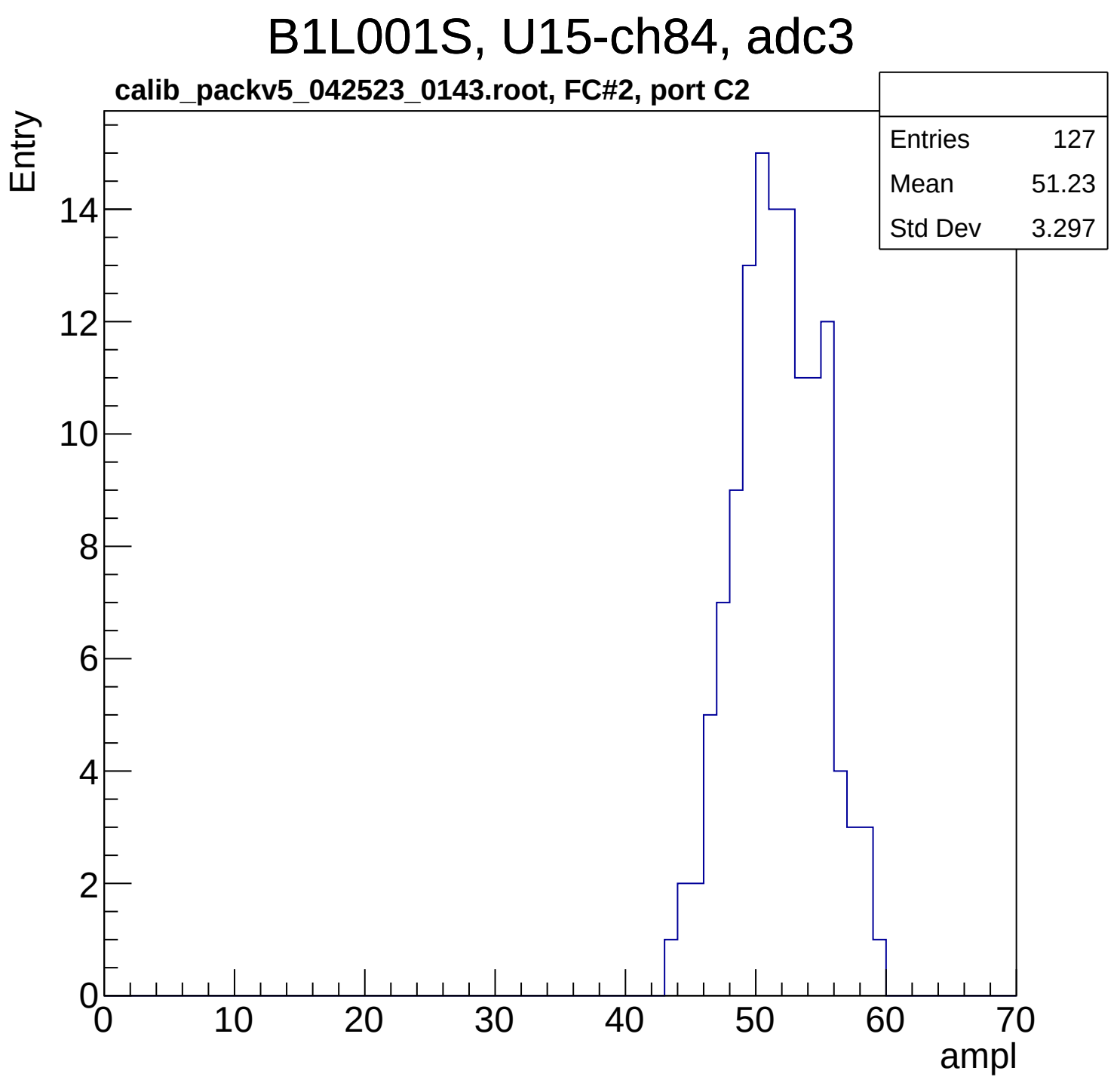
B1L001S, U15-ch84, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 51.23
Std Dev 3.297
Entry
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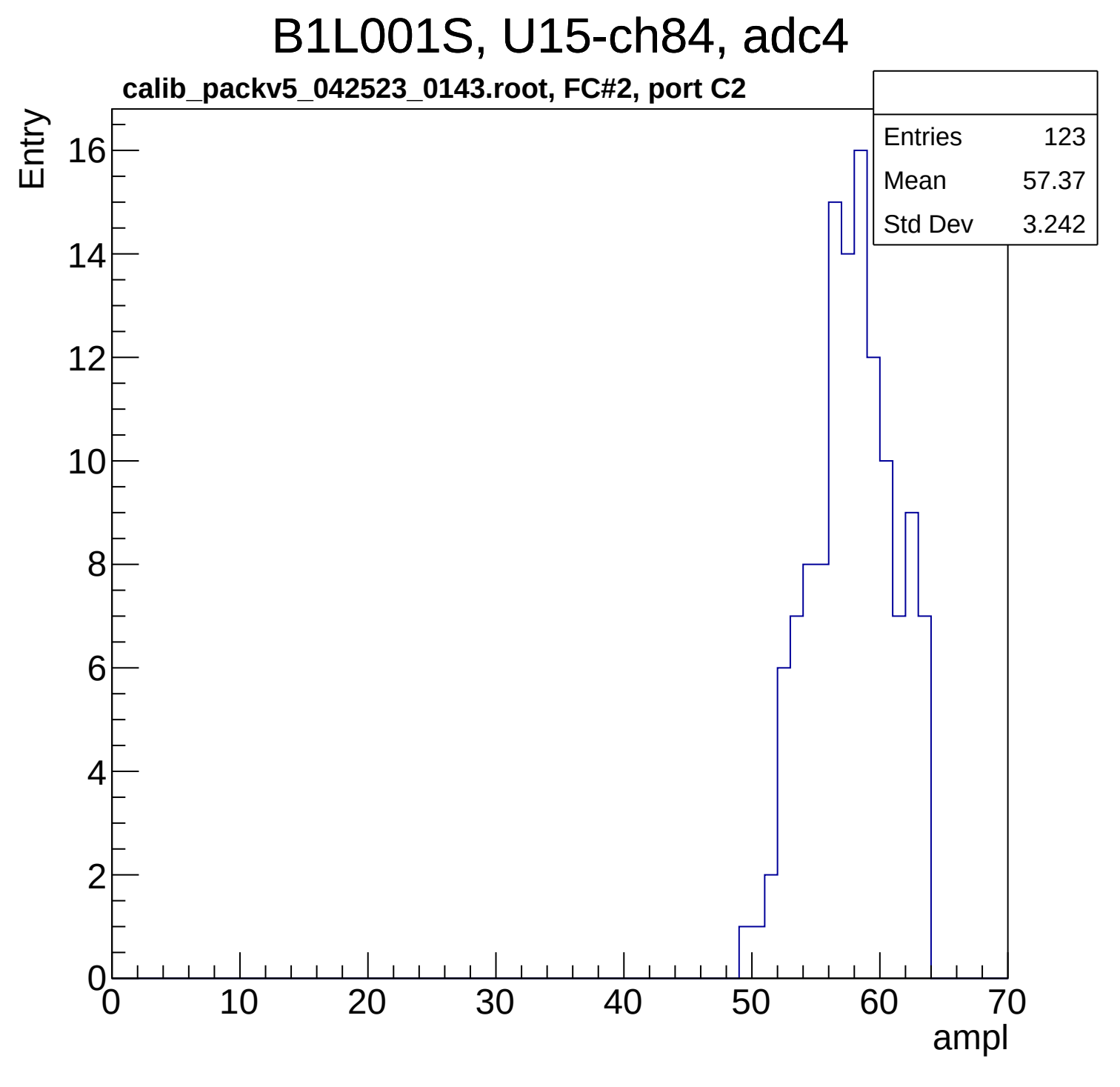
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch84, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 57.37
Std Dev 3.242
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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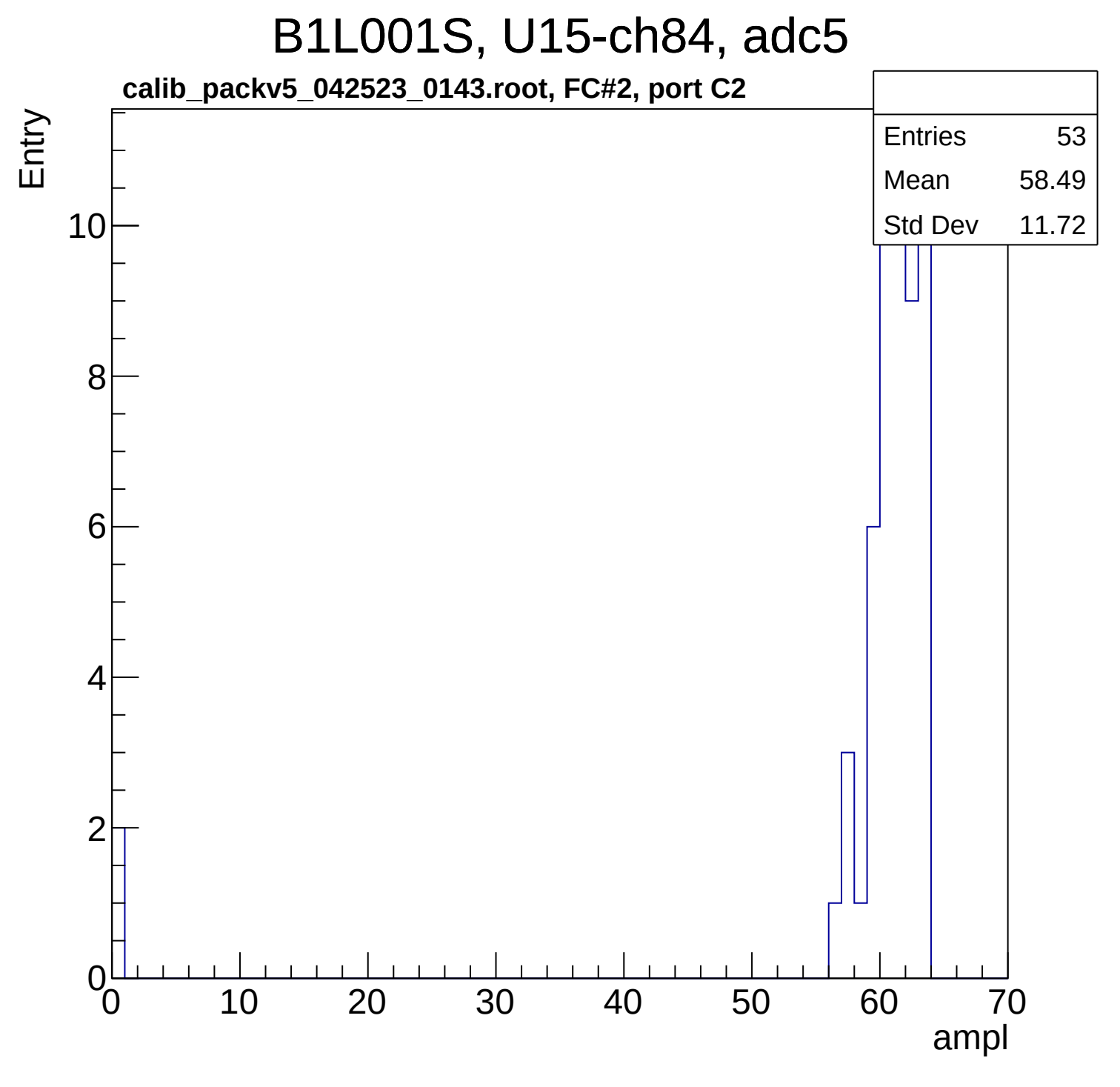
B1L001S, U15-ch84, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 53
Mean 58.49
Std Dev 11.72
Entry
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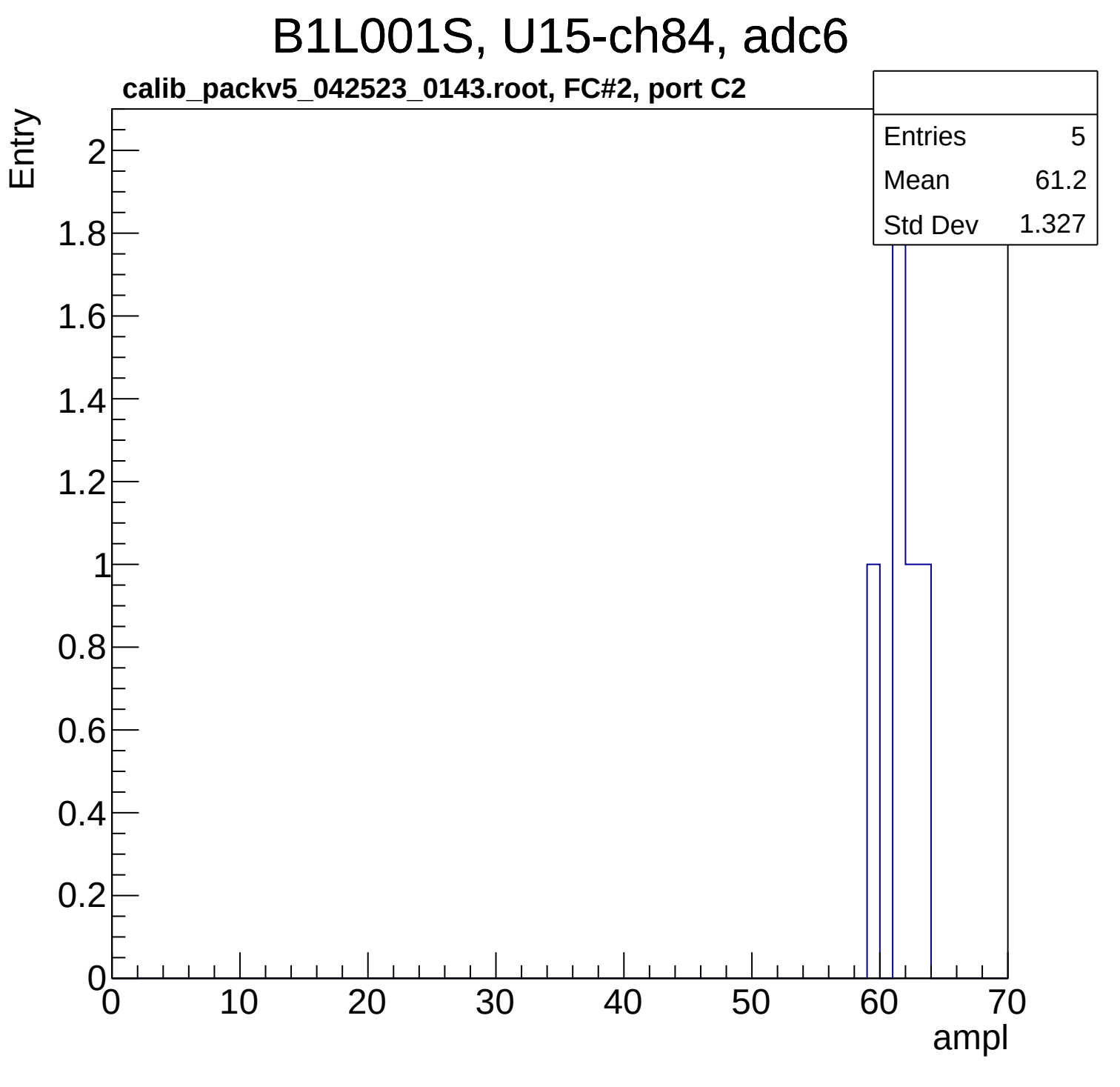
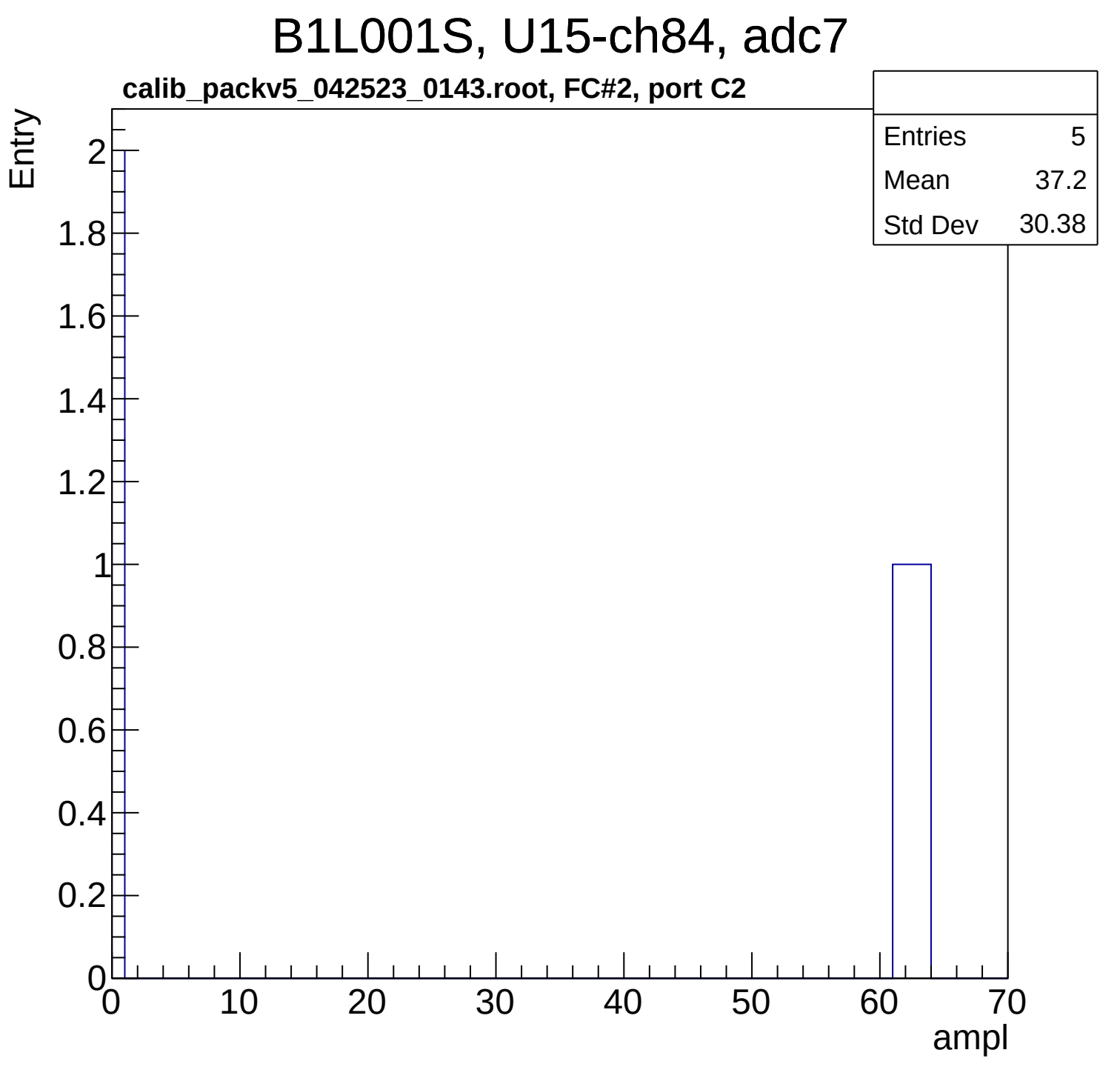
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch84, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch84, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 37.2
Std Dev 30.38
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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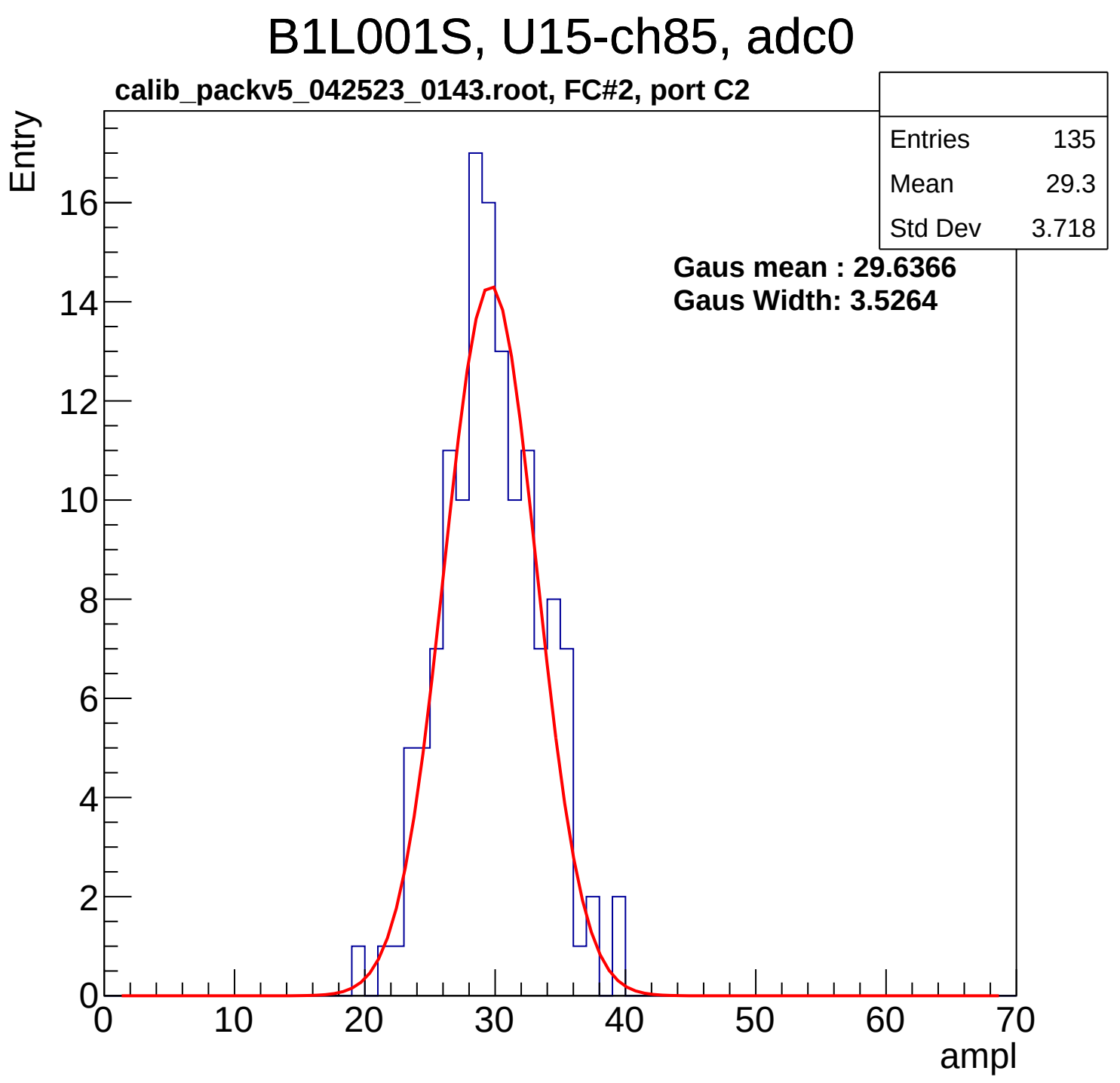
B1L001S, U15-ch85, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 29.3
Std Dev 3.718
Gaus mean : 29.6366
Gaus Width: 3.5264
Entry
ampl
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2
4
6
8
10
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14
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0
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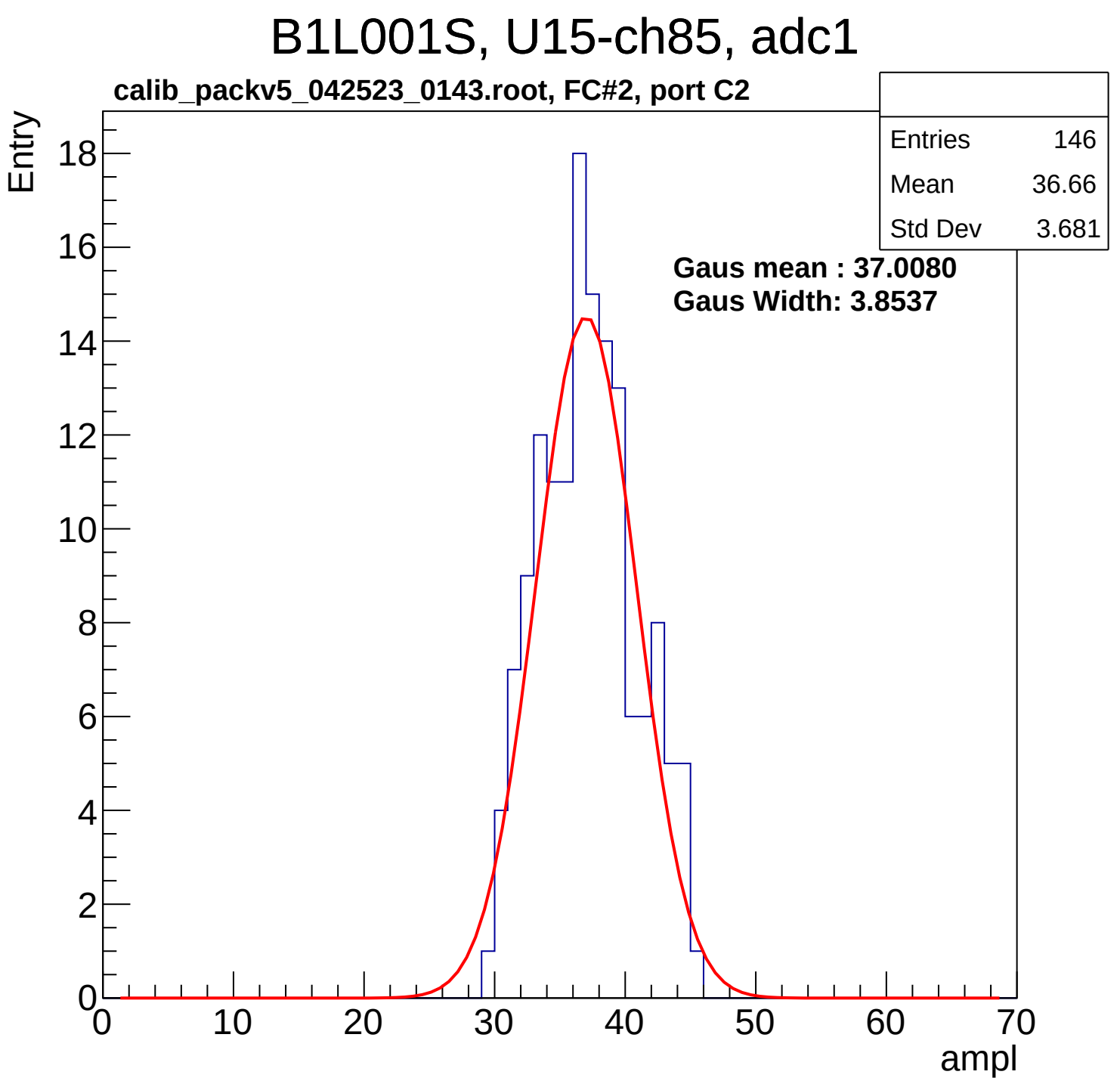
B1L001S, U15-ch85, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 36.66
Std Dev 3.681
Gaus mean : 37.0080
Gaus Width: 3.8537
Entry
ampl
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4
6
8
10
12
14
16
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0
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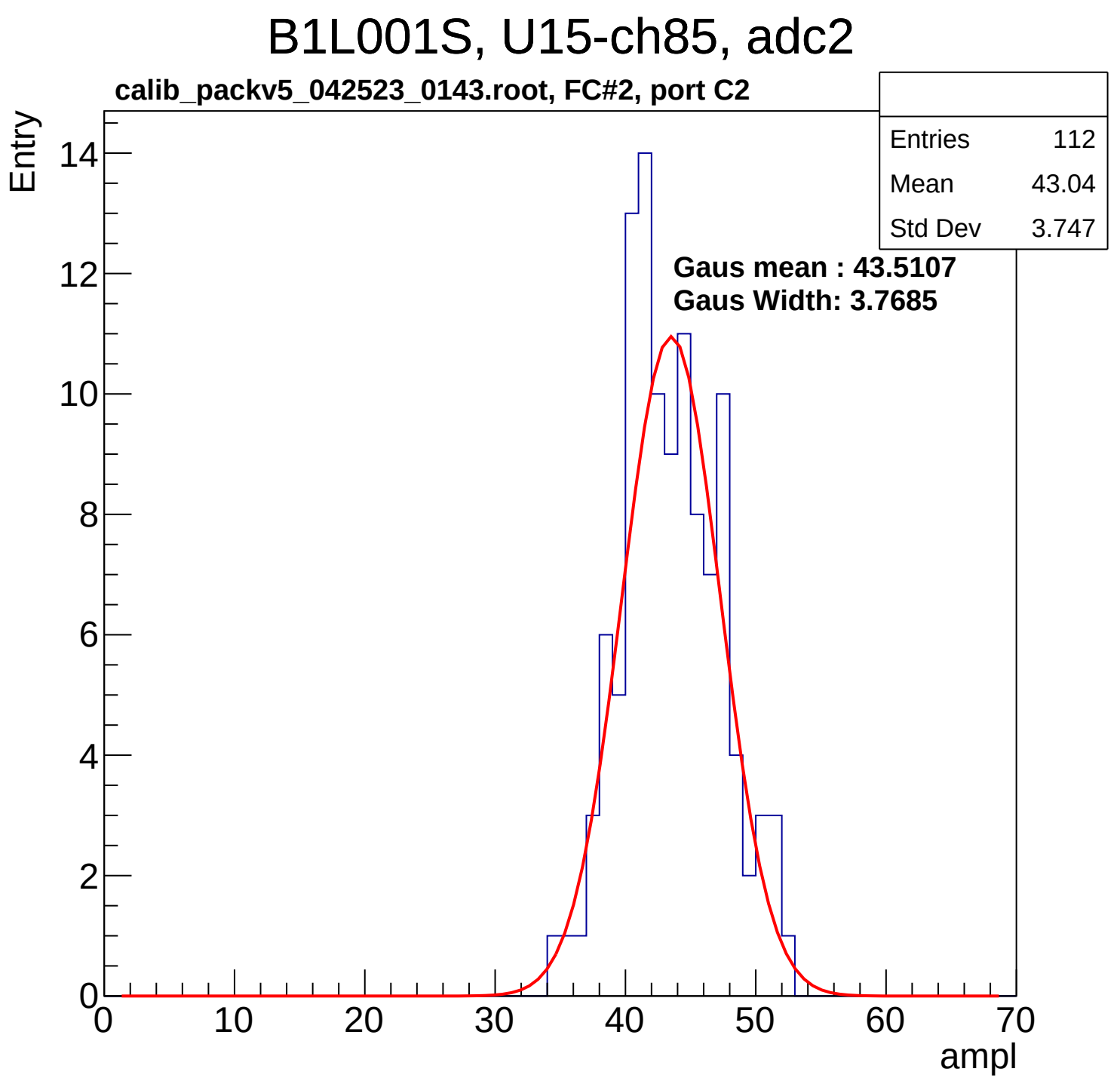
B1L001S, U15-ch85, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43.04
Std Dev 3.747
Gaus mean : 43.5107
Gaus Width: 3.7685
Entry
ampl
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2
4
6
8
10
12
14
0
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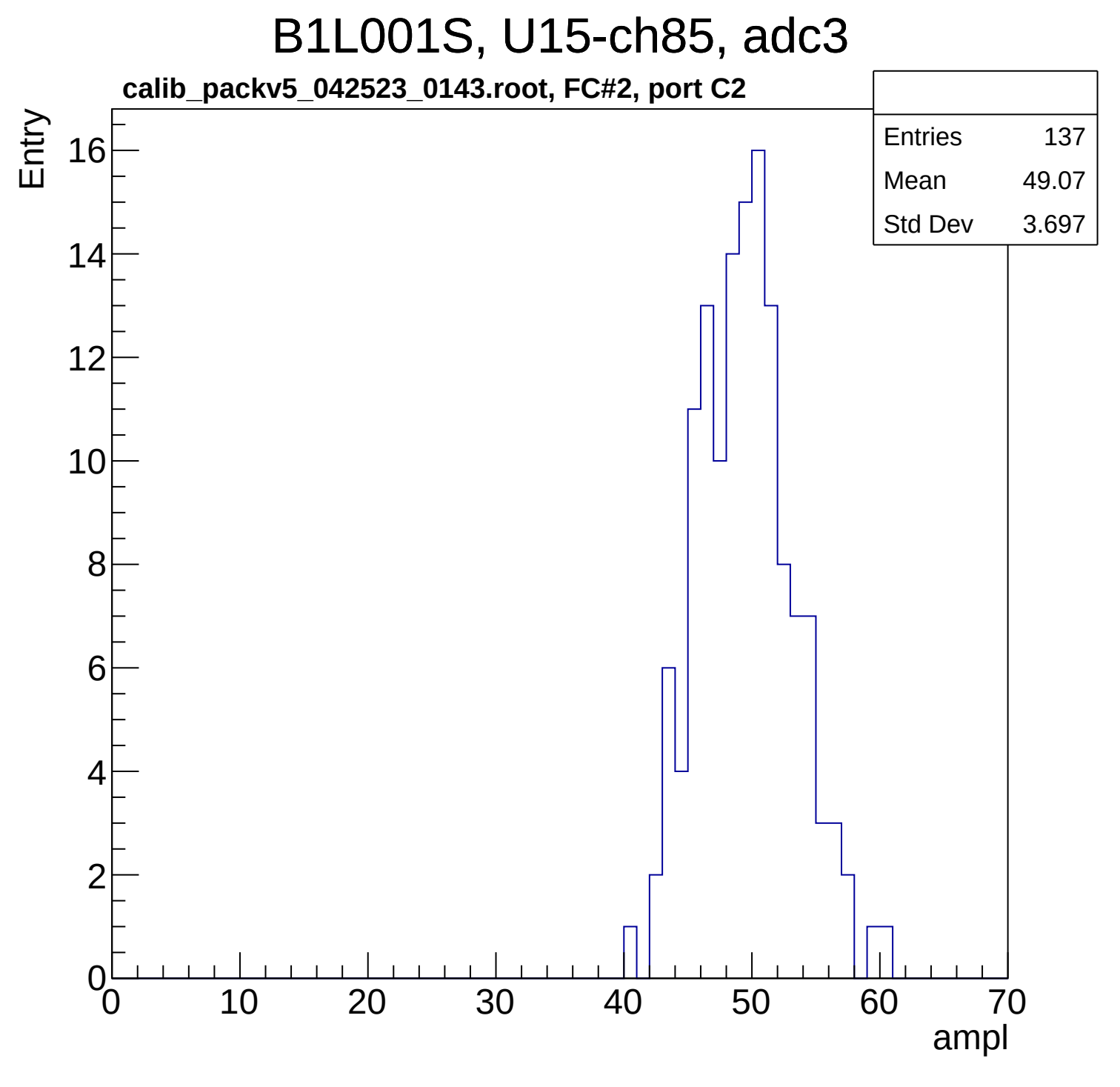
B1L001S, U15-ch85, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 49.07
Std Dev 3.697
Entry
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4
6
8
10
12
14
16
0
10
20
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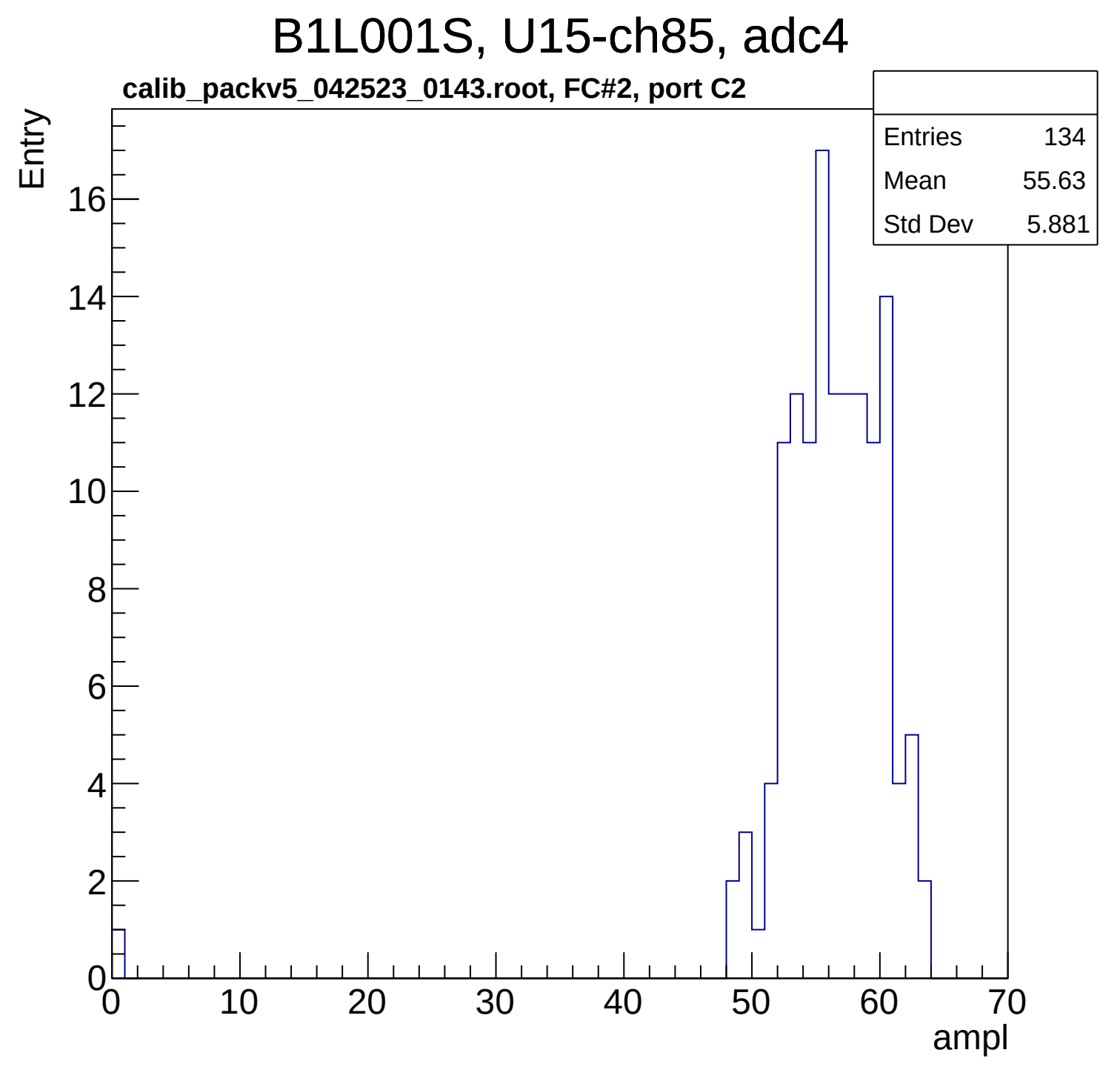
50
60
70
ampl


B1L001S, U15-ch85, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 55.63
Std Dev 5.881
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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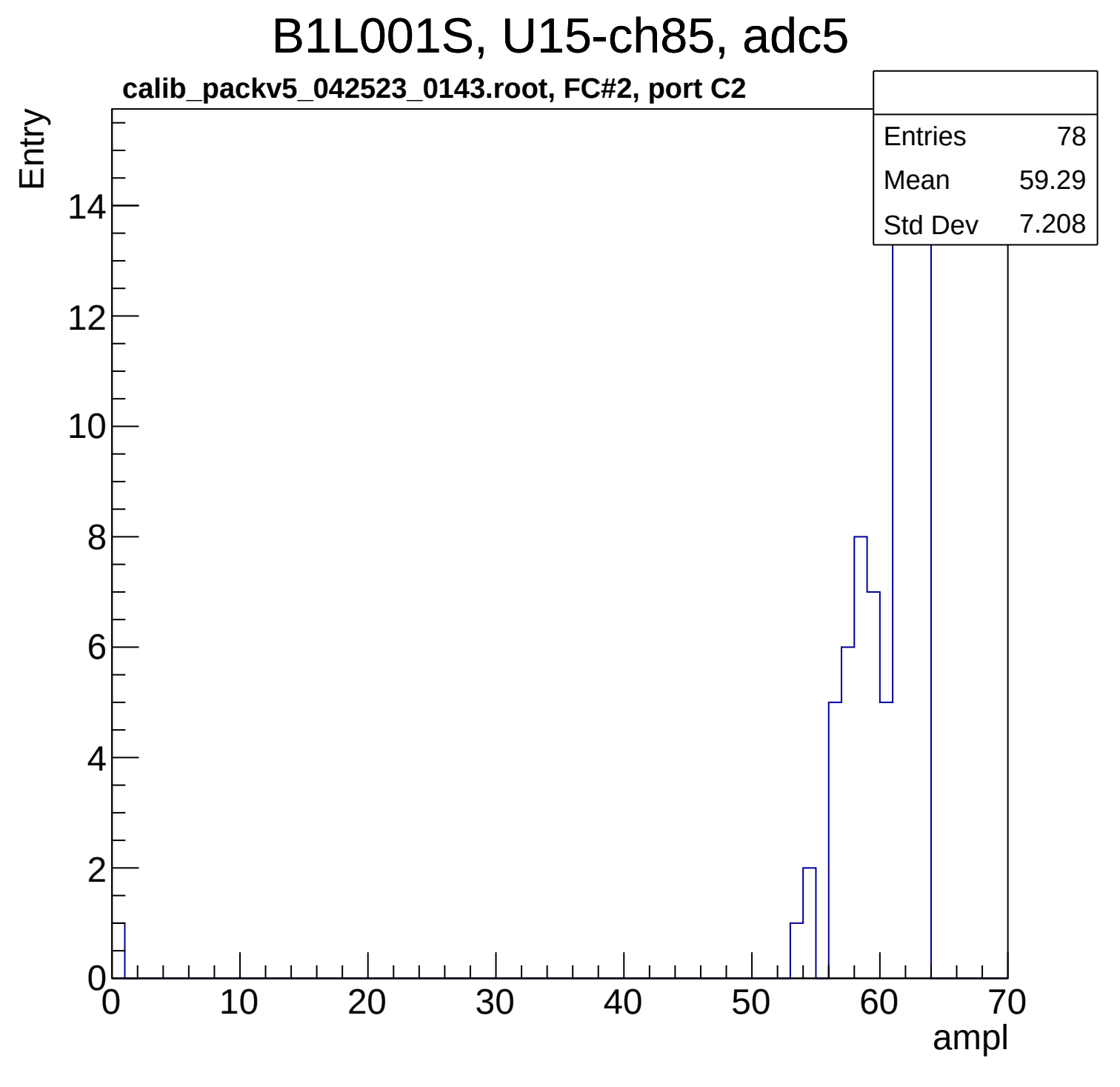
B1L001S, U15-ch85, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 78
Mean 59.29
Std Dev 7.208
Entry
ampl
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2
4
6
8
10
12
14
0
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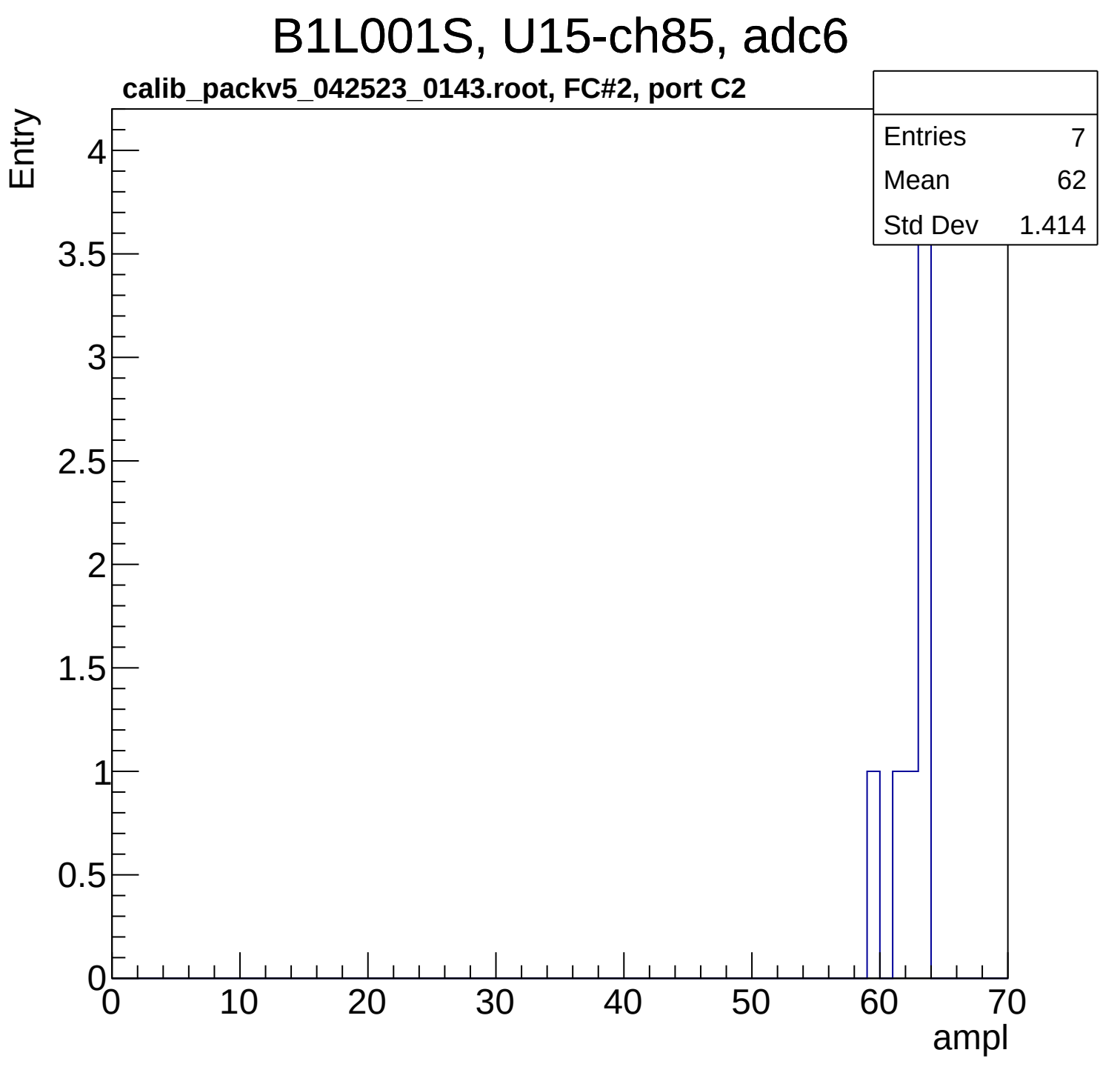
B1L001S, U15-ch85, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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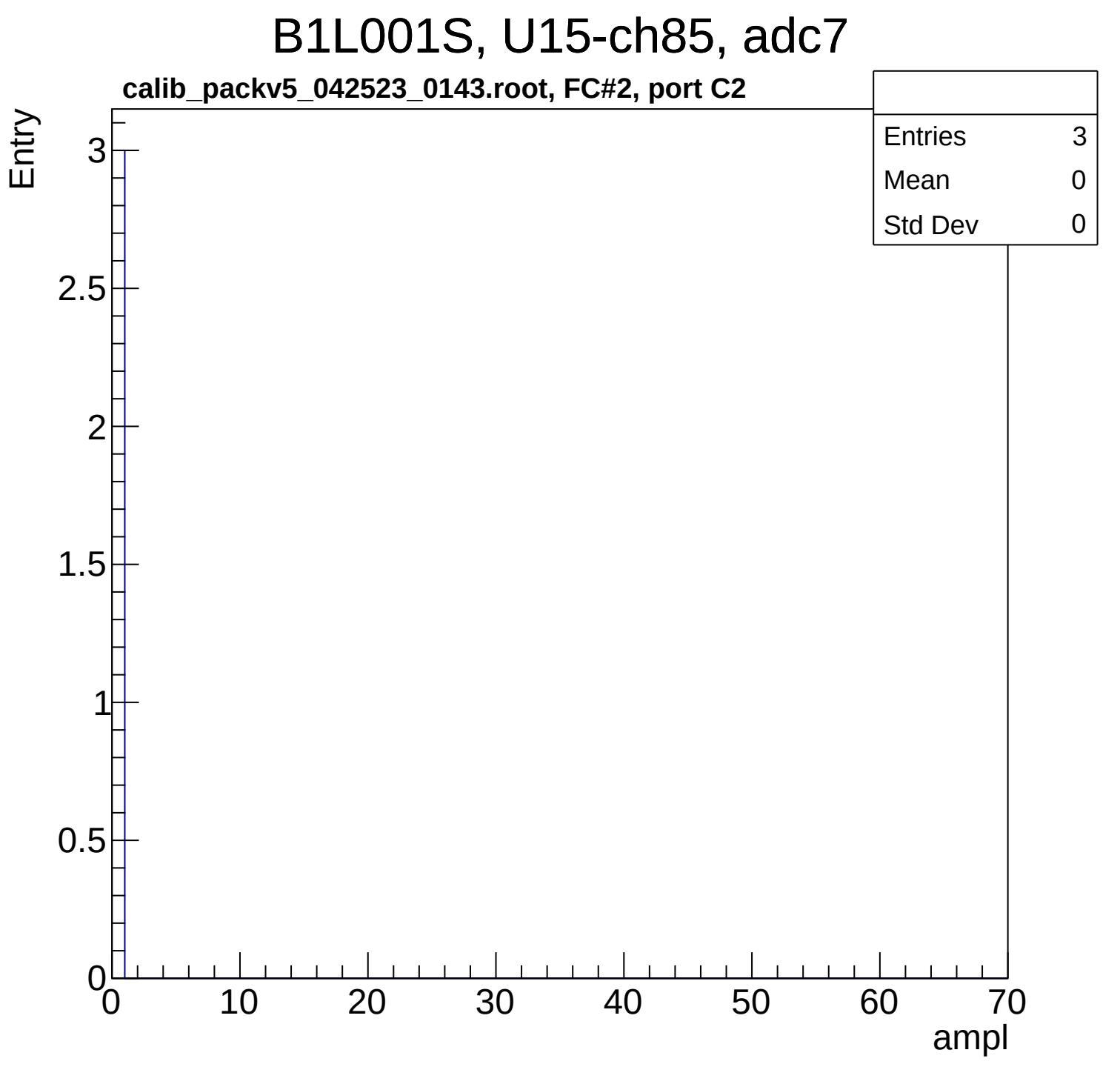
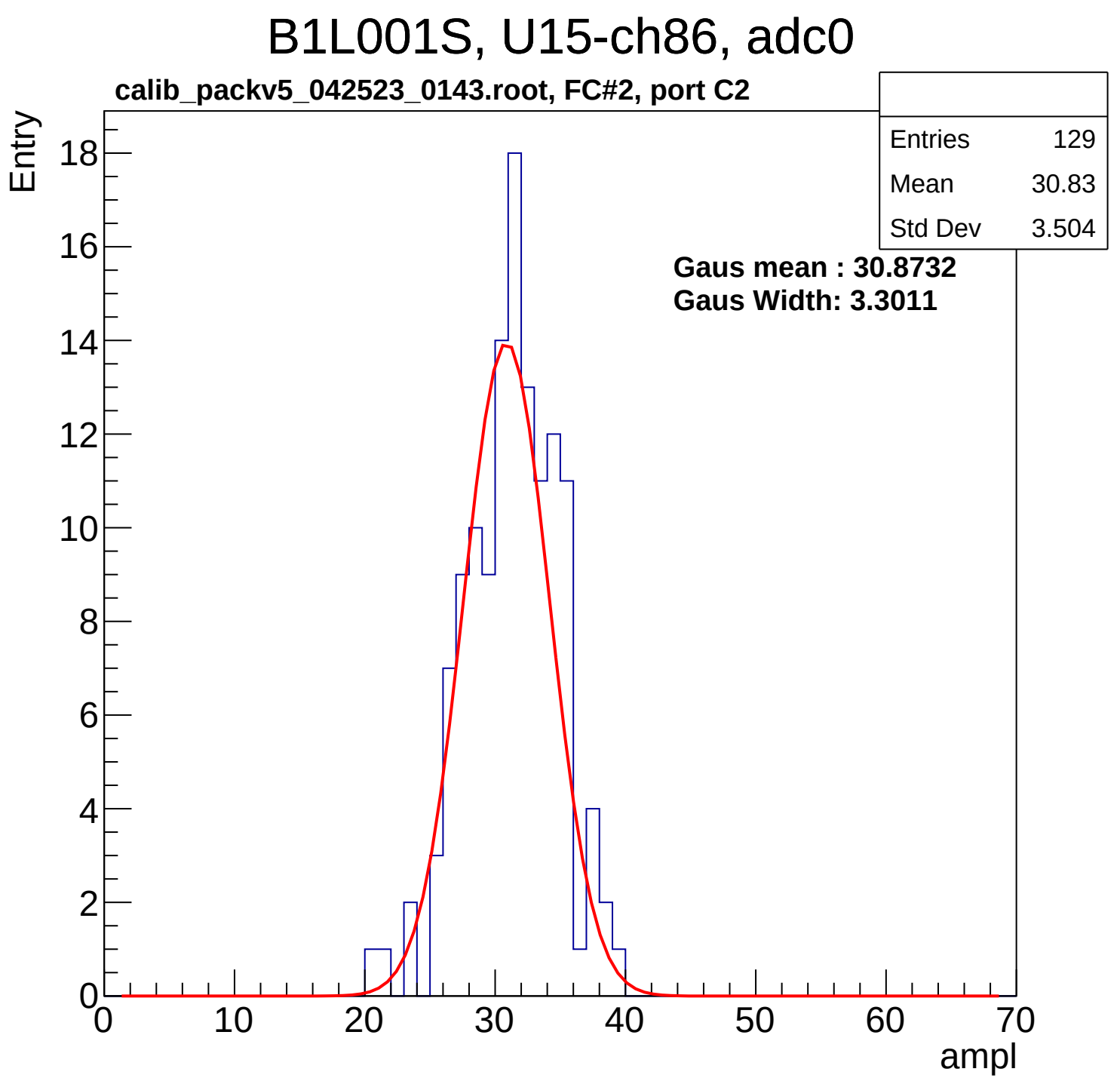
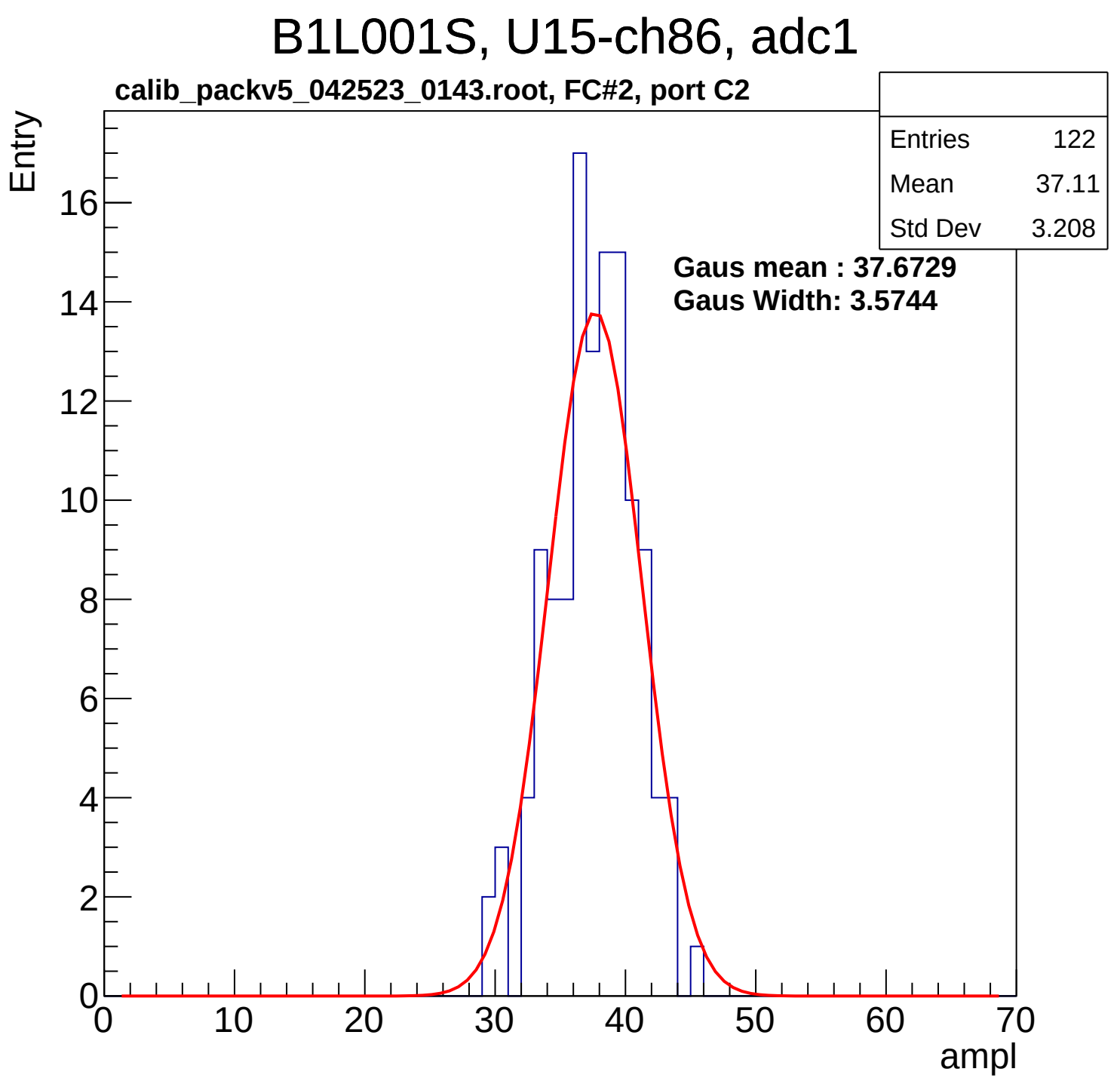
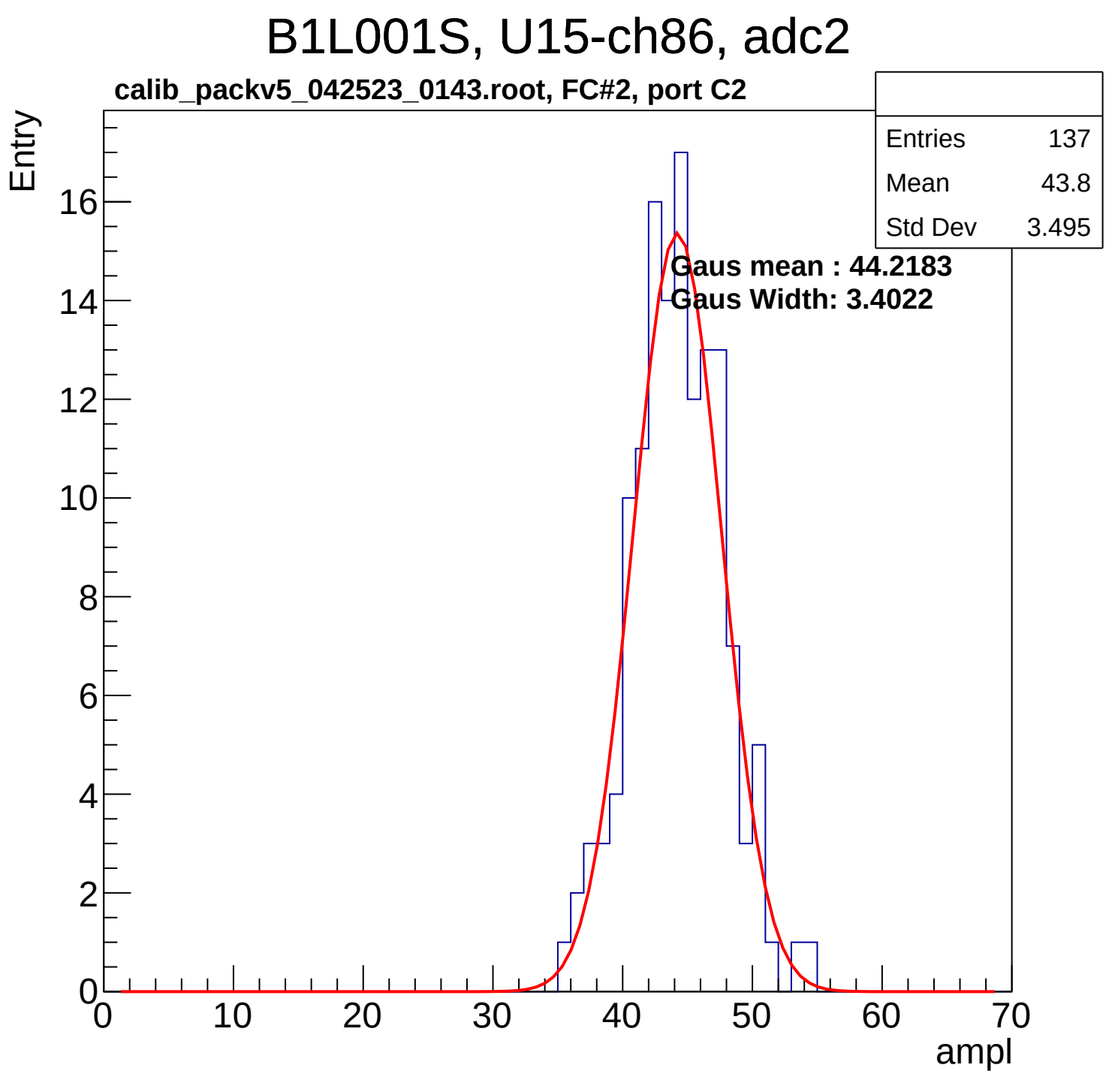

B1L001S, U15-ch85, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch86, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 30.83
Std Dev 3.504
Gaus mean : 30.8732
Gaus Width: 3.3011
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U15-ch86, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 37.11
Std Dev 3.208
Gaus mean : 37.6729
Gaus Width: 3.5744
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch86, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 43.8
Std Dev 3.495
Gaus mean : 44.2183
Gaus Width: 3.4022
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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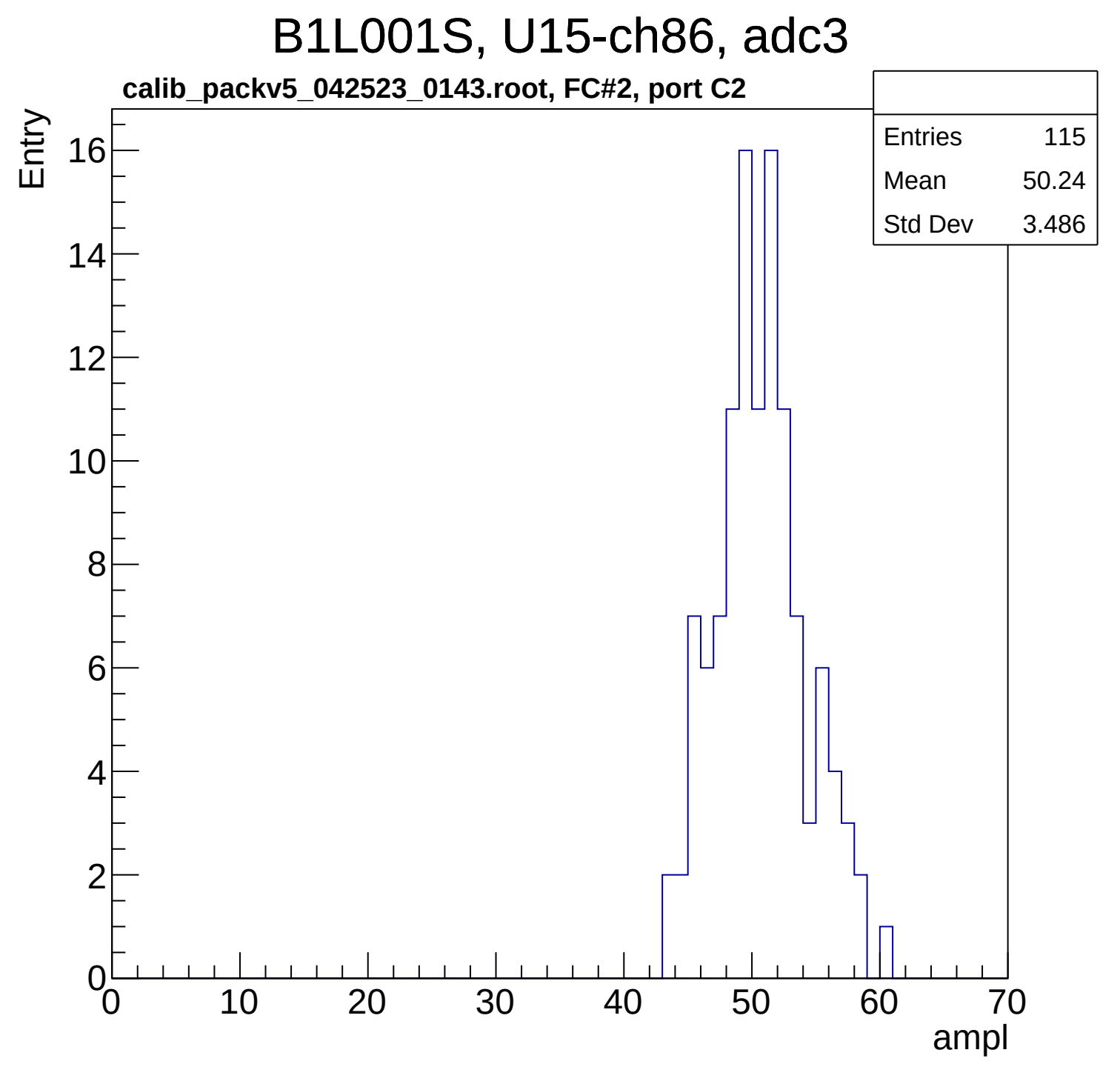
B1L001S, U15-ch86, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 50.24
Std Dev 3.486
Entry
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4
6
8
10
12
14
16
0
10
20
30
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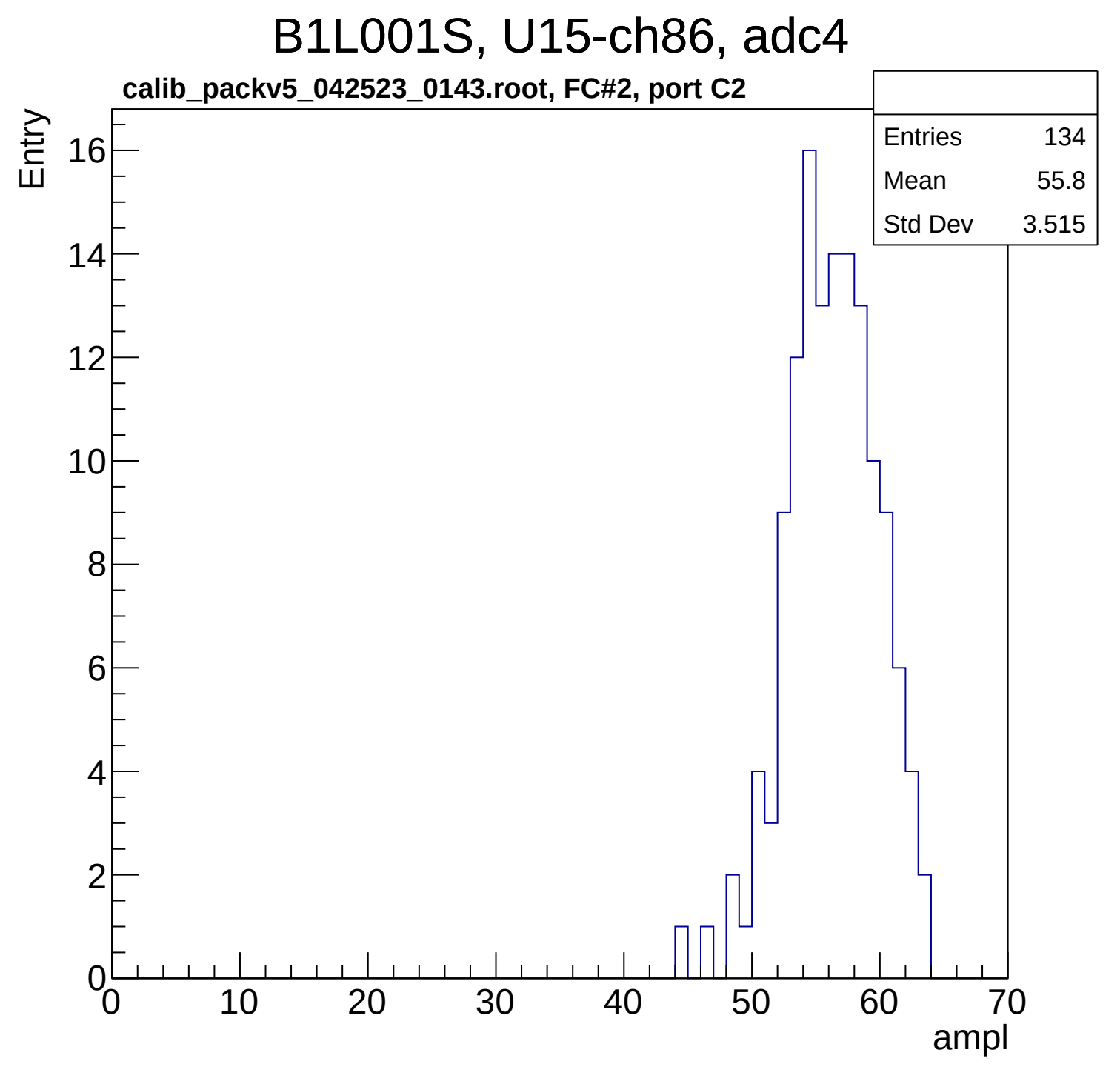
50
60
70
ampl


B1L001S, U15-ch86, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 55.8
Std Dev 3.515
Entry
ampl
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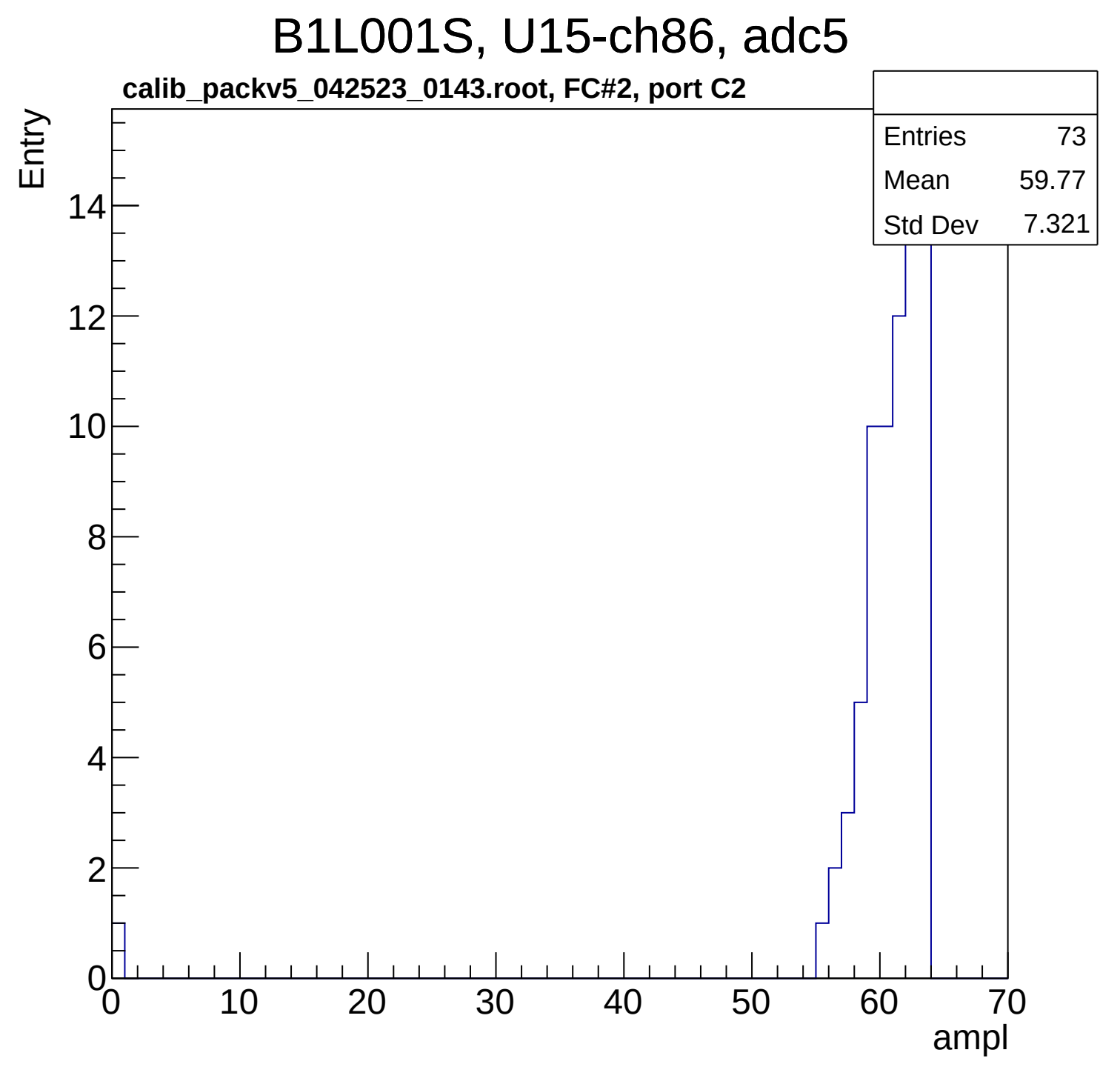
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch86, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 73
Mean 59.77
Std Dev 7.321
Entry
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2
4
6
8
10
12
14
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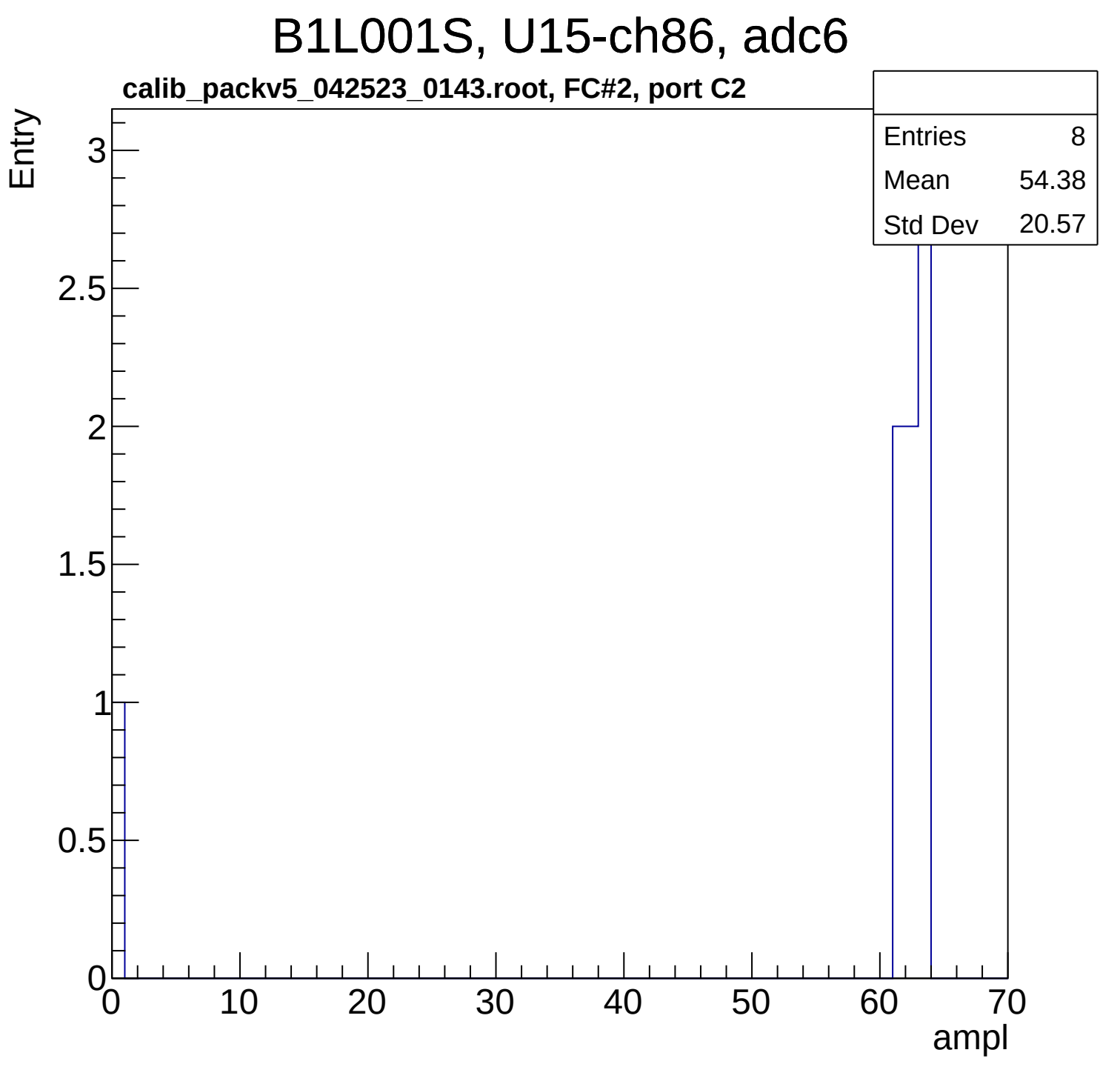
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch86, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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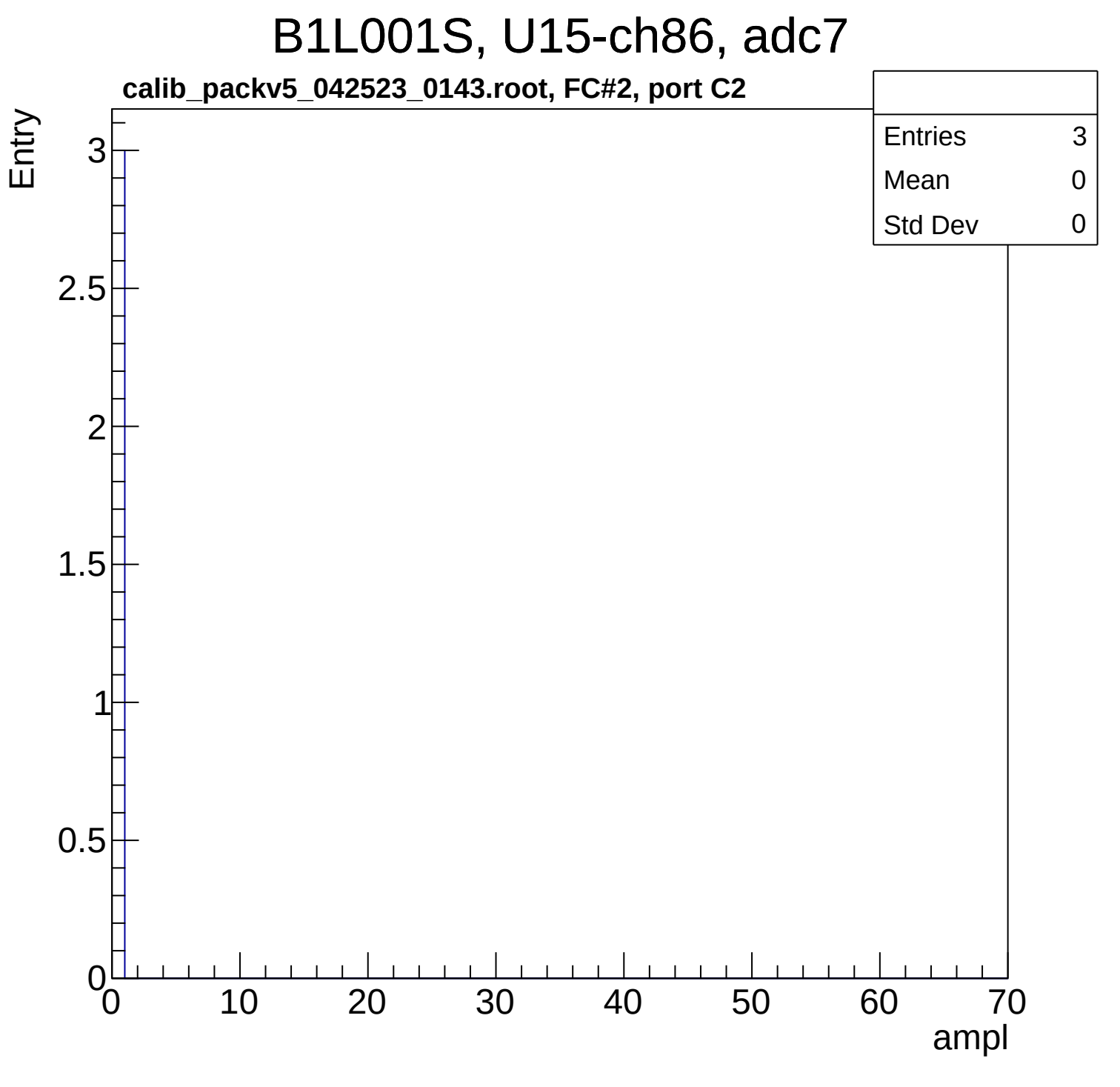
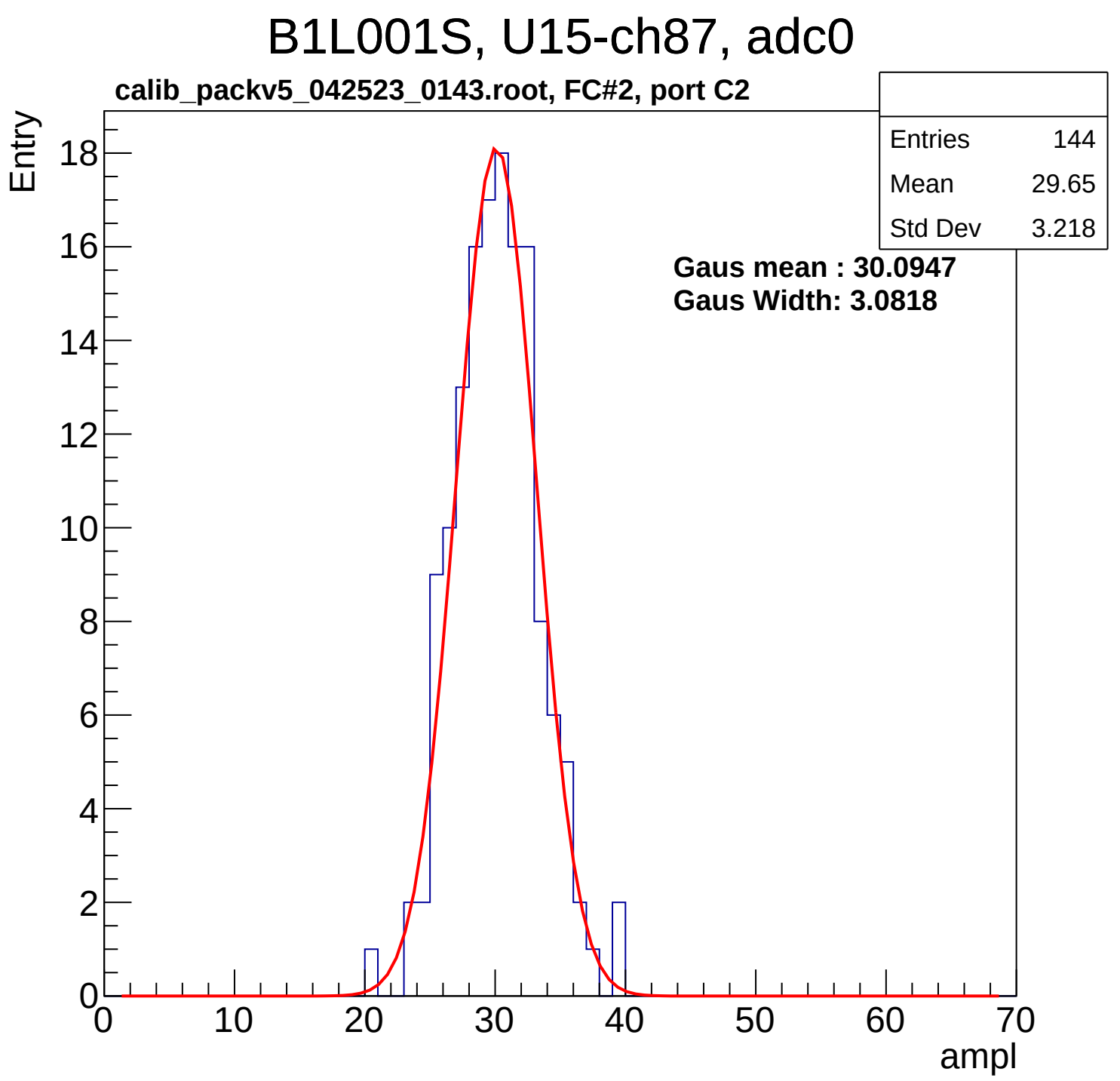
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch86, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch87, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 29.65
Std Dev 3.218
Gaus mean : 30.0947
Gaus Width: 3.0818
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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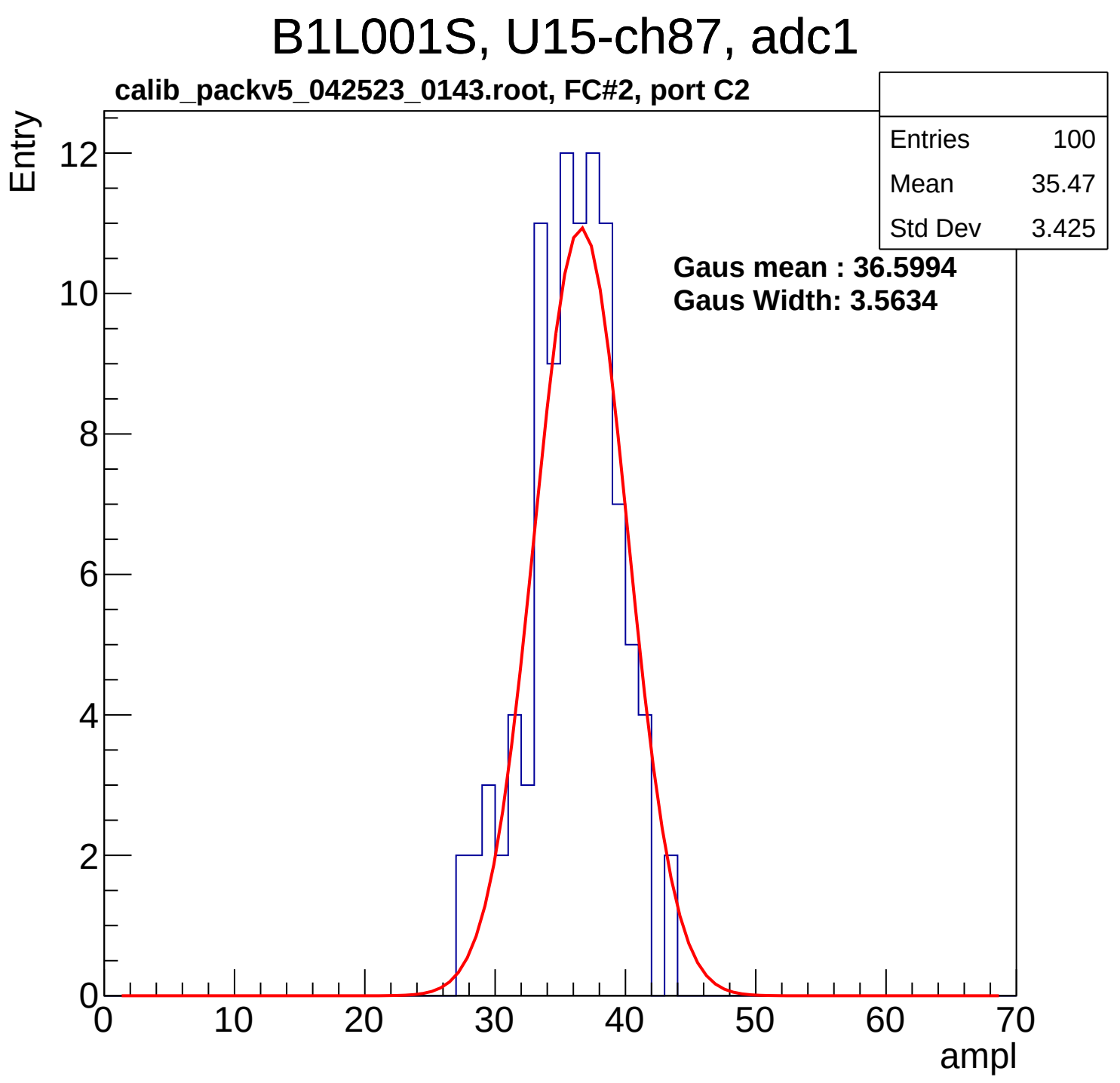
B1L001S, U15-ch87, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 35.47
Std Dev 3.425
Gaus mean : 36.5994
Gaus Width: 3.5634
Entry
ampl
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6
8
10
12
0
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20
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40
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60
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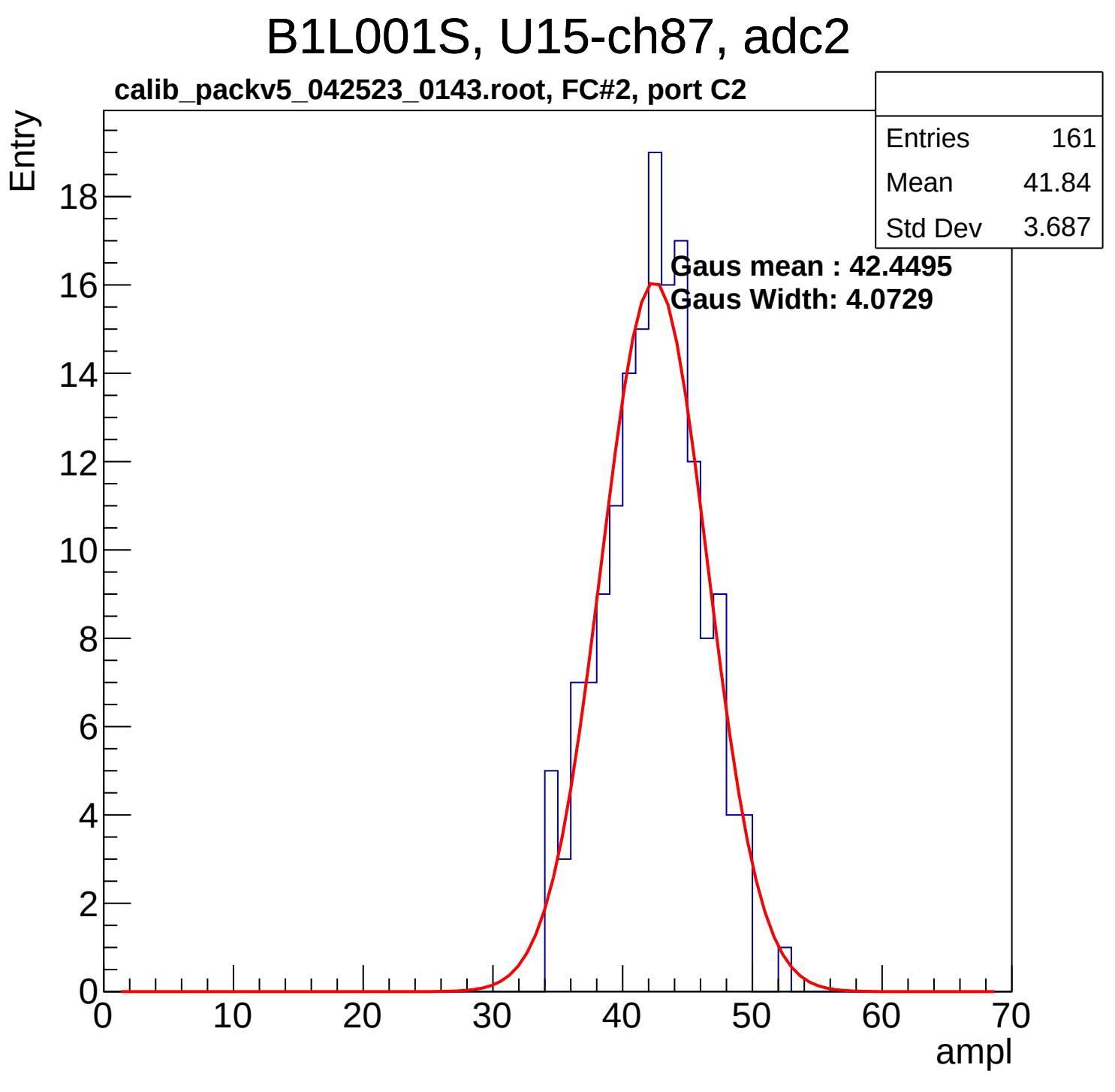
B1L001S, U15-ch87, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 41.84
Std Dev 3.687
Gaus mean : 42.4495
Gaus Width: 4.0729
Entry
ampl
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4
6
8
10
12
14
16
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0
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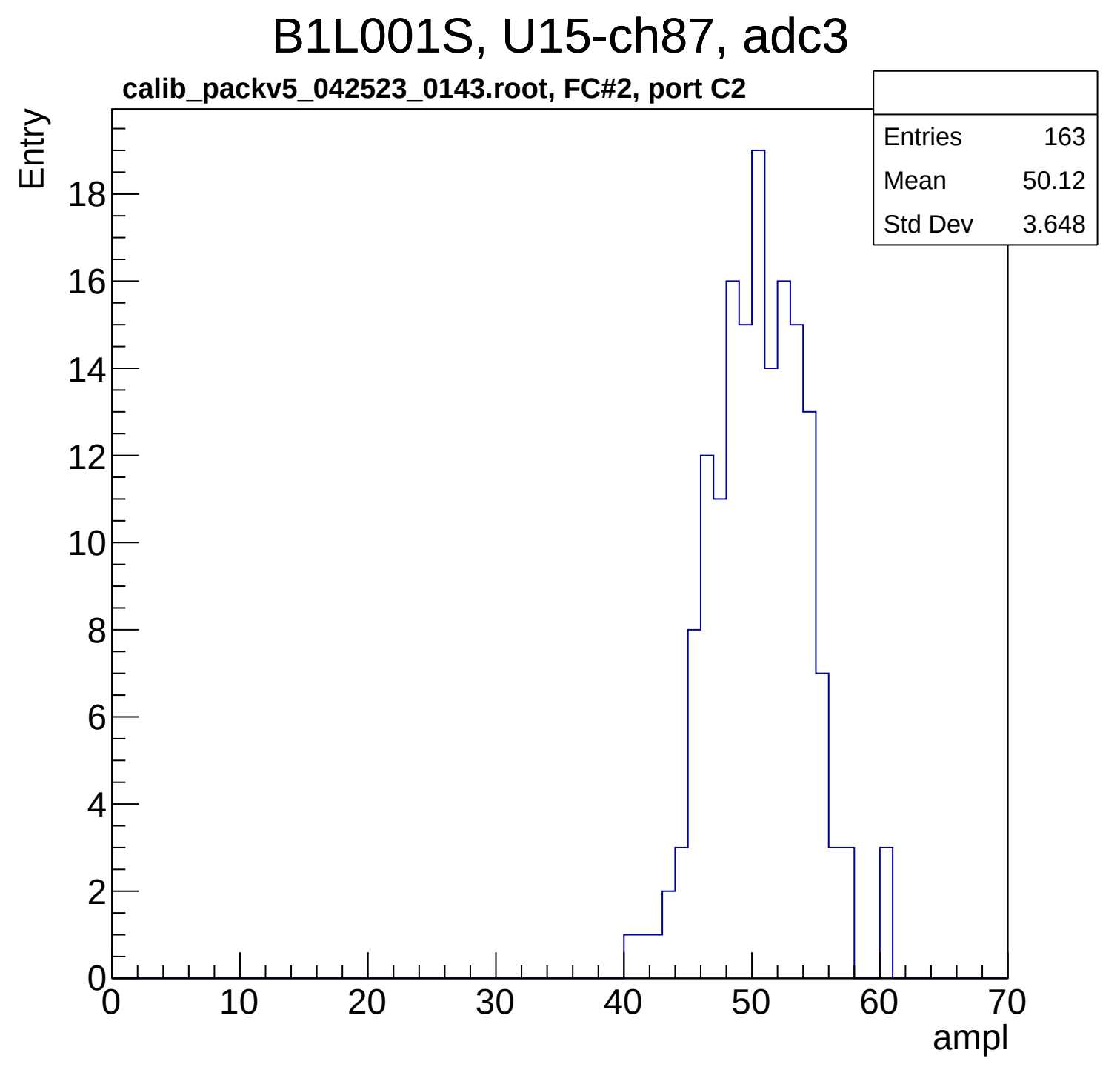
B1L001S, U15-ch87, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 50.12
Std Dev 3.648
Entry
ampl
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10
12
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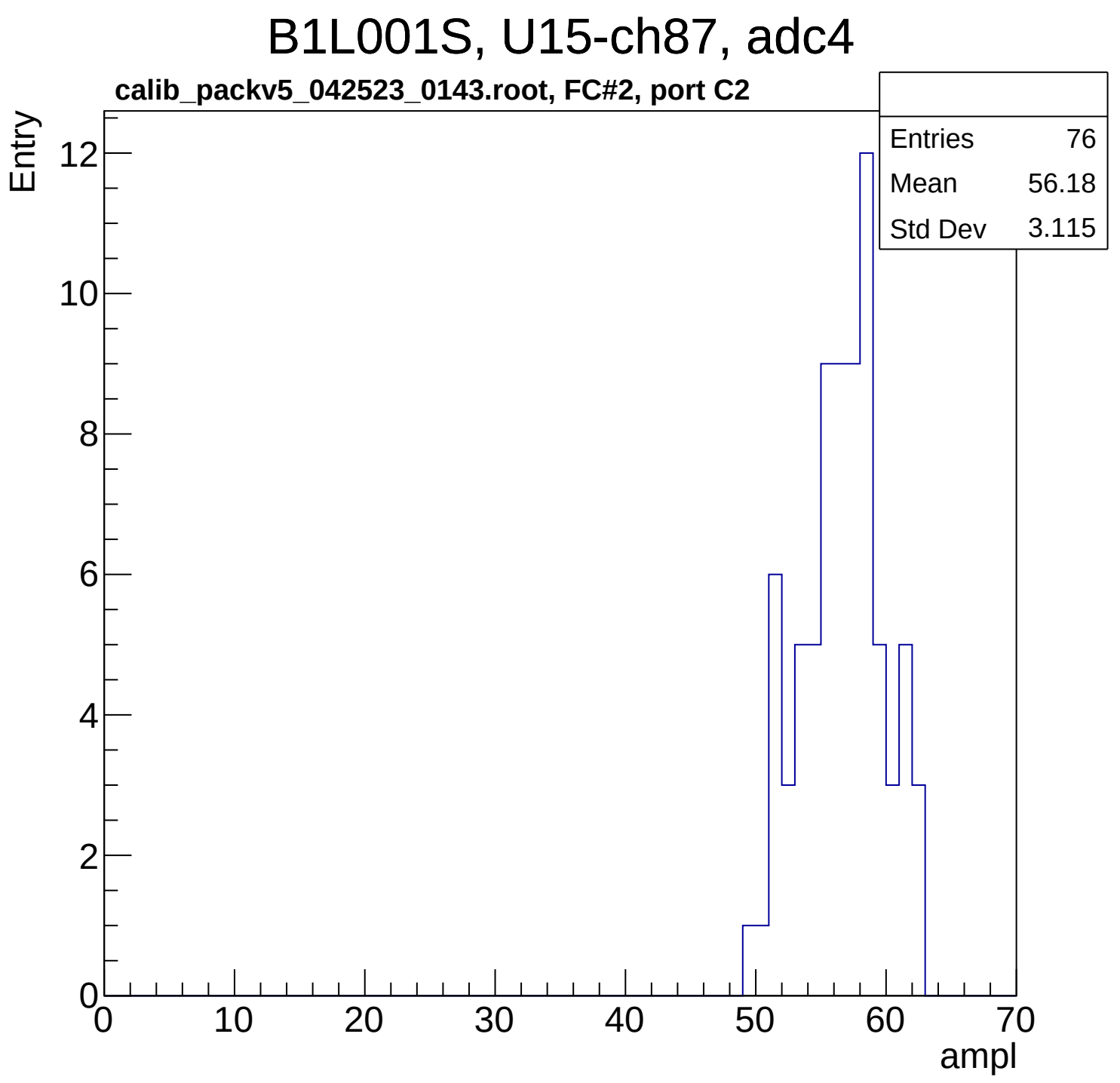
B1L001S, U15-ch87, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 56.18
Std Dev 3.115
Entry
ampl
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6
8
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0
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40
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60
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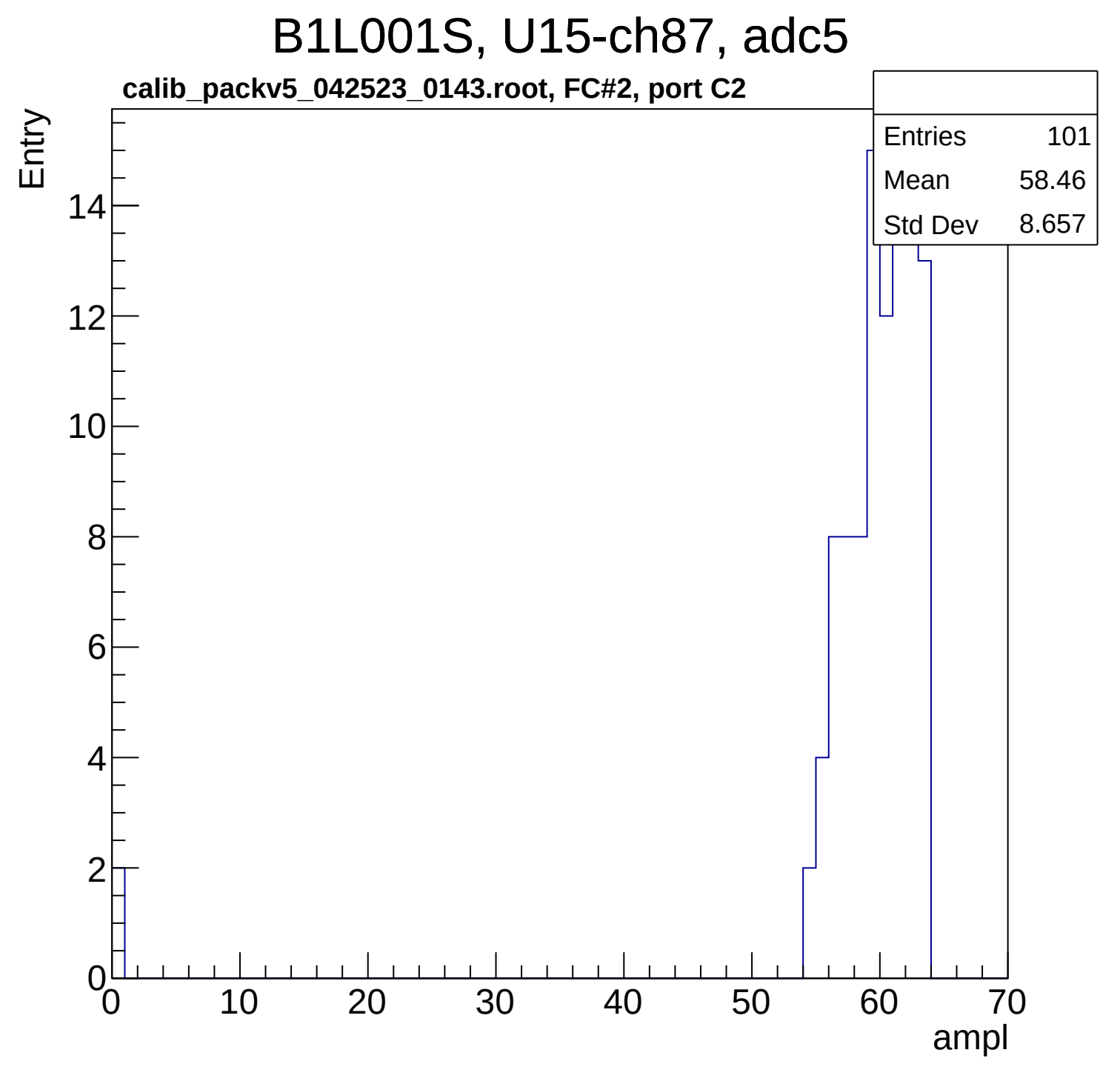
B1L001S, U15-ch87, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 58.46
Std Dev 8.657
Entry
ampl
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8
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0
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20
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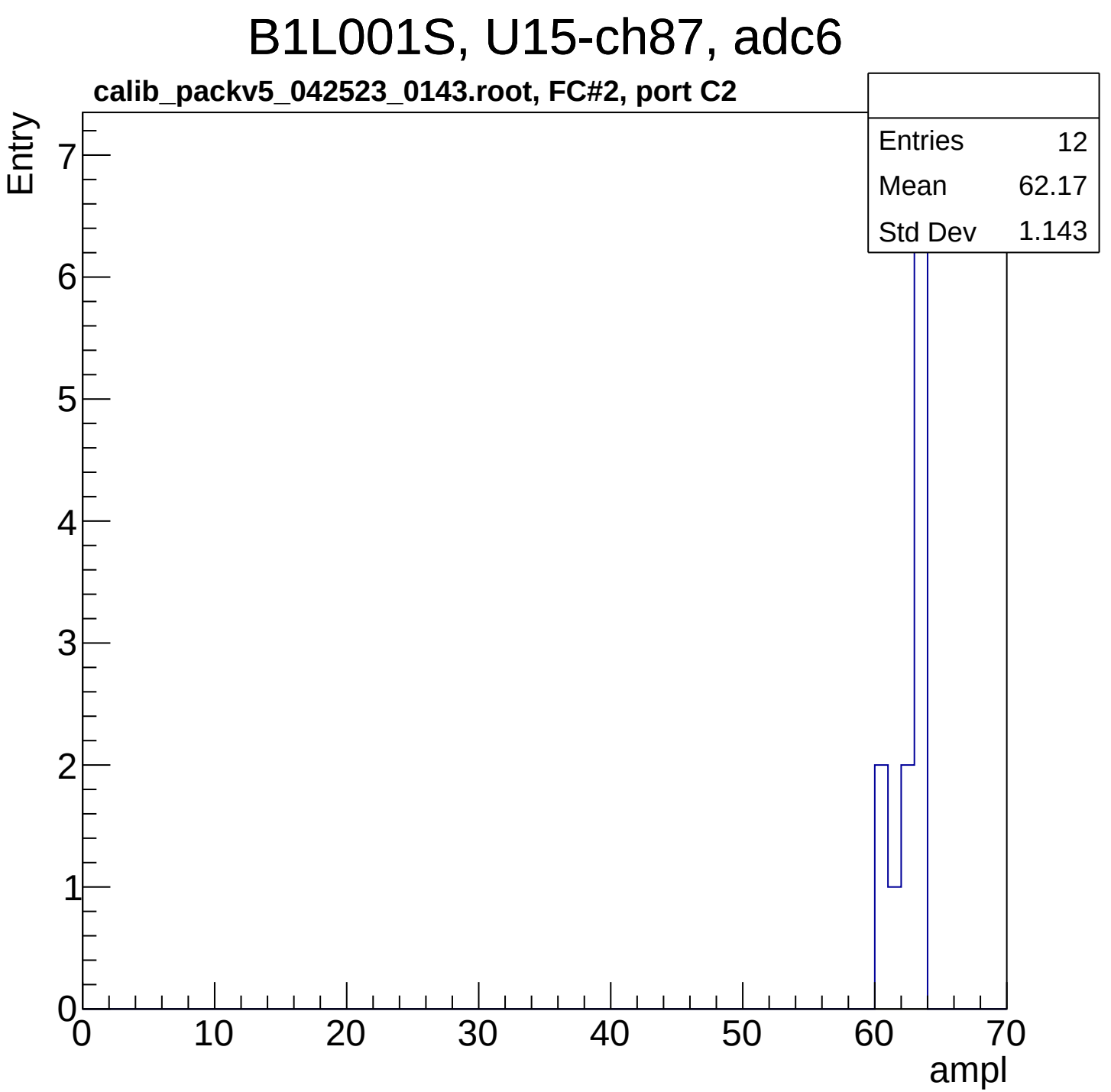
B1L001S, U15-ch87, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 62.17
Std Dev 1.143
Entry
ampl
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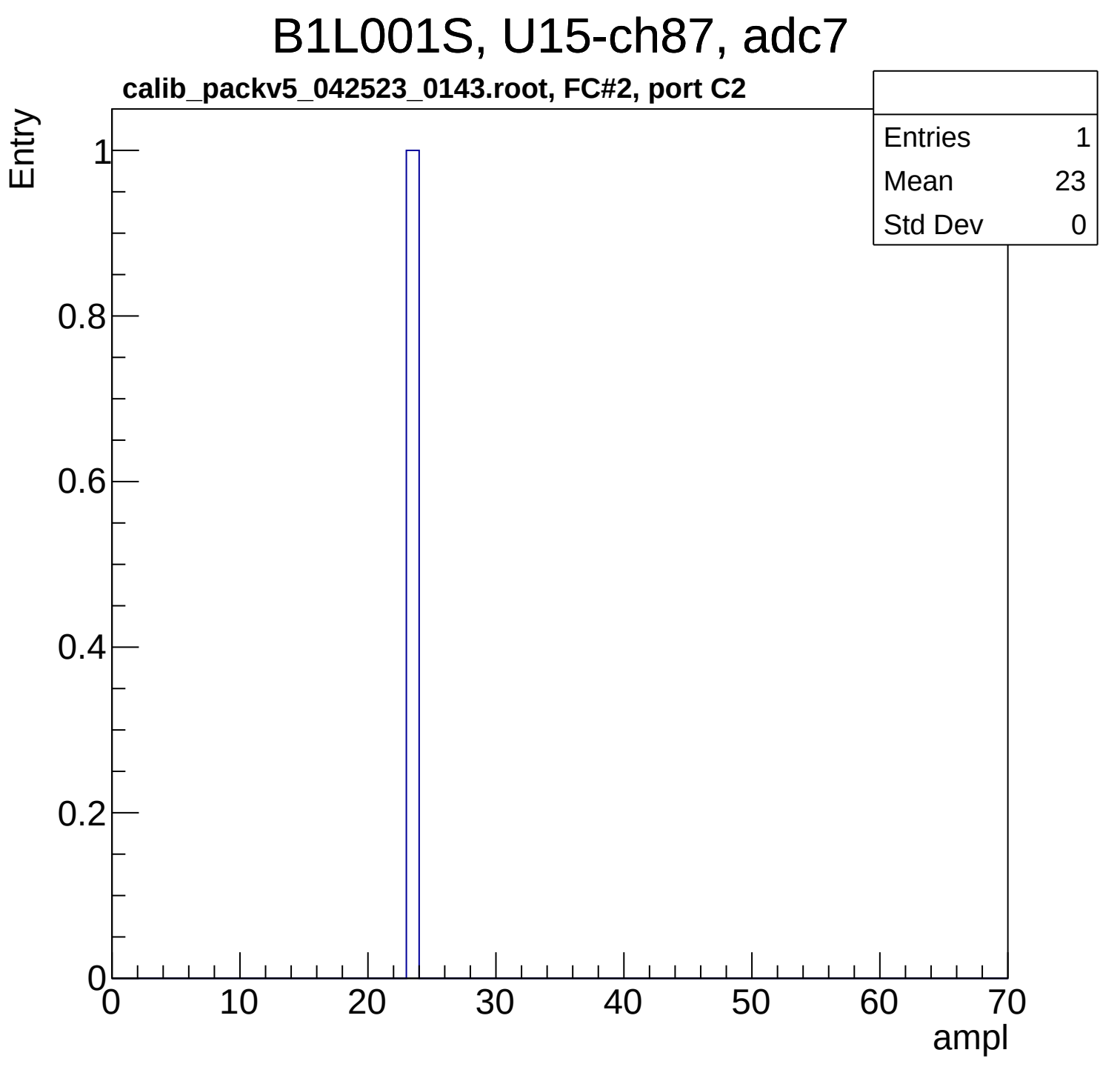
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch87, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
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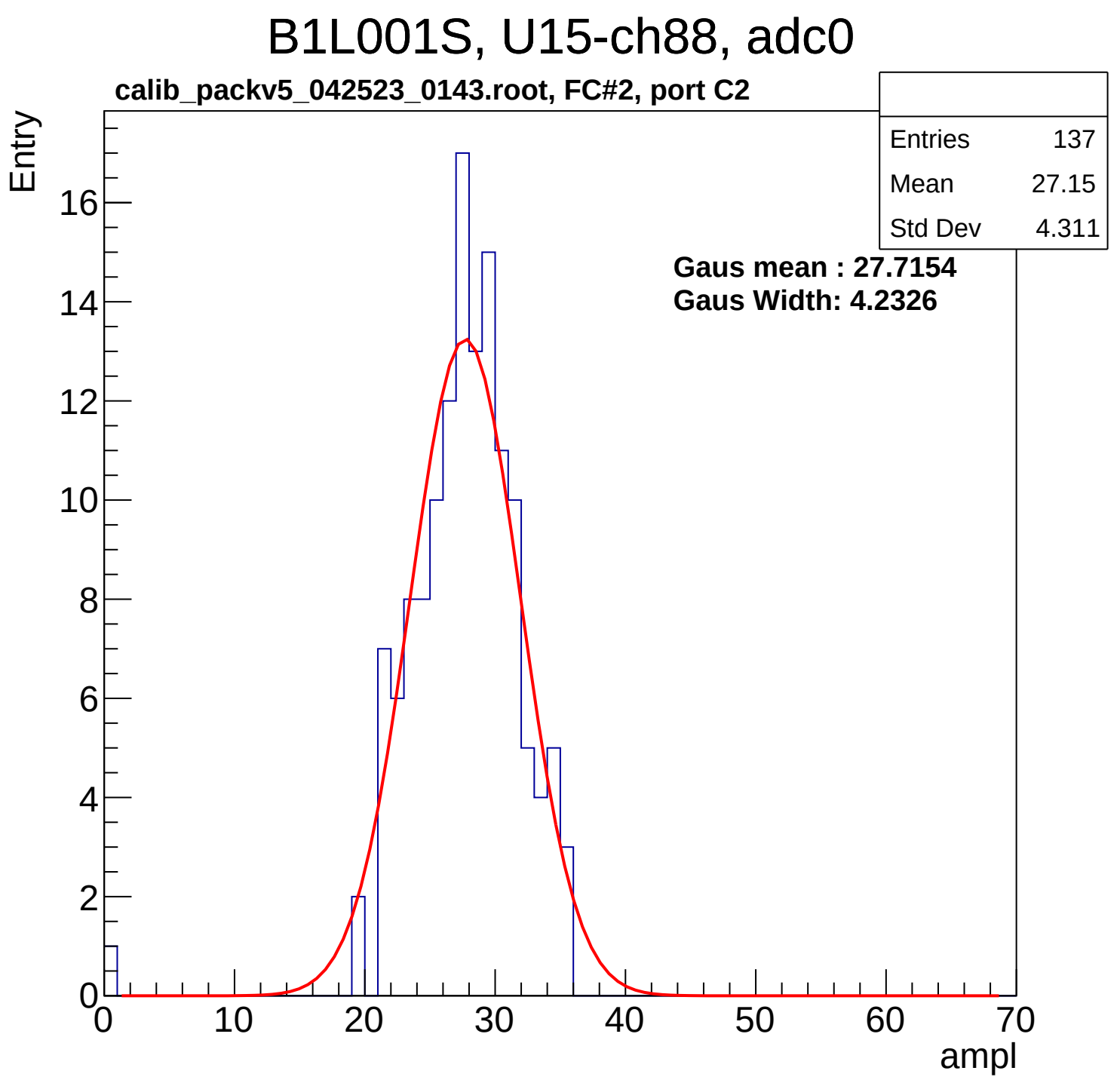
50
60
70
ampl


B1L001S, U15-ch88, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 27.15
Std Dev 4.311
Gaus mean : 27.7154
Gaus Width: 4.2326
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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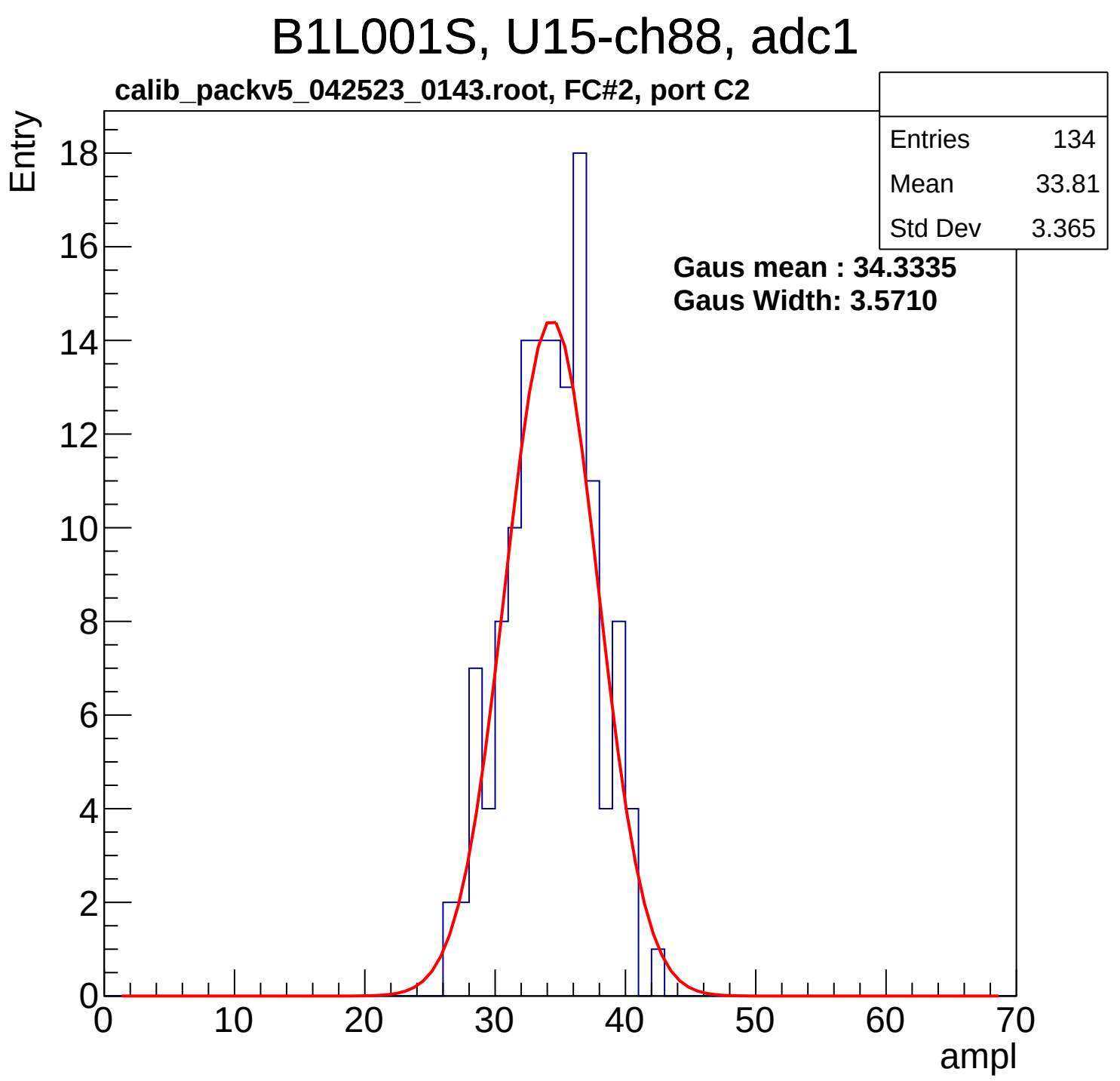
B1L001S, U15-ch88, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 33.81
Std Dev 3.365
Gaus mean : 34.3335
Gaus Width: 3.5710
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
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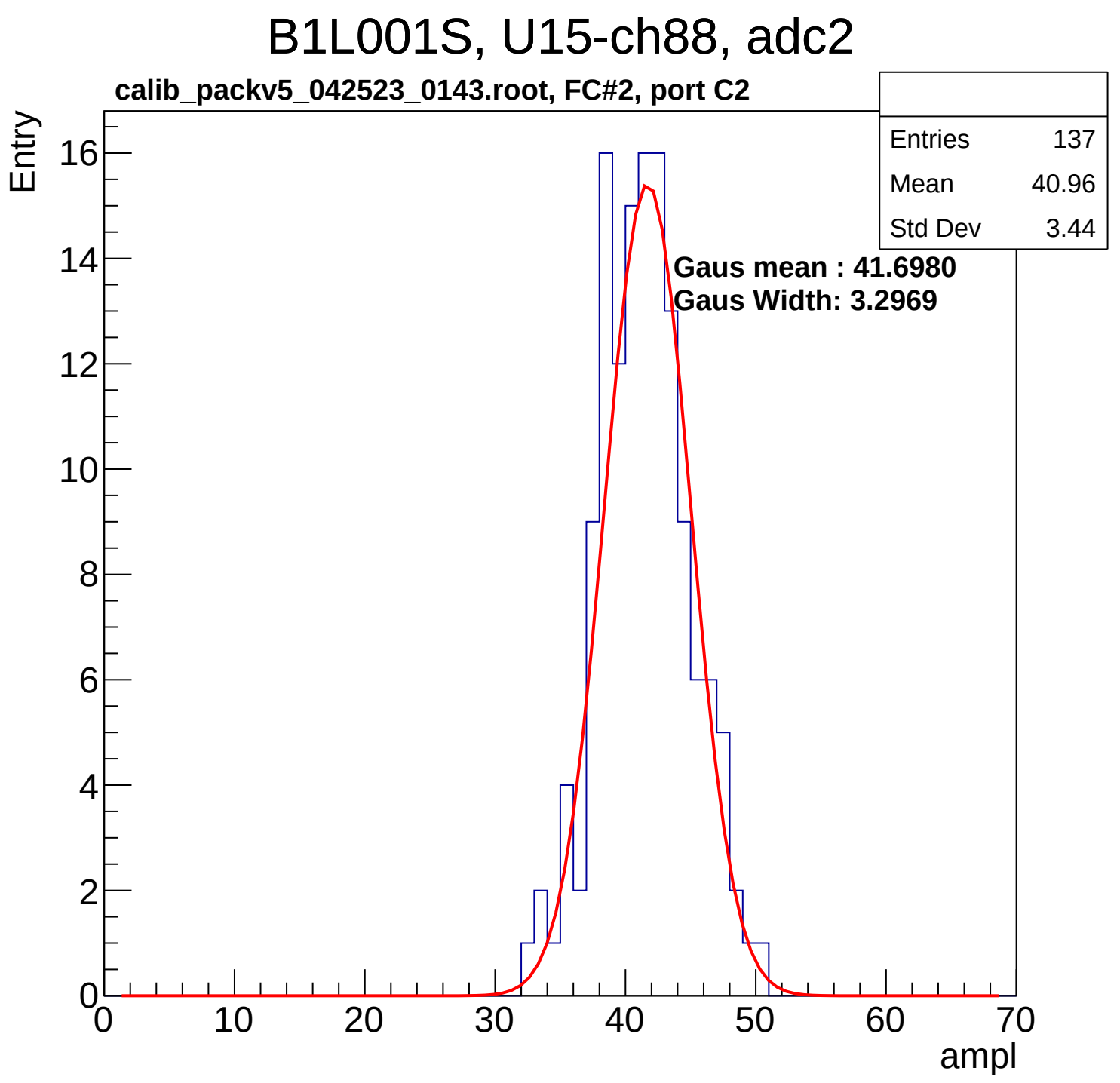
B1L001S, U15-ch88, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 40.96
Std Dev 3.44
Gaus mean : 41.6980
Gaus Width: 3.2969
Entry
ampl
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4
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8
10
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0
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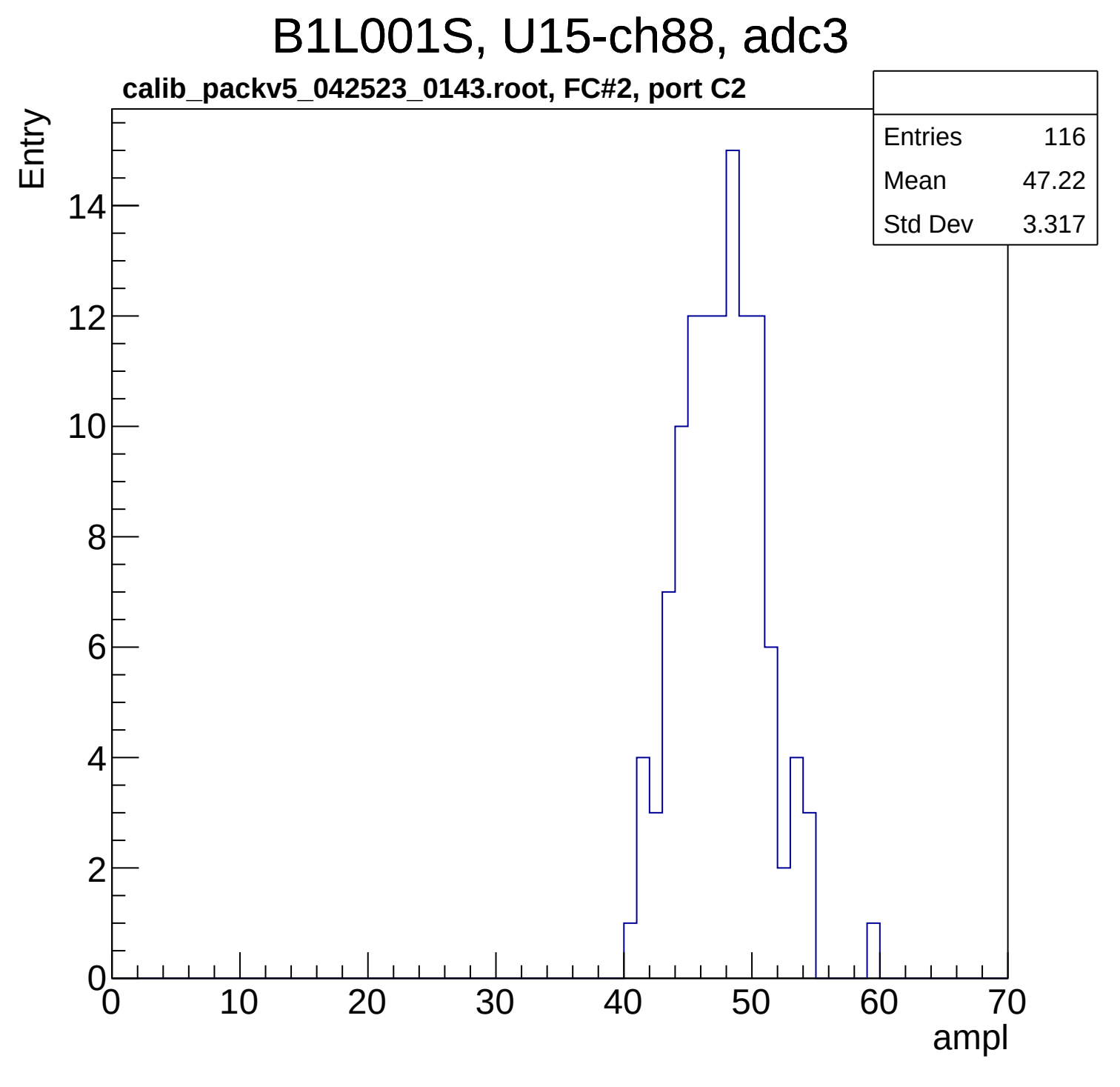
B1L001S, U15-ch88, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 47.22
Std Dev 3.317
Entry
ampl
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6
8
10
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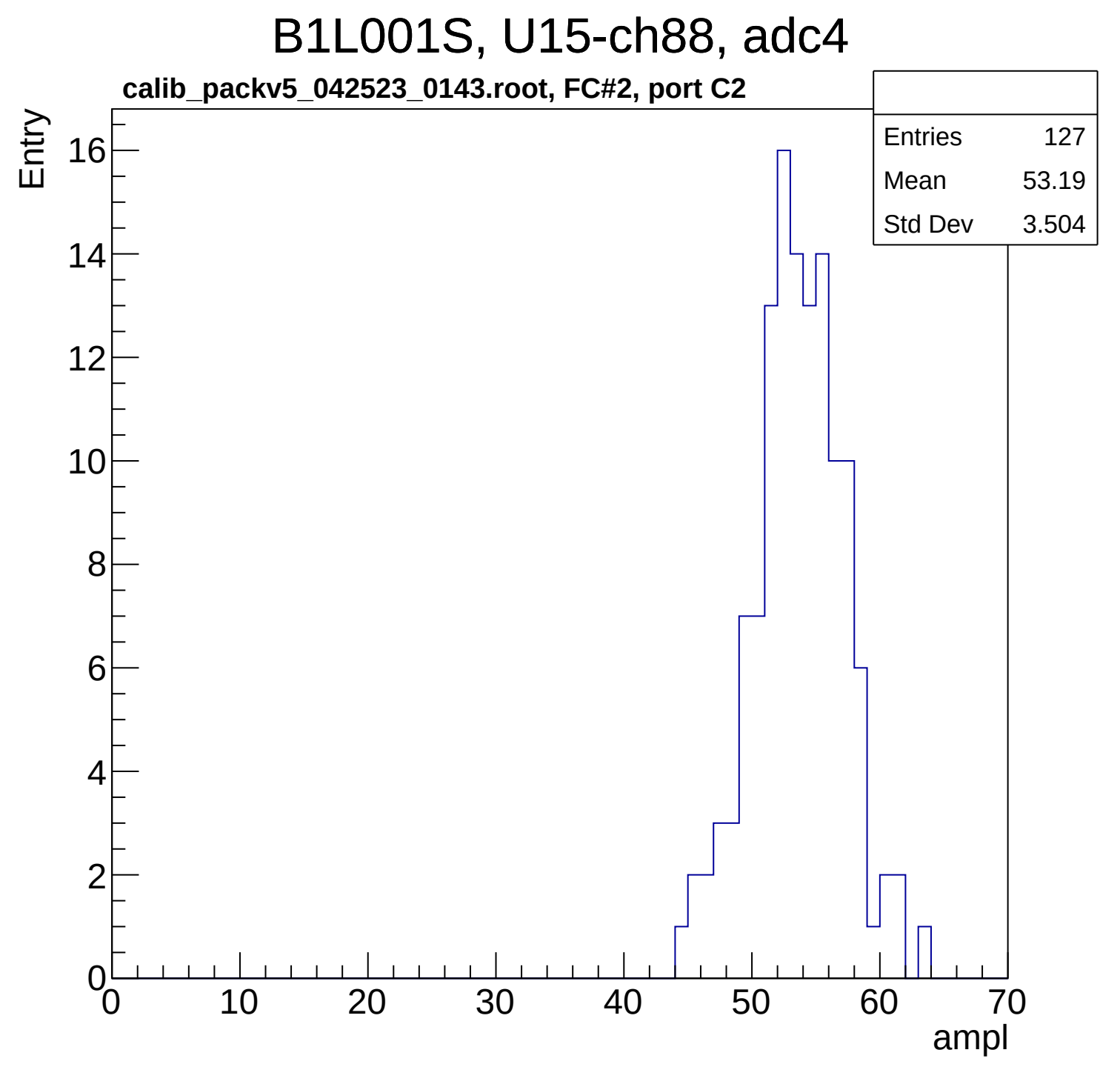
B1L001S, U15-ch88, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 53.19
Std Dev 3.504
Entry
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4
6
8
10
12
14
16
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10
20
30
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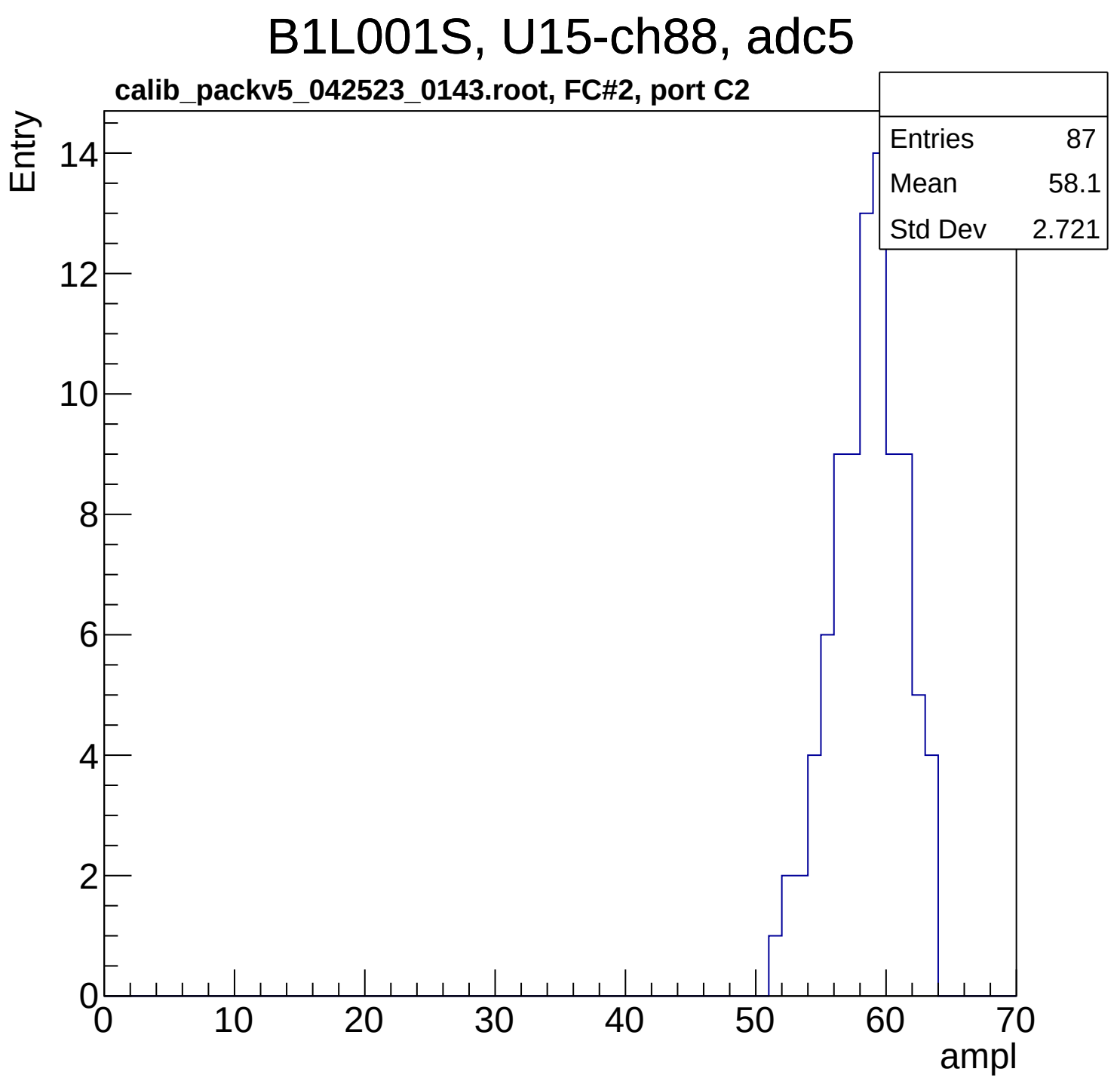
50
60
70
ampl


B1L001S, U15-ch88, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 58.1
Std Dev 2.721
Entry
ampl
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6
8
10
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14
0
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60
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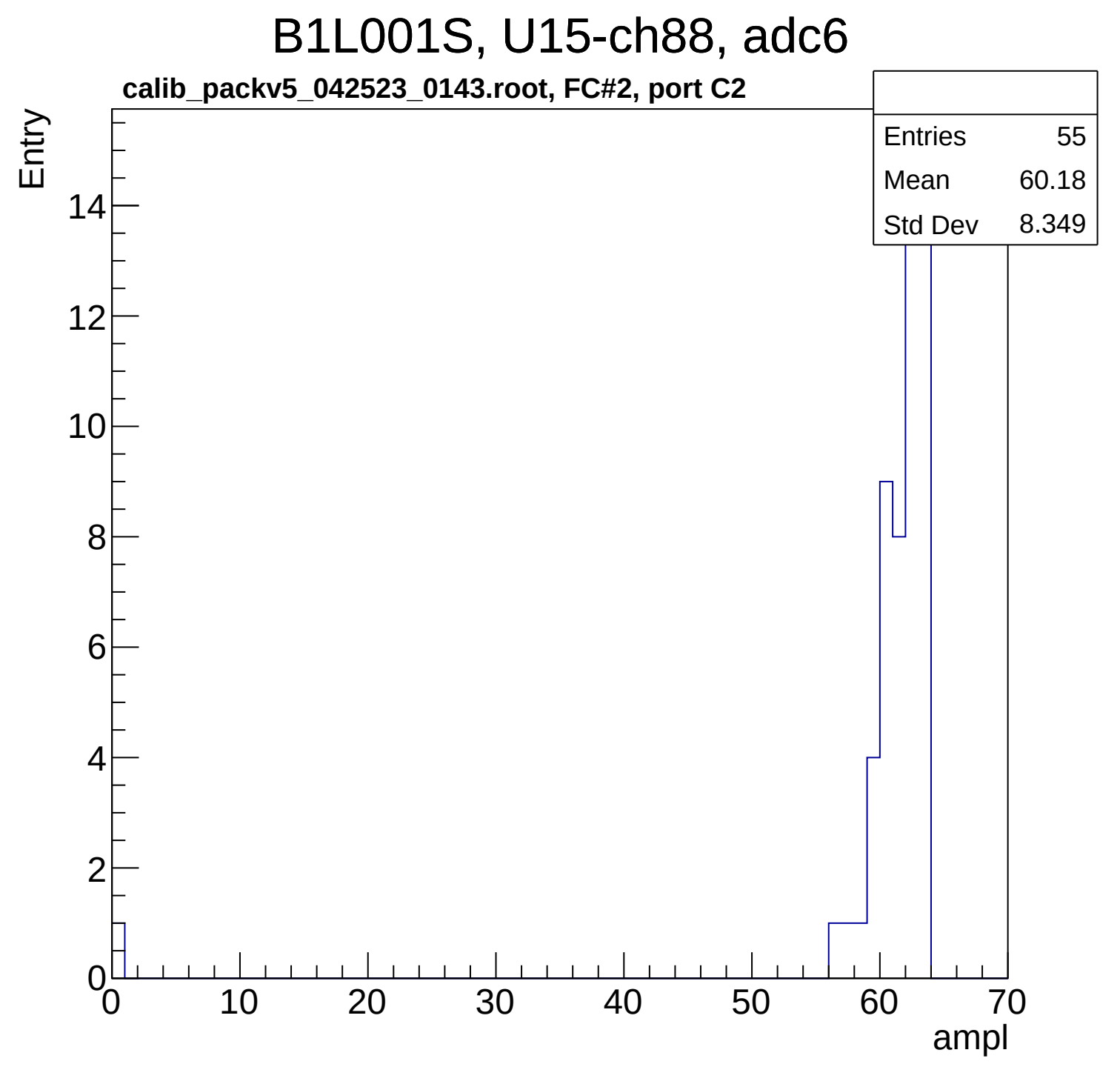
B1L001S, U15-ch88, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 55
Mean 60.18
Std Dev 8.349
Entry
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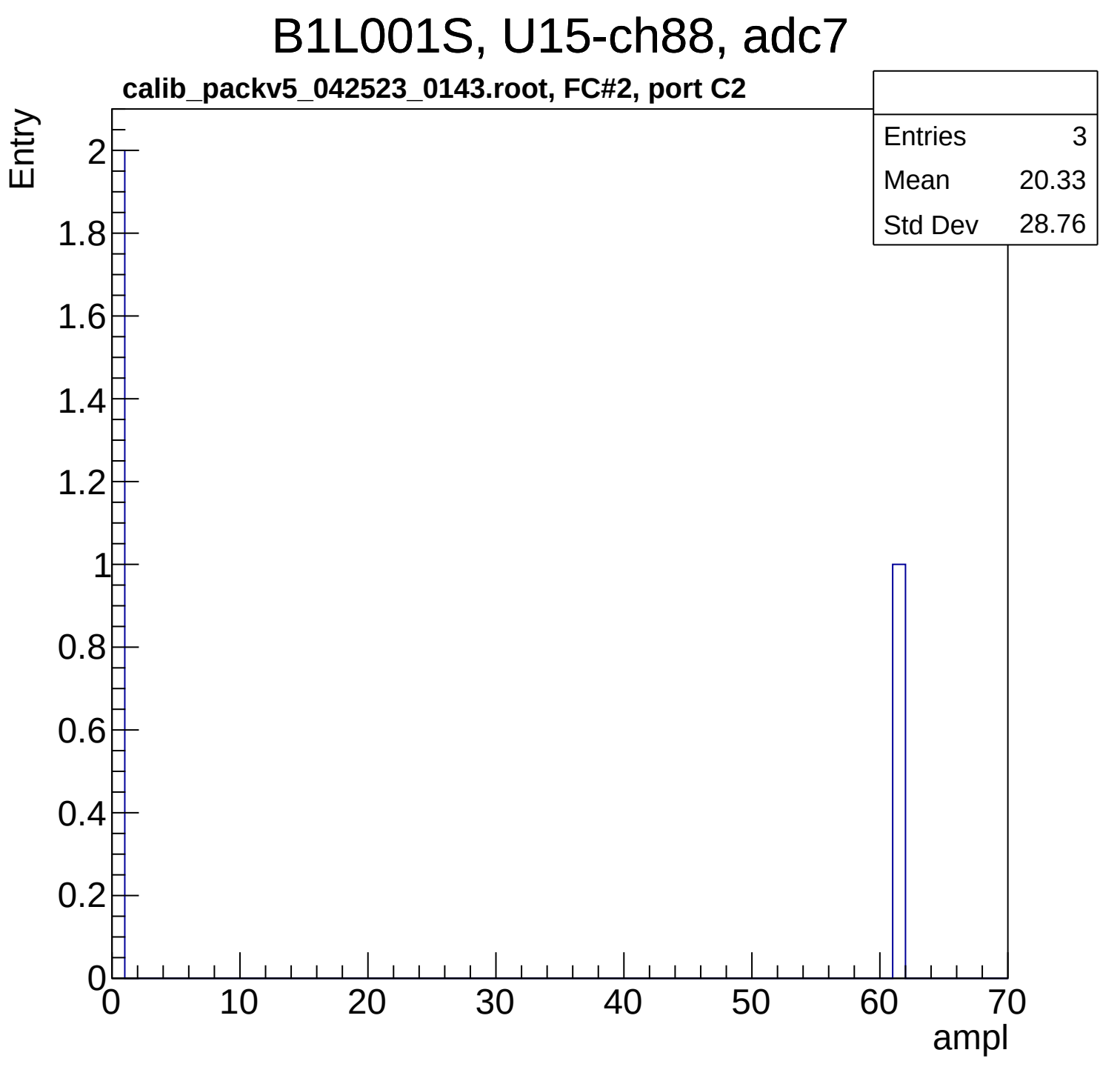
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch88, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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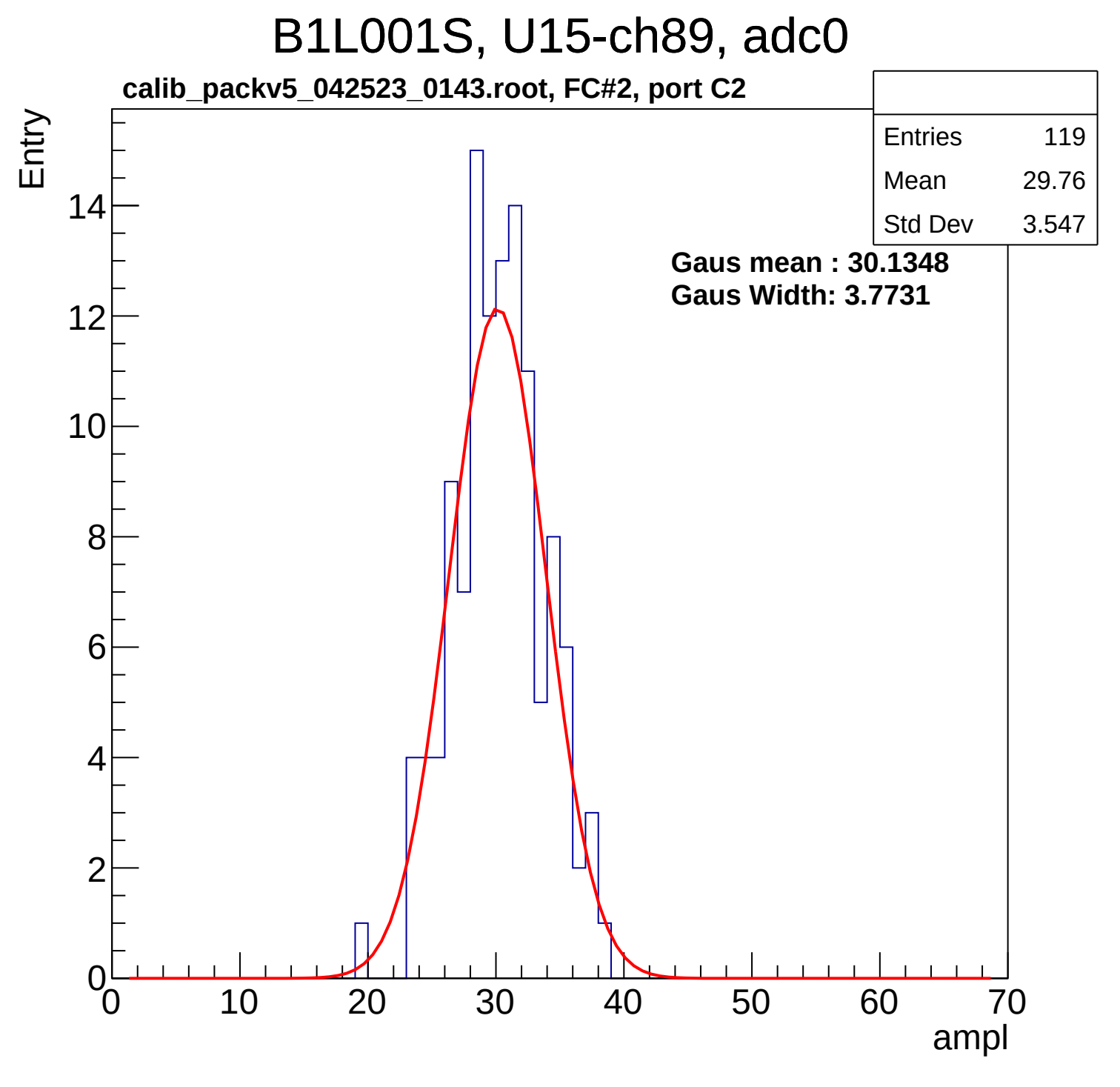
B1L001S, U15-ch89, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 29.76
Std Dev 3.547
Gaus mean : 30.1348
Gaus Width: 3.7731
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
30
40
50
60
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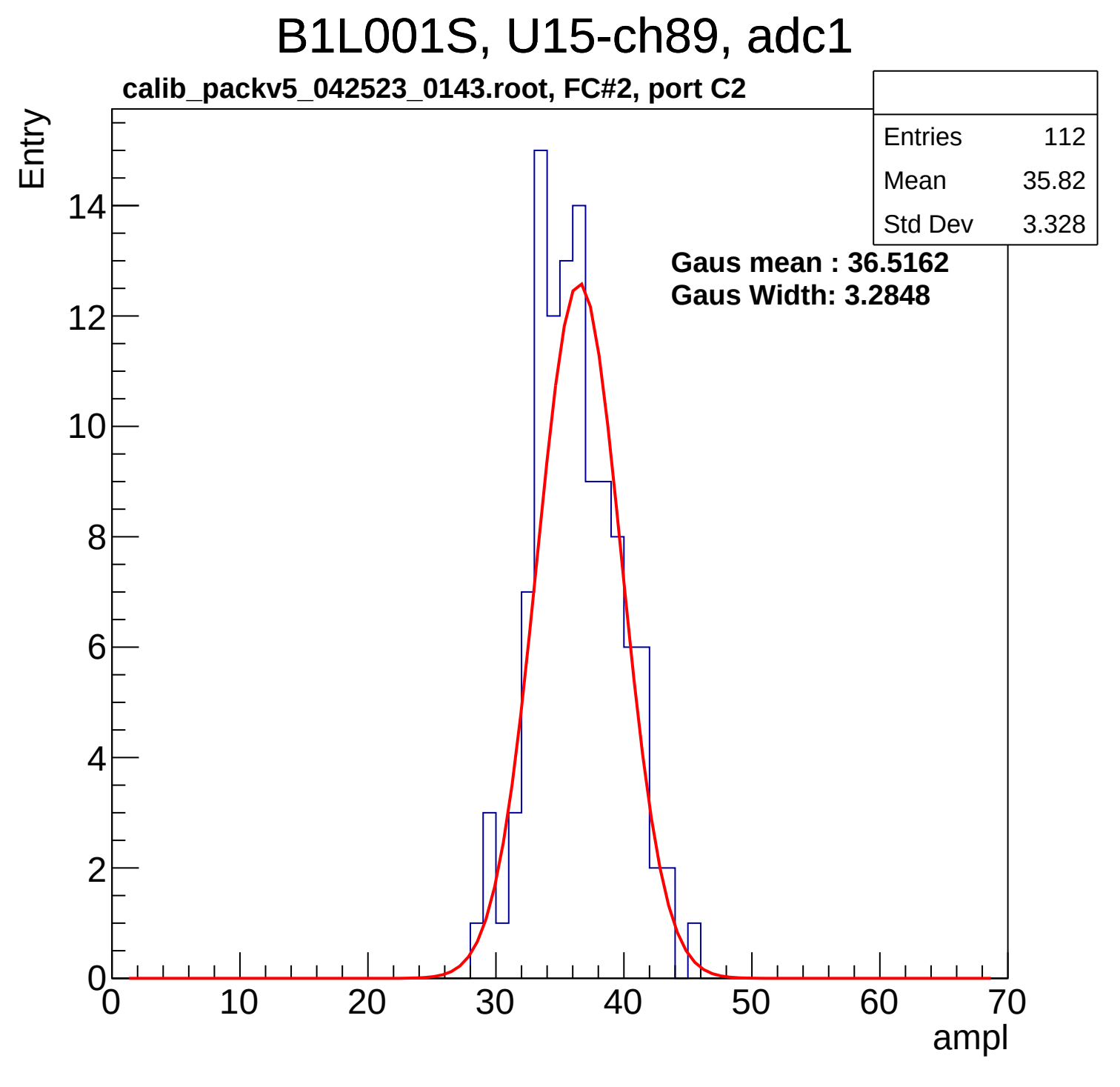
B1L001S, U15-ch89, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 35.82
Std Dev 3.328
Gaus mean : 36.5162
Gaus Width: 3.2848
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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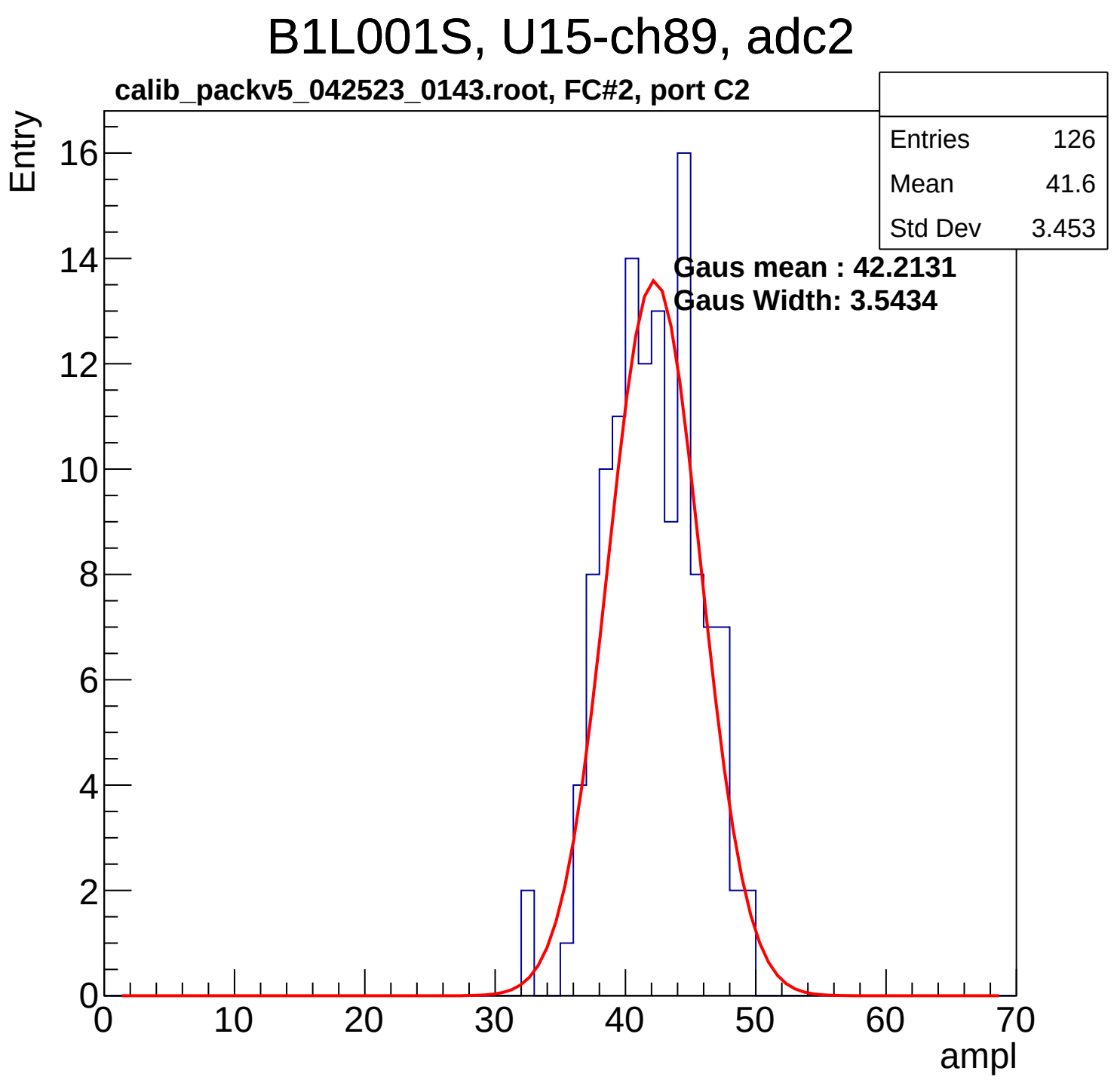
B1L001S, U15-ch89, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 41.6
Std Dev 3.453
Gaus mean : 42.2131
Gaus Width: 3.5434
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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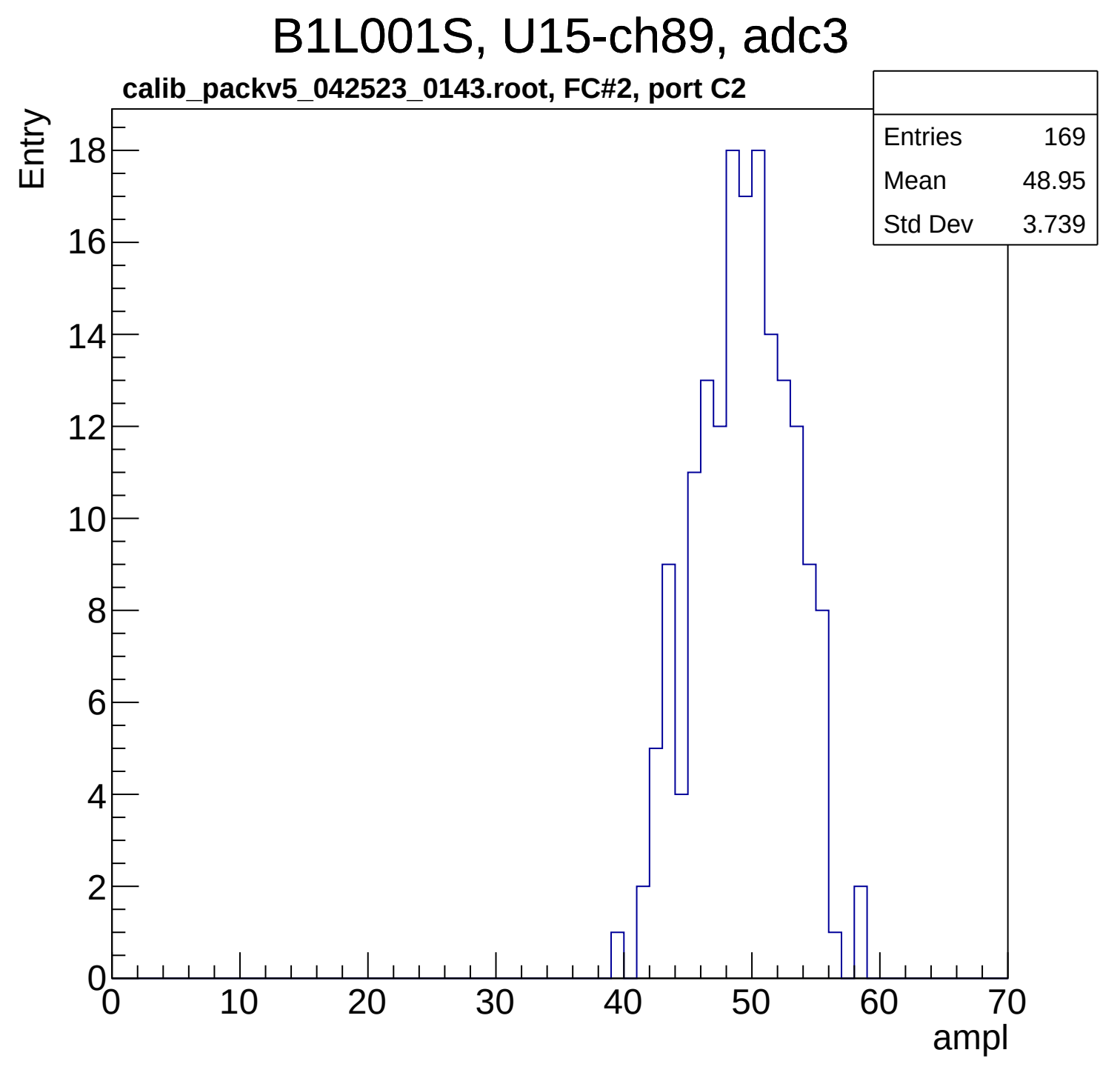
B1L001S, U15-ch89, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 169
Mean 48.95
Std Dev 3.739
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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60
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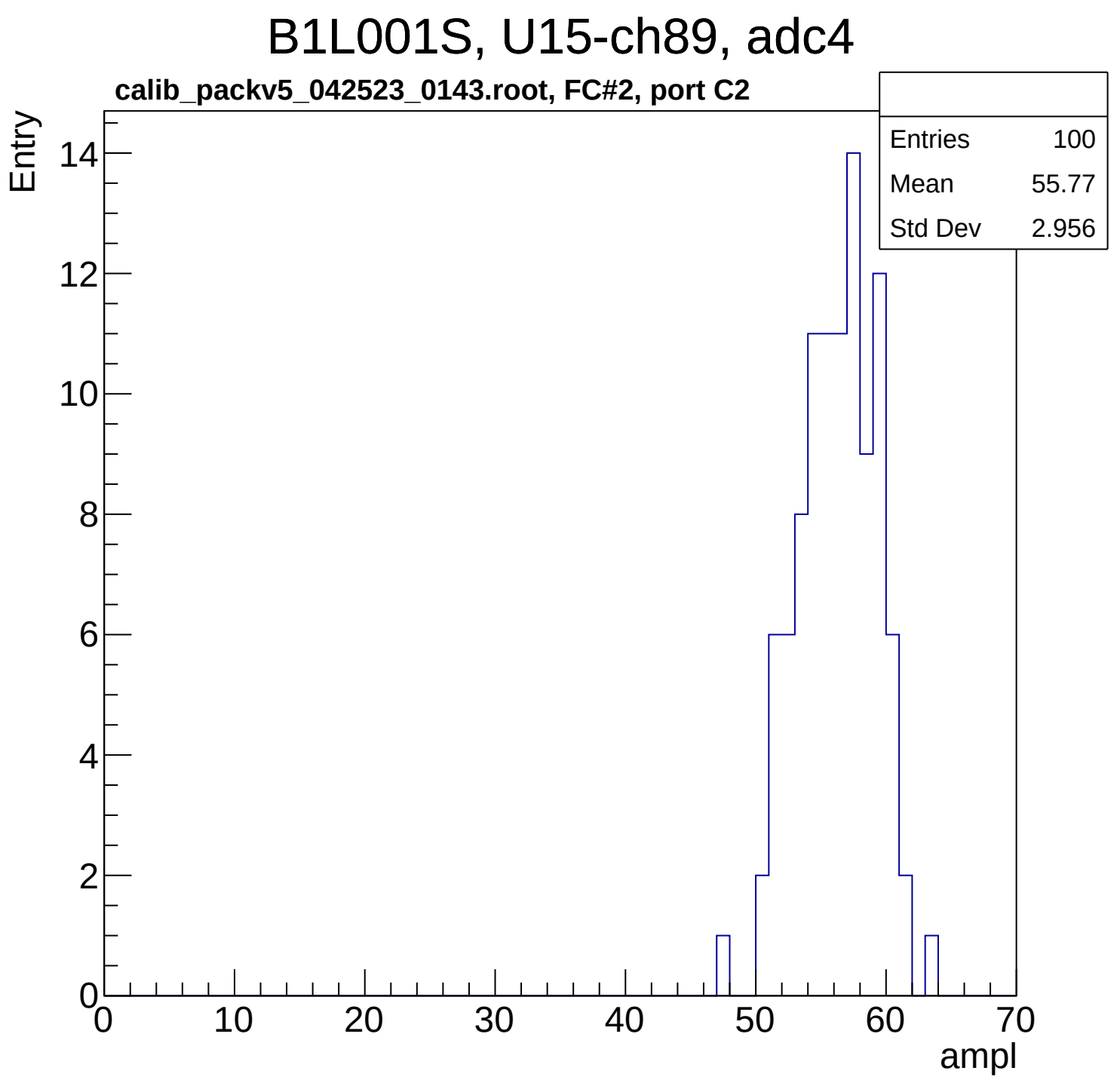
B1L001S, U15-ch89, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 55.77
Std Dev 2.956
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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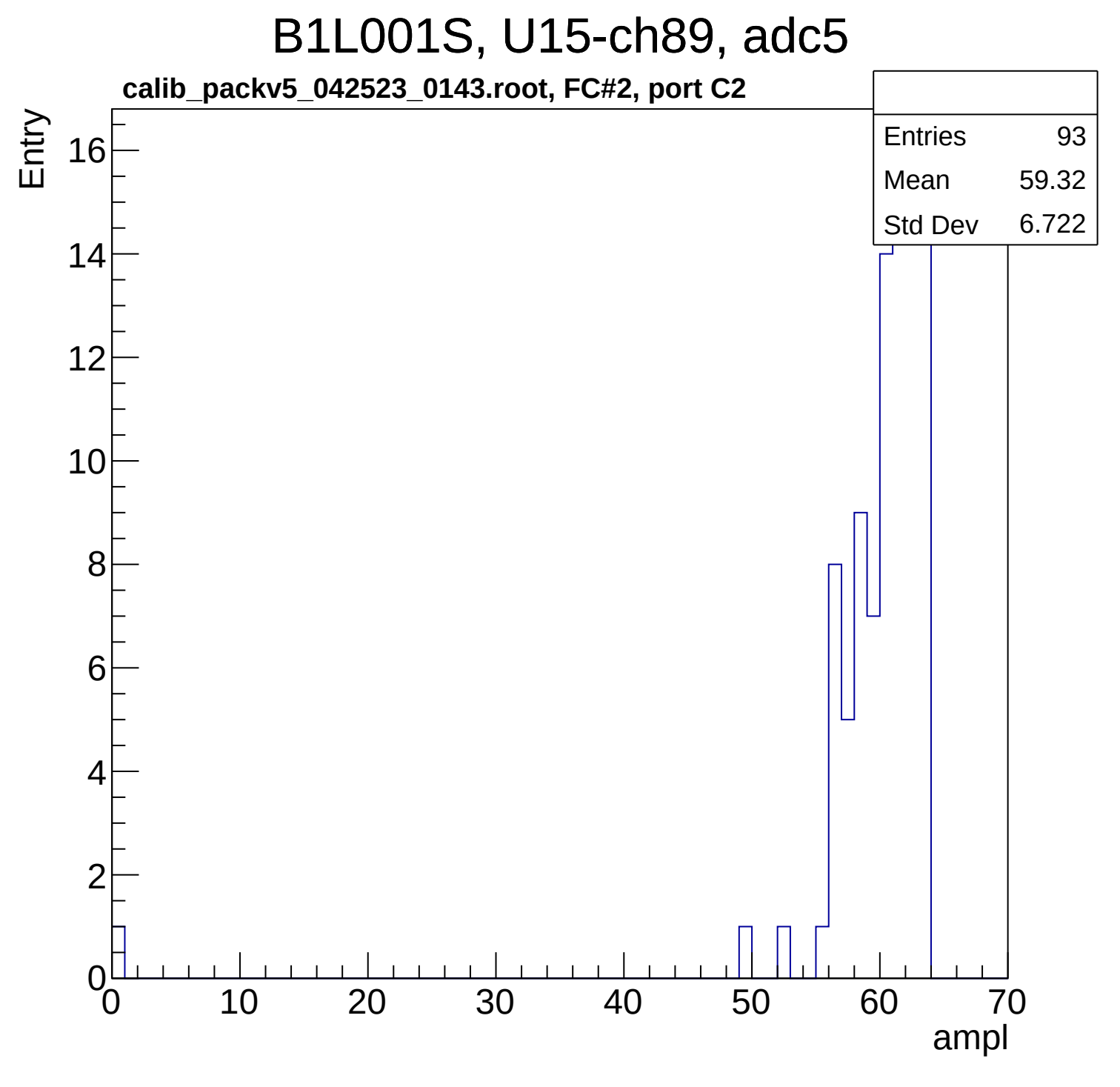
B1L001S, U15-ch89, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 59.32
Std Dev 6.722
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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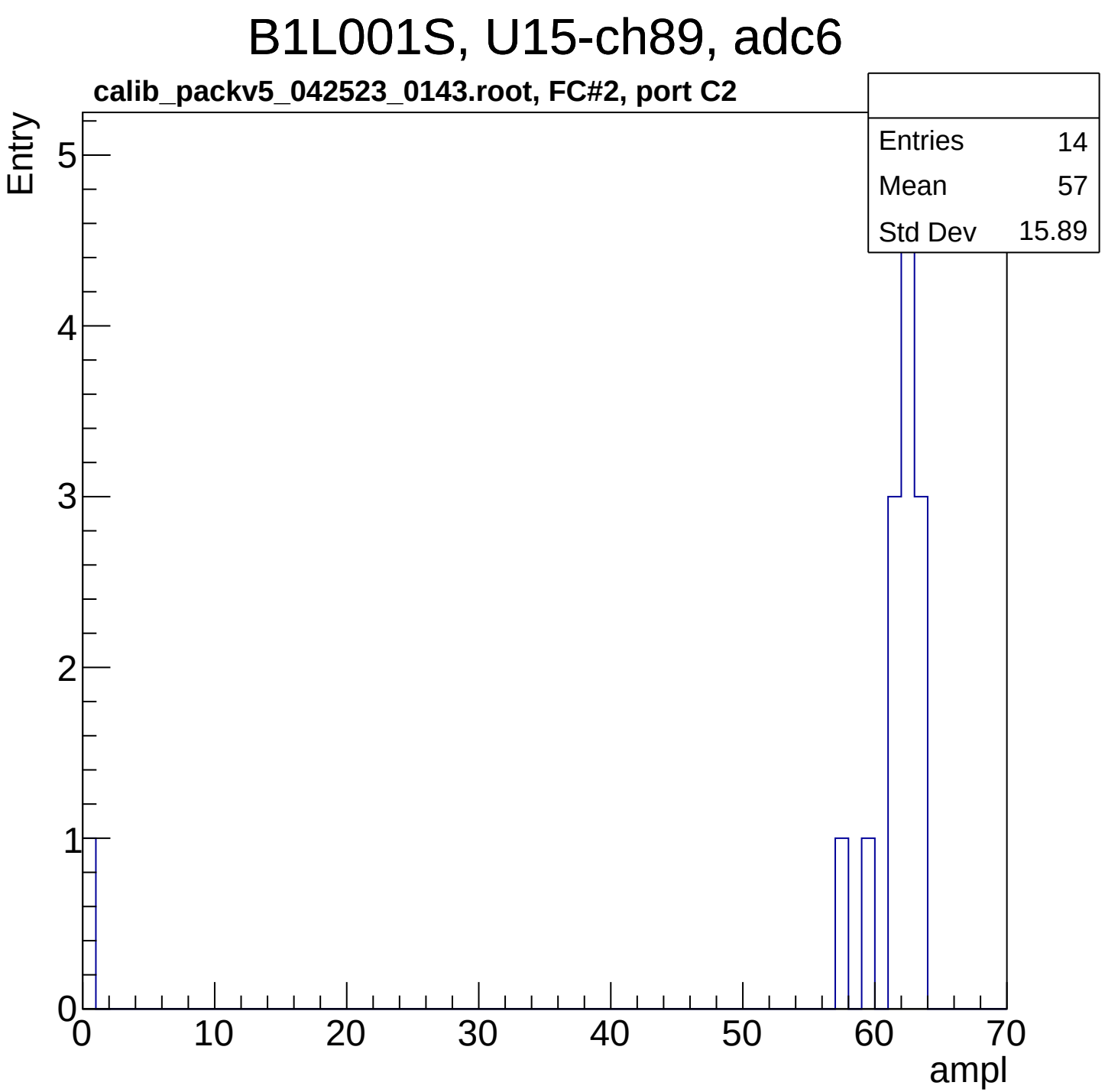
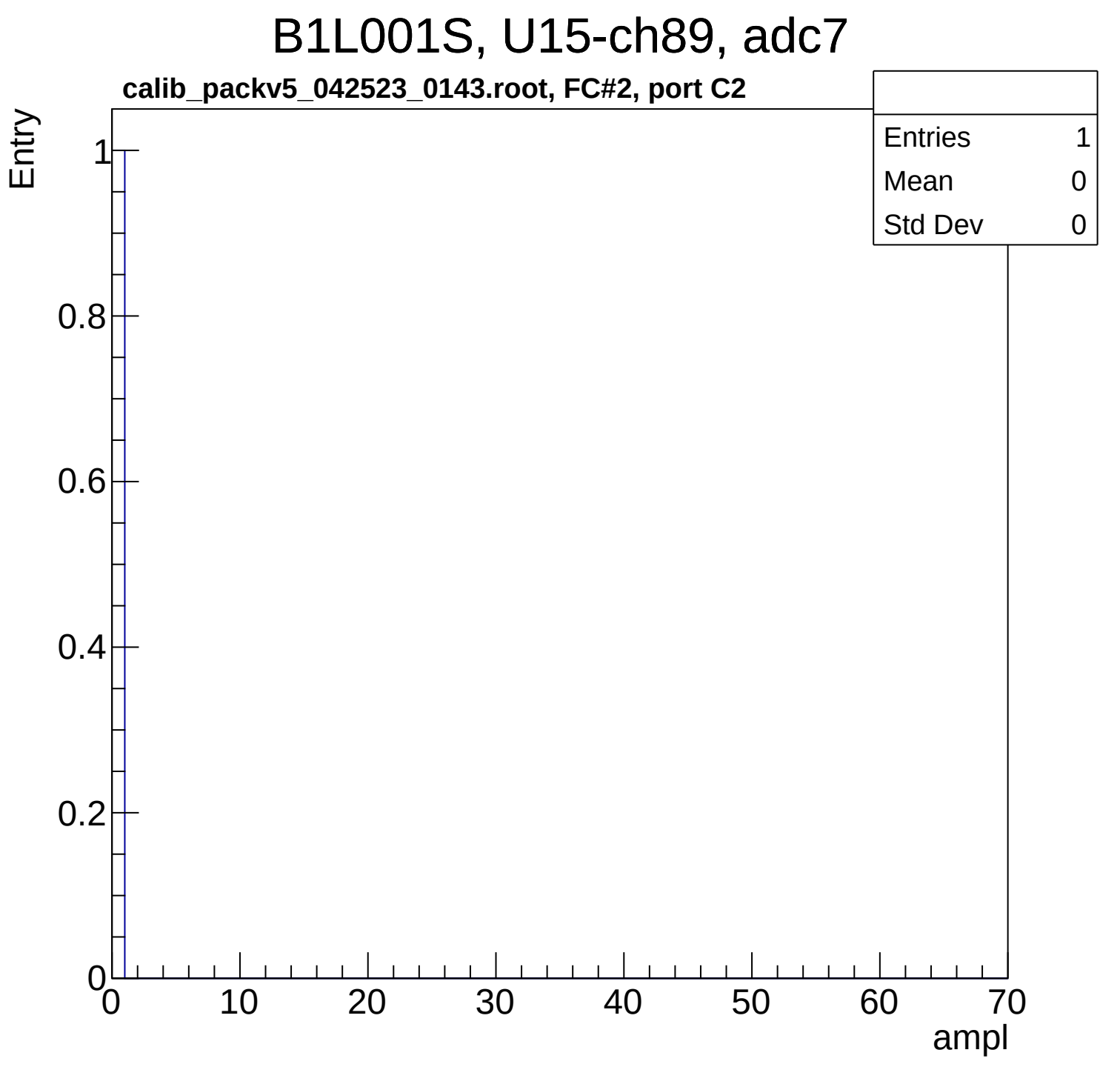
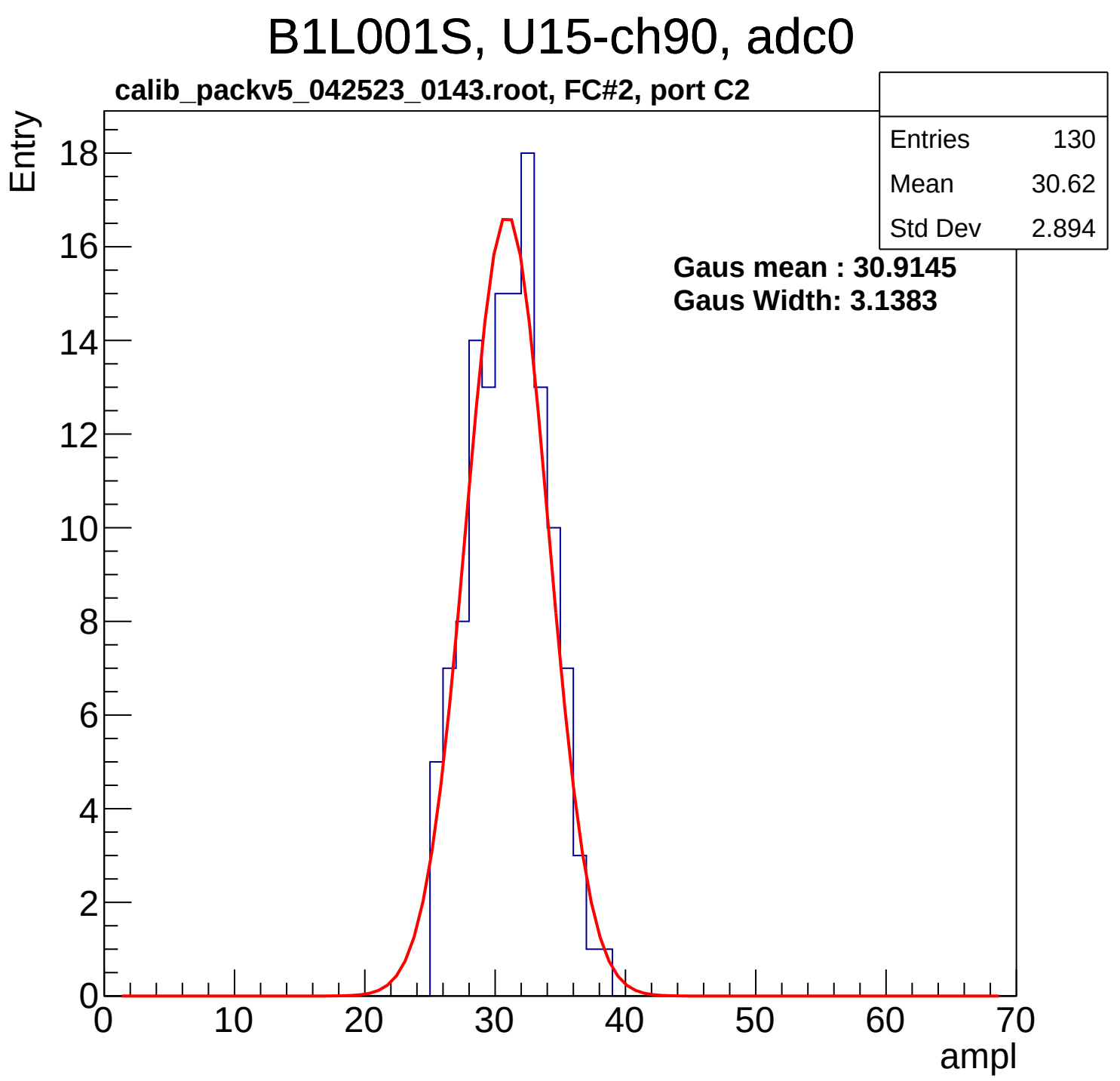

B1L001S, U15-ch89, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 57
Std Dev 15.89
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch89, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch90, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 30.62
Std Dev 2.894
Gaus mean : 30.9145
Gaus Width: 3.1383
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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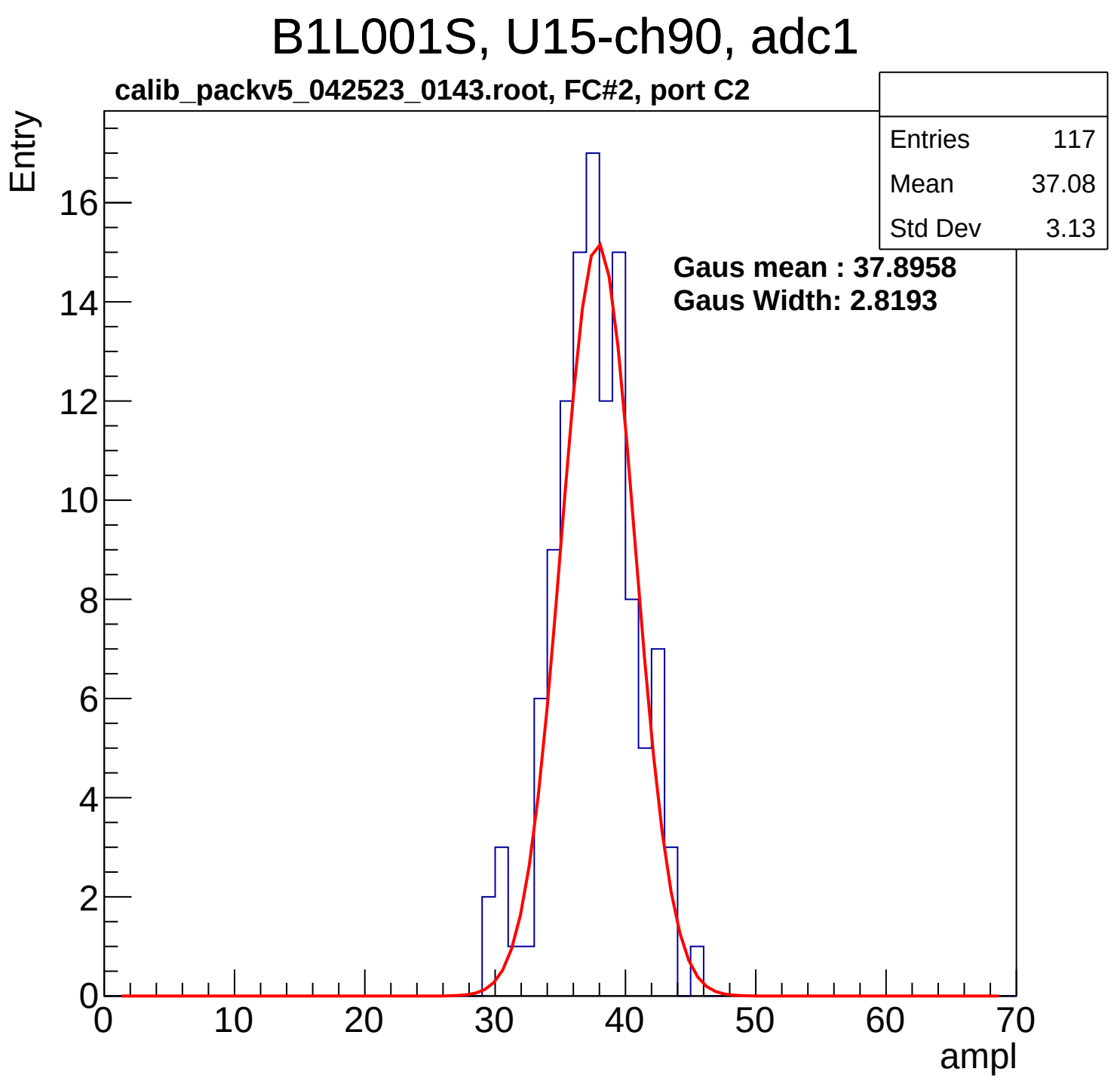
B1L001S, U15-ch90, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 37.08
Std Dev 3.13
Gaus mean : 37.8958
Gaus Width: 2.8193
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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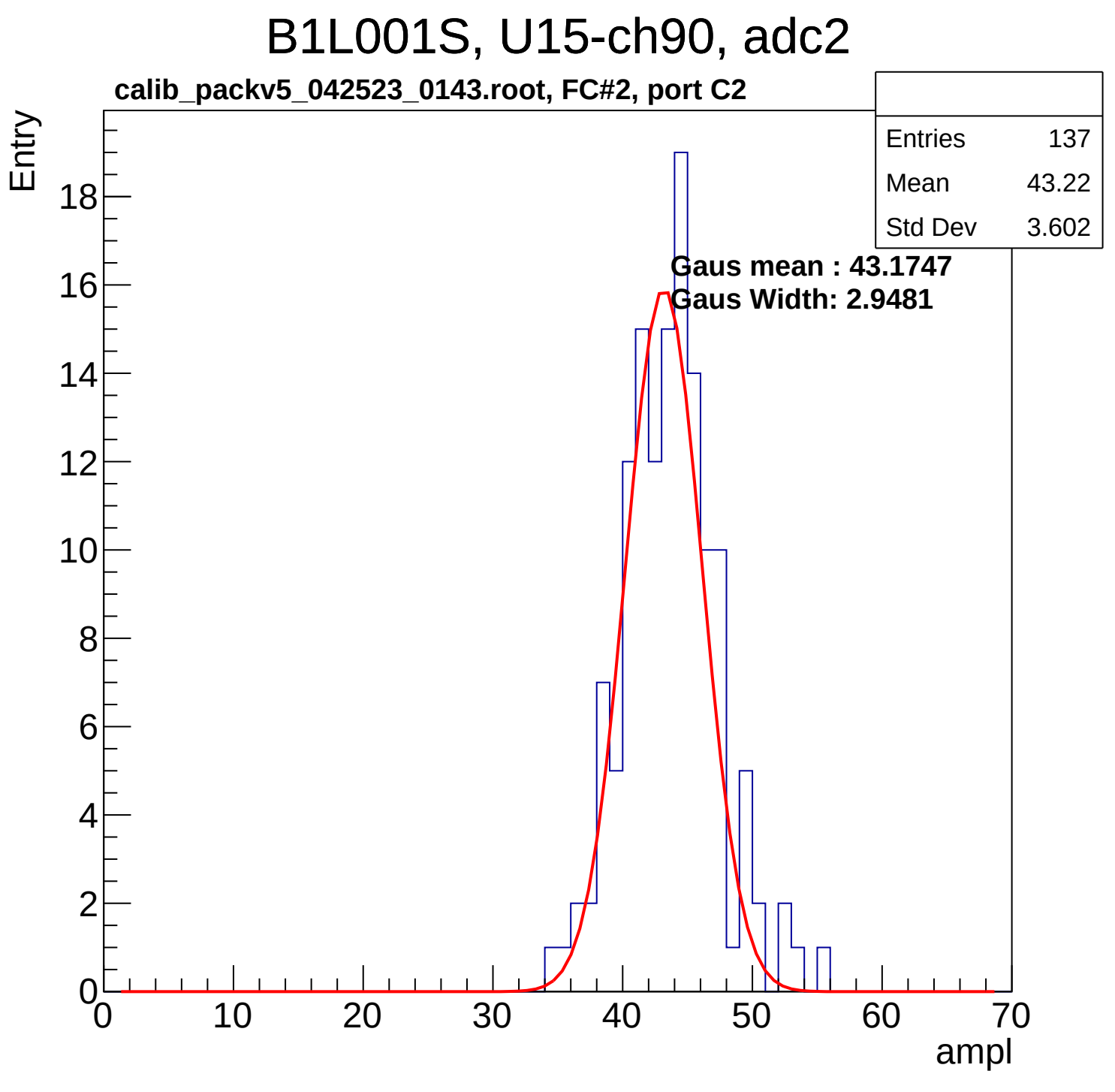
B1L001S, U15-ch90, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 43.22
Std Dev 3.602
Gaus mean : 43.1747
Gaus Width: 2.9481
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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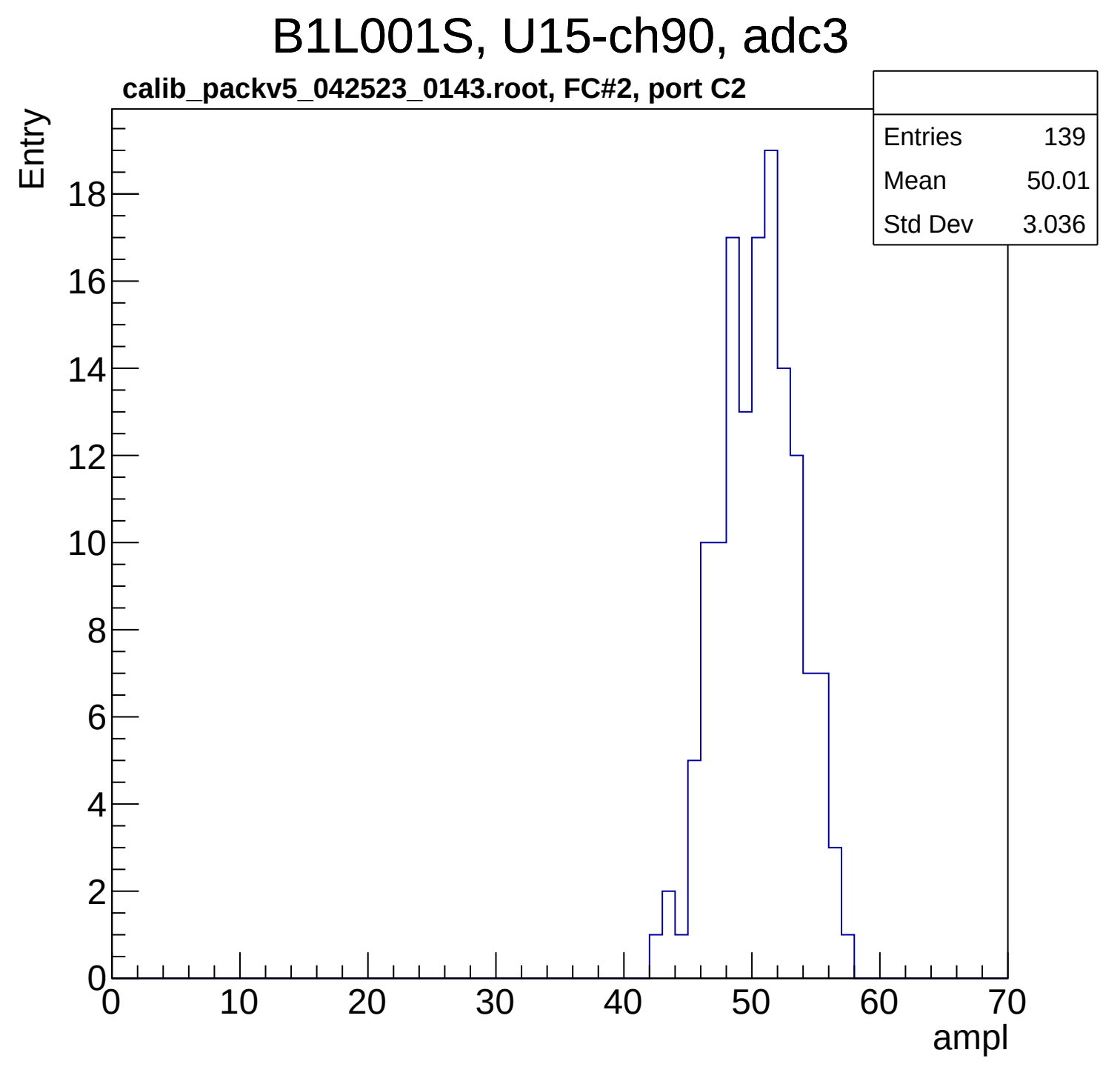
B1L001S, U15-ch90, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 50.01
Std Dev 3.036
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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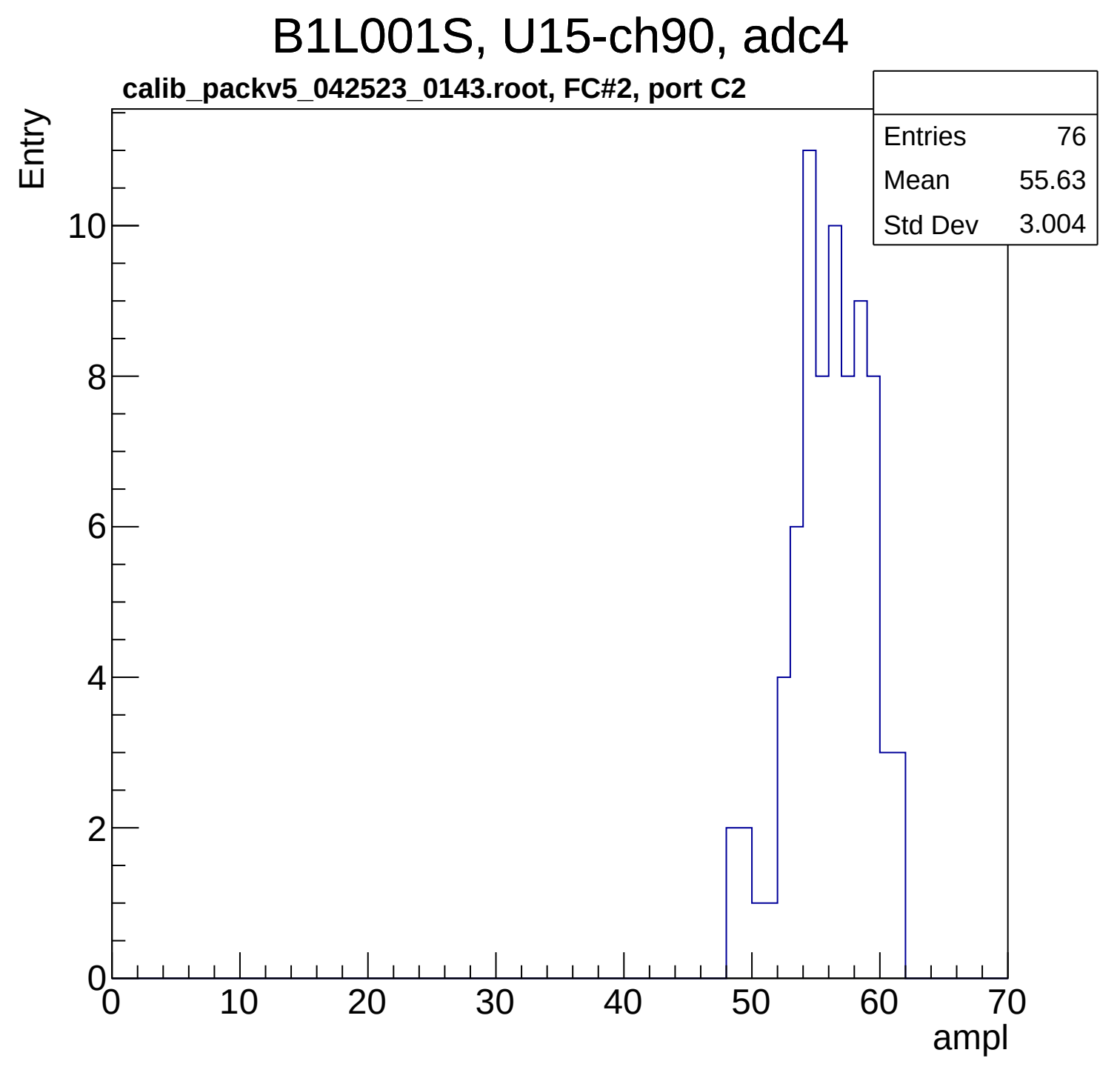
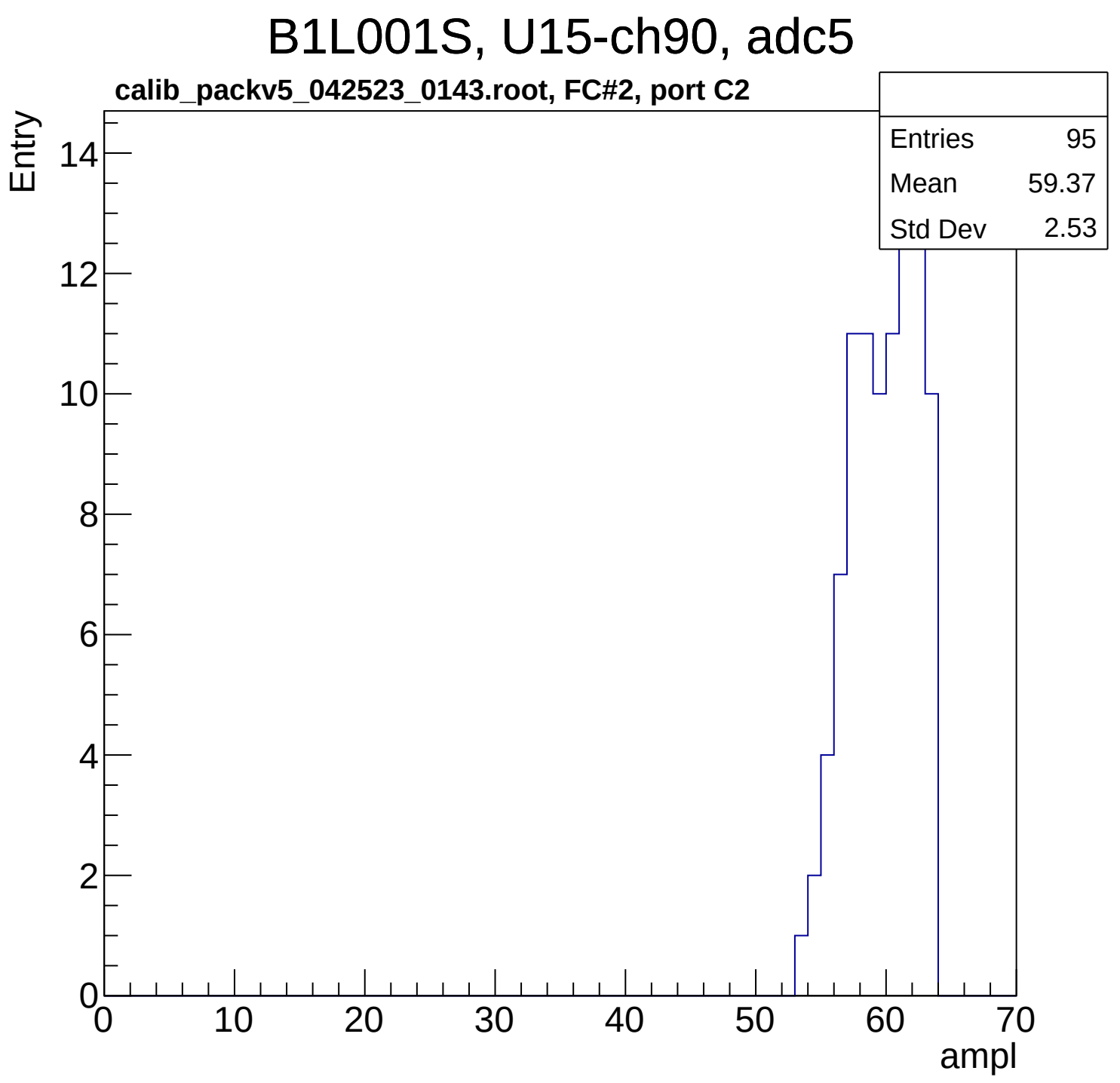

B1L001S, U15-ch90, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 55.63
Std Dev 3.004
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch90, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 59.37
Std Dev 2.53
Entry
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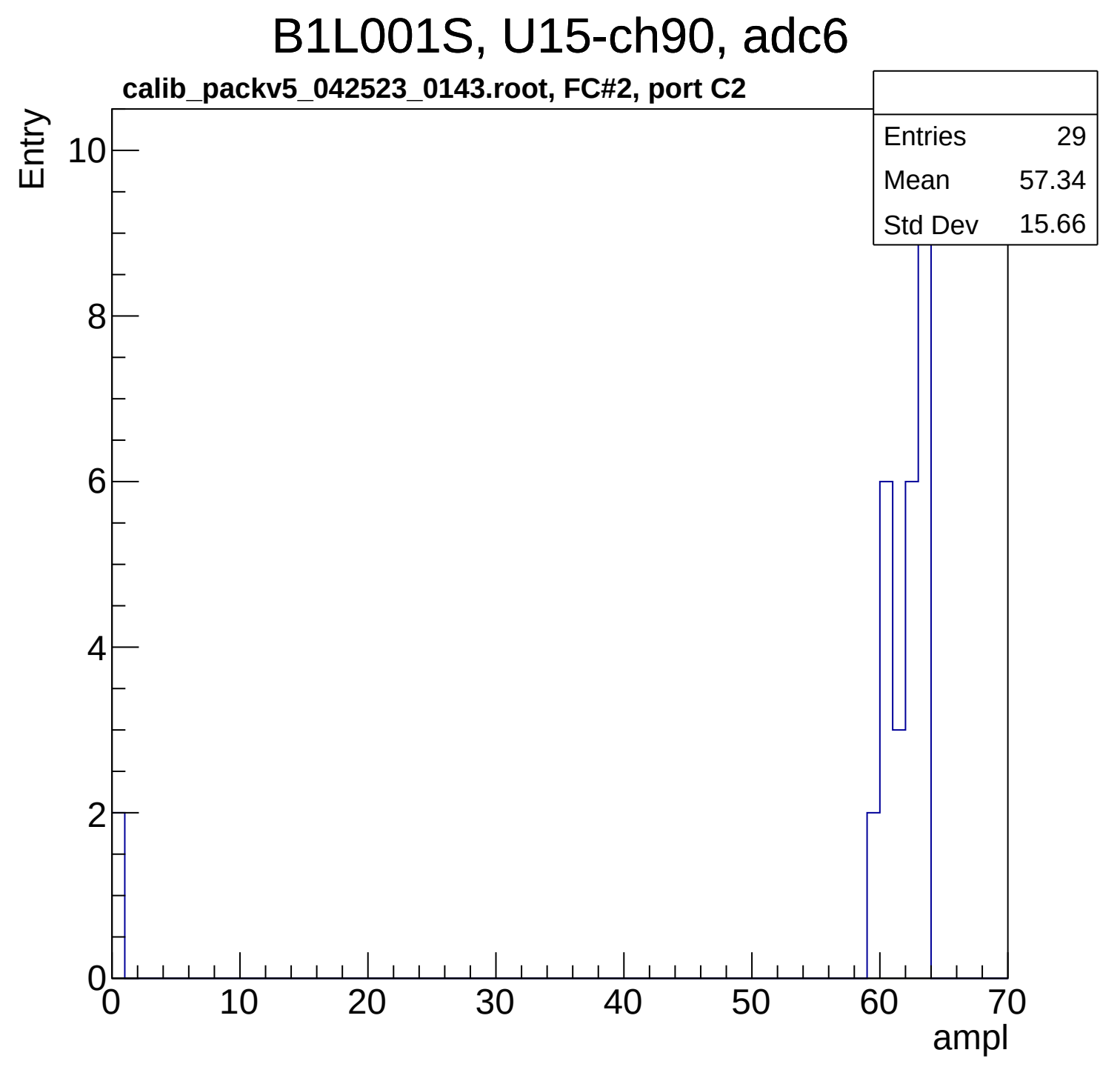
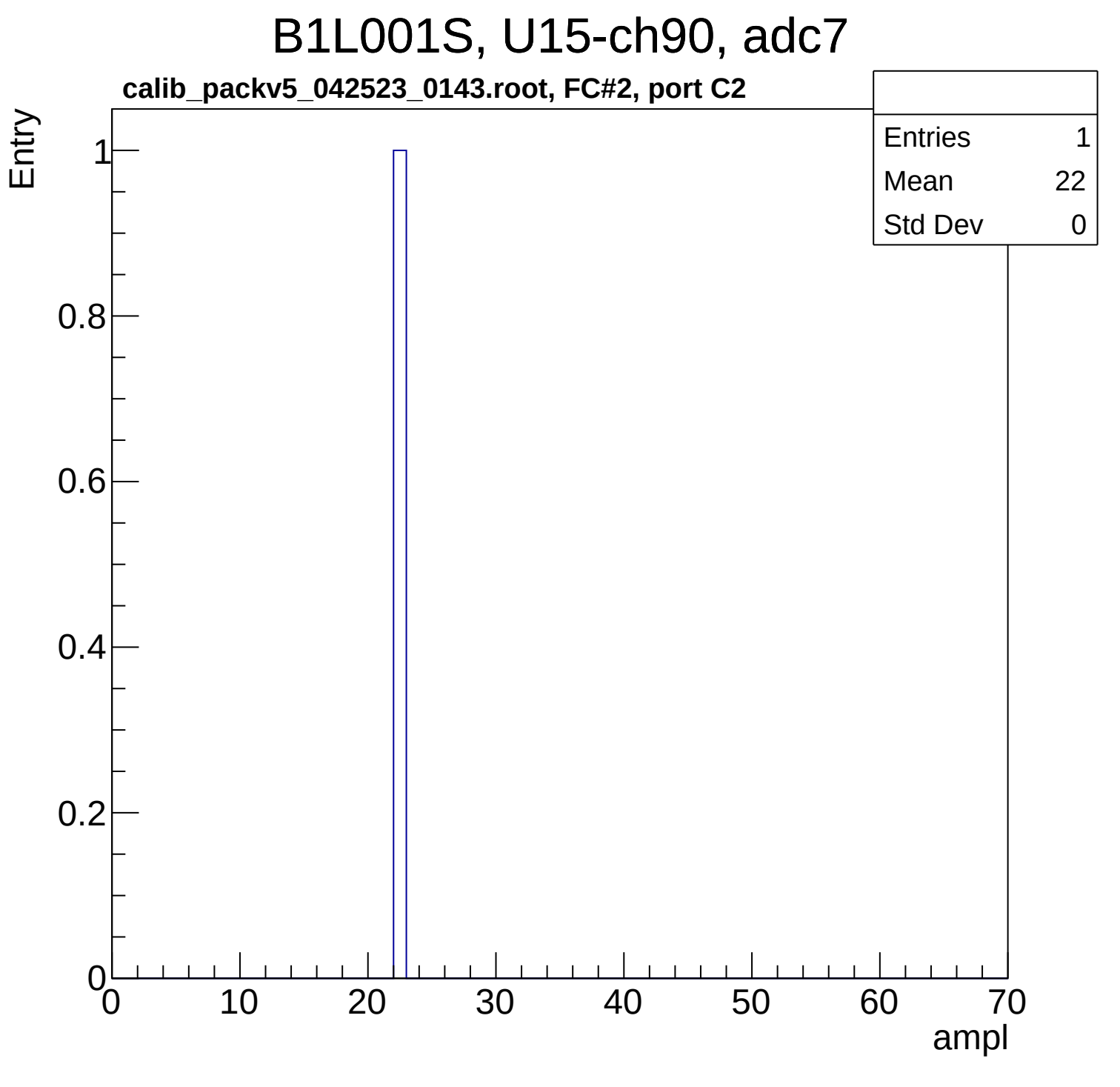
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch90, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 29
Mean 57.34
Std Dev 15.66
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L001S, U15-ch90, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
ampl
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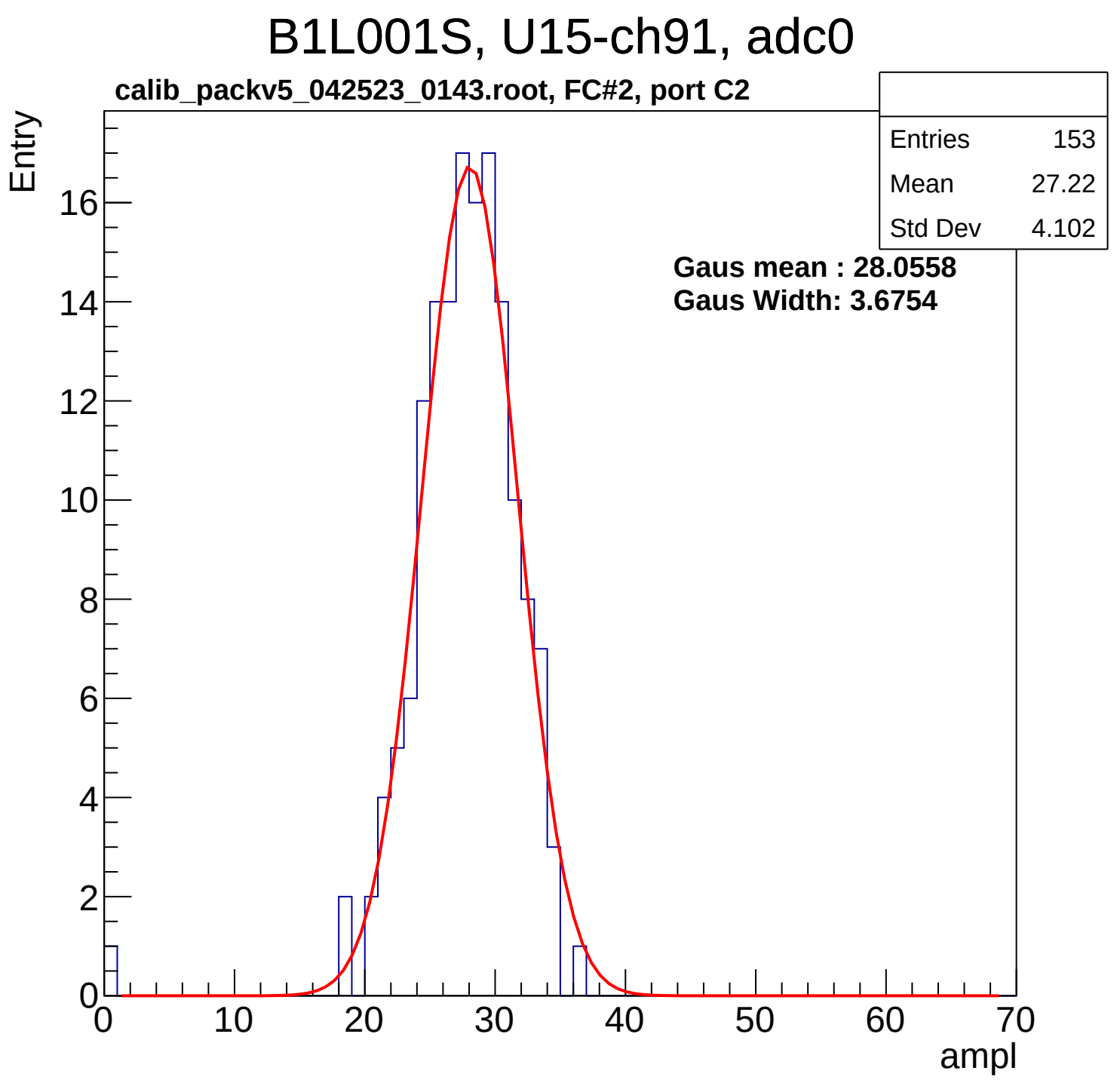
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U15-ch91, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 27.22
Std Dev 4.102
Gaus mean : 28.0558
Gaus Width: 3.6754
Entry
ampl
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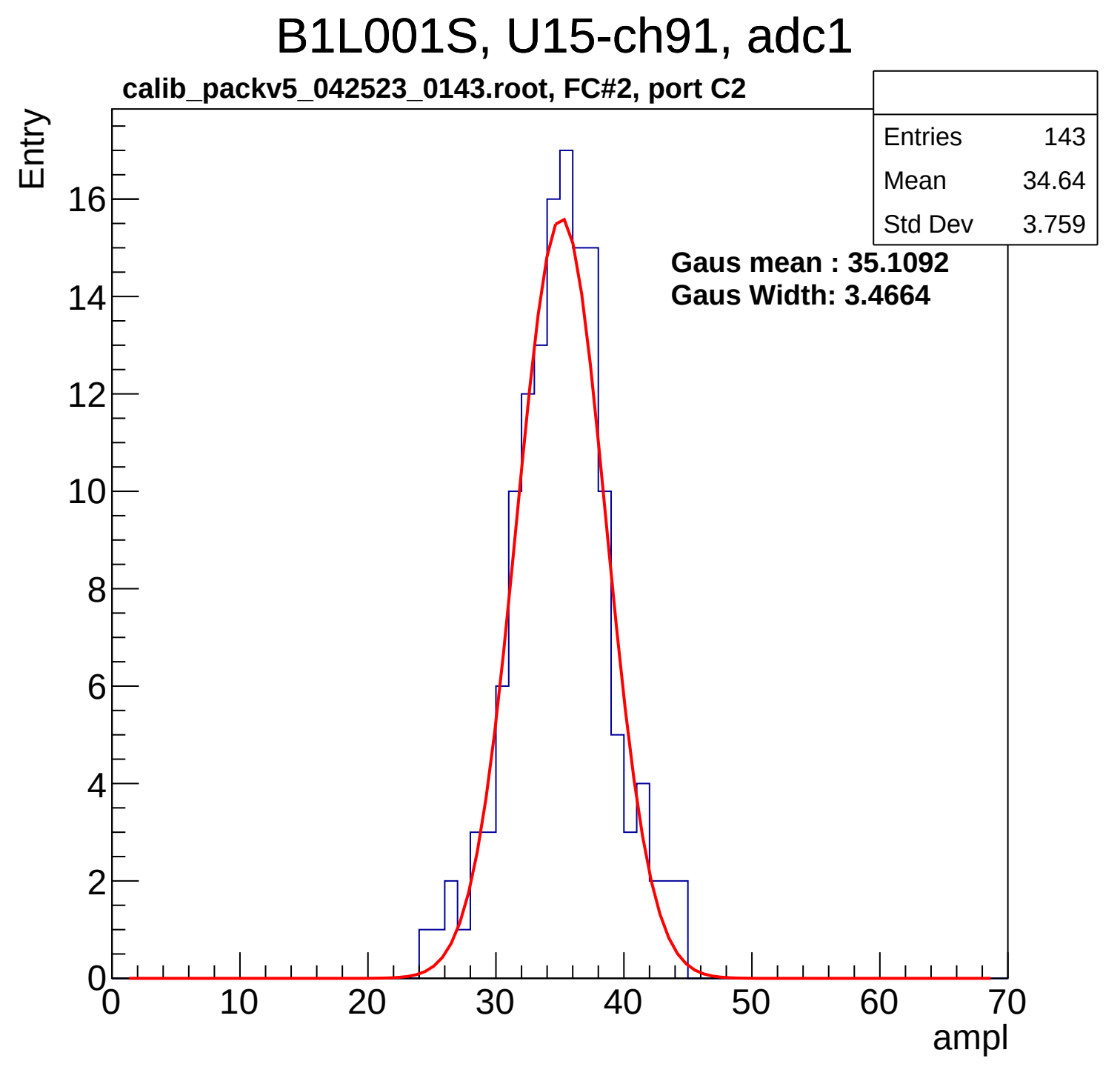
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch91, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 34.64
Std Dev 3.759
Gaus mean : 35.1092
Gaus Width: 3.4664
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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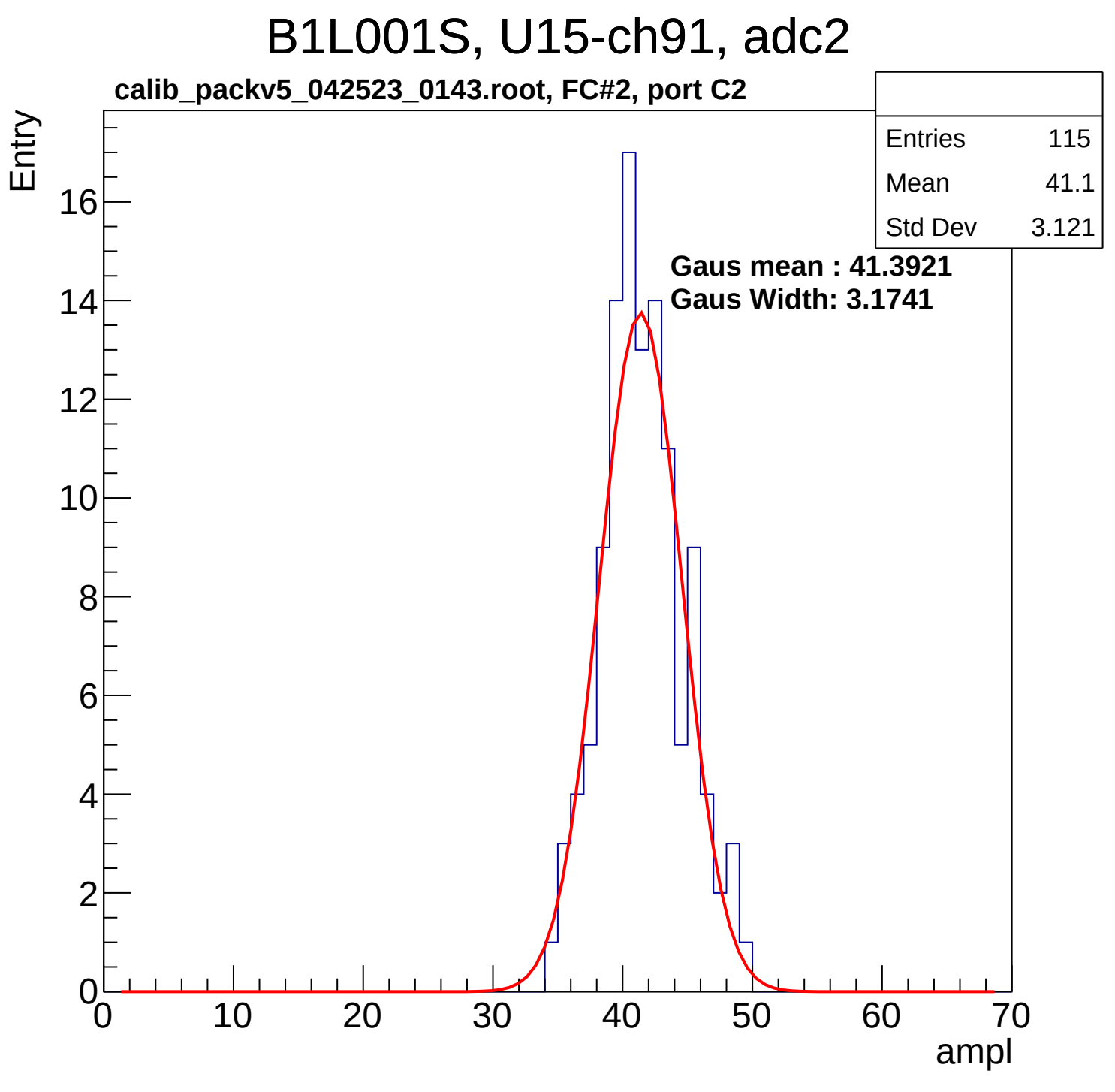
B1L001S, U15-ch91, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 41.1
Std Dev 3.121
Gaus mean : 41.3921
Gaus Width: 3.1741
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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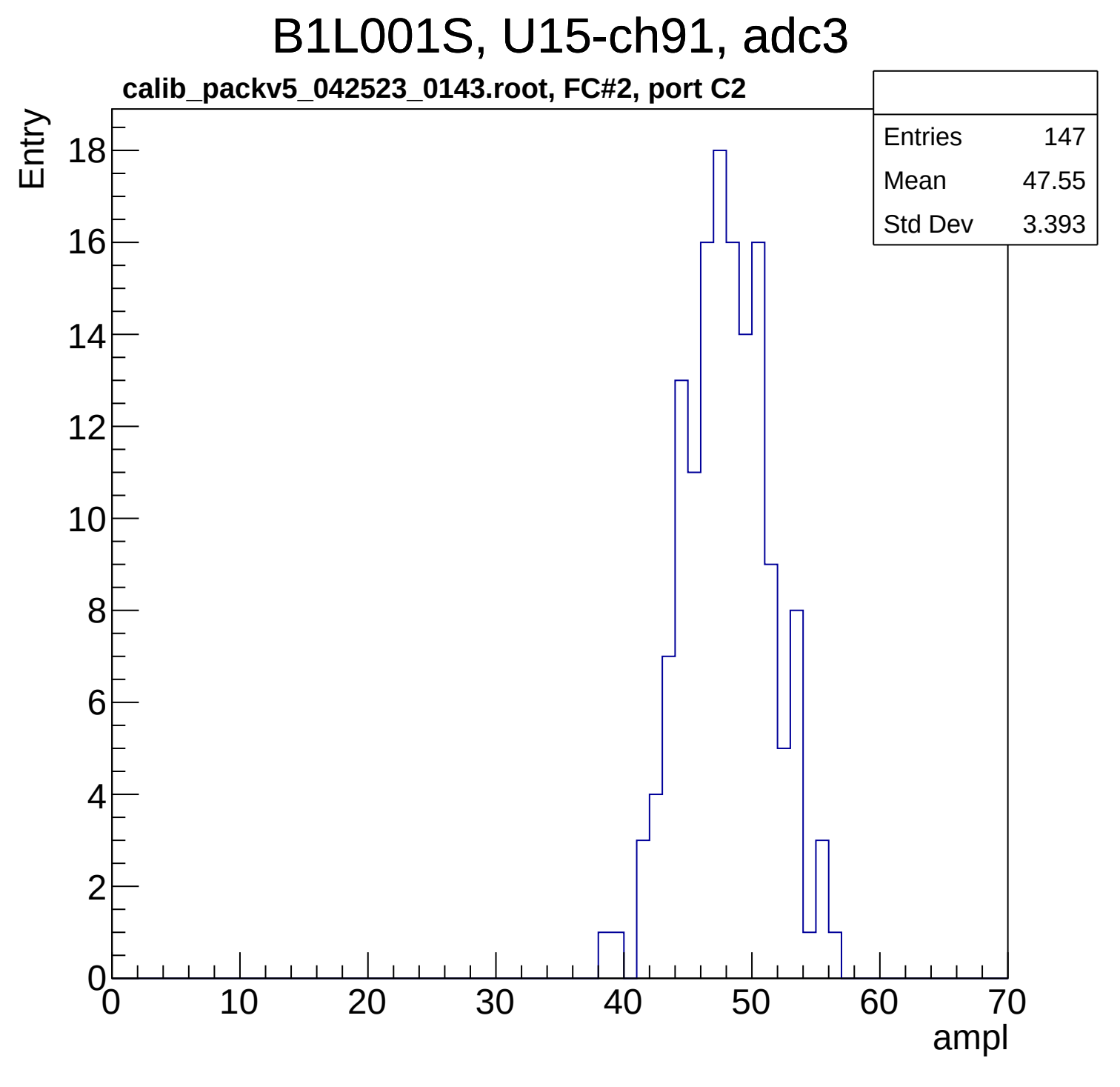
B1L001S, U15-ch91, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 47.55
Std Dev 3.393
Entry
ampl
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4
6
8
10
12
14
16
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0
10
20
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40
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60
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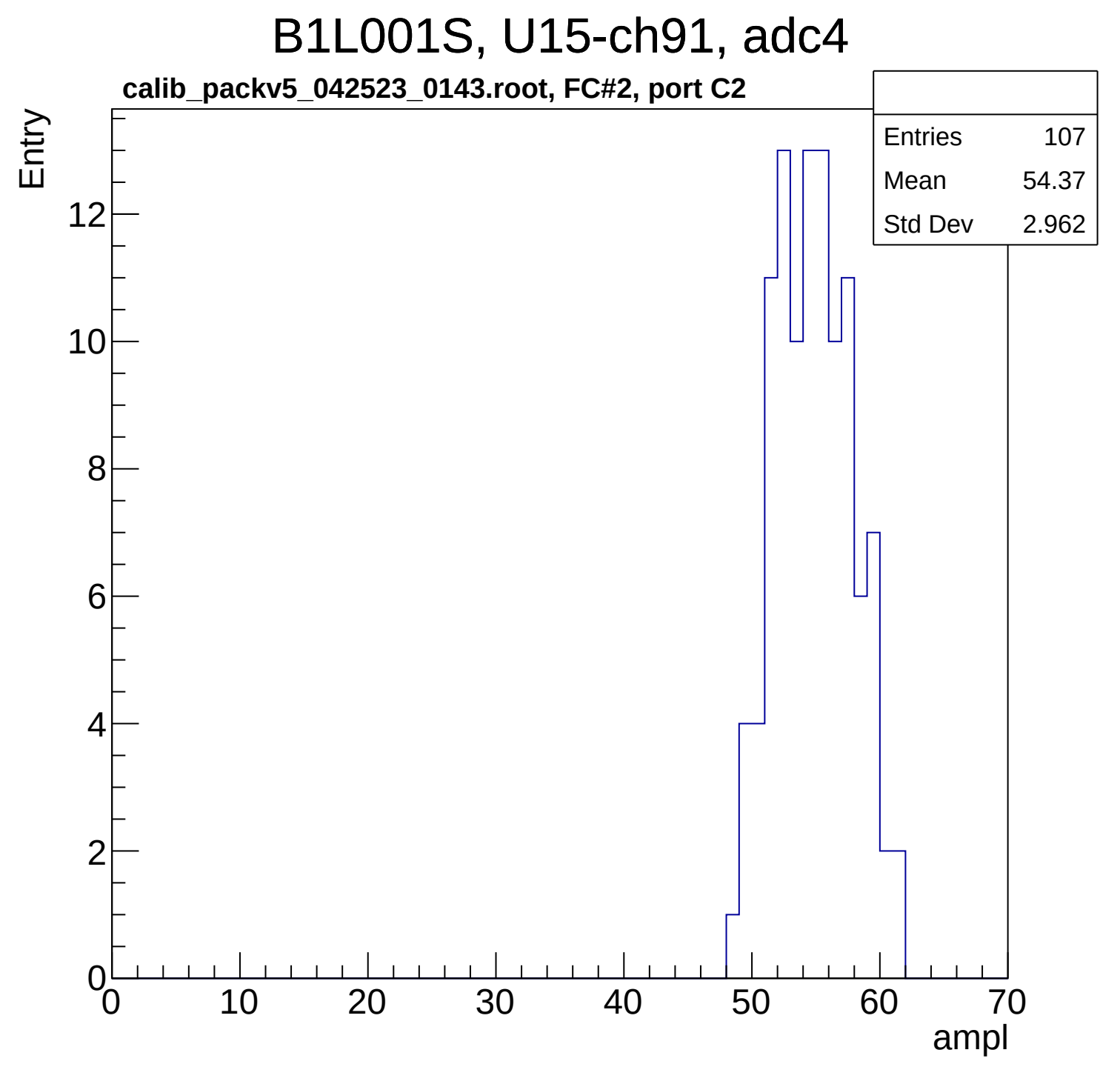
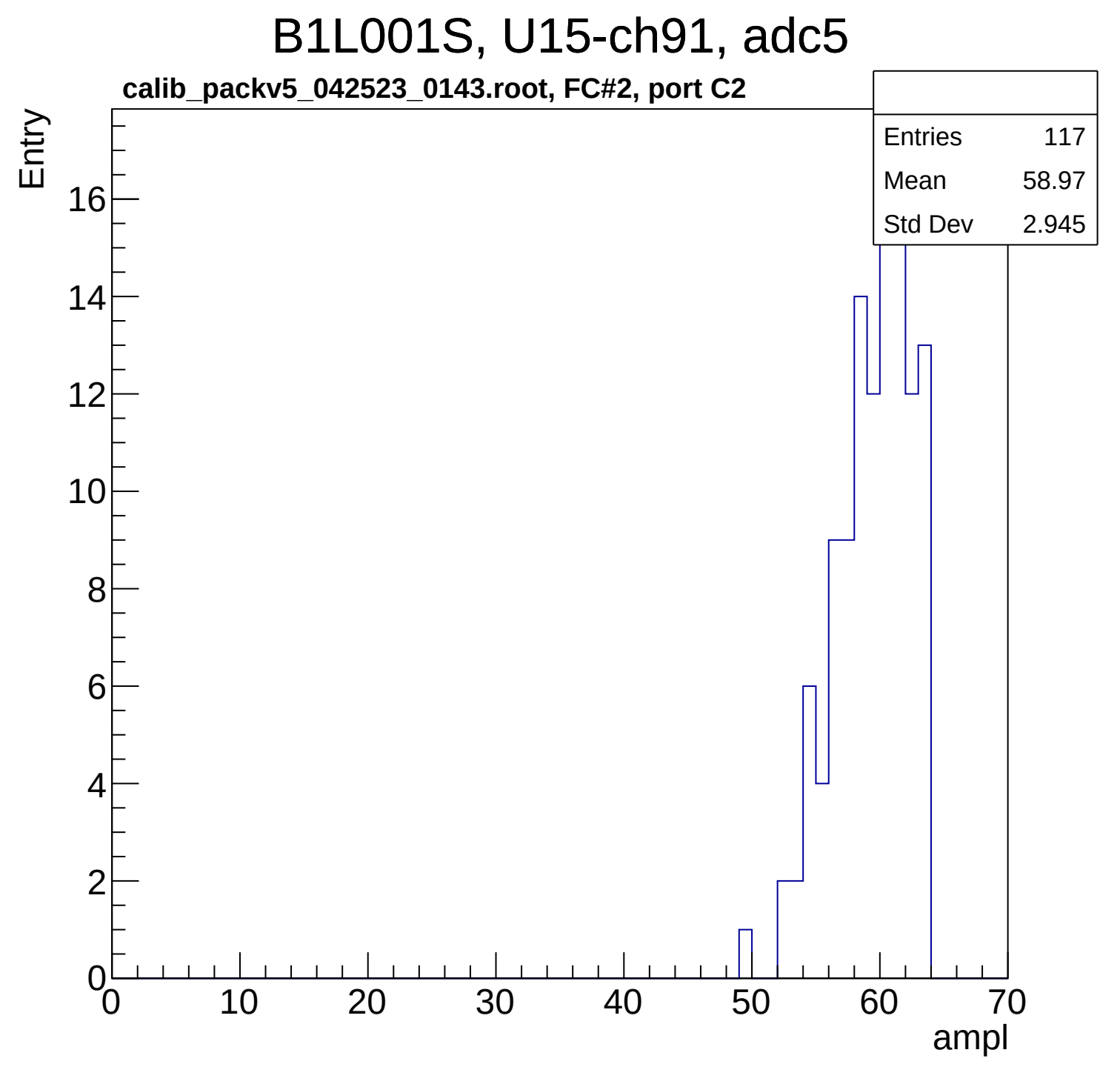

B1L001S, U15-ch91, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 54.37
Std Dev 2.962
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch91, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 58.97
Std Dev 2.945
Entry
ampl
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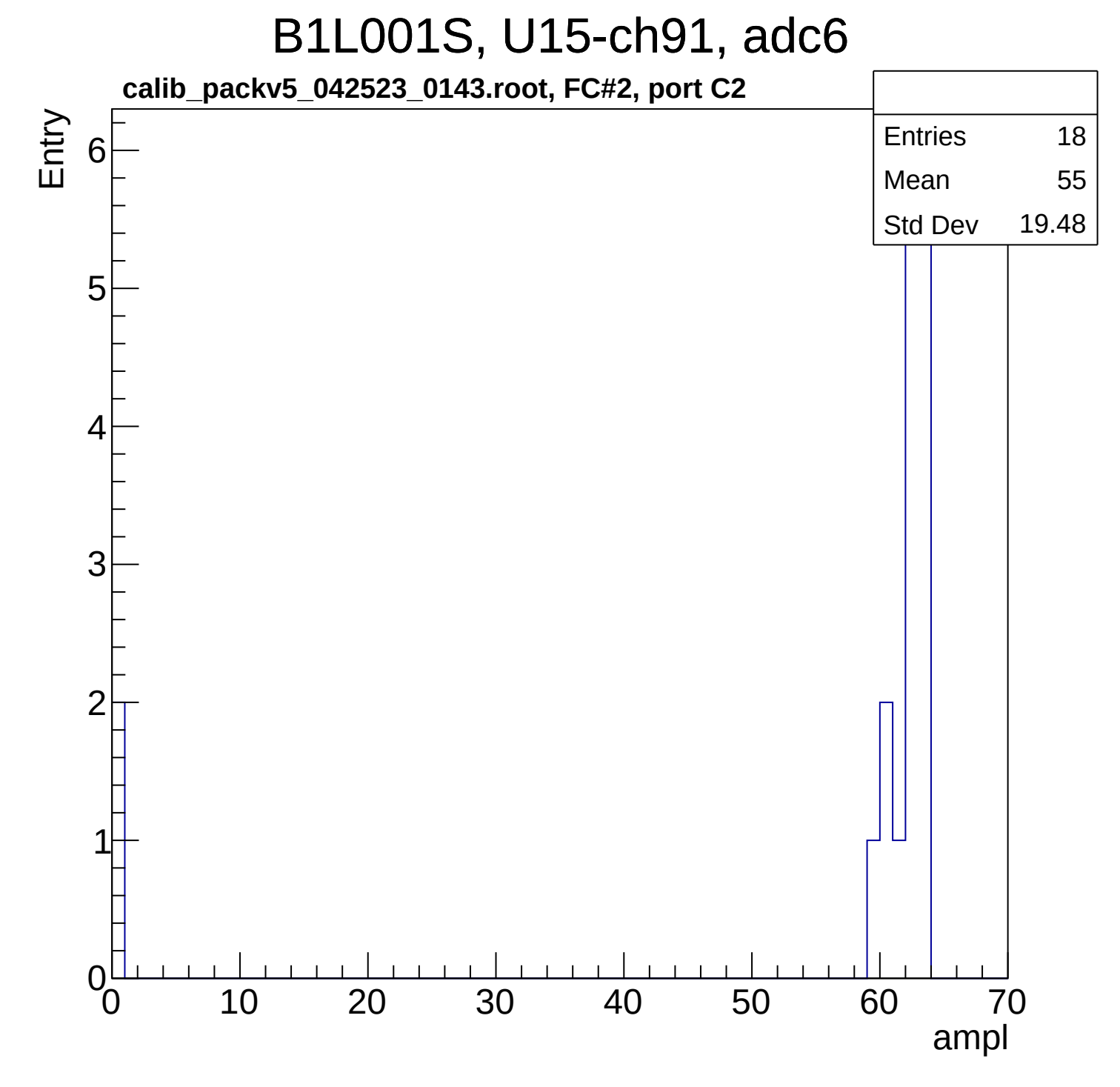
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch91, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 55
Std Dev 19.48
Entry
ampl
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1
2
3
4
5
6
0
10
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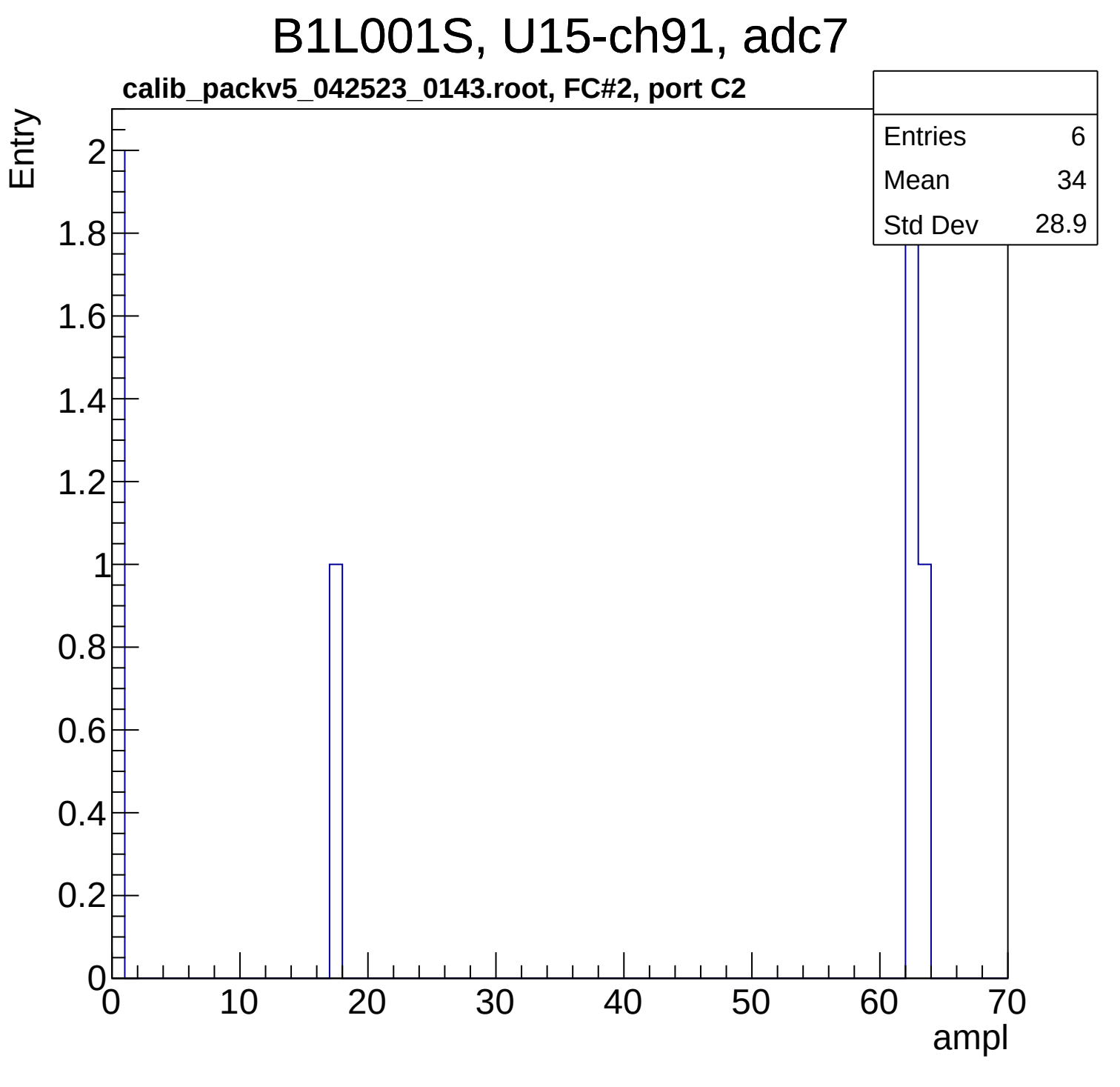
40
50
60
70


B1L001S, U15-ch91, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 34
Std Dev 28.9
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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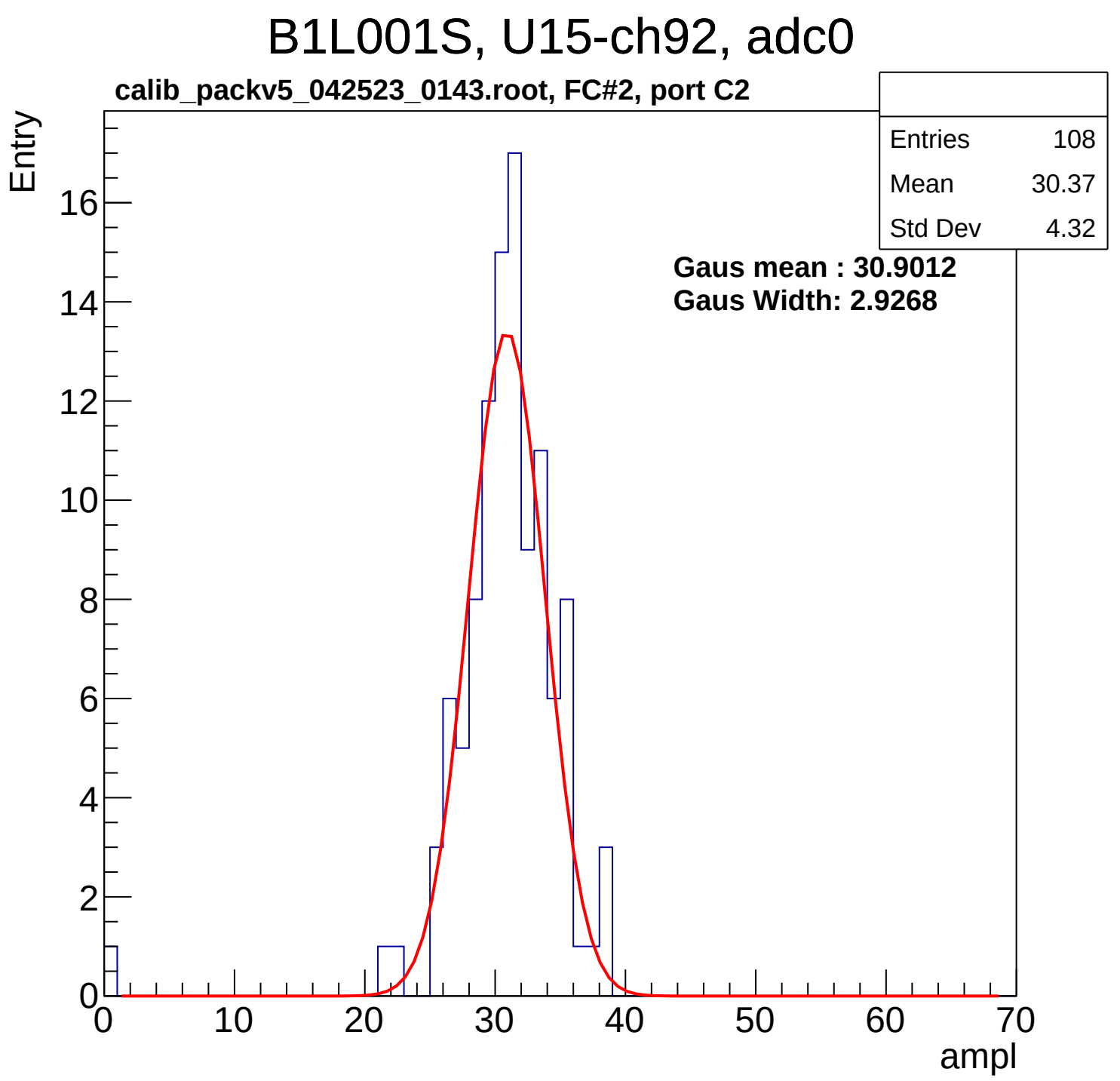
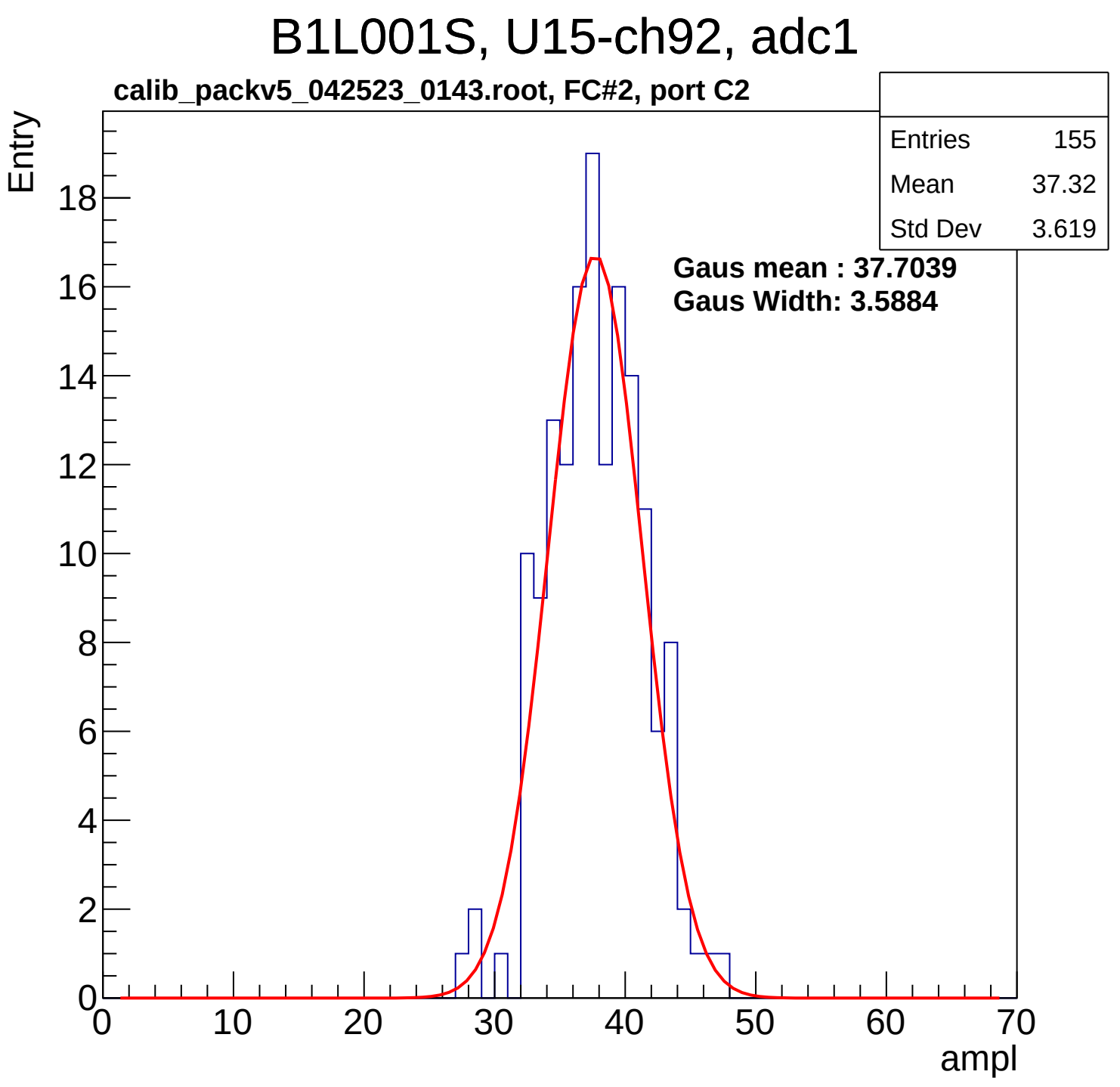

B1L001S, U15-ch92, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 30.37
Std Dev 4.32
Gaus mean : 30.9012
Gaus Width: 2.9268
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch92, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 37.32
Std Dev 3.619
Gaus mean : 37.7039
Gaus Width: 3.5884
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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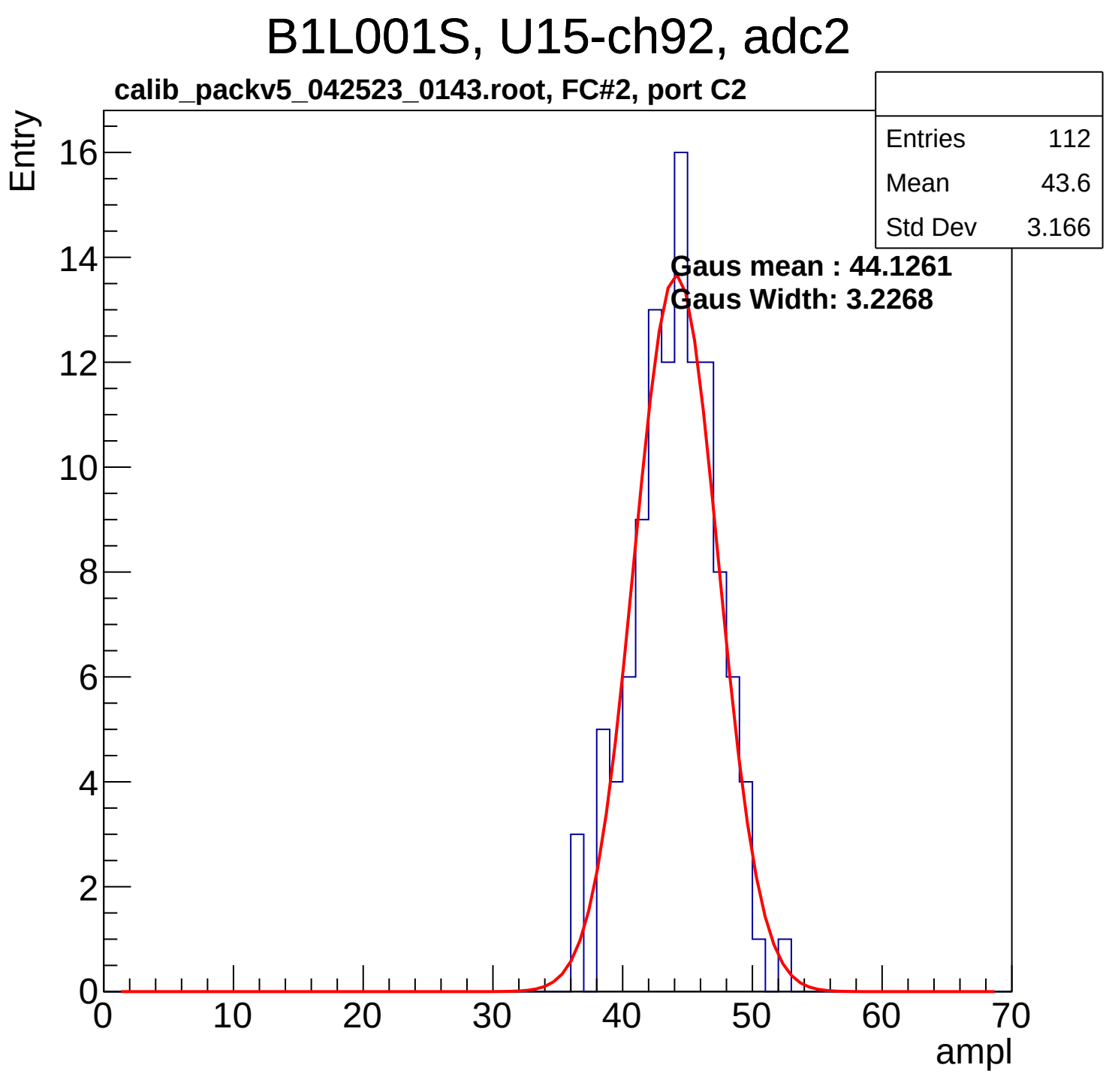
B1L001S, U15-ch92, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43.6
Std Dev 3.166
Gaus mean : 44.1261
Gaus Width: 3.2268
Entry
ampl
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2
4
6
8
10
12
14
16
0
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50
60
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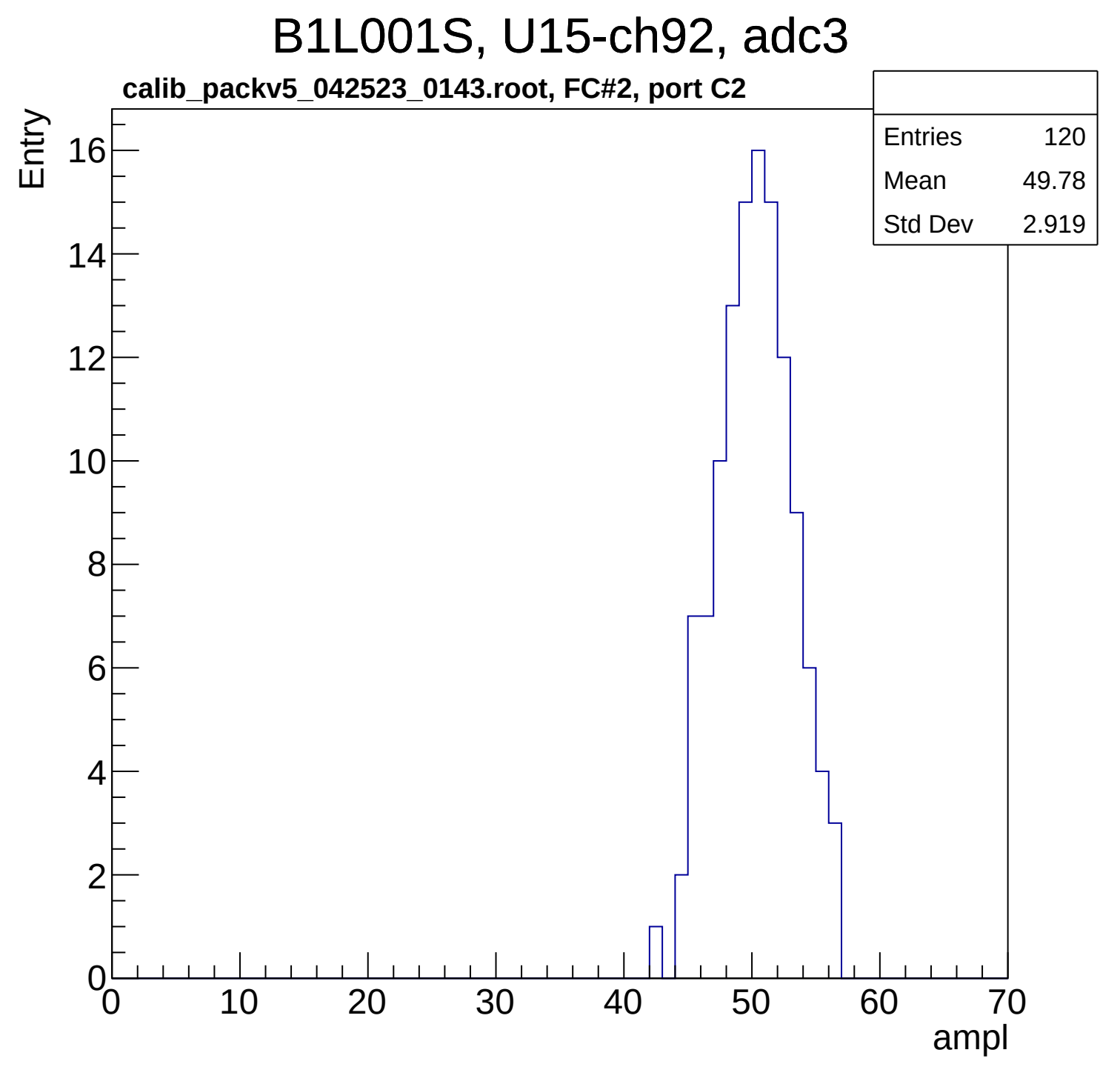
B1L001S, U15-ch92, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 49.78
Std Dev 2.919
Entry
ampl
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2
4
6
8
10
12
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16
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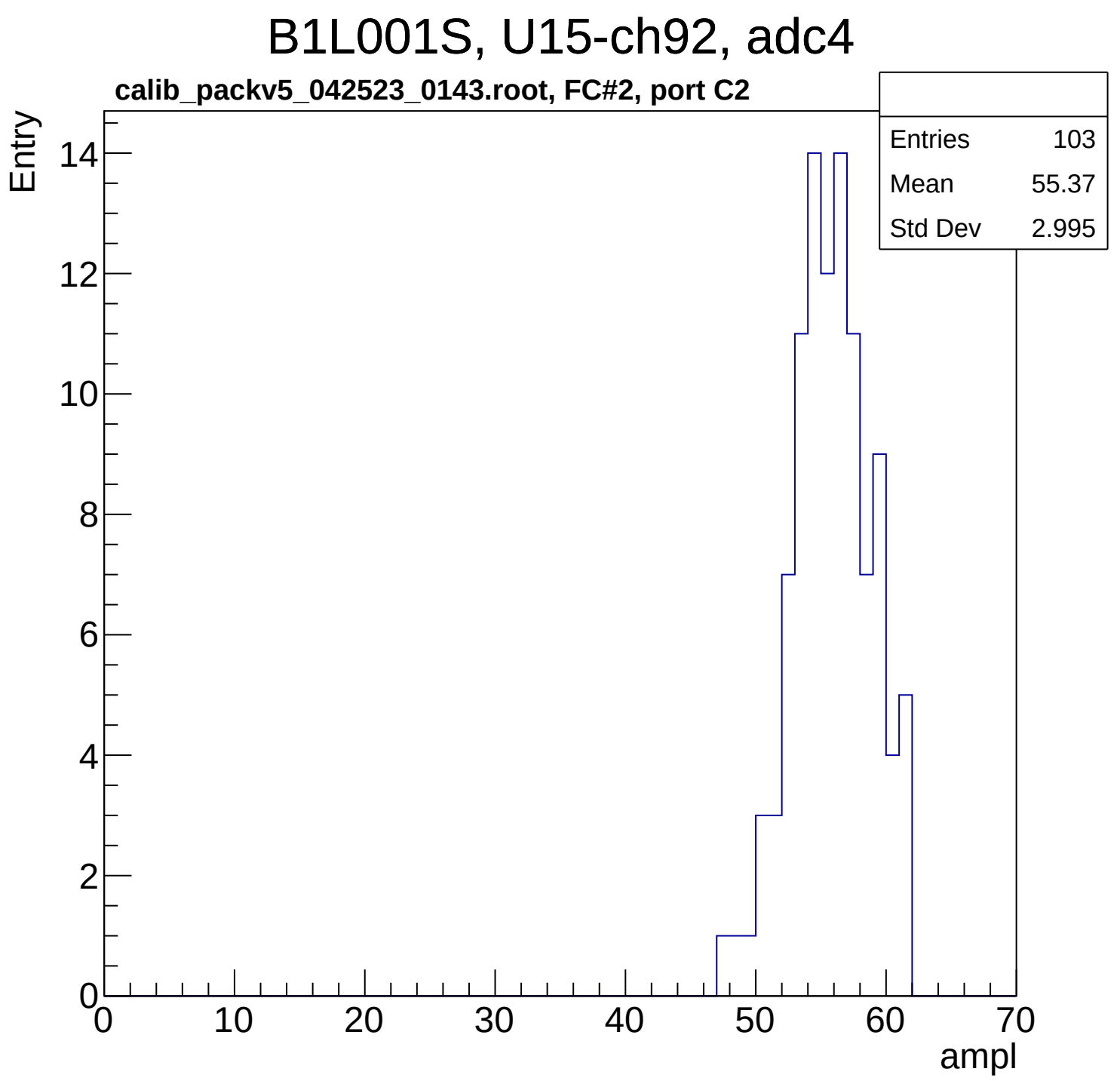
B1L001S, U15-ch92, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 55.37
Std Dev 2.995
Entry
ampl
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6
8
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14
0
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60
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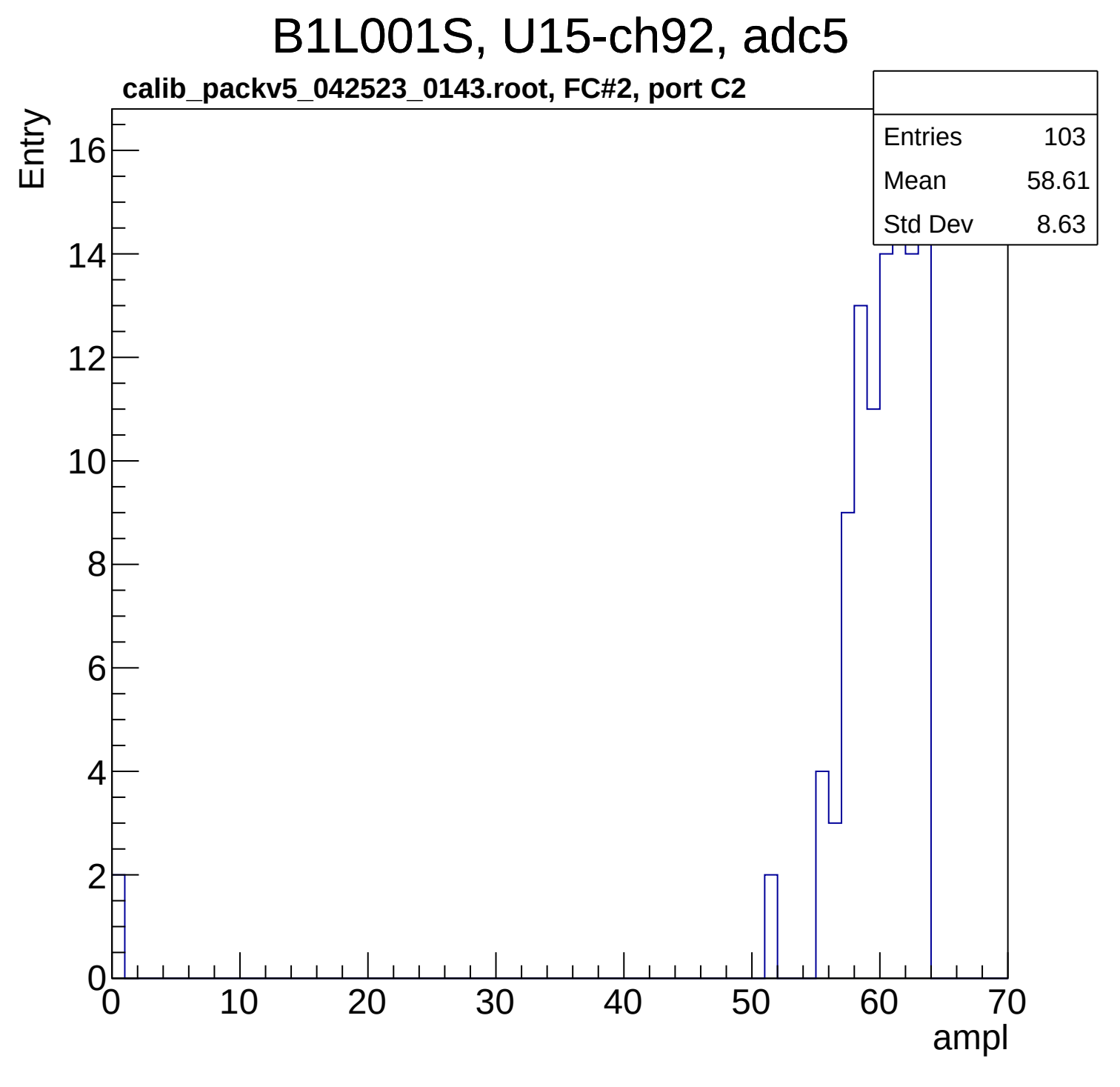
B1L001S, U15-ch92, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.61
Std Dev 8.63
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
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60
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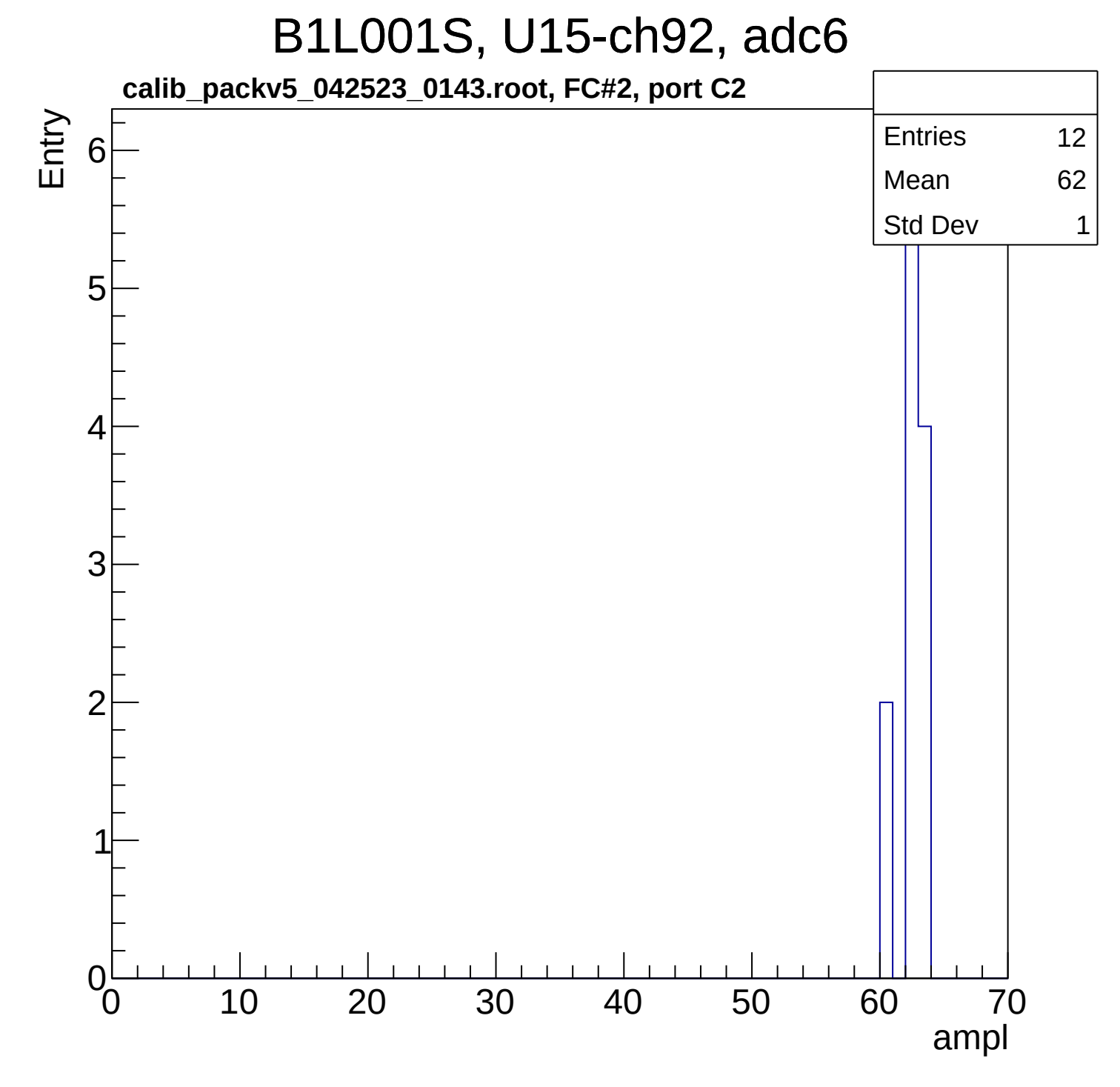
B1L001S, U15-ch92, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 62
Std Dev 1
Entry
0
1
2
3
4
5
6
0
10
20
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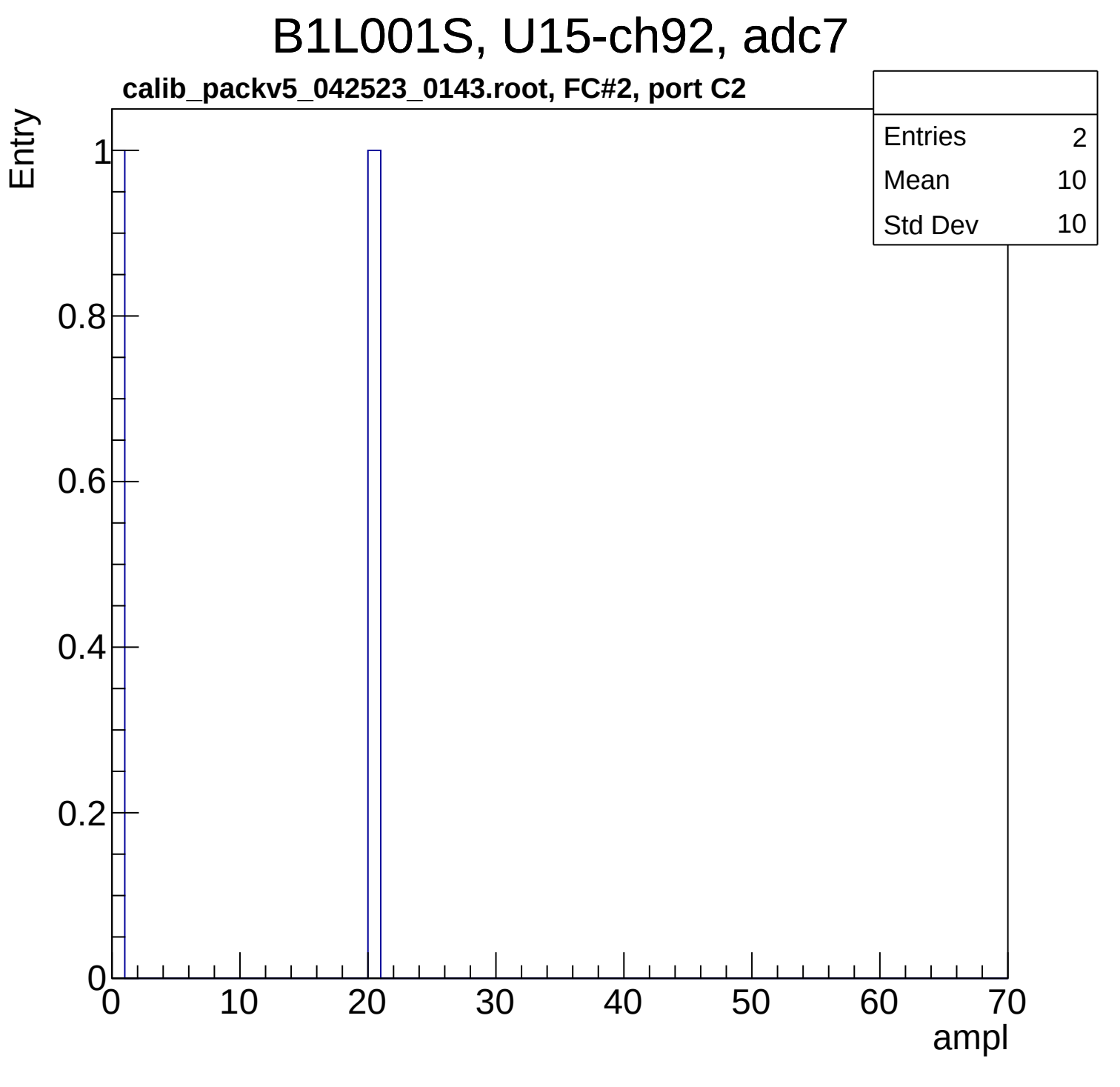
50
60
70
ampl


B1L001S, U15-ch92, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10
Std Dev 10
Entry
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0.6
0.4
0.2
0
0
10
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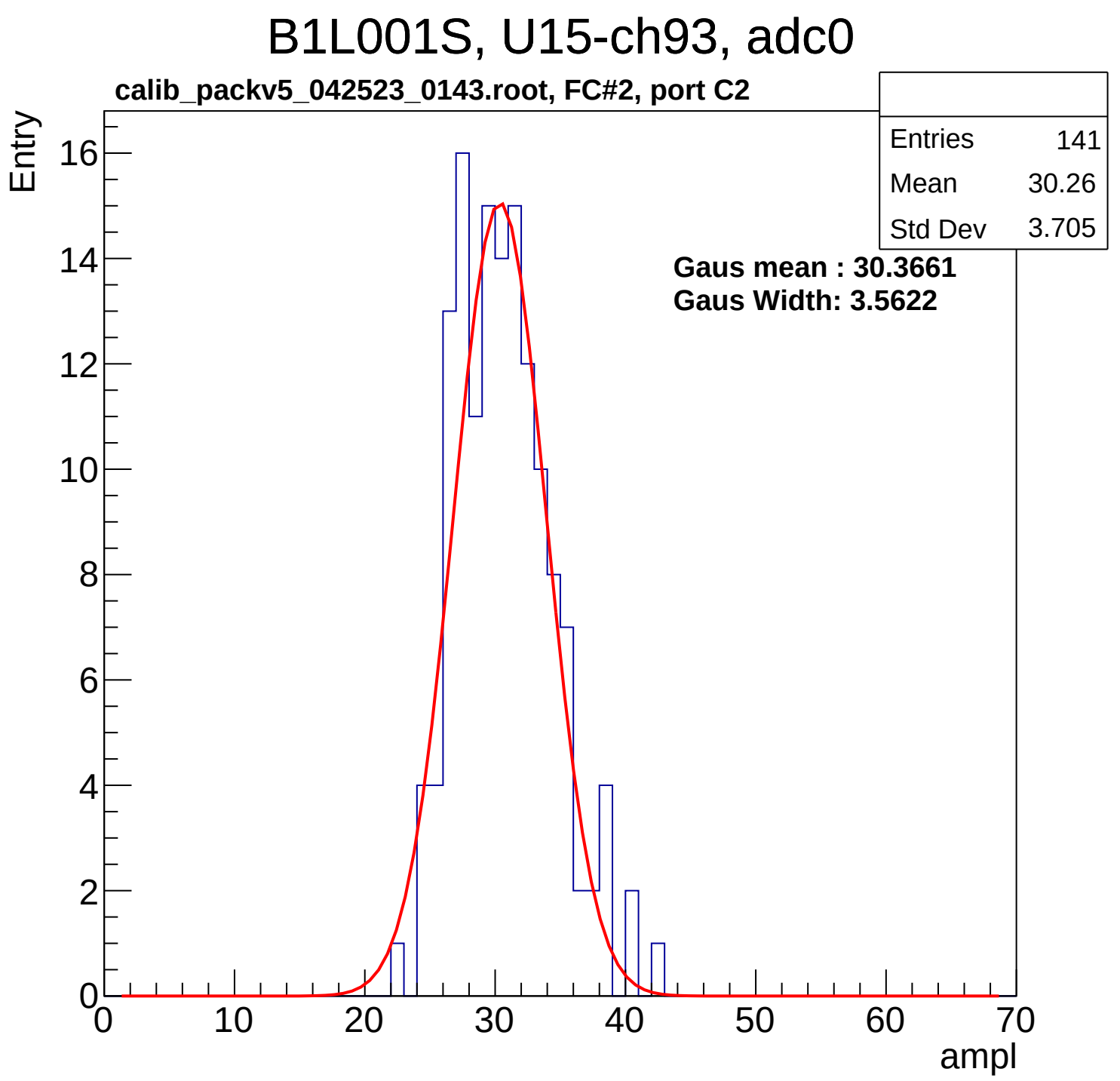
50
60
70
ampl


B1L001S, U15-ch93, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 30.26
Std Dev 3.705
Gaus mean : 30.3661
Gaus Width: 3.5622
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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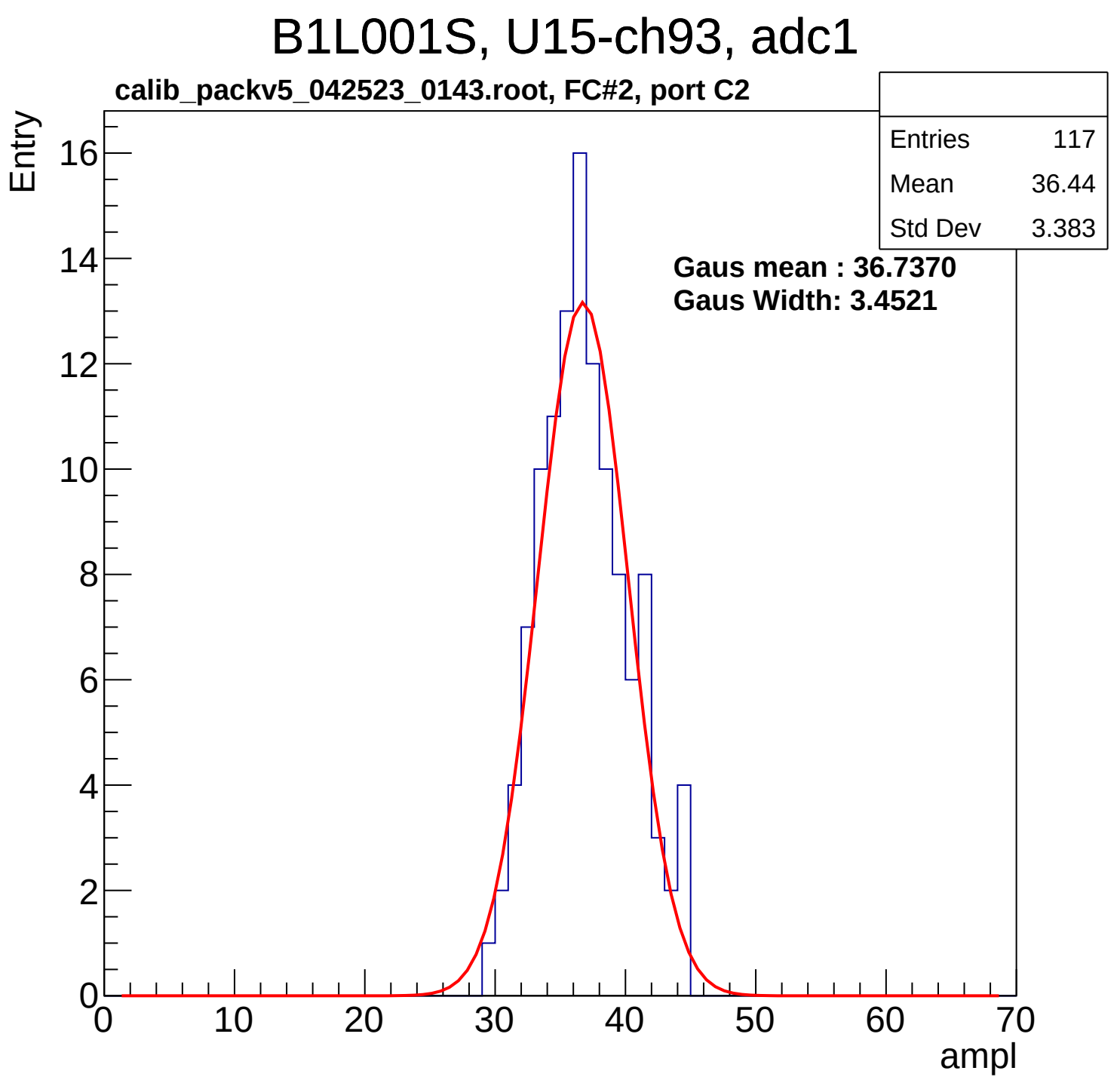
B1L001S, U15-ch93, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 36.44
Std Dev 3.383
Gaus mean : 36.7370
Gaus Width: 3.4521
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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20
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40
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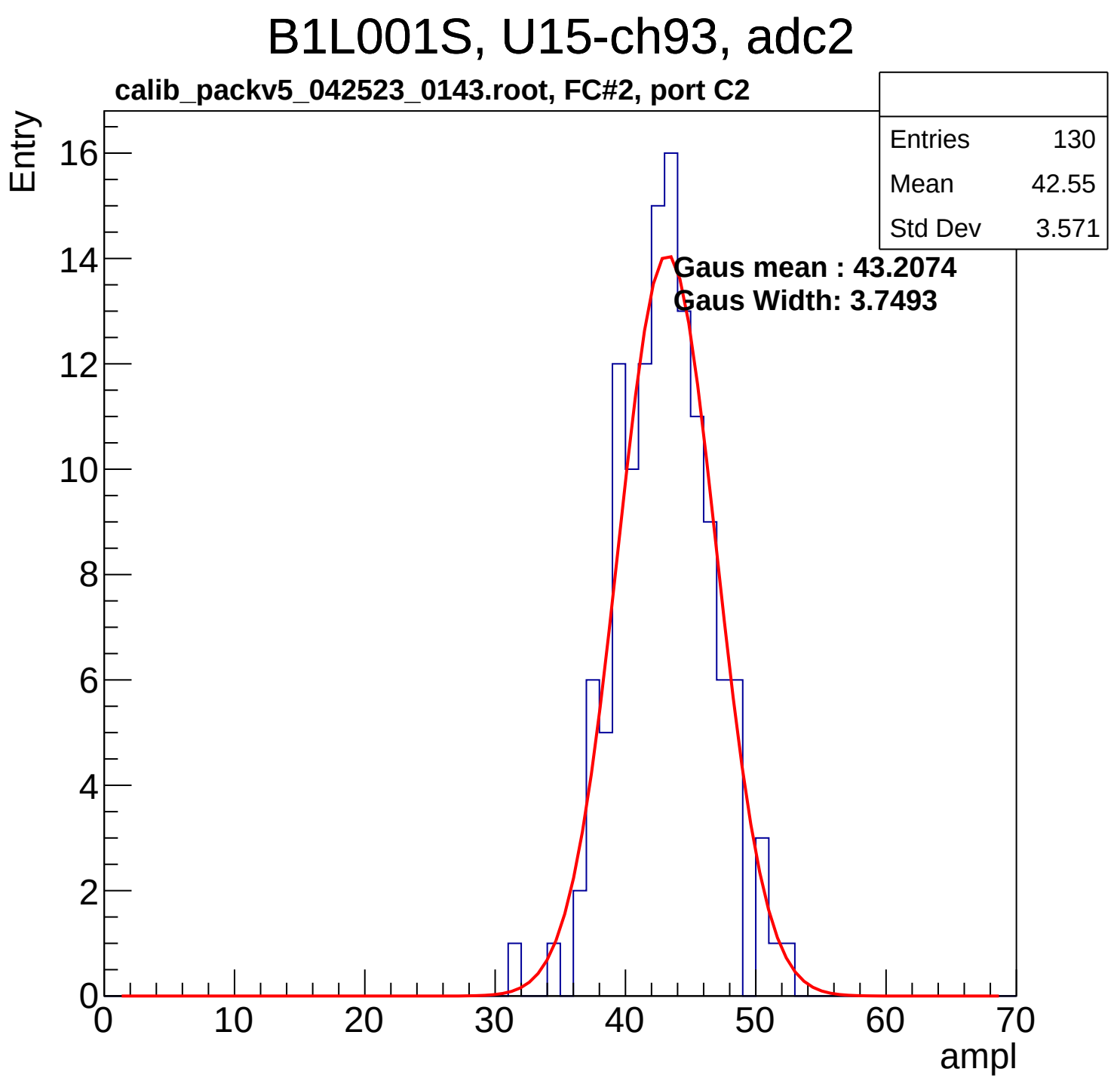
B1L001S, U15-ch93, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.55
Std Dev 3.571
Gaus mean : 43.2074
Gaus Width: 3.7493
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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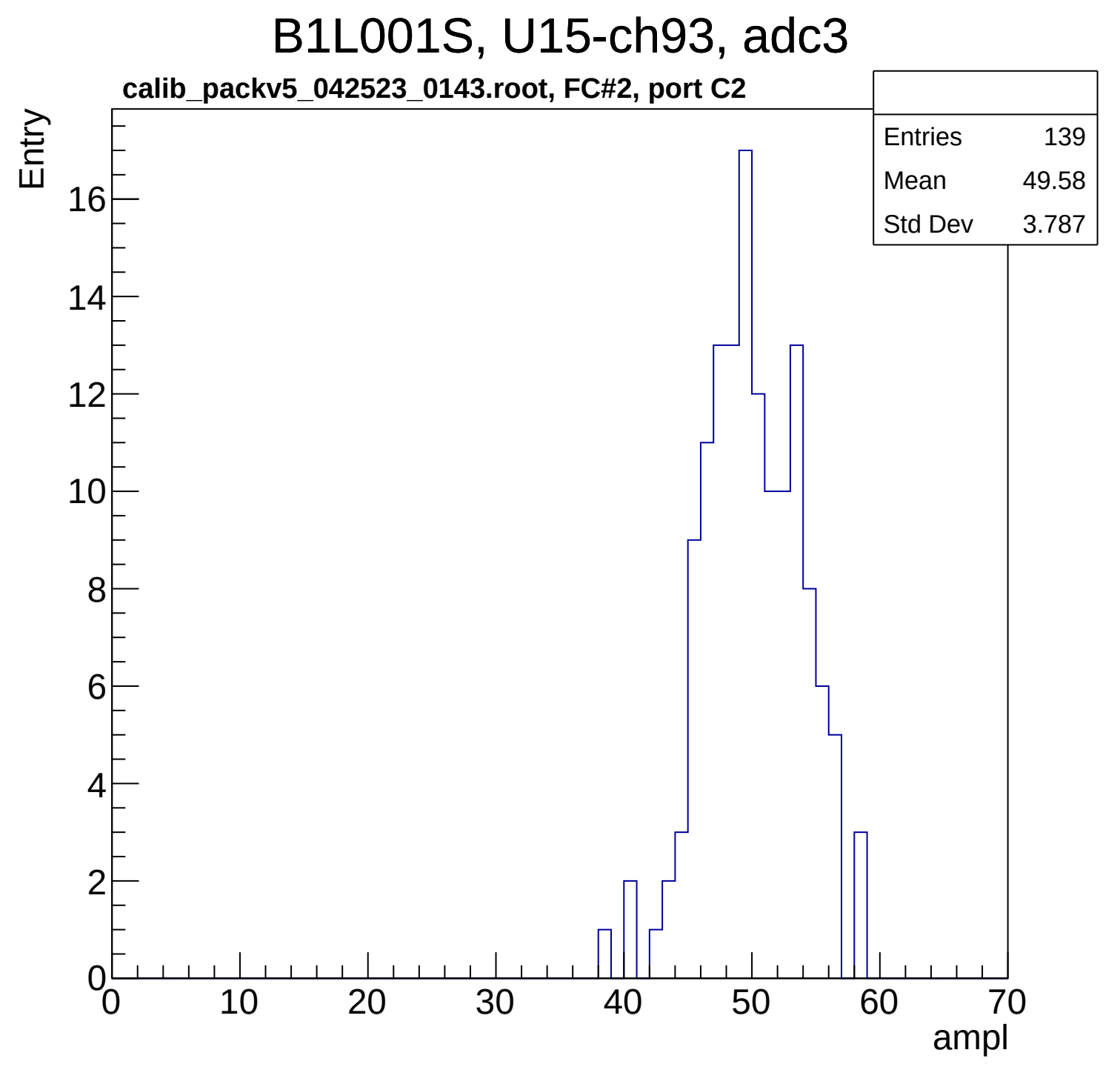
B1L001S, U15-ch93, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.58
Std Dev 3.787
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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40
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60
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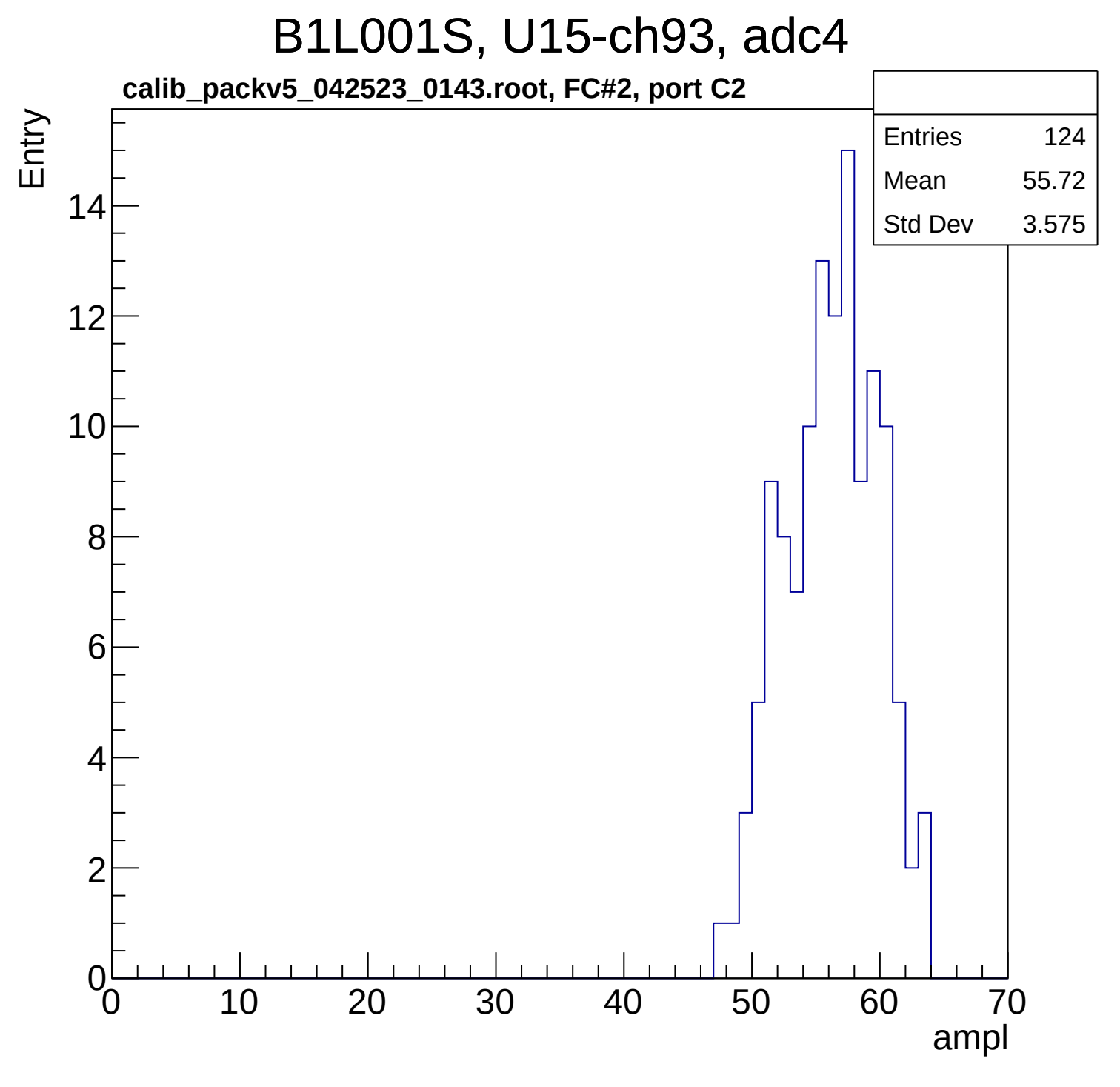
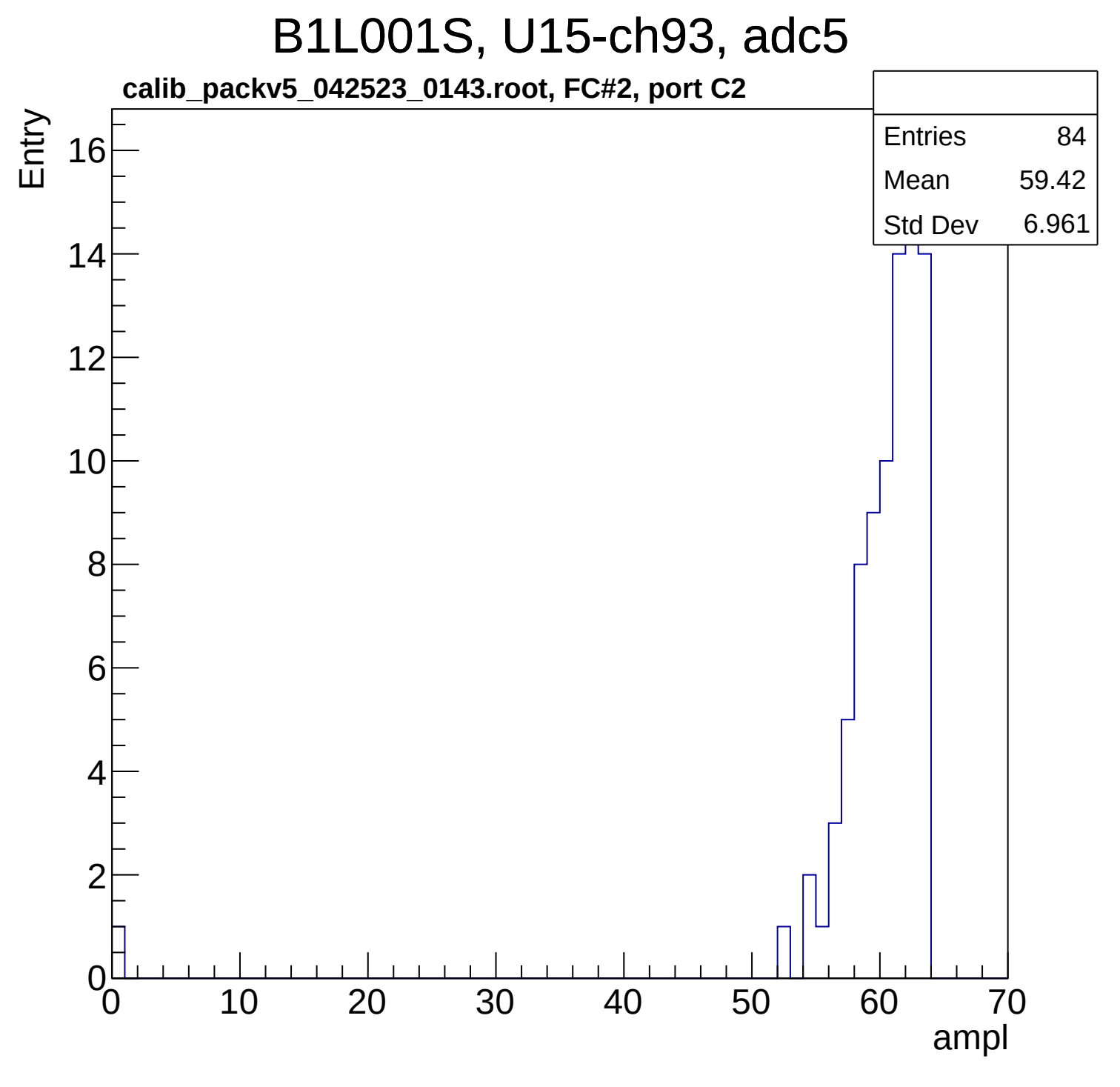

B1L001S, U15-ch93, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.72
Std Dev 3.575
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch93, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 59.42
Std Dev 6.961
Entry
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4
6
8
10
12
14
16
0
10
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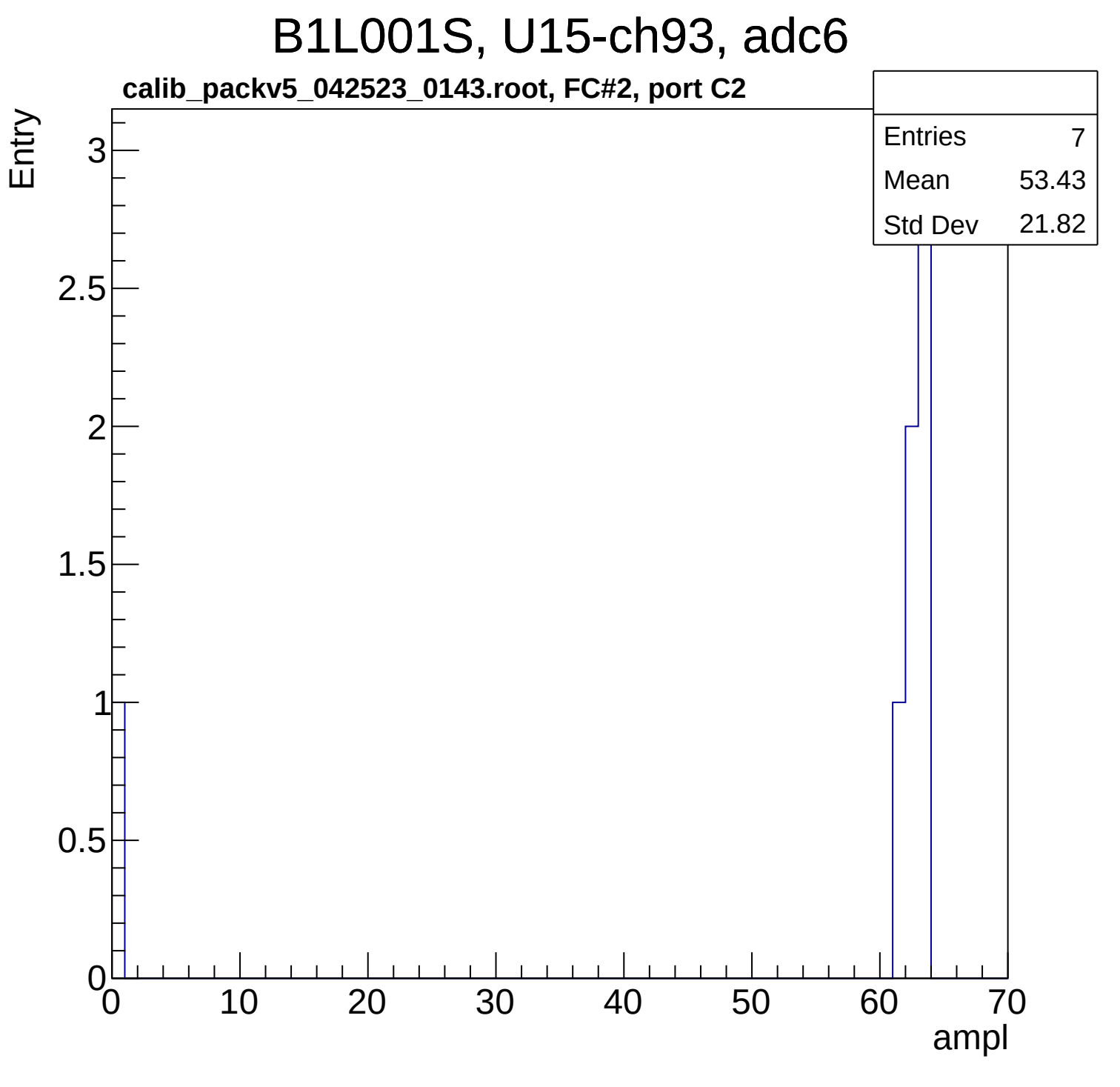
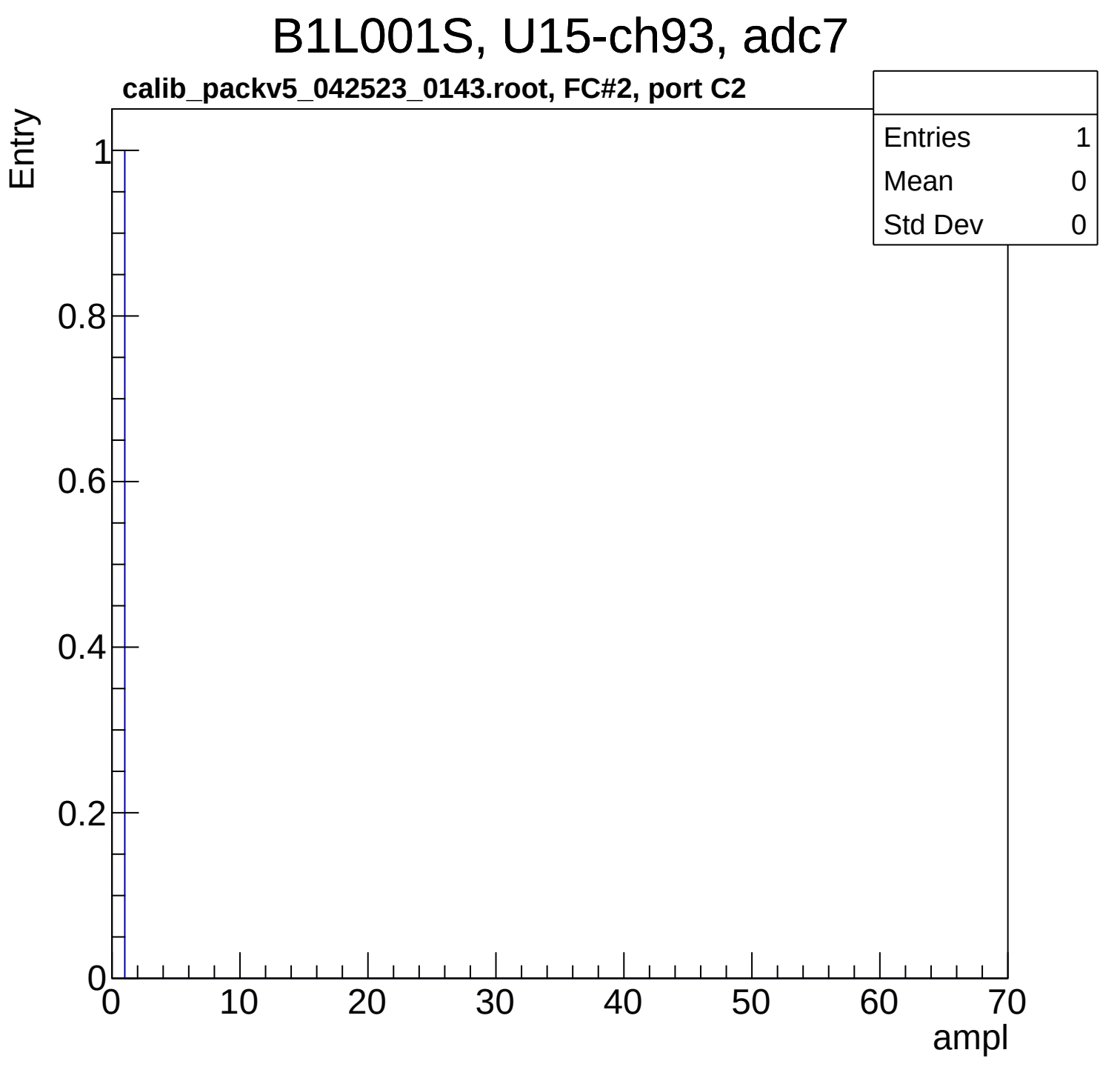
50
60
70
ampl


B1L001S, U15-ch93, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53.43
Std Dev 21.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch93, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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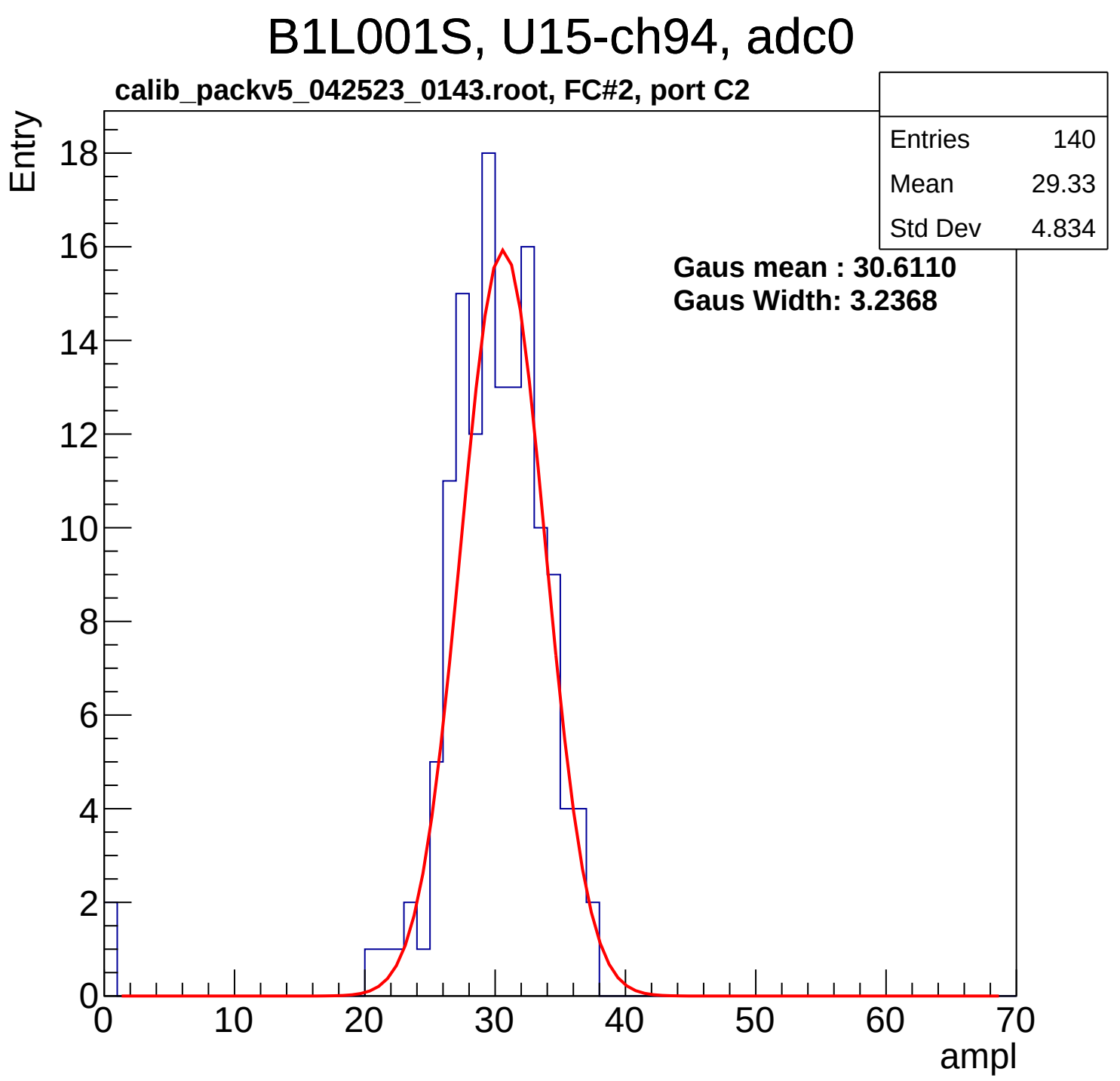
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch94, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 29.33
Std Dev 4.834
Gaus mean : 30.6110
Gaus Width: 3.2368
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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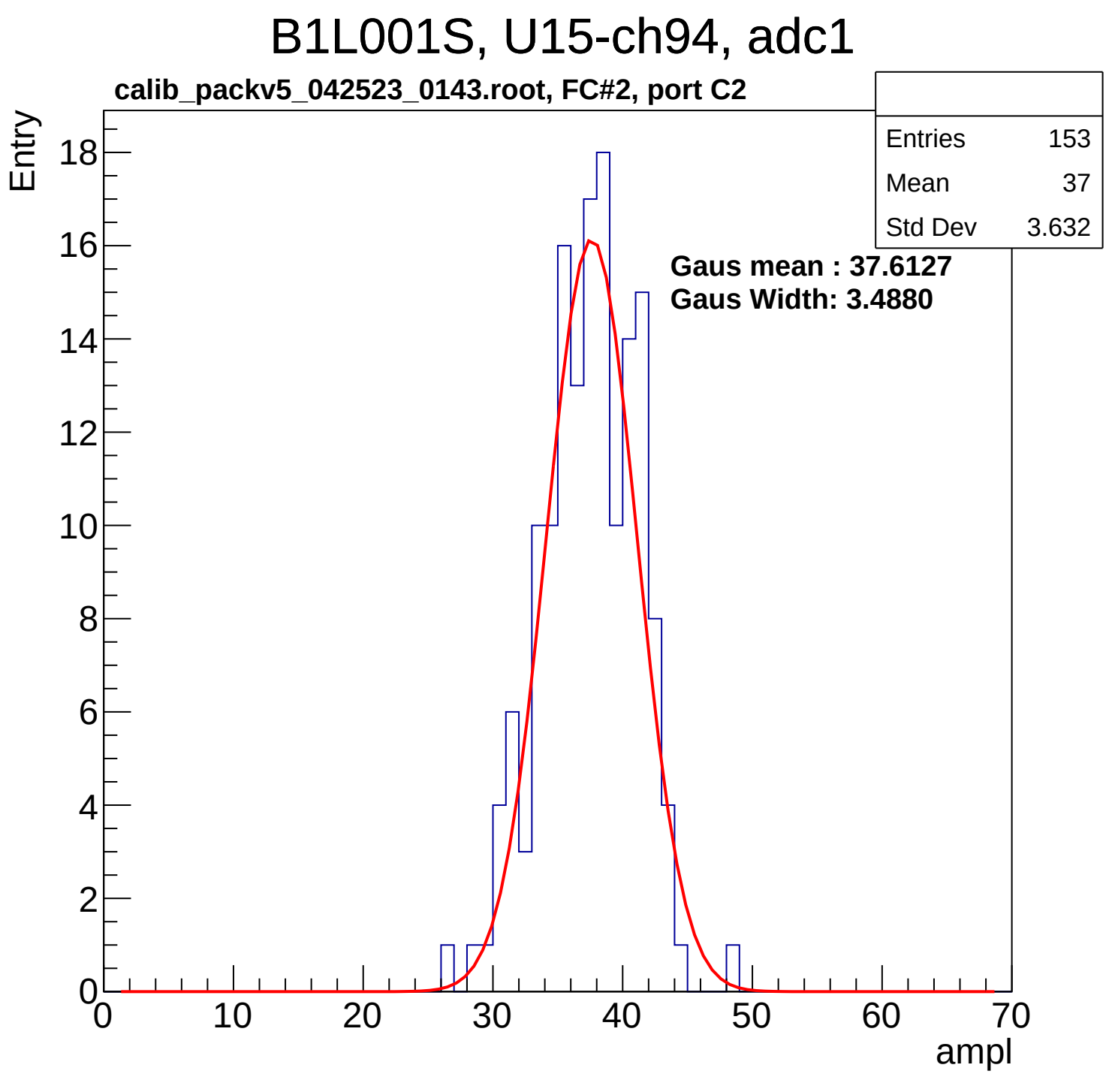
B1L001S, U15-ch94, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 37
Std Dev 3.632
Gaus mean : 37.6127
Gaus Width: 3.4880
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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20
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40
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60
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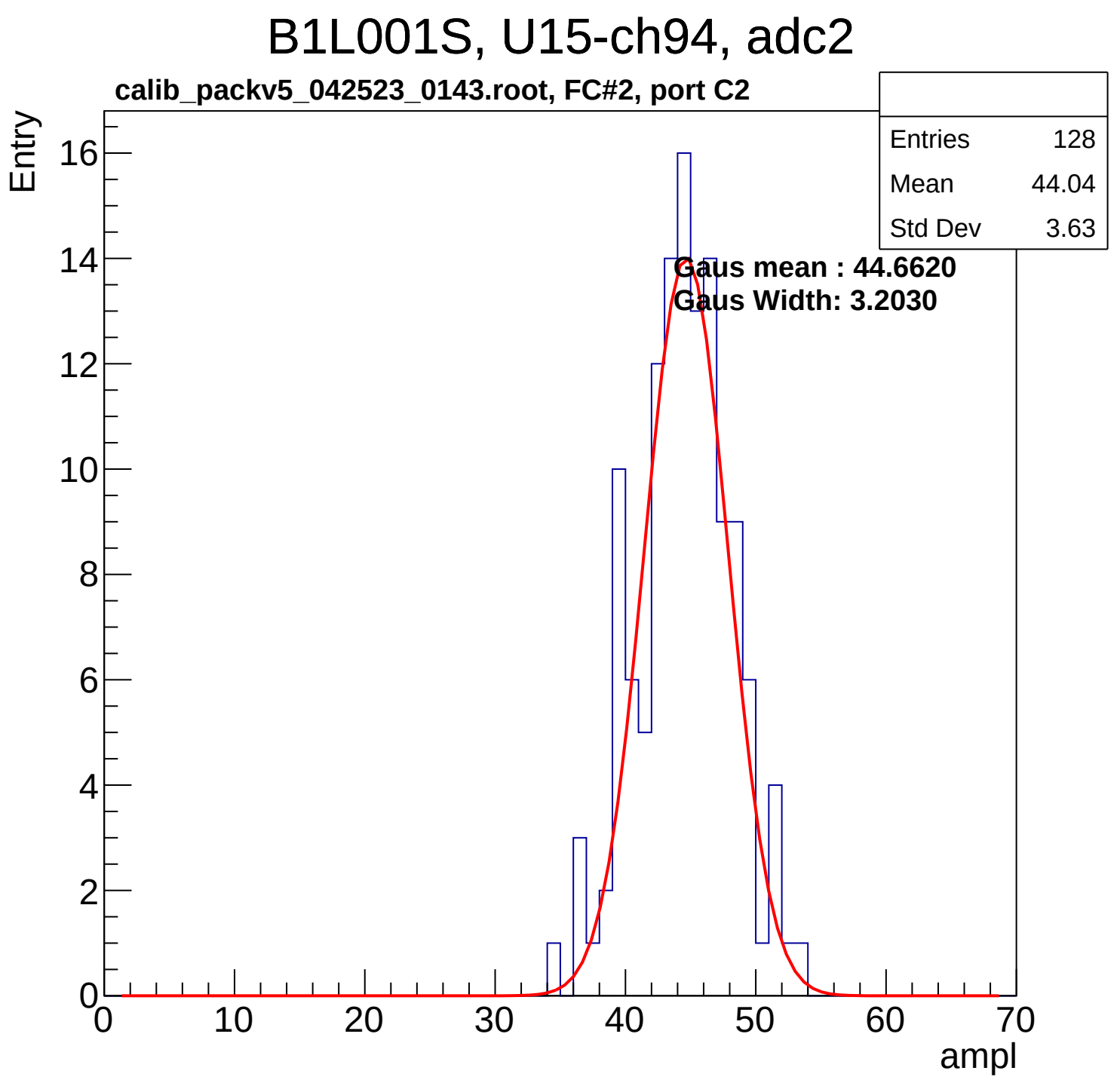
B1L001S, U15-ch94, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 44.04
Std Dev 3.63
Gaus mean : 44.6620
Gaus Width: 3.2030
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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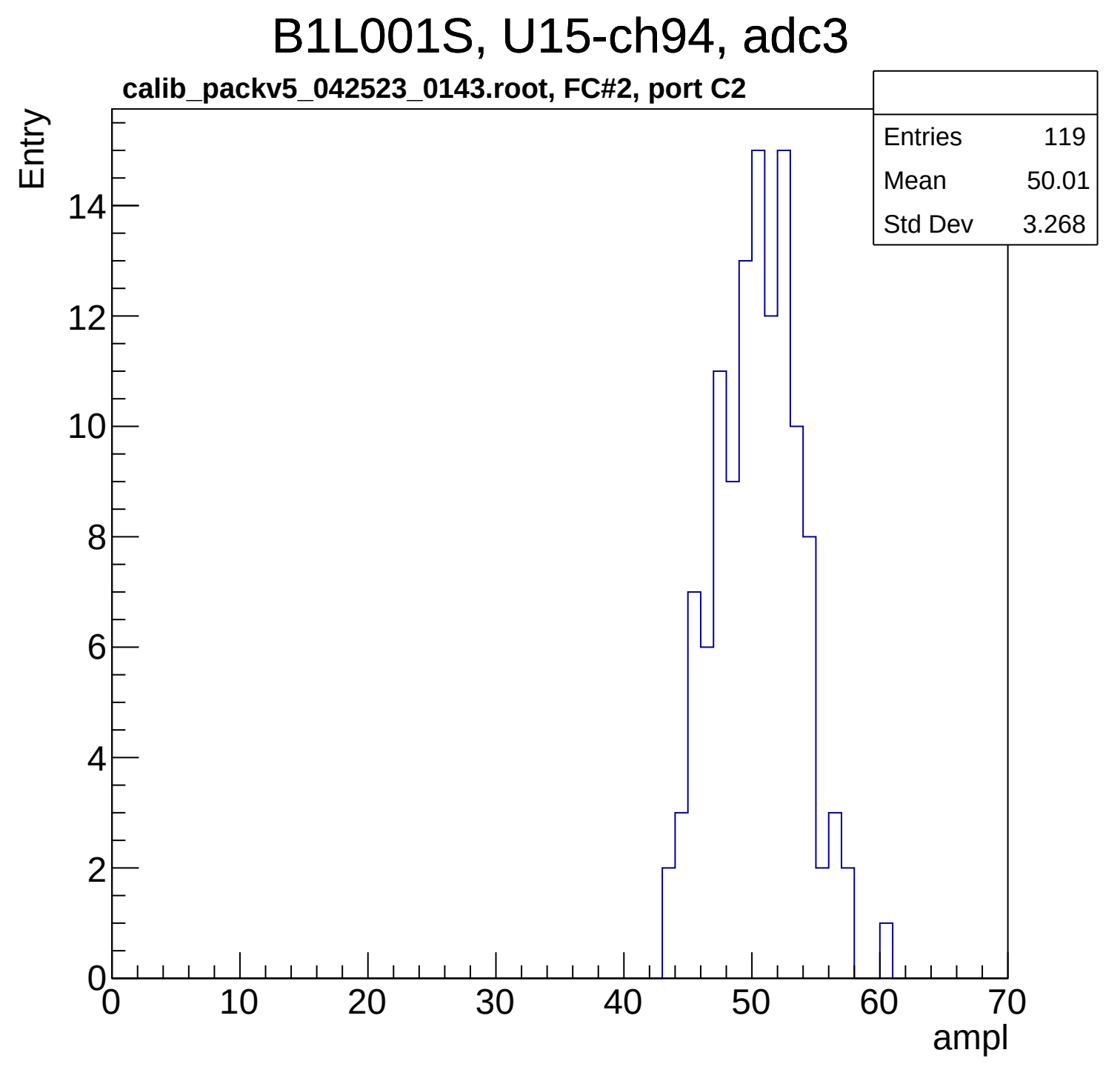
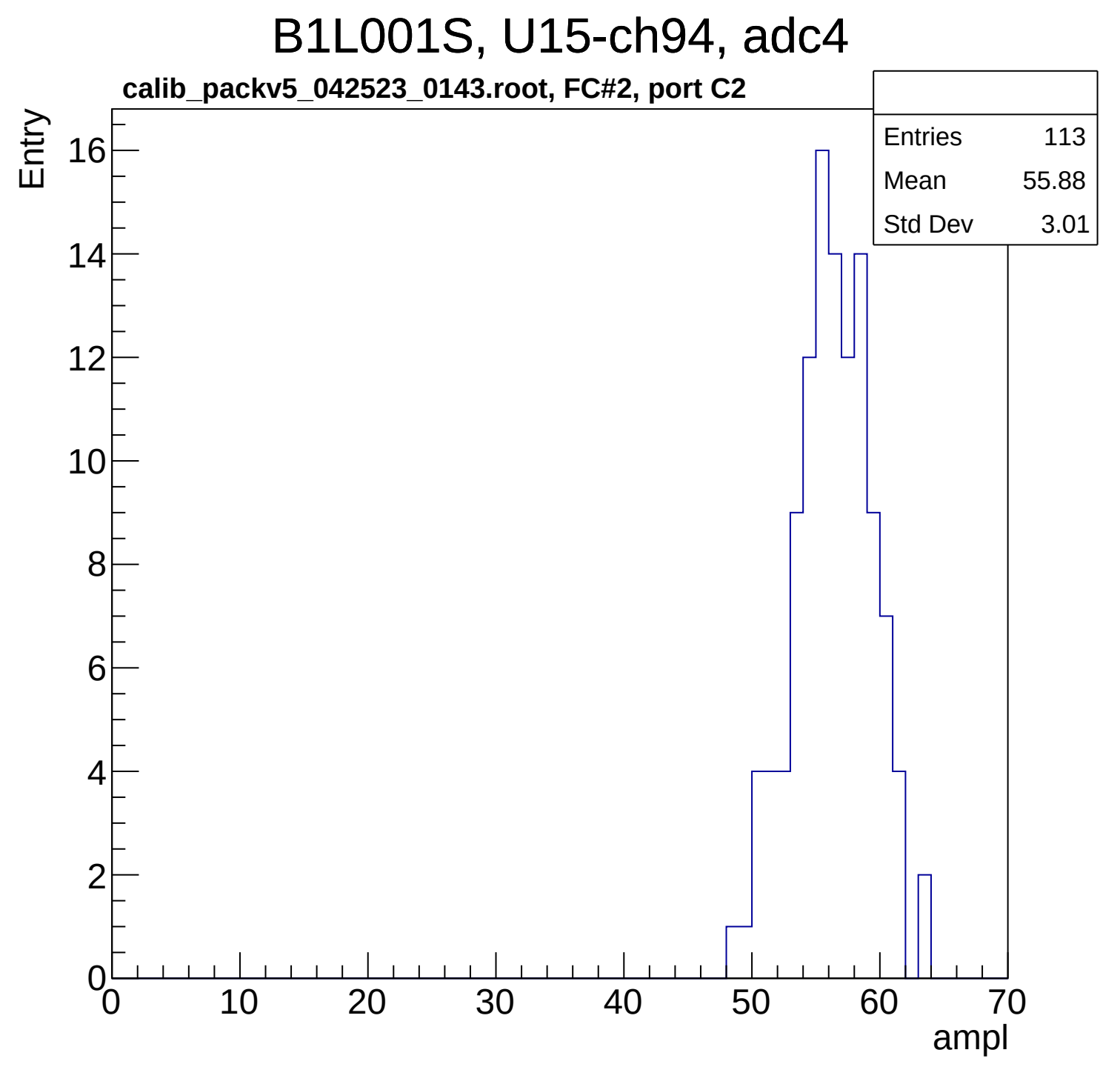
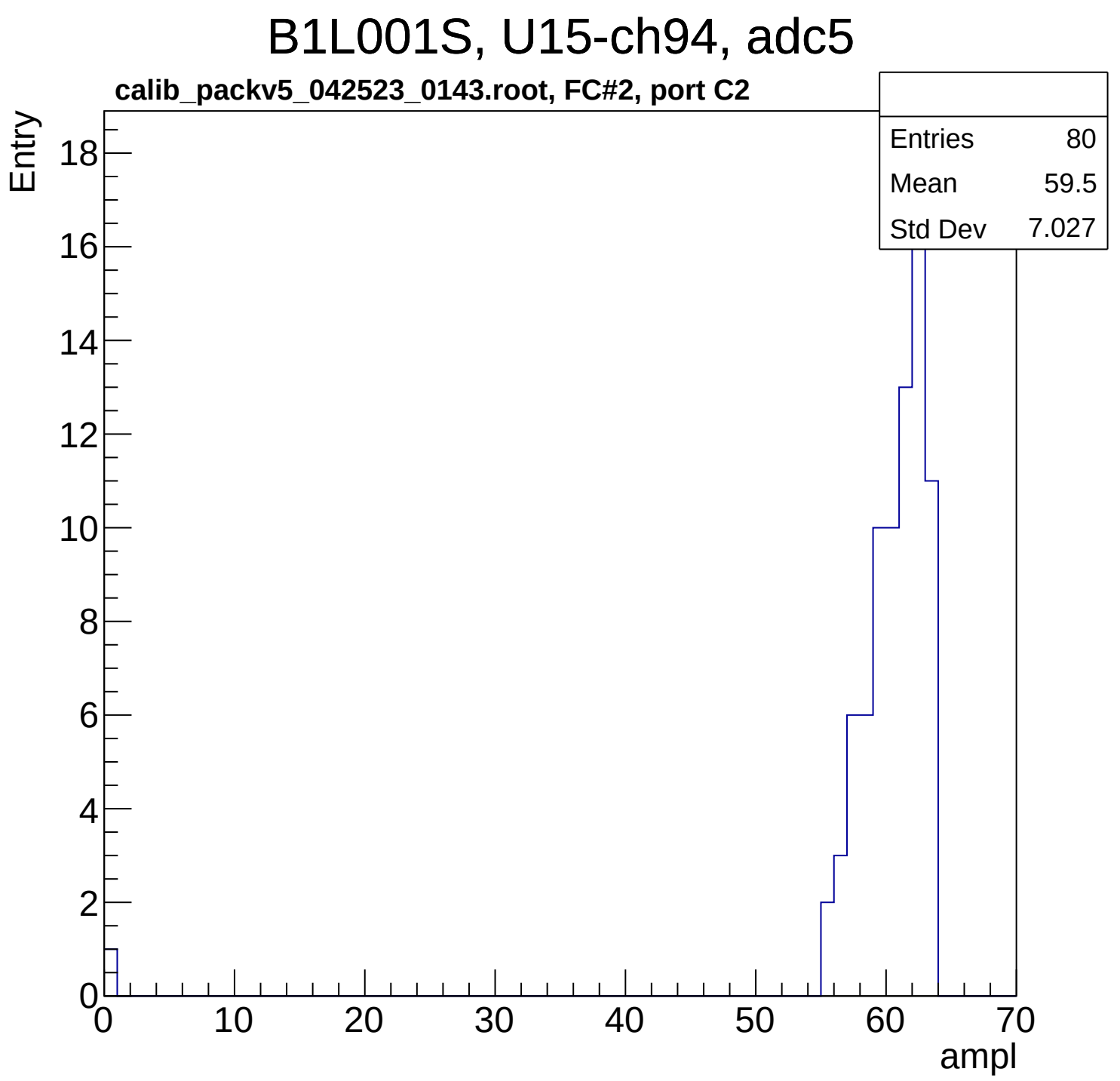

B1L001S, U15-ch94, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 50.01
Std Dev 3.268
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch94, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 55.88
Std Dev 3.01
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch94, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 59.5
Std Dev 7.027
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
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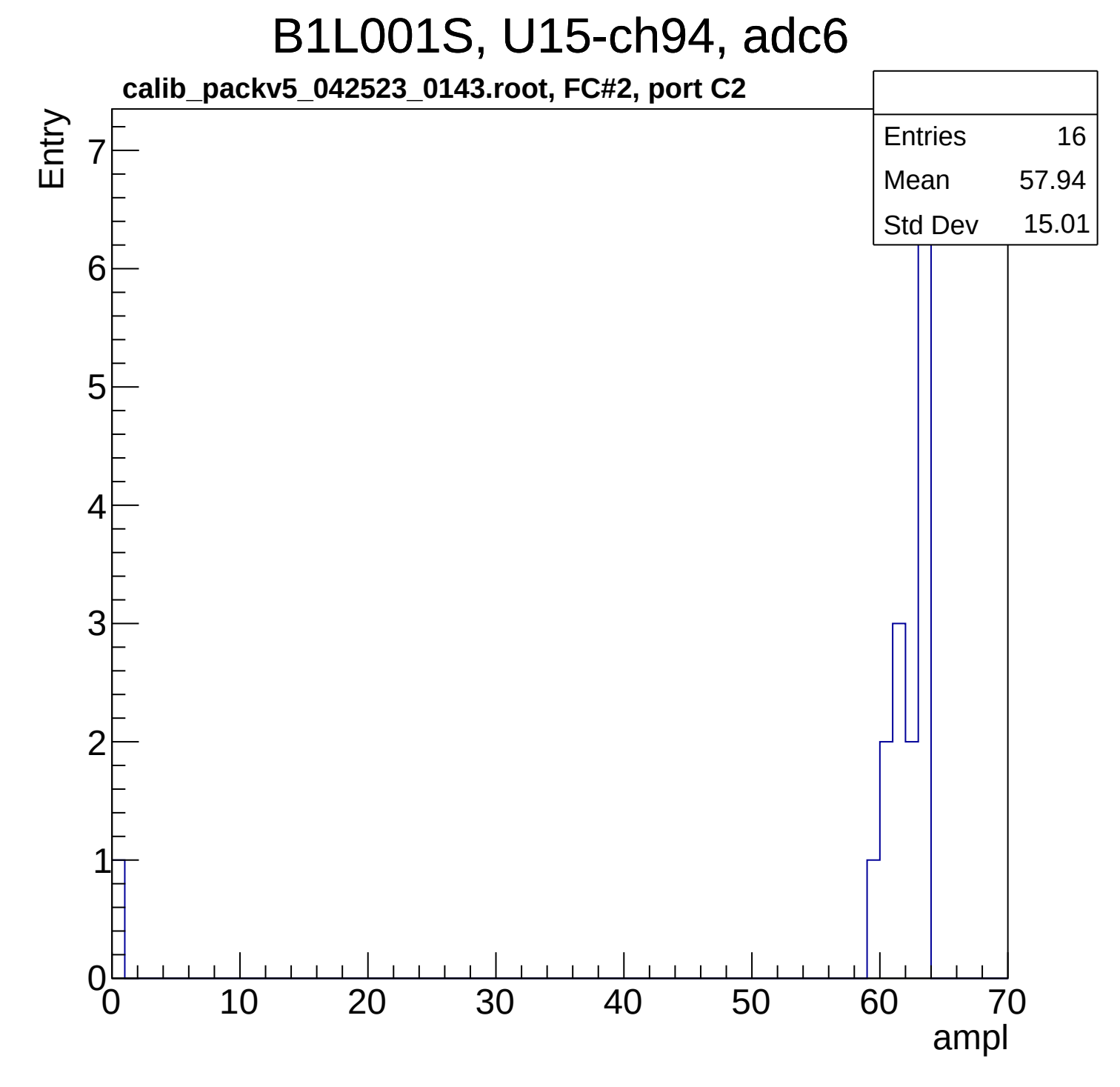
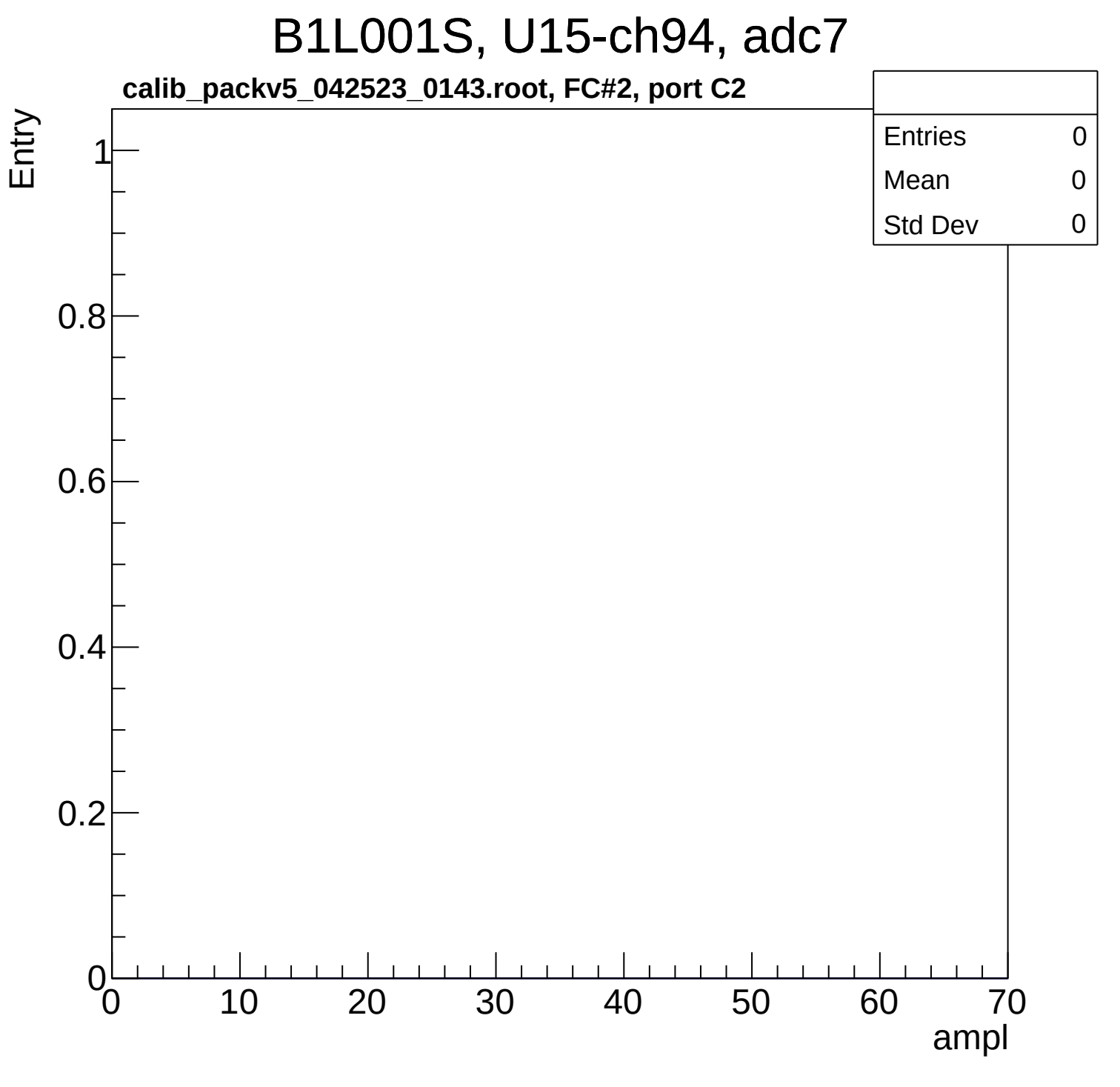
50
60
70
ampl


B1L001S, U15-ch94, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 57.94
Std Dev 15.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch94, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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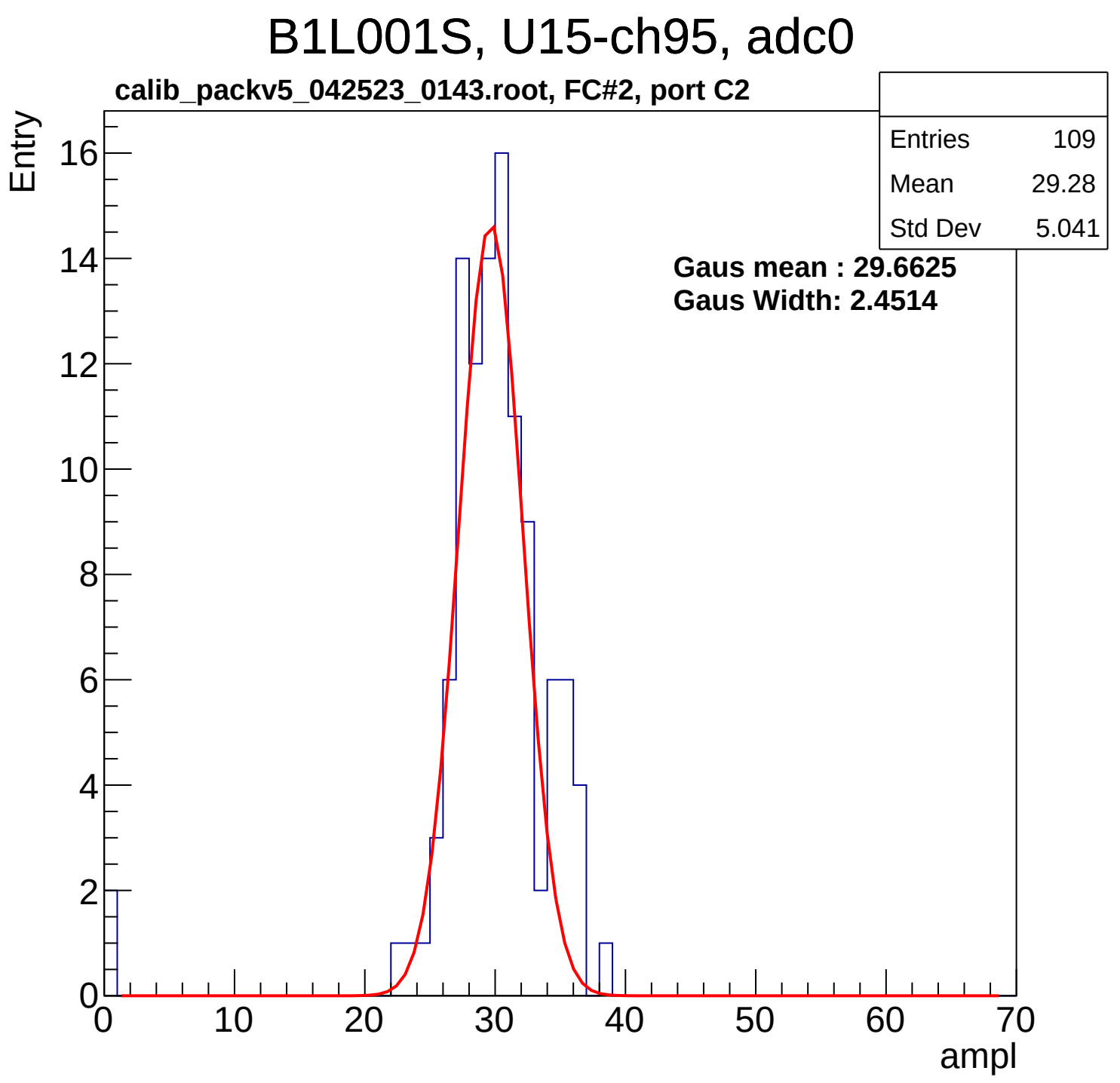
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch95, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 29.28
Std Dev 5.041
Gaus mean : 29.6625
Gaus Width: 2.4514
Entry
ampl
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2
4
6
8
10
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14
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0
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20
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40
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60
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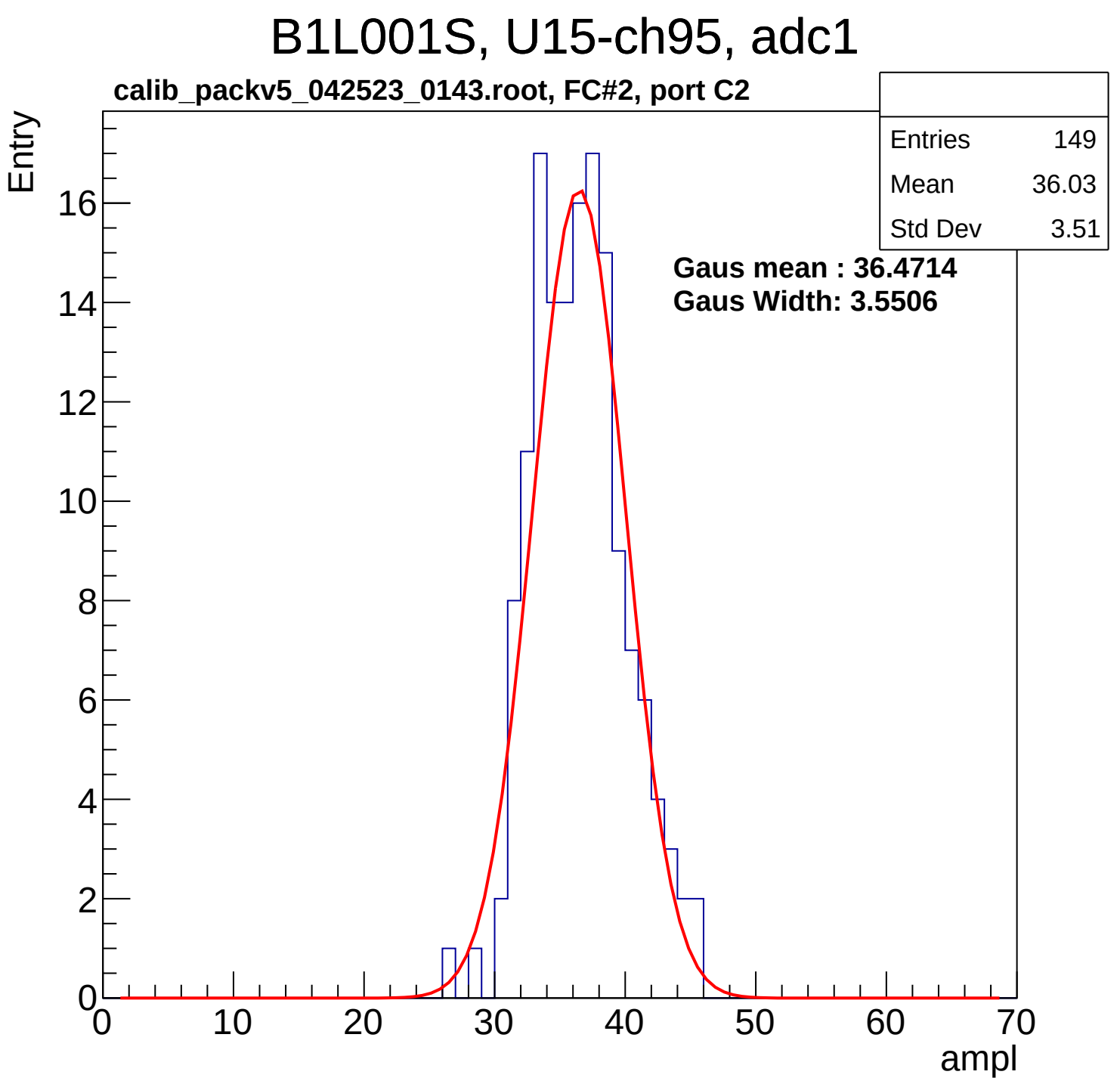
B1L001S, U15-ch95, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 36.03
Std Dev 3.51
Gaus mean : 36.4714
Gaus Width: 3.5506
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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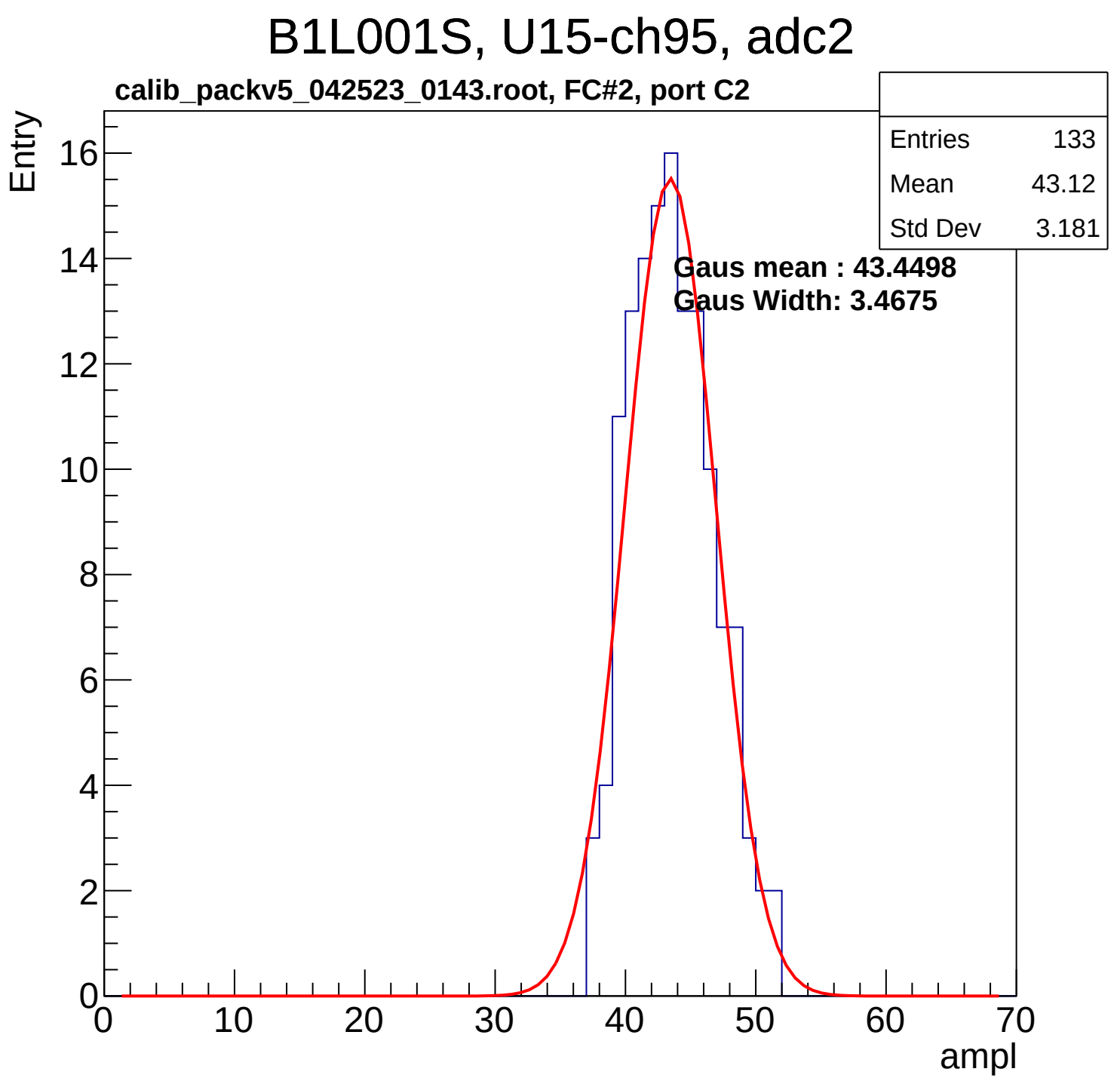
B1L001S, U15-ch95, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 43.12
Std Dev 3.181
Gaus mean : 43.4498
Gaus Width: 3.4675
Entry
ampl
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4
6
8
10
12
14
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0
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20
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40
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60
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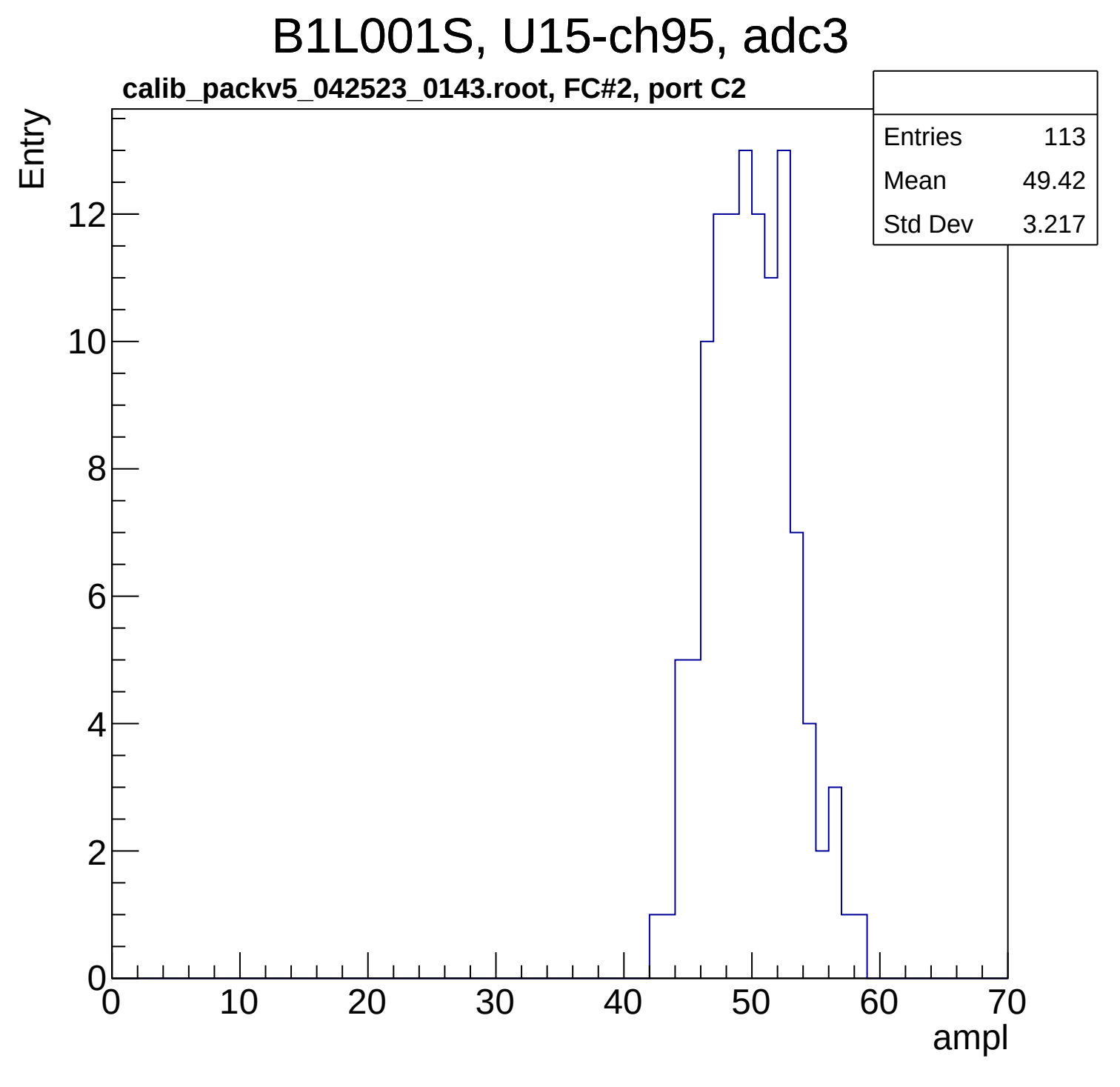
B1L001S, U15-ch95, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.42
Std Dev 3.217
Entry
0
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4
6
8
10
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10
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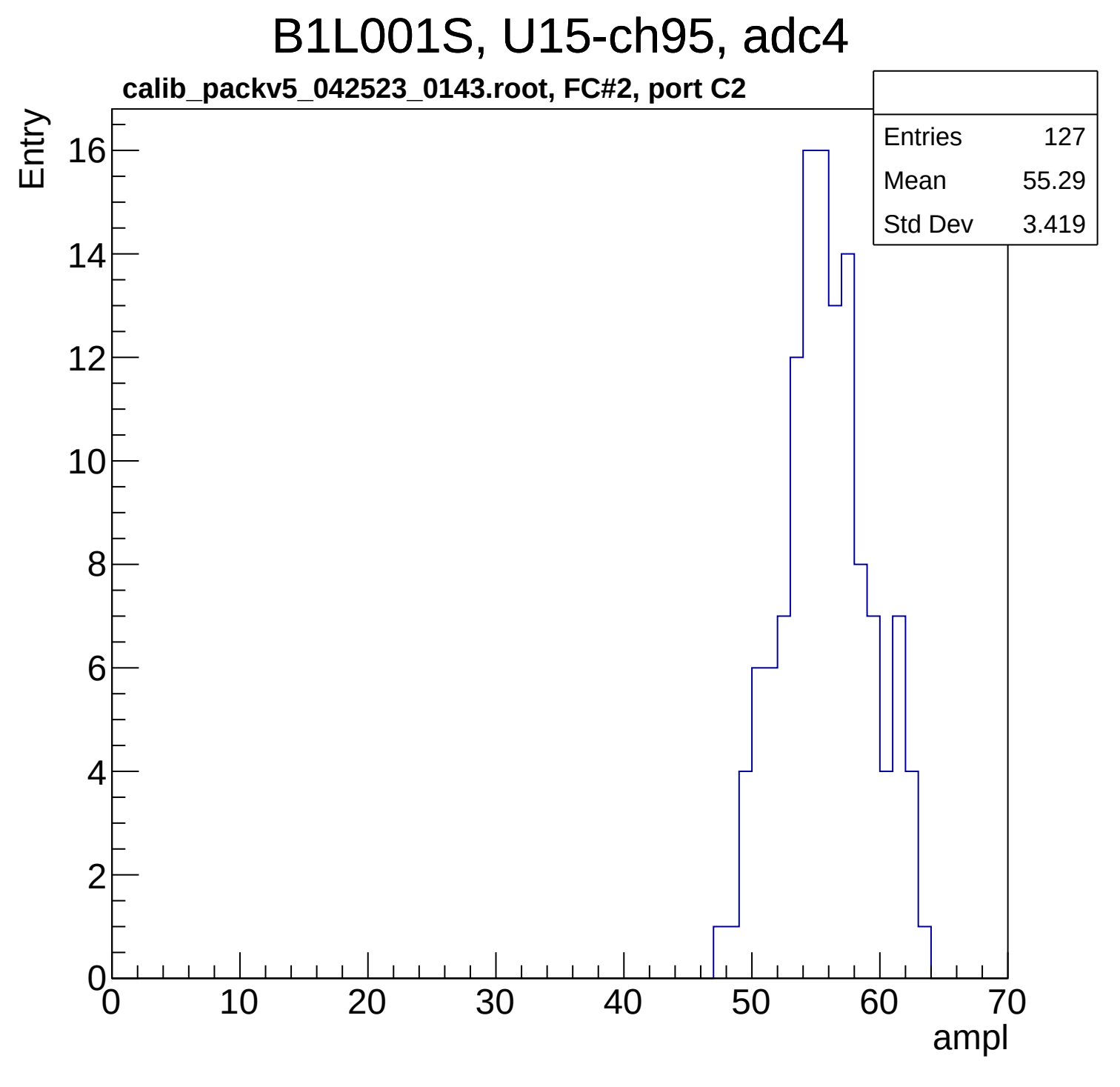
50
60
70
ampl


B1L001S, U15-ch95, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 55.29
Std Dev 3.419
Entry
ampl
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4
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8
10
12
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0
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60
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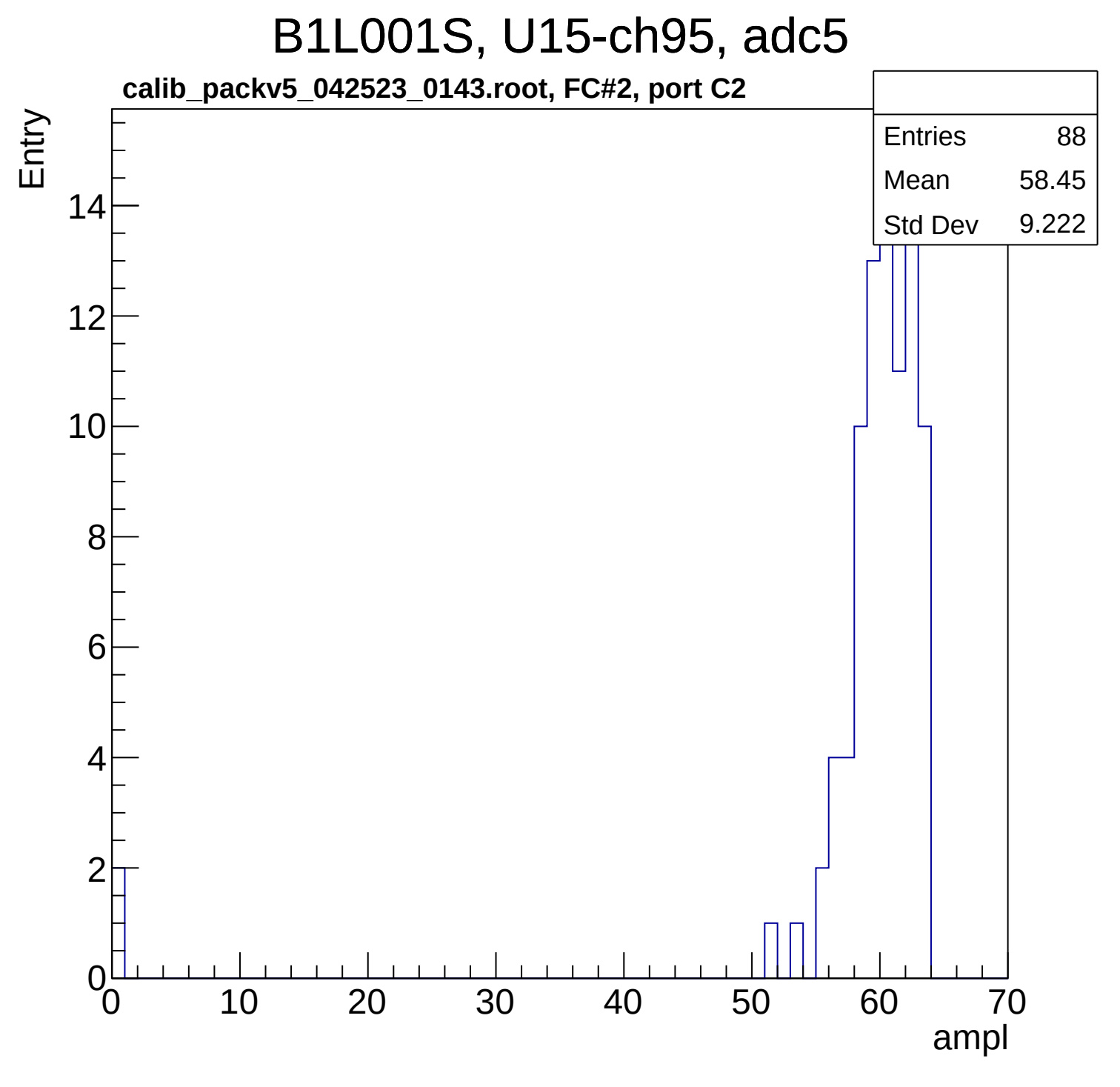
B1L001S, U15-ch95, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 58.45
Std Dev 9.222
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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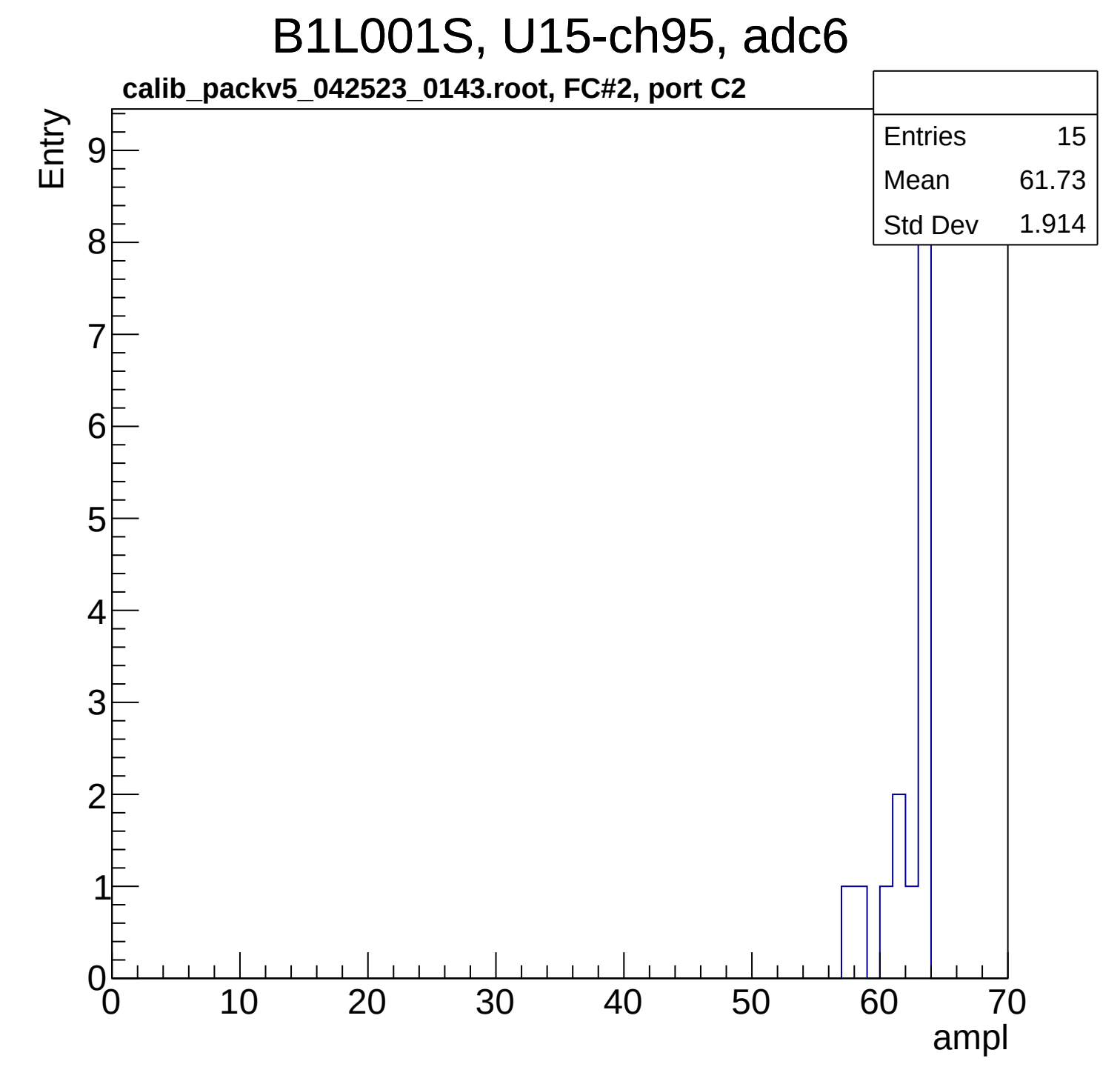
B1L001S, U15-ch95, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 61.73
Std Dev 1.914
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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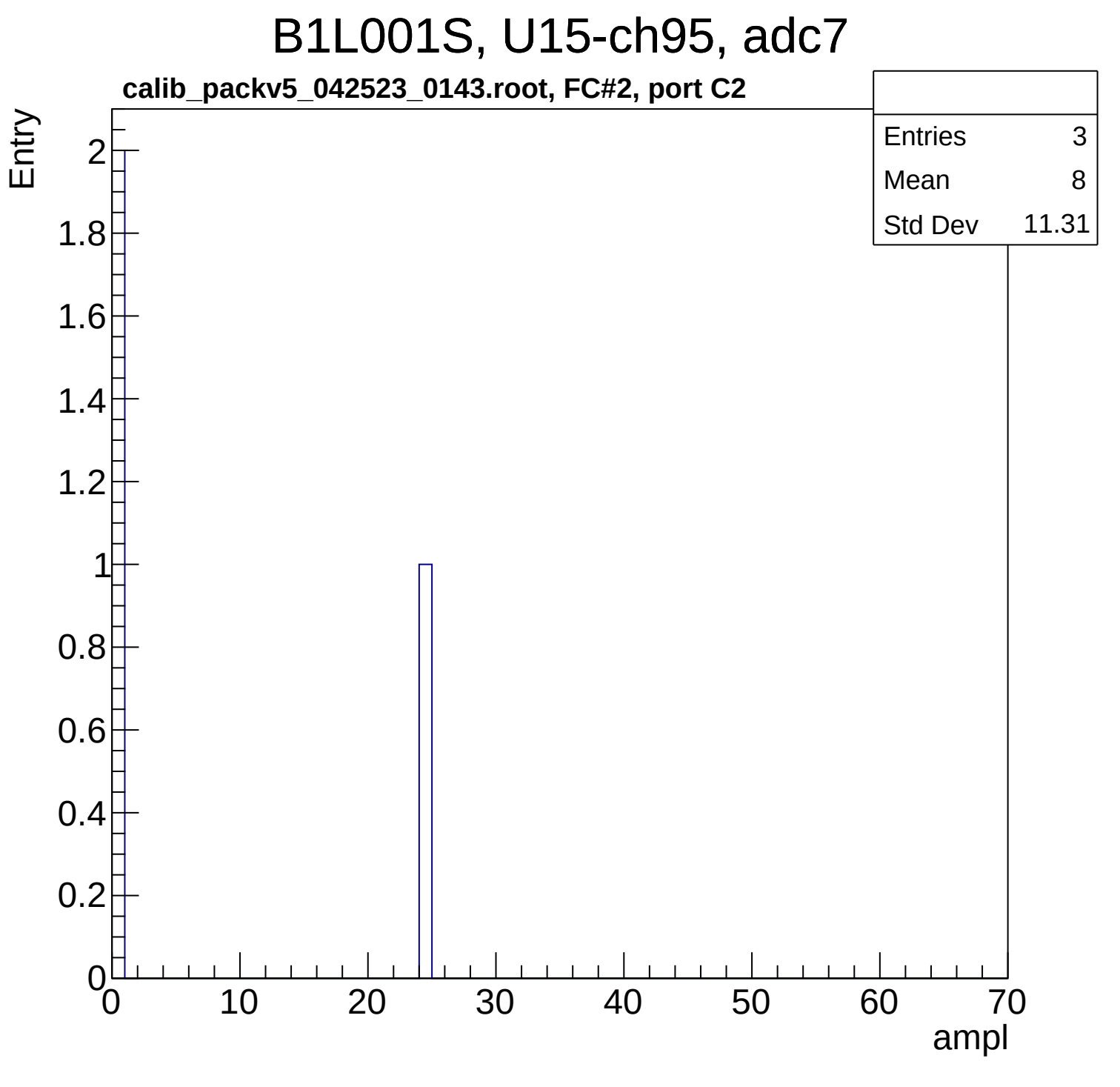
B1L001S, U15-ch95, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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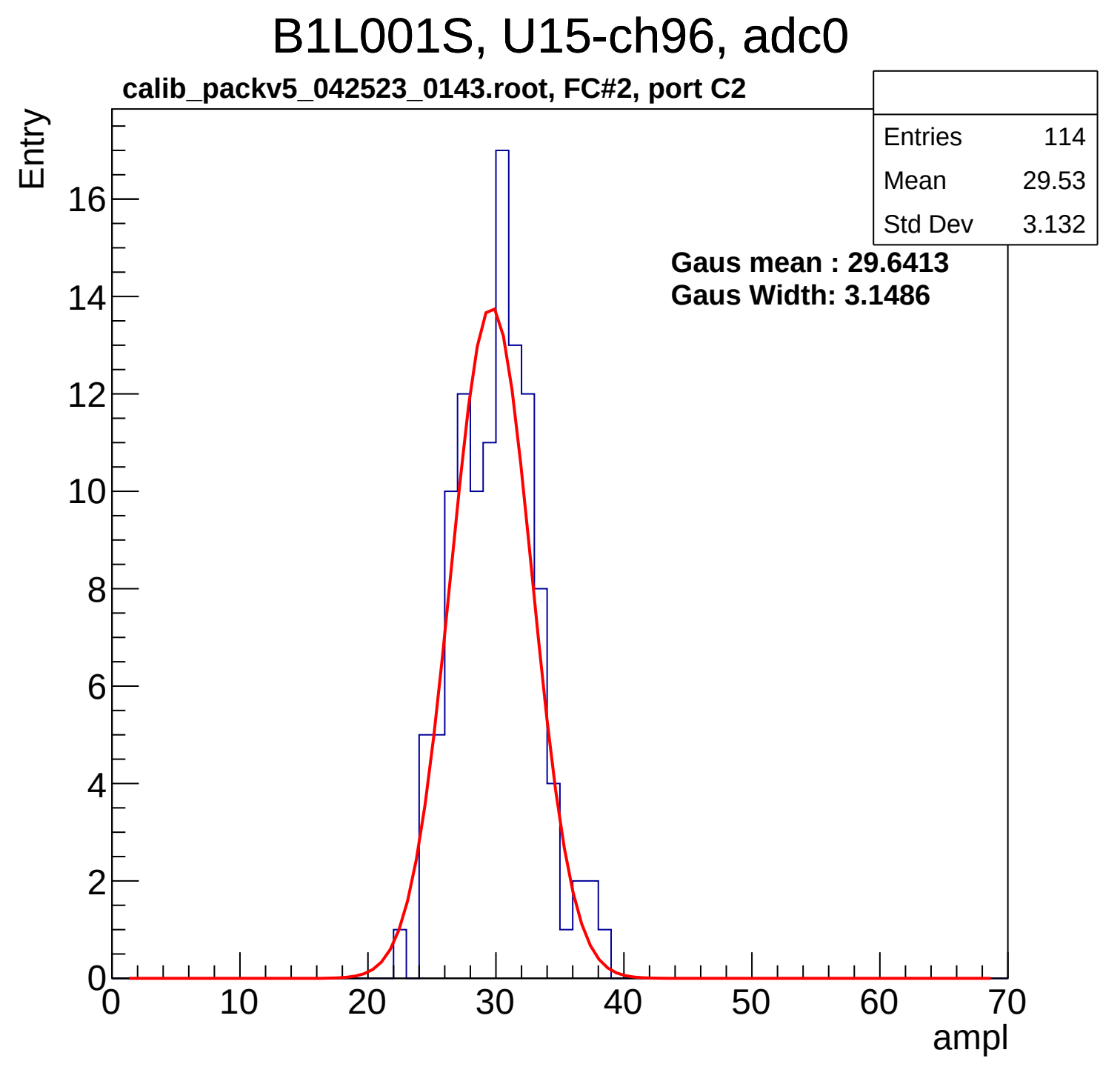
B1L001S, U15-ch96, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 29.53
Std Dev 3.132
Gaus mean : 29.6413
Gaus Width: 3.1486
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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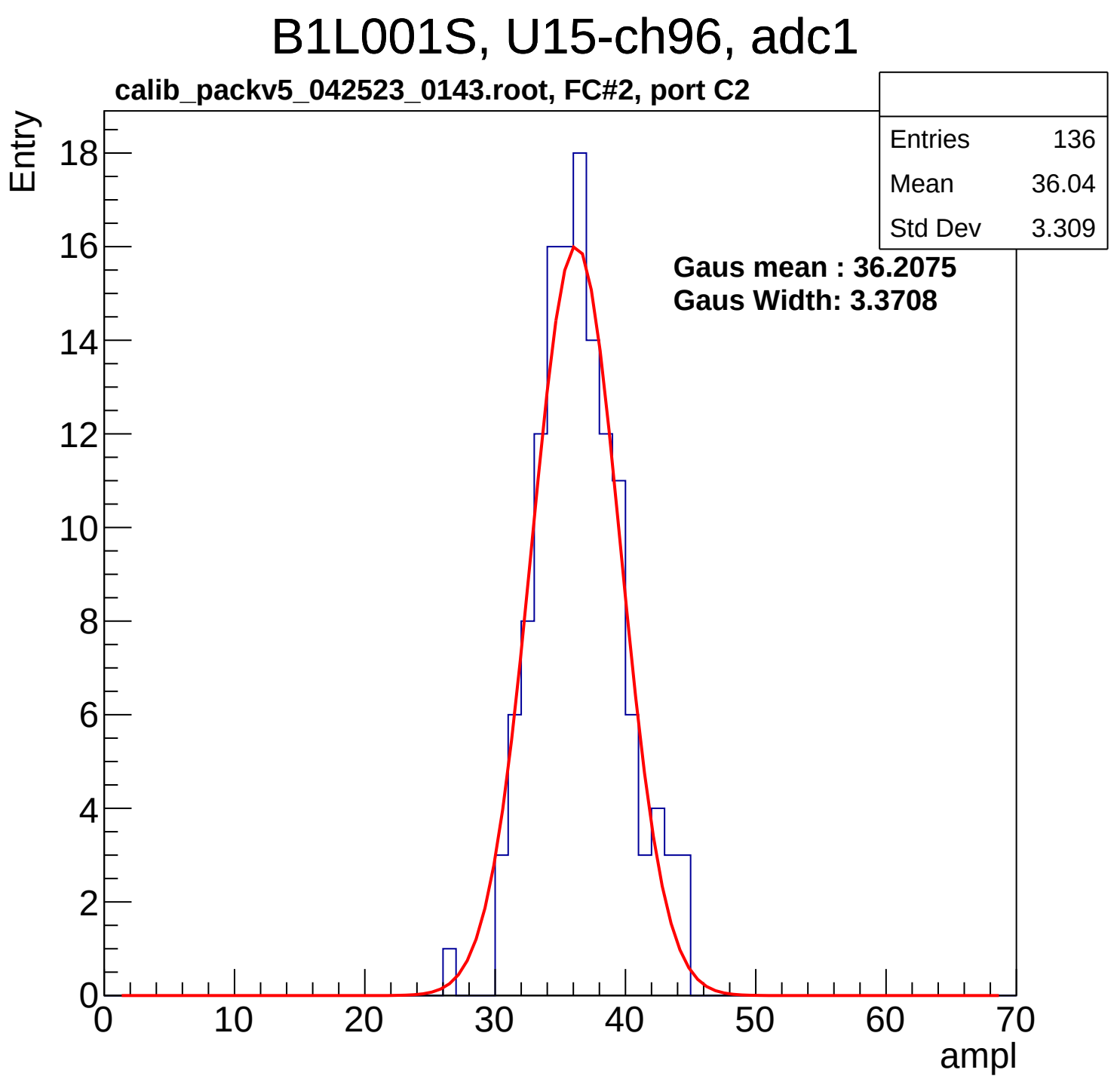
B1L001S, U15-ch96, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 36.04
Std Dev 3.309
Gaus mean : 36.2075
Gaus Width: 3.3708
Entry
ampl
0
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4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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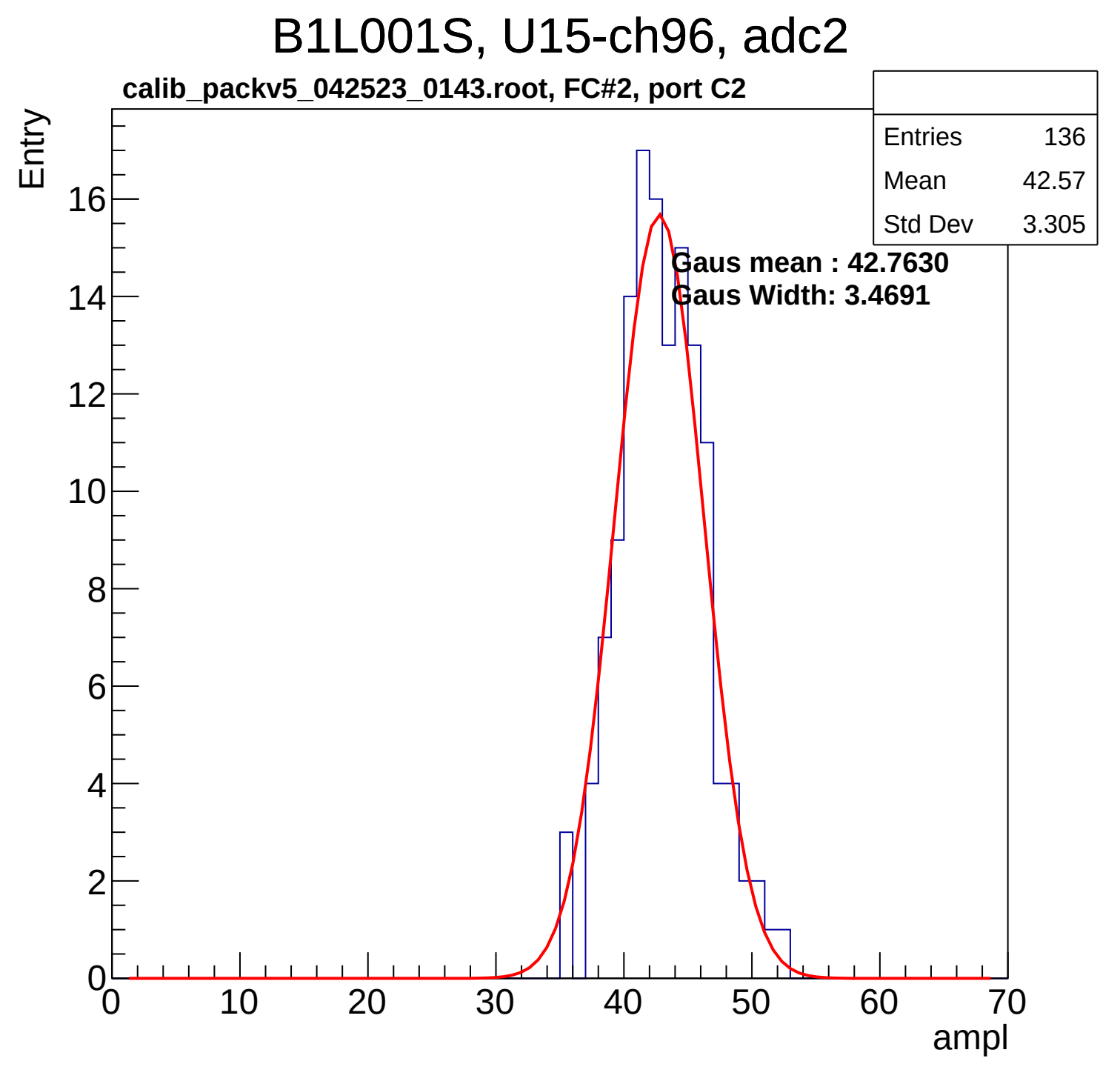
B1L001S, U15-ch96, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 42.57
Std Dev 3.305
Gaus mean : 42.7630
Gaus Width: 3.4691
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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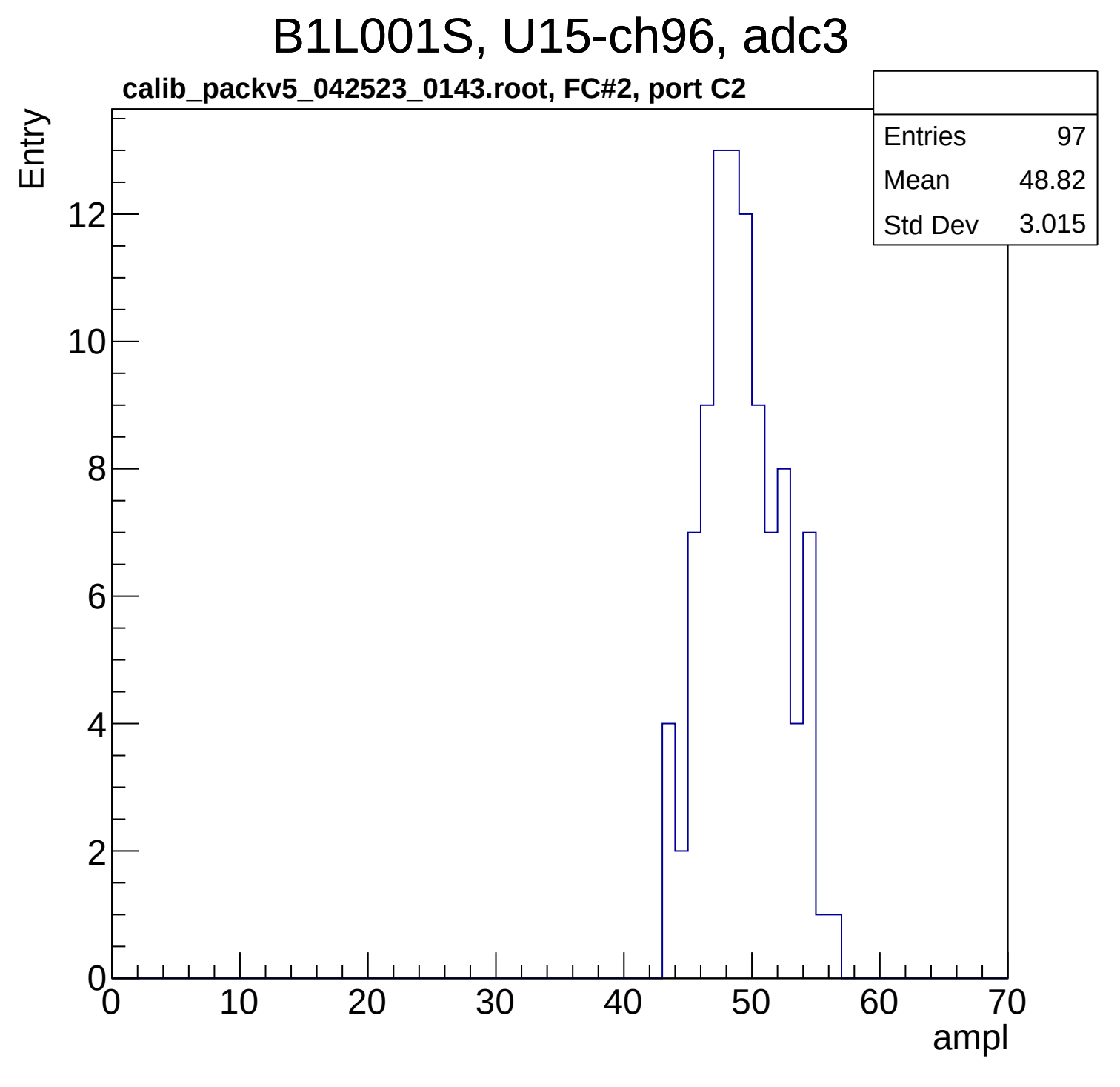
B1L001S, U15-ch96, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 48.82
Std Dev 3.015
Entry
0
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4
6
8
10
12
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10
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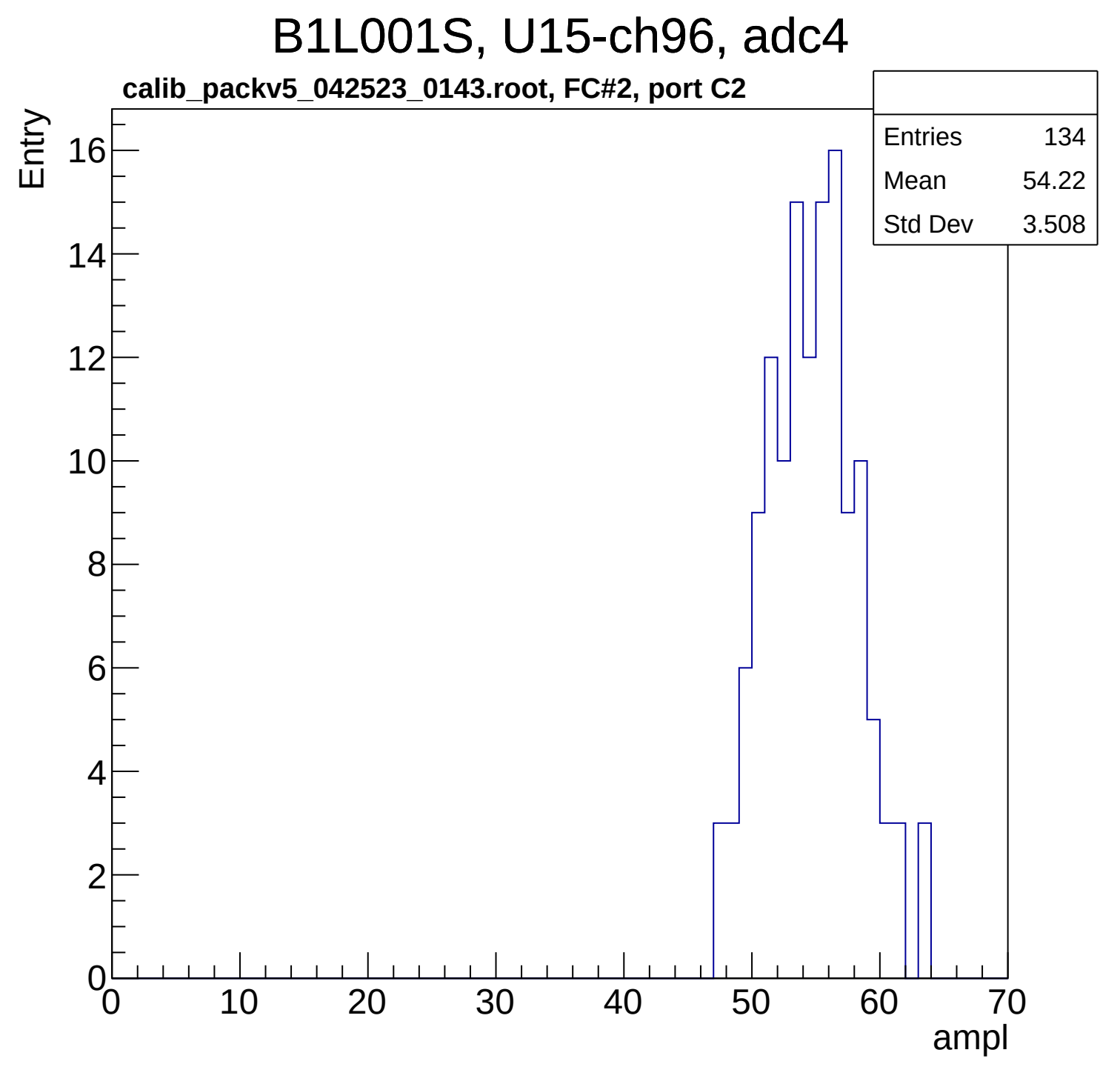
50
60
70
ampl


B1L001S, U15-ch96, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 54.22
Std Dev 3.508
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
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60
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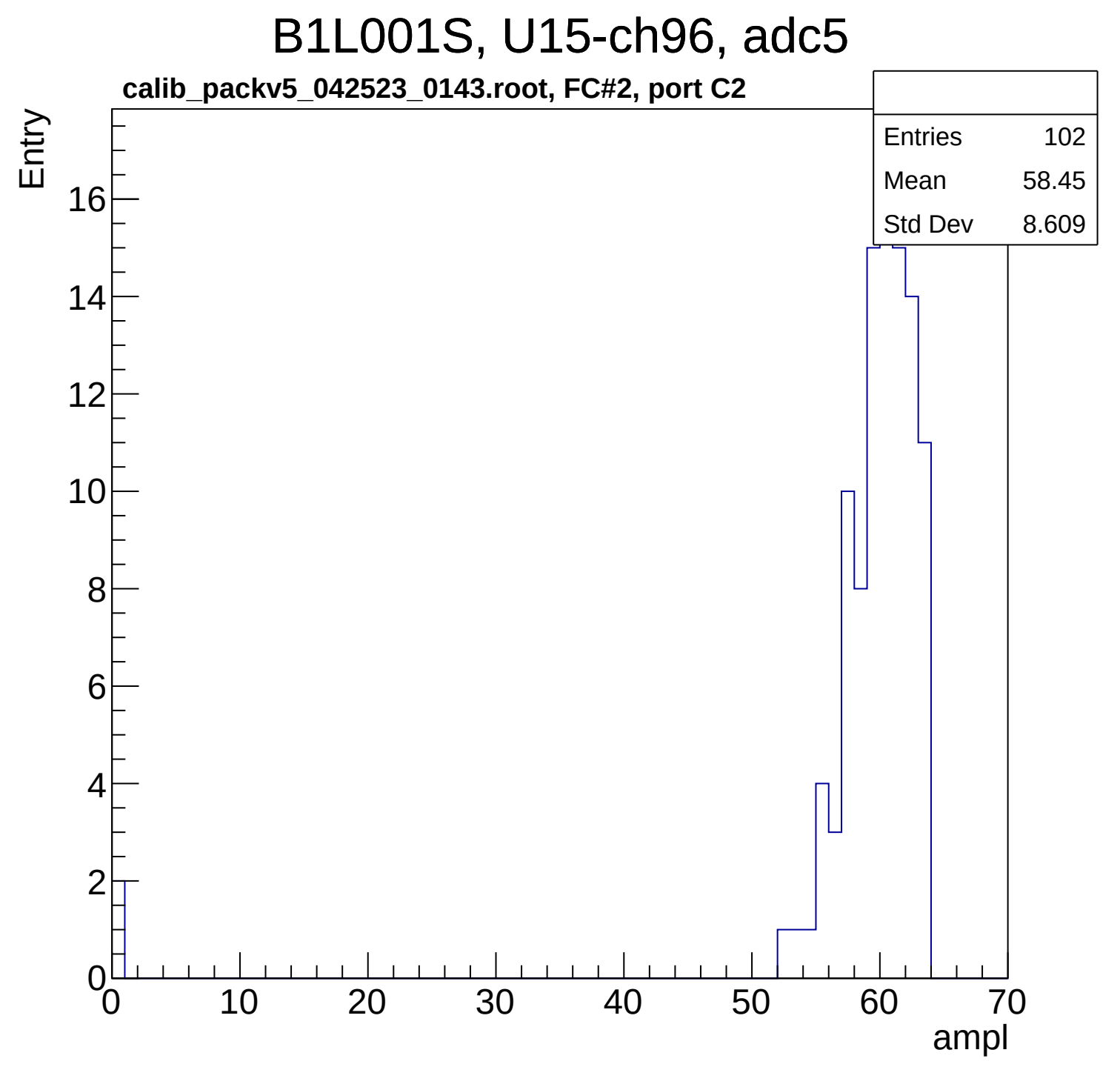
B1L001S, U15-ch96, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 58.45
Std Dev 8.609
Entry
ampl
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4
6
8
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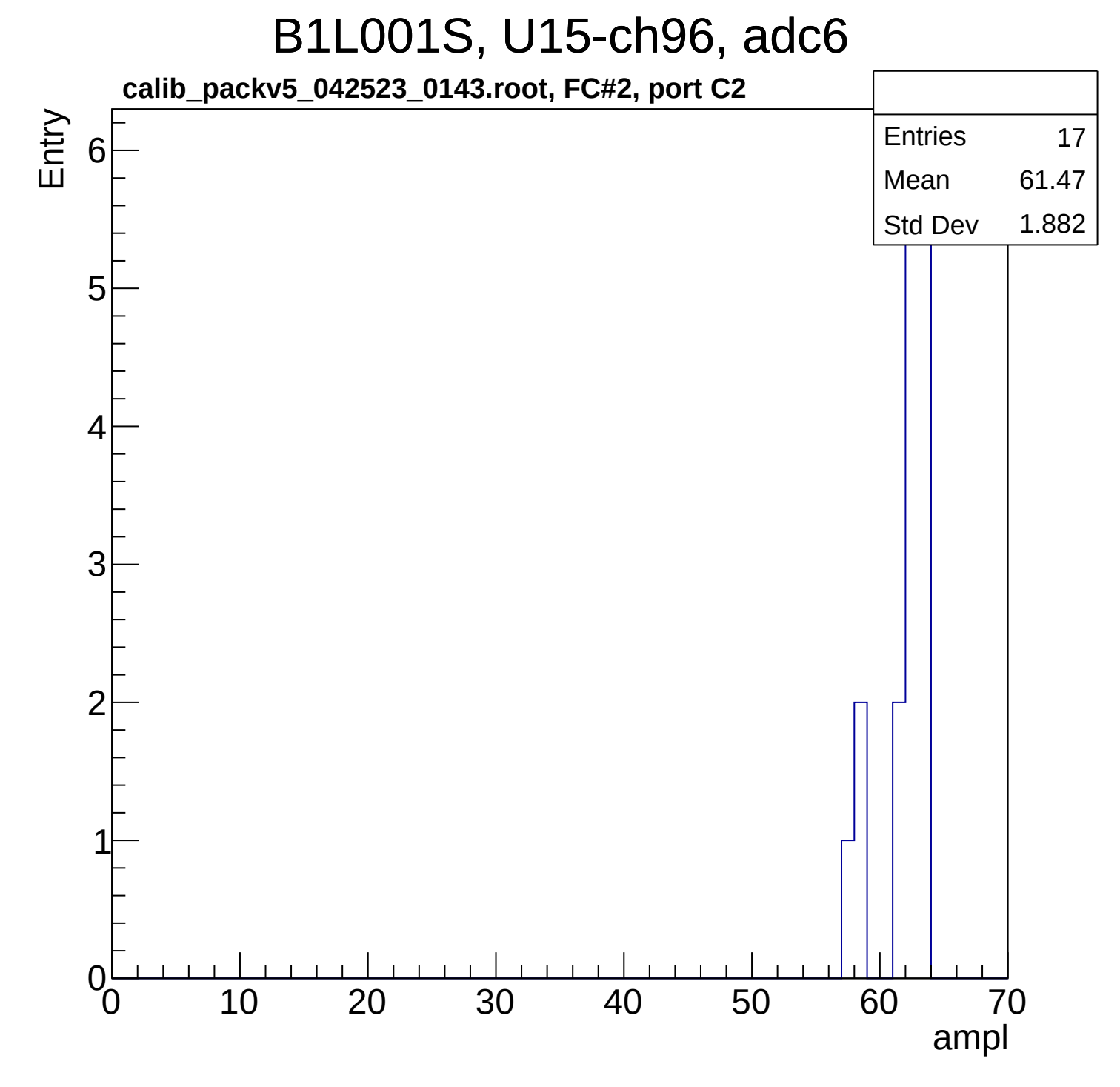
B1L001S, U15-ch96, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 17
Mean 61.47
Std Dev 1.882
Entry
ampl
0
1
2
3
4
5
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0
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20
30
40
50
60
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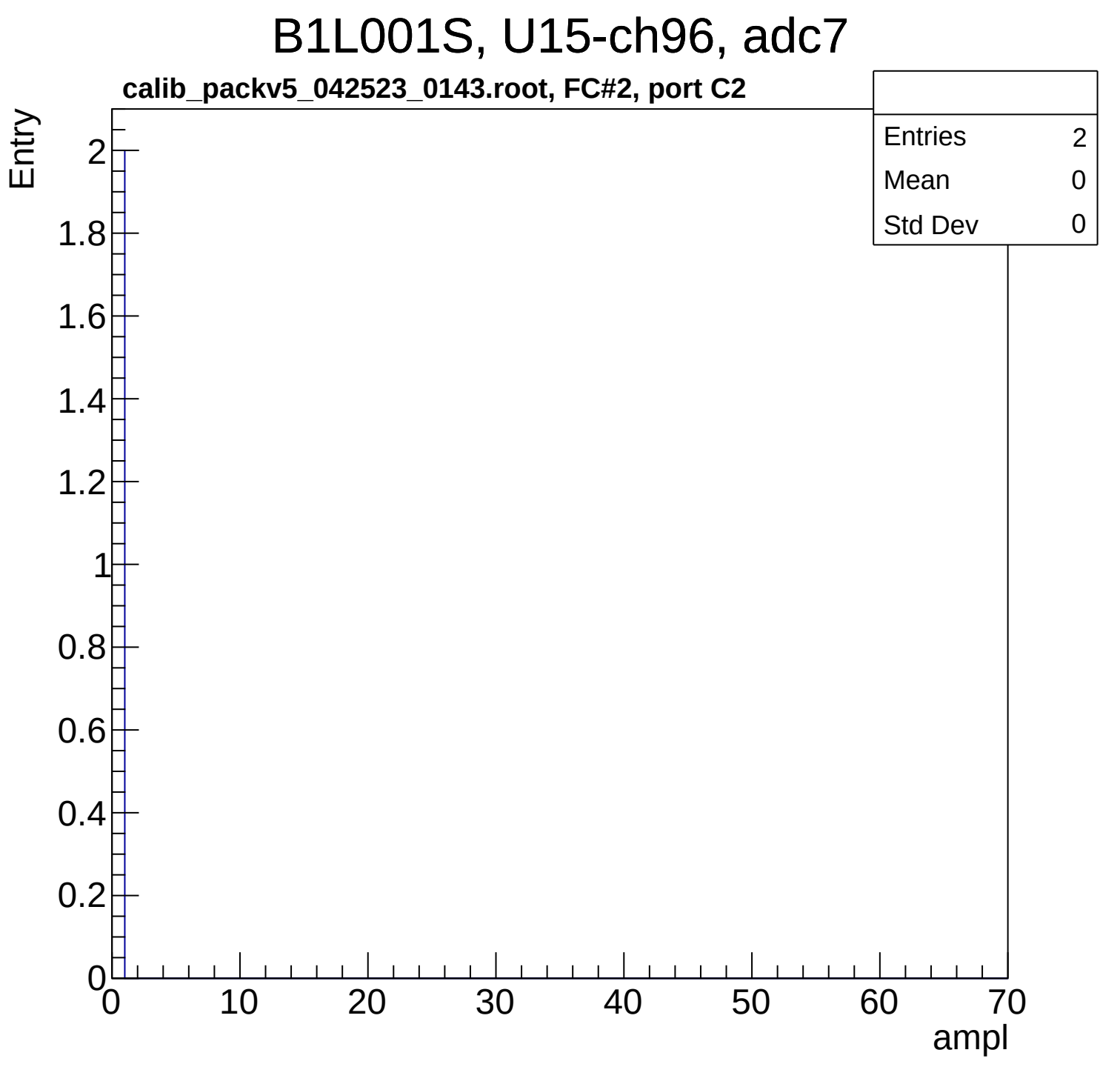
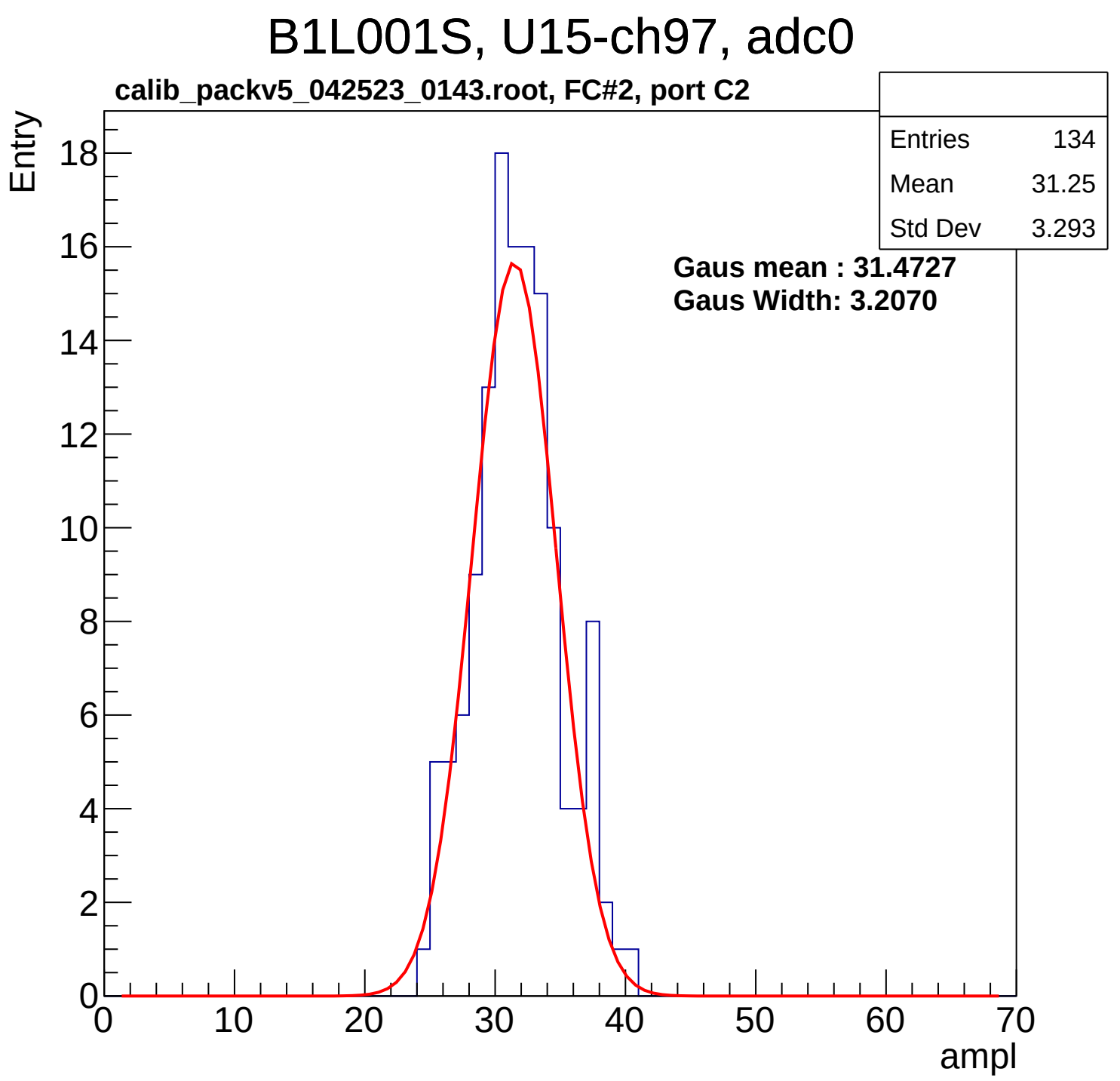

B1L001S, U15-ch96, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch97, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 31.25
Std Dev 3.293
Gaus mean : 31.4727
Gaus Width: 3.2070
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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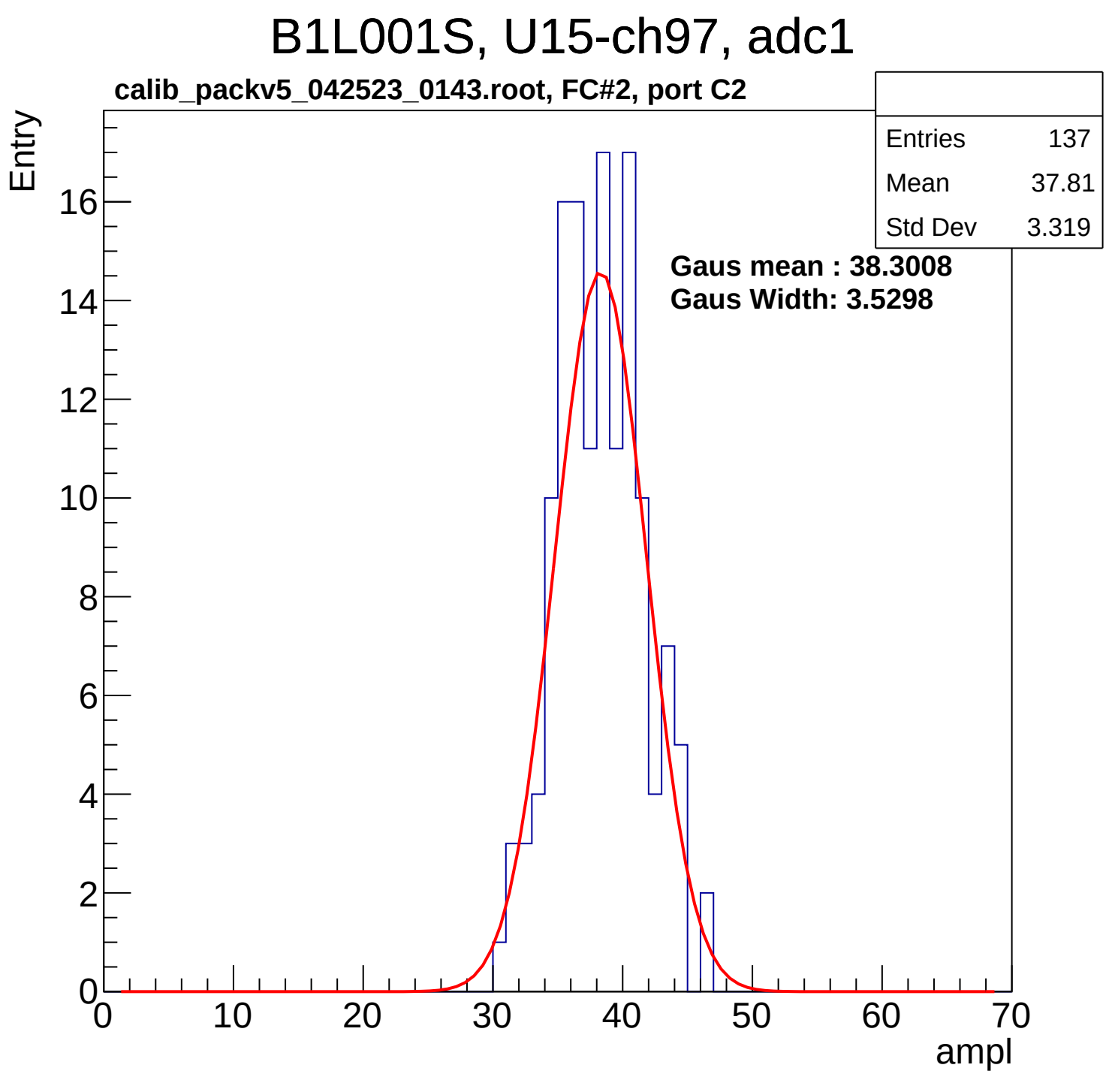
B1L001S, U15-ch97, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 37.81
Std Dev 3.319
Gaus mean : 38.3008
Gaus Width: 3.5298
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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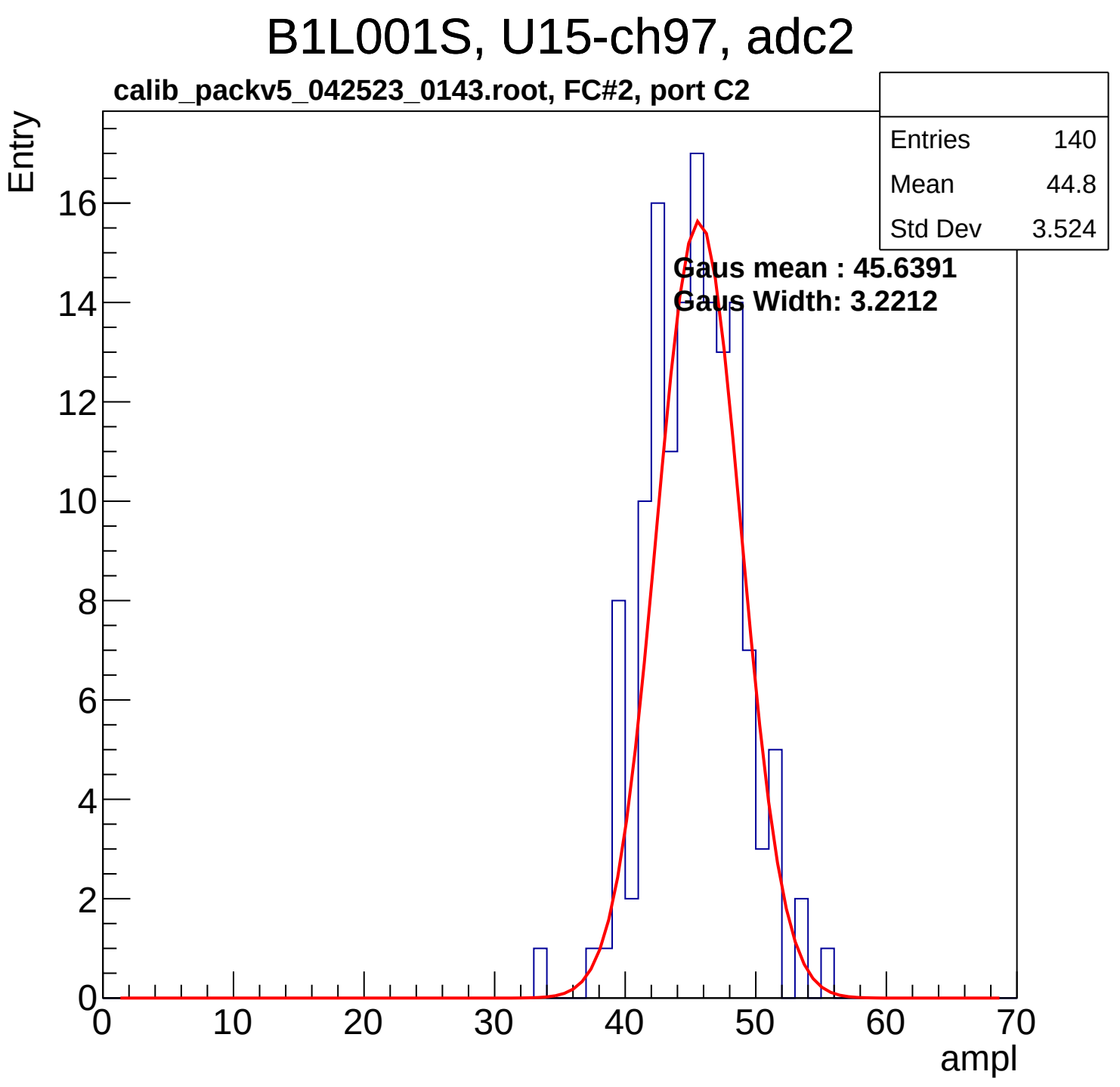
B1L001S, U15-ch97, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 44.8
Std Dev 3.524
Gaus mean : 45.6391
Gaus Width: 3.2212
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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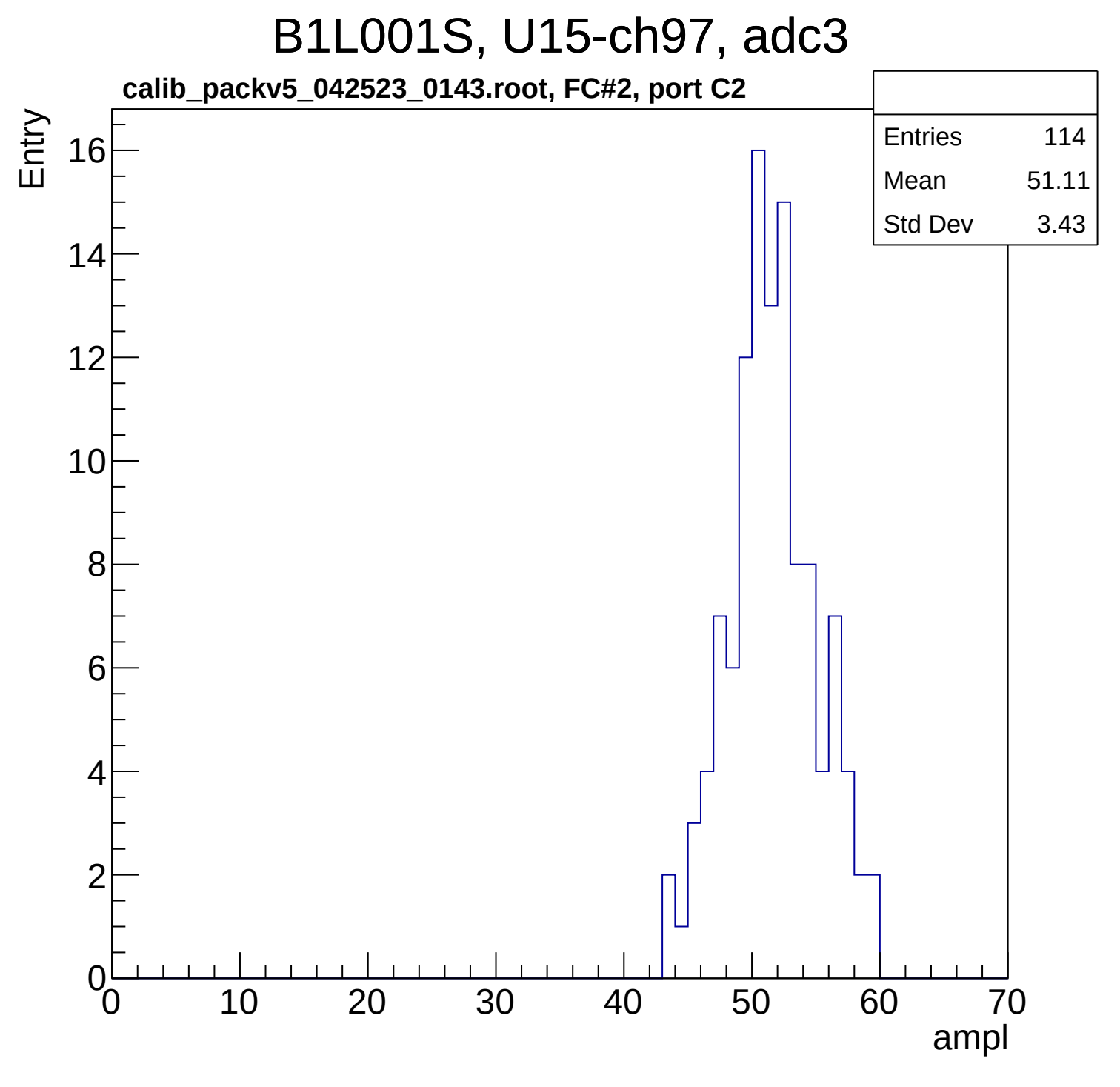
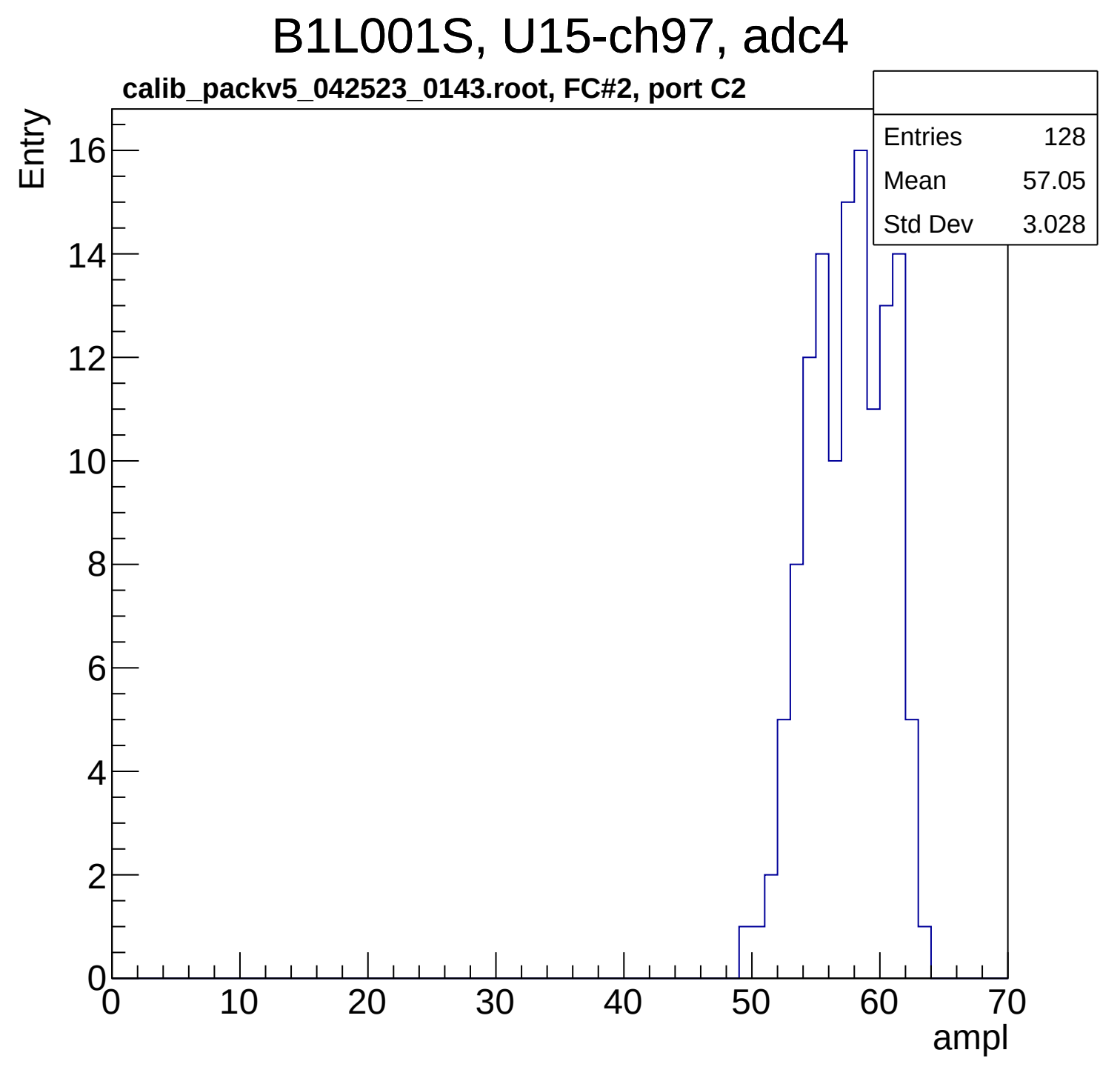
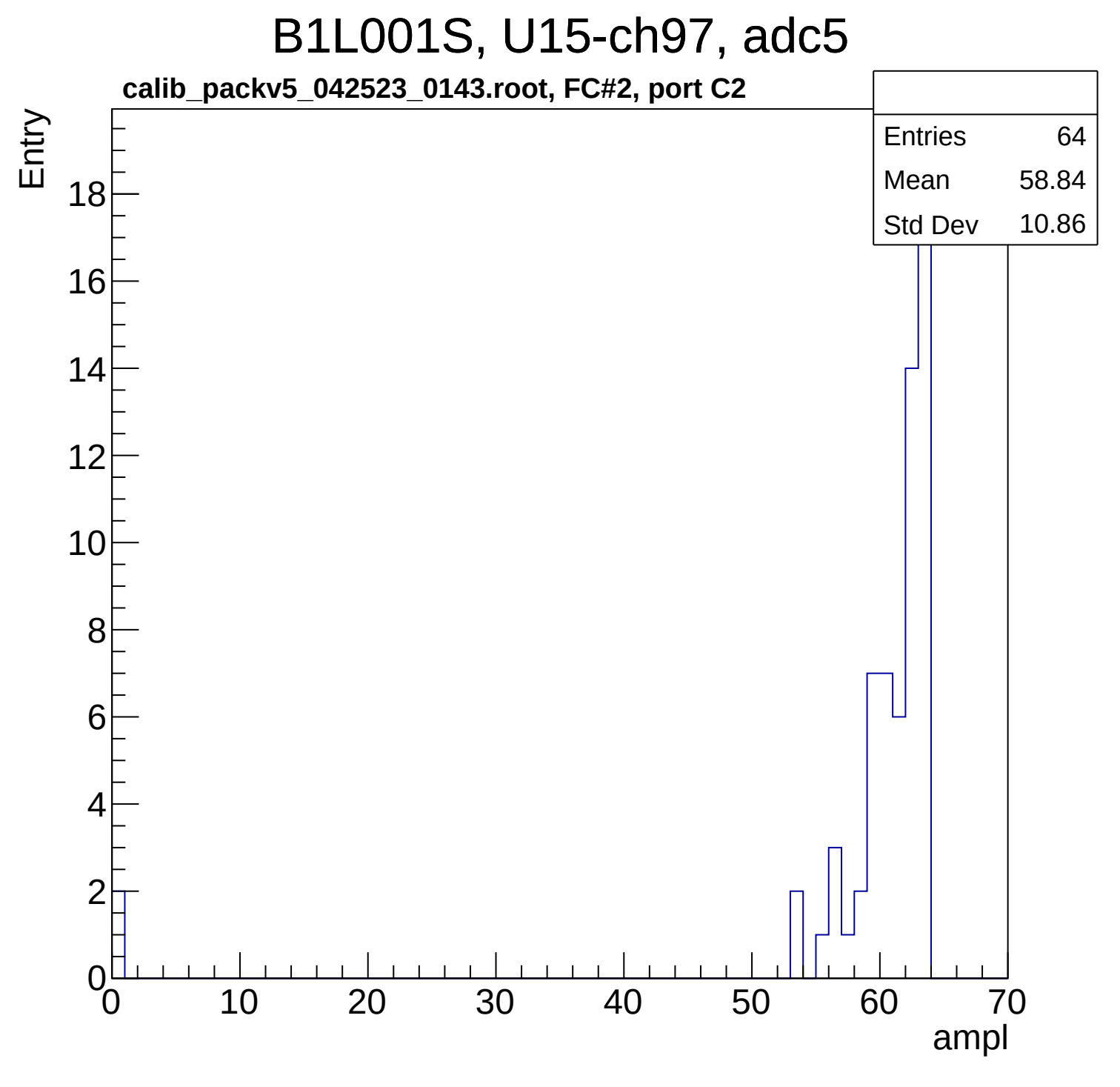

B1L001S, U15-ch97, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 51.11
Std Dev 3.43
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch97, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 57.05
Std Dev 3.028
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch97, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 64
Mean 58.84
Std Dev 10.86
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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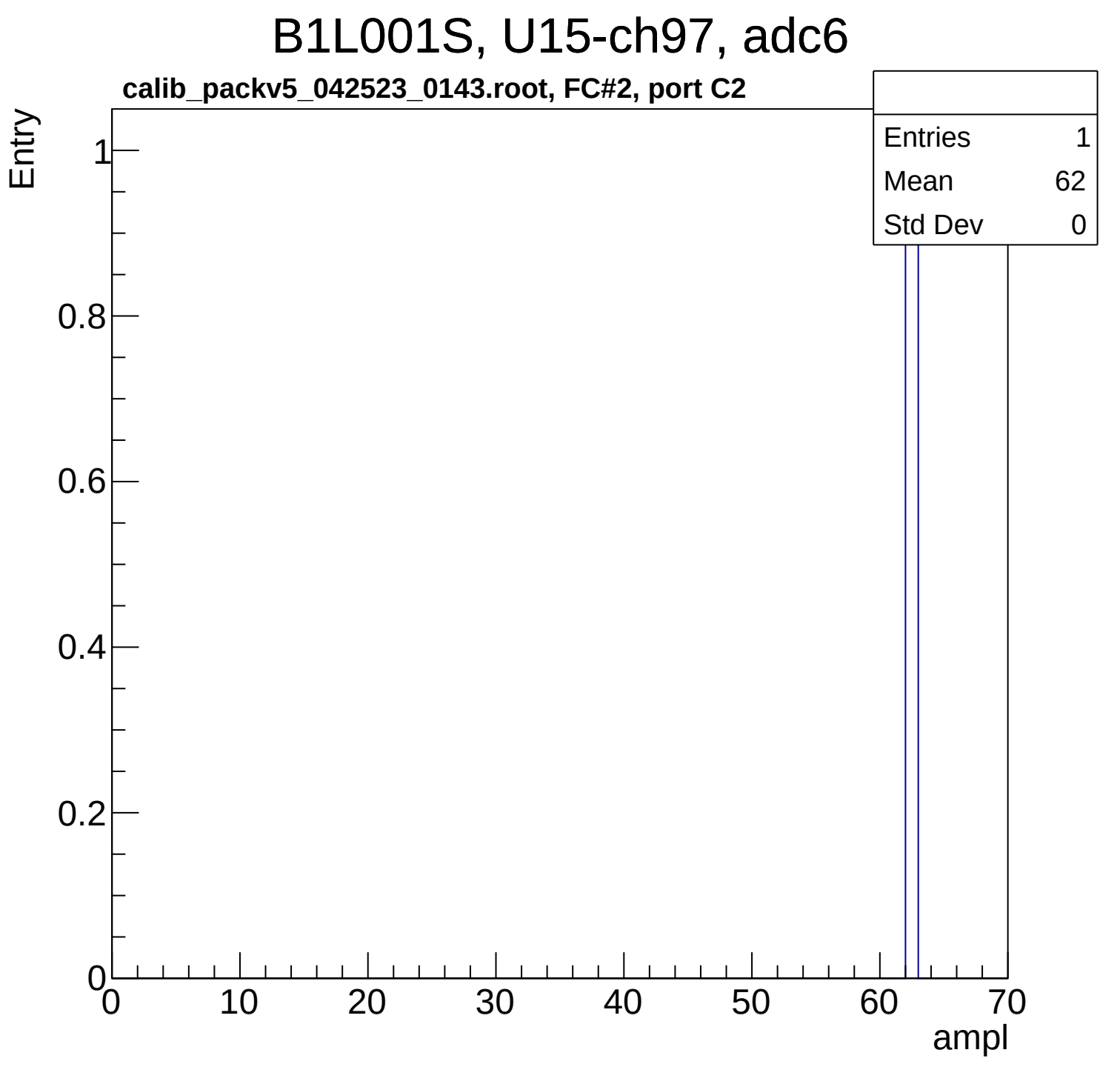
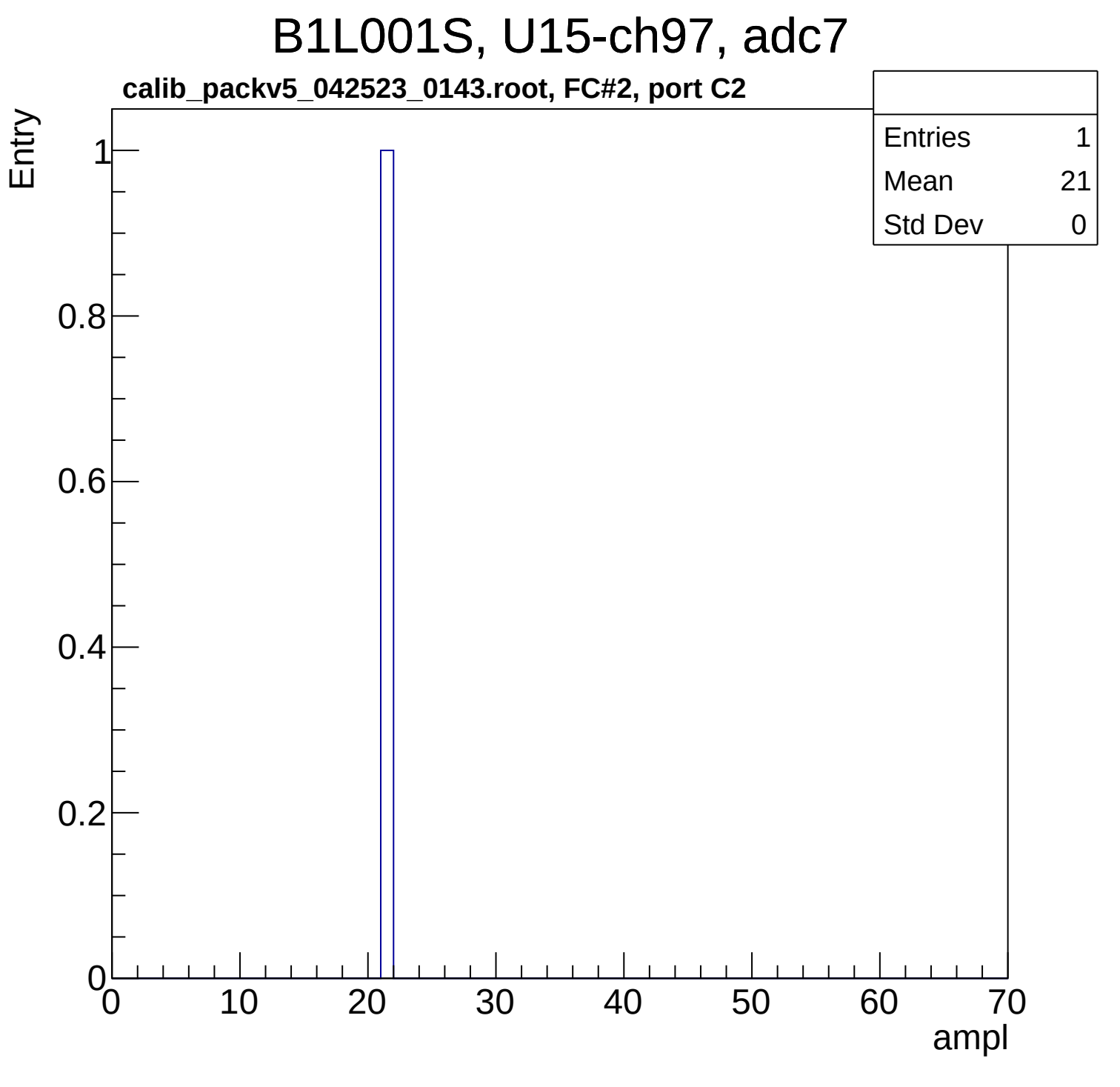

B1L001S, U15-ch97, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch97, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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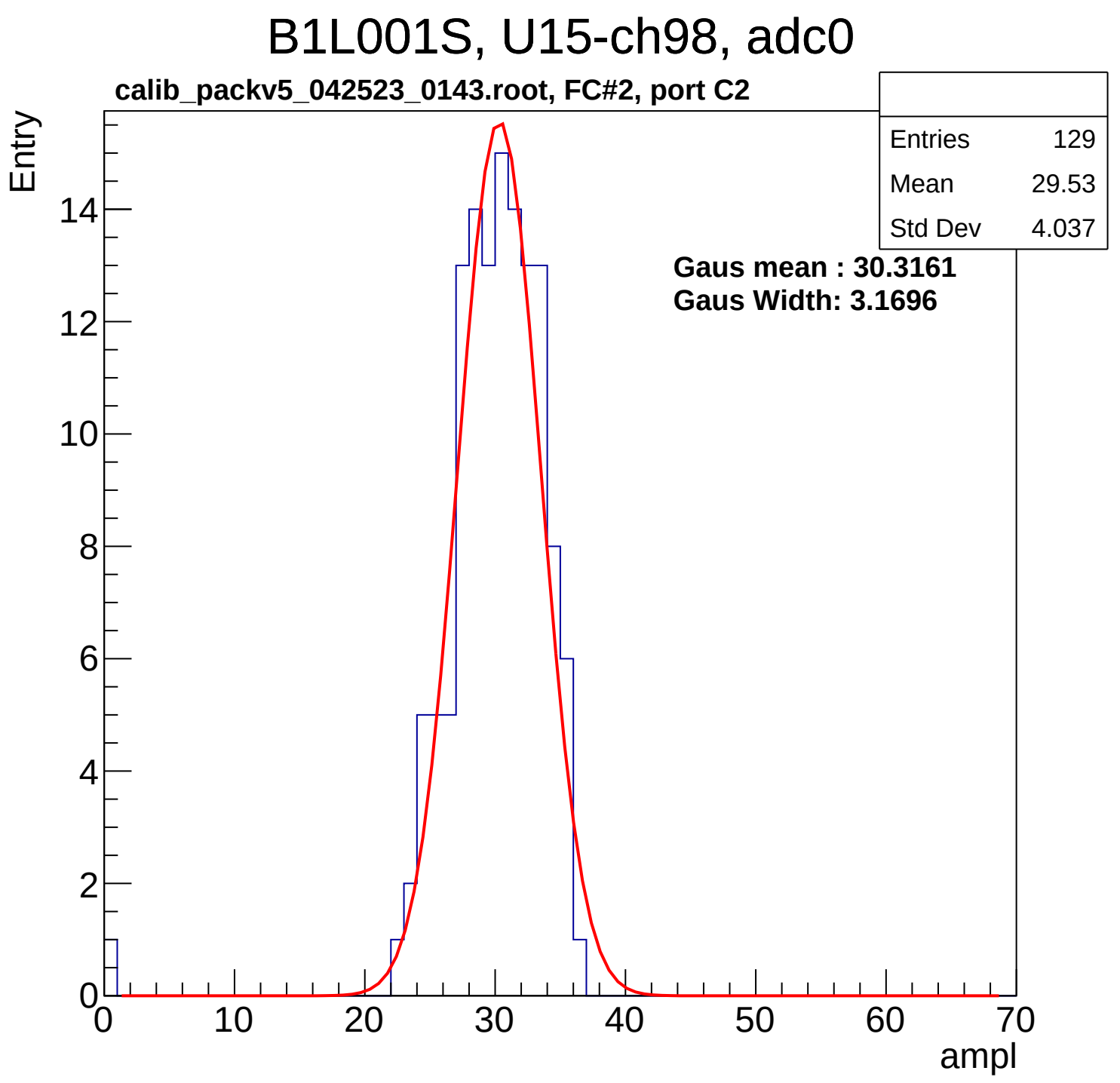
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch98, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 29.53
Std Dev 4.037
Gaus mean : 30.3161
Gaus Width: 3.1696
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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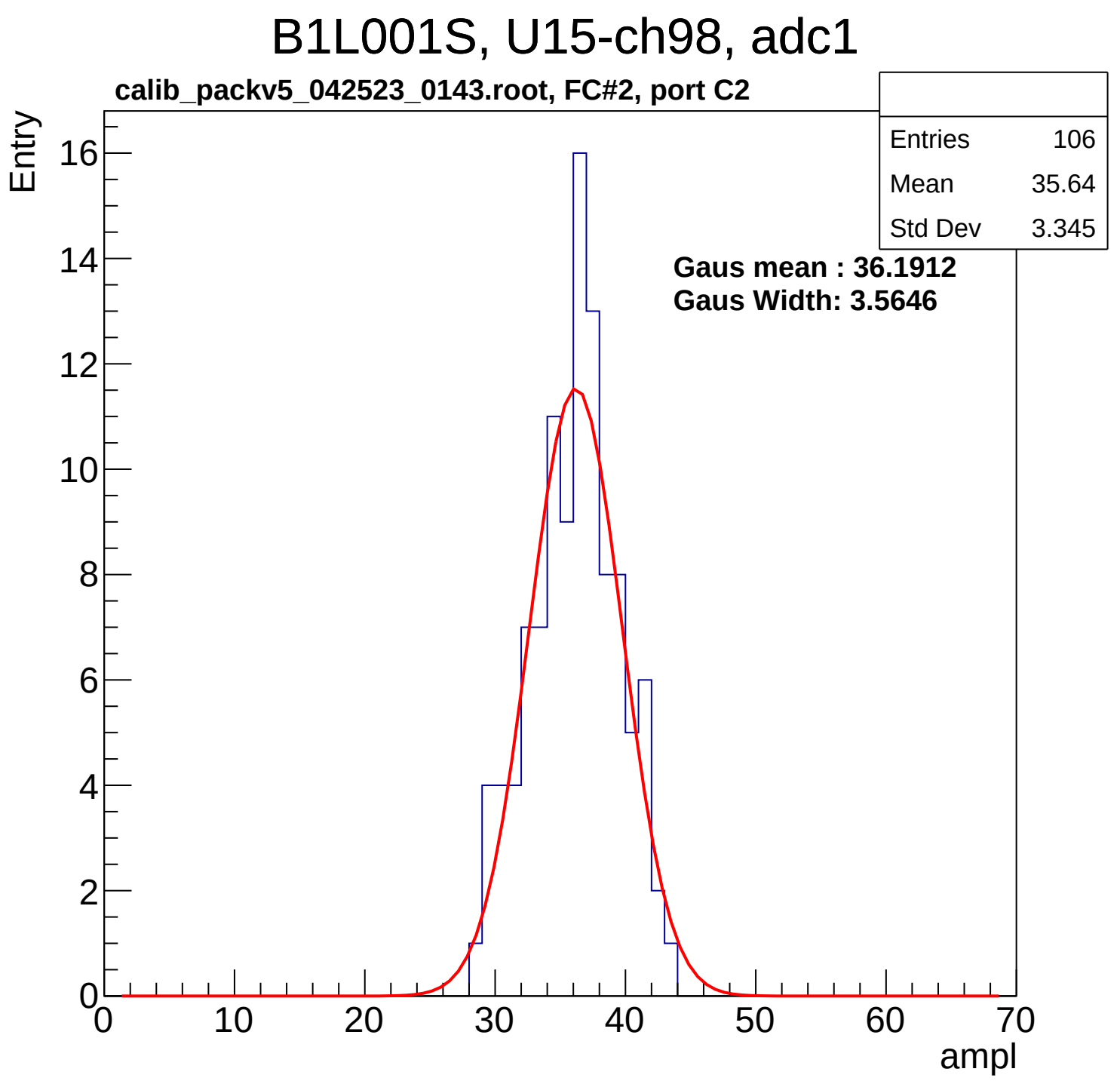
B1L001S, U15-ch98, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 35.64
Std Dev 3.345
Gaus mean : 36.1912
Gaus Width: 3.5646
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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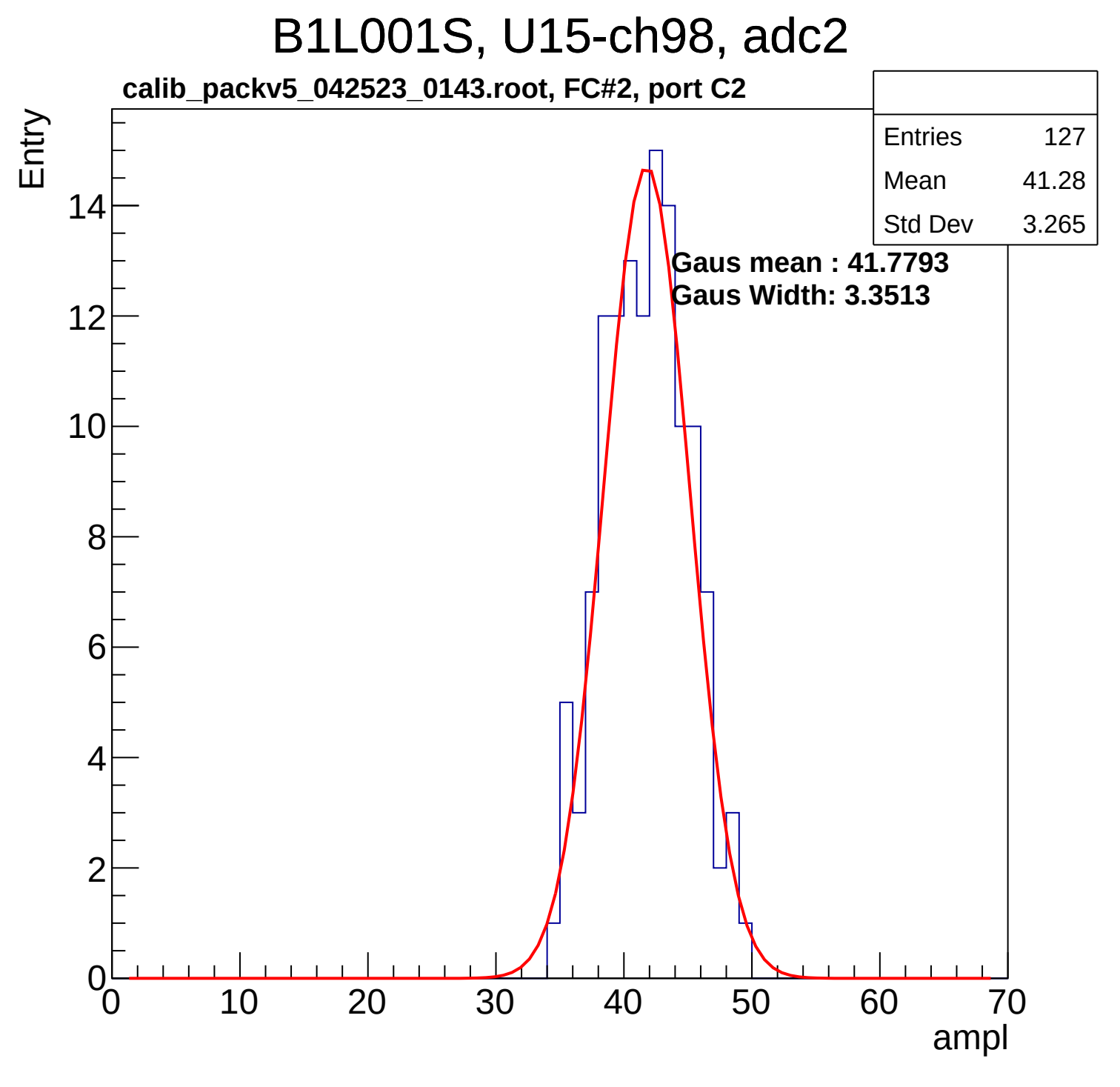
B1L001S, U15-ch98, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 41.28
Std Dev 3.265
Gaus mean : 41.7793
Gaus Width: 3.3513
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
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60
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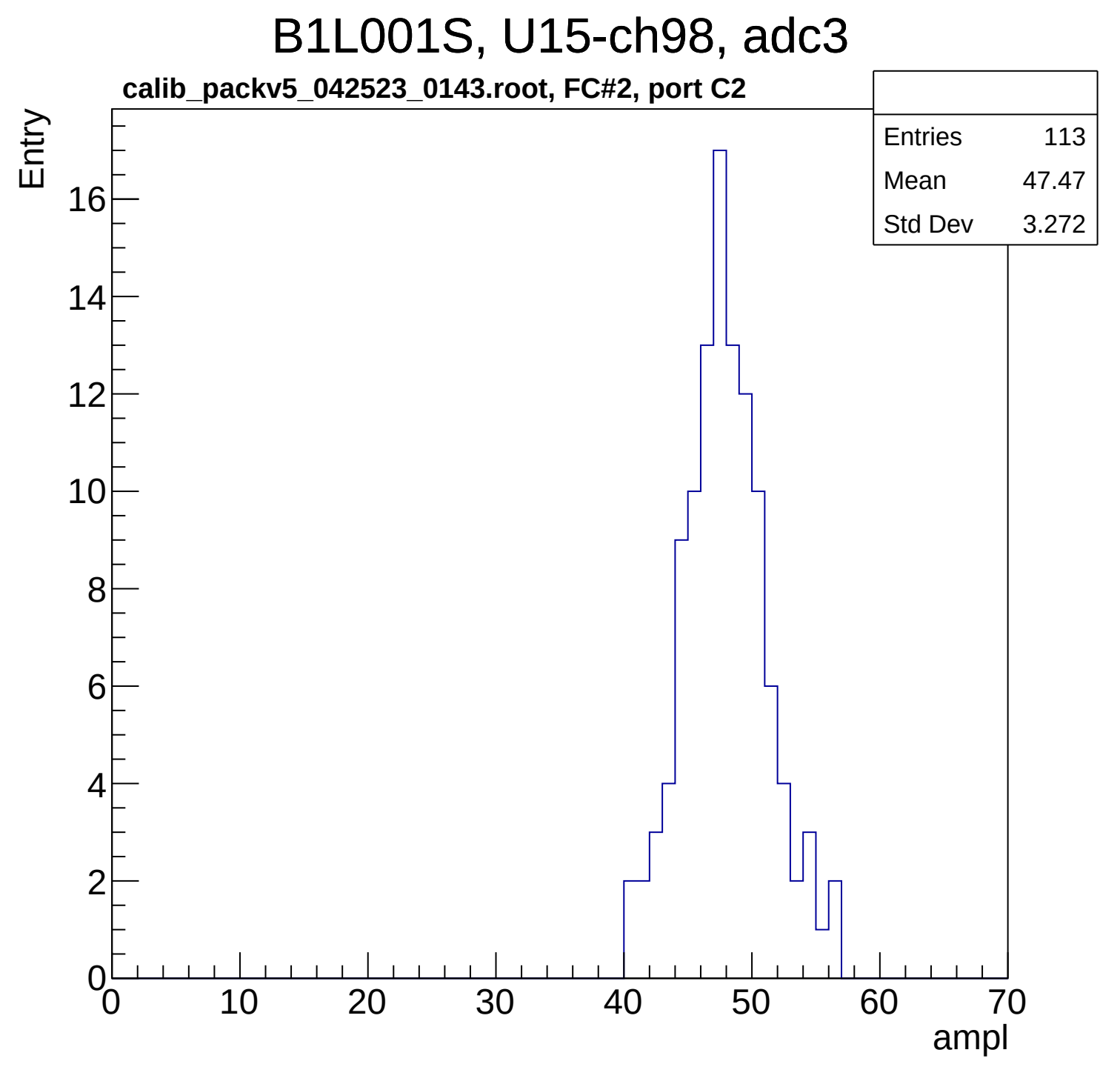
B1L001S, U15-ch98, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 47.47
Std Dev 3.272
Entry
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2
4
6
8
10
12
14
16
0
10
20
30
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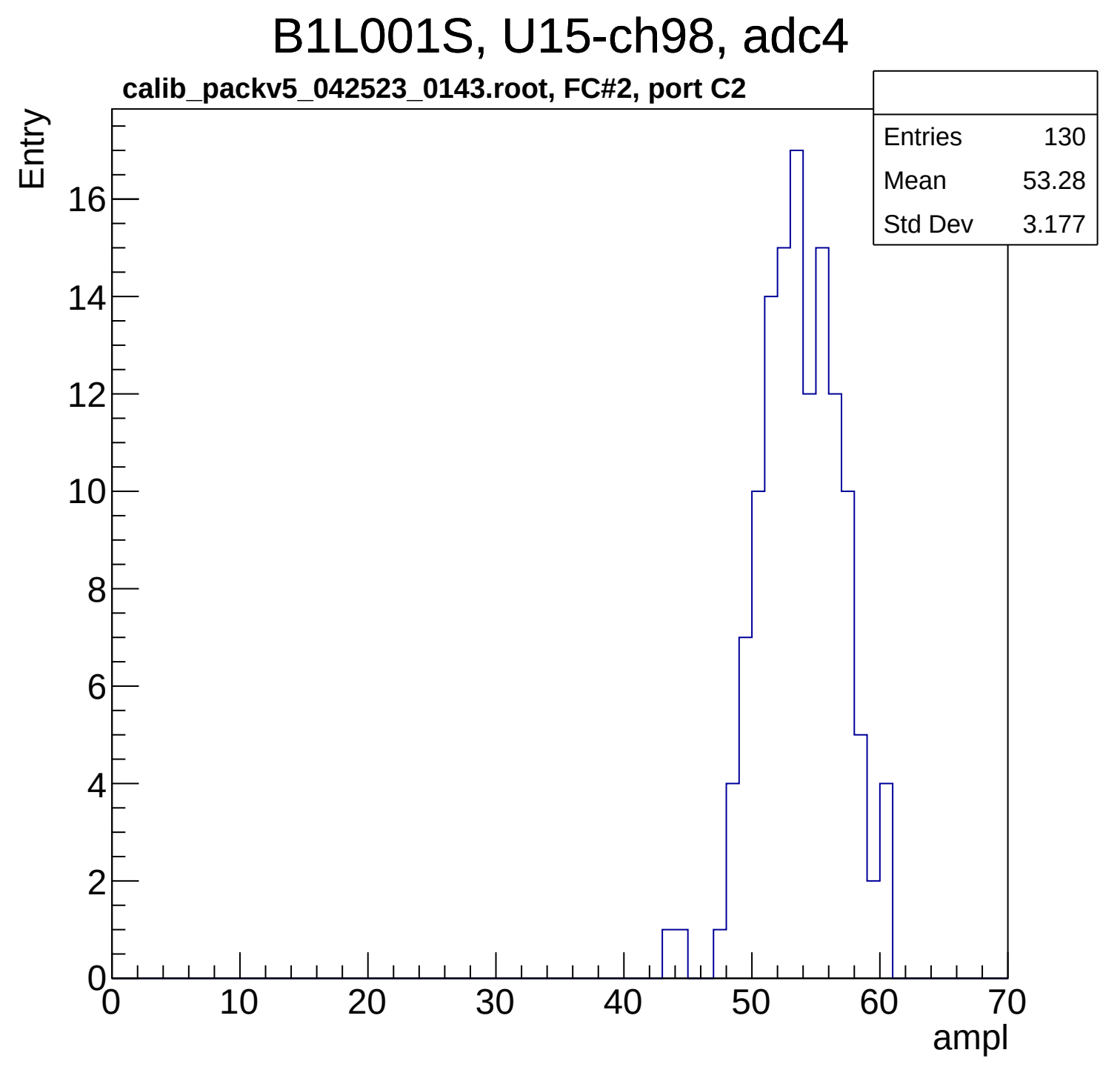
50
60
70
ampl


B1L001S, U15-ch98, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 53.28
Std Dev 3.177
Entry
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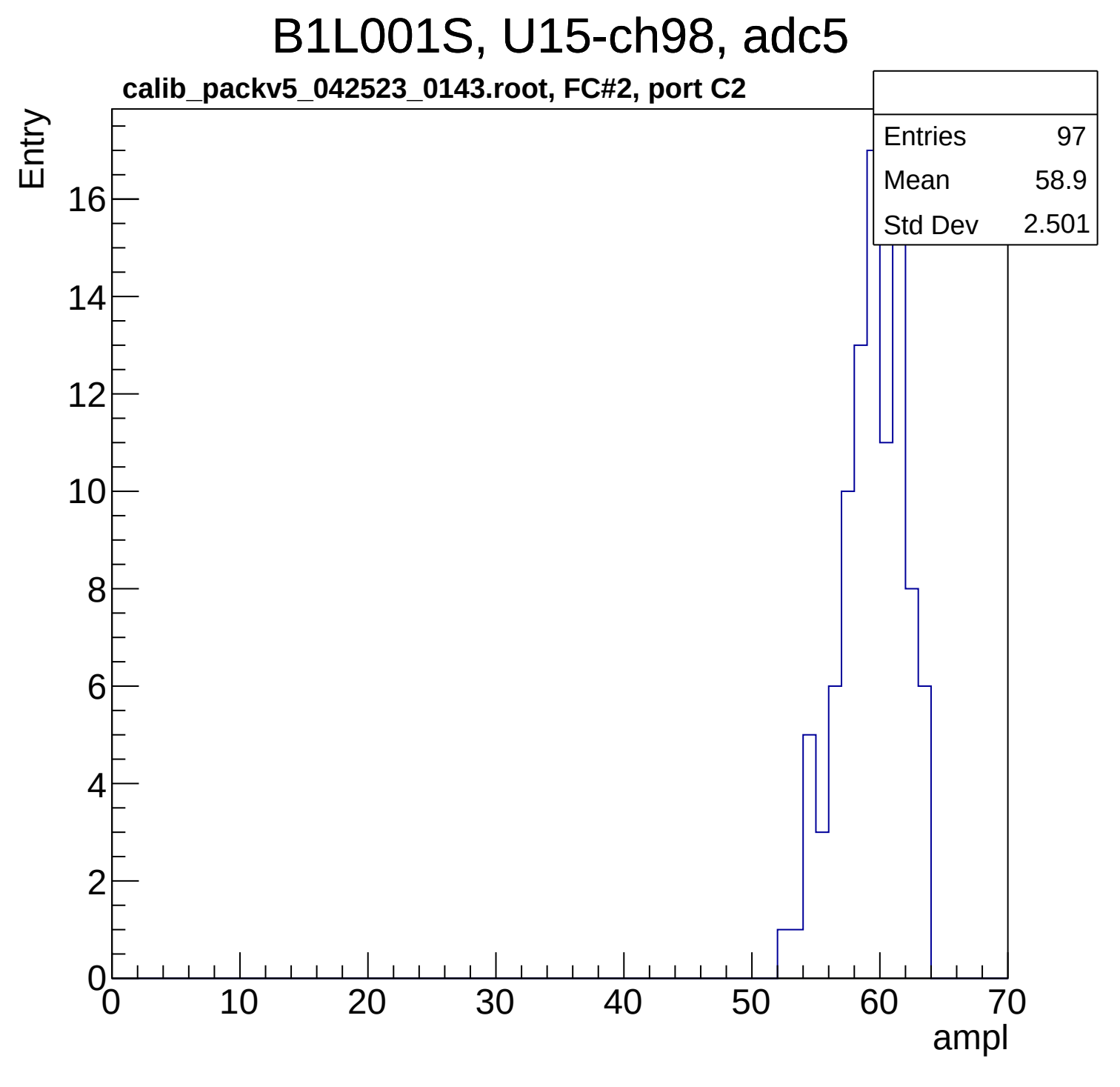
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch98, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 58.9
Std Dev 2.501
Entry
ampl
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6
8
10
12
14
16
0
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20
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60
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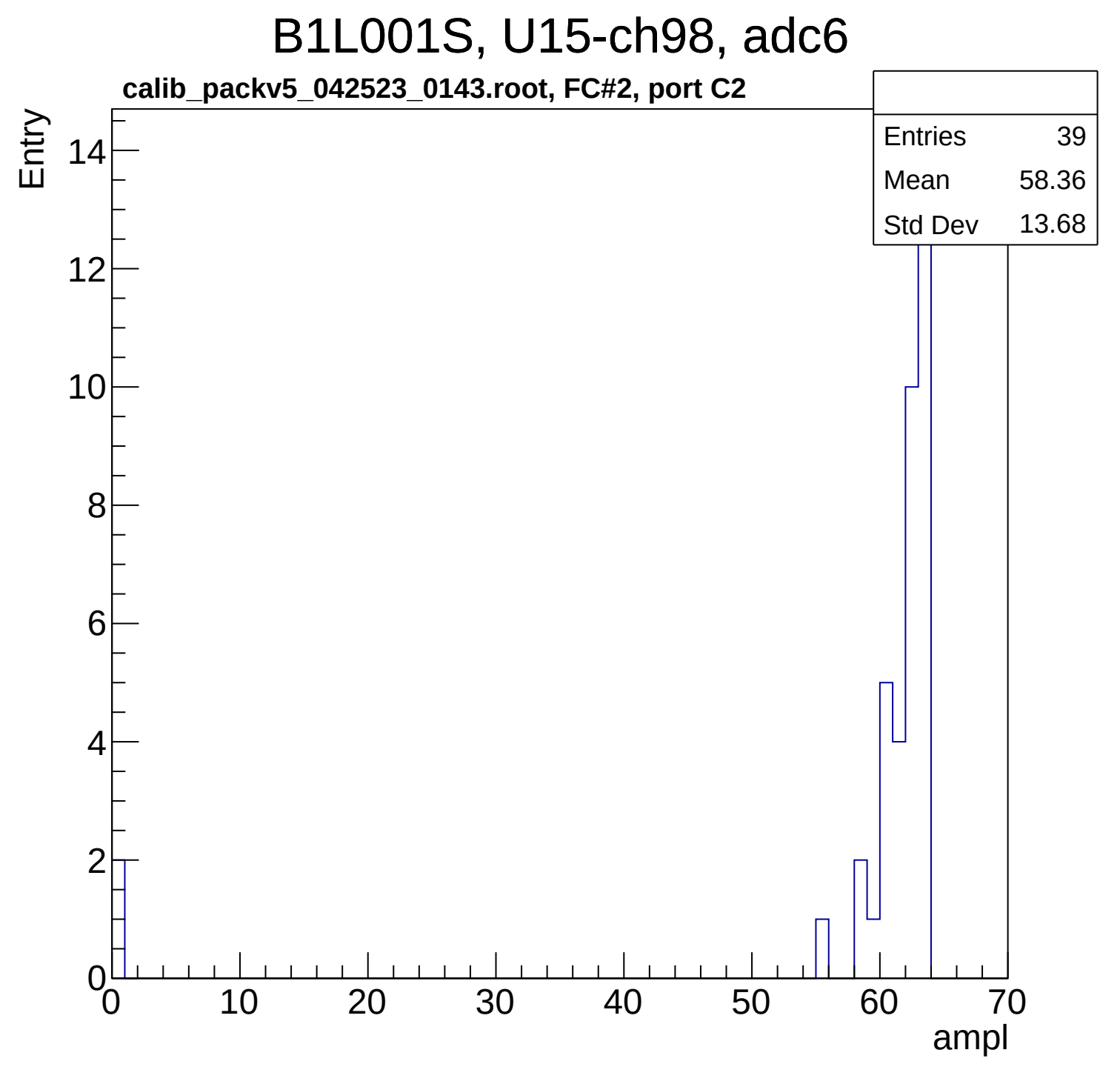
B1L001S, U15-ch98, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 39
Mean 58.36
Std Dev 13.68
Entry
ampl
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6
8
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12
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0
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60
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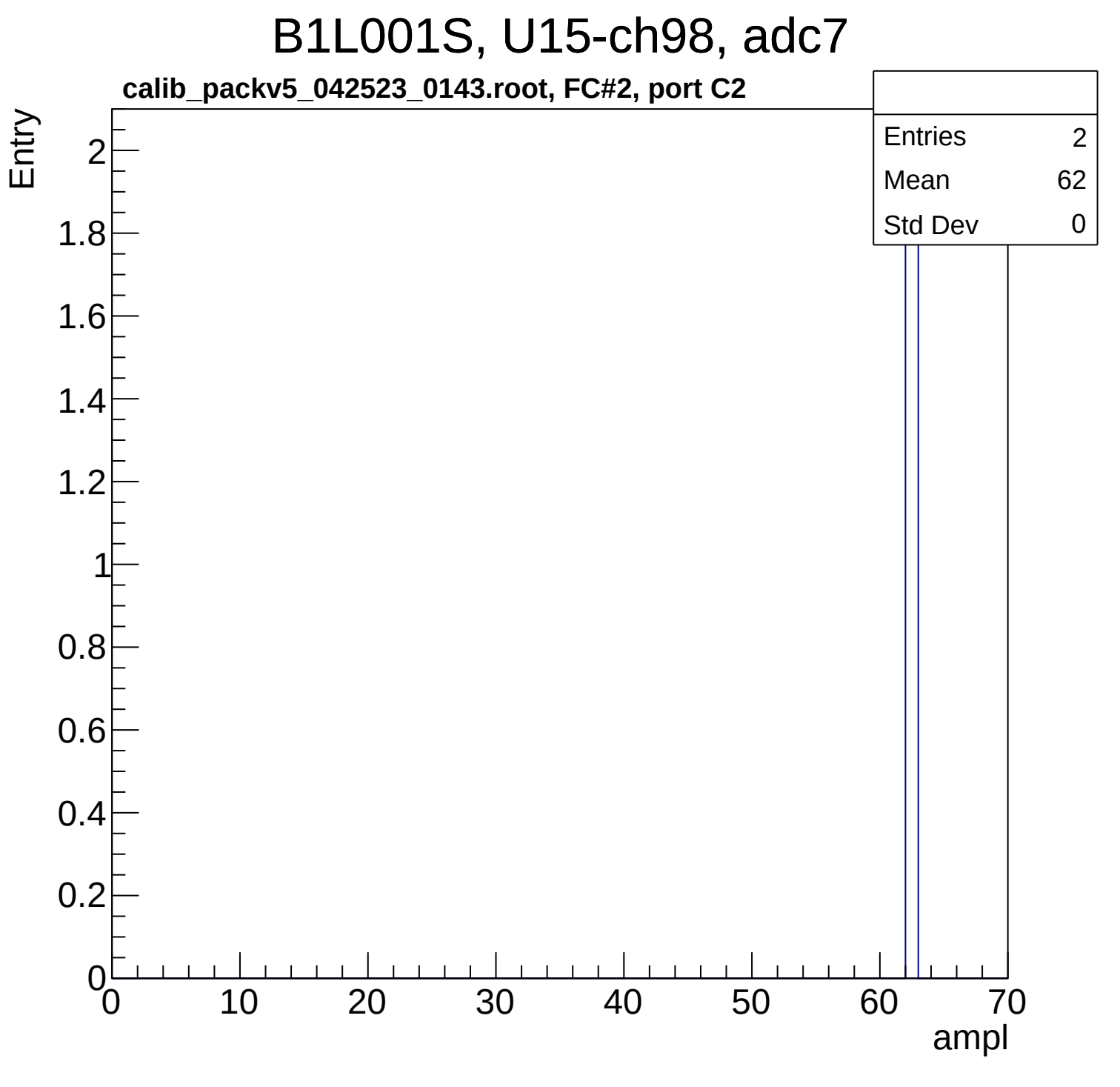
B1L001S, U15-ch98, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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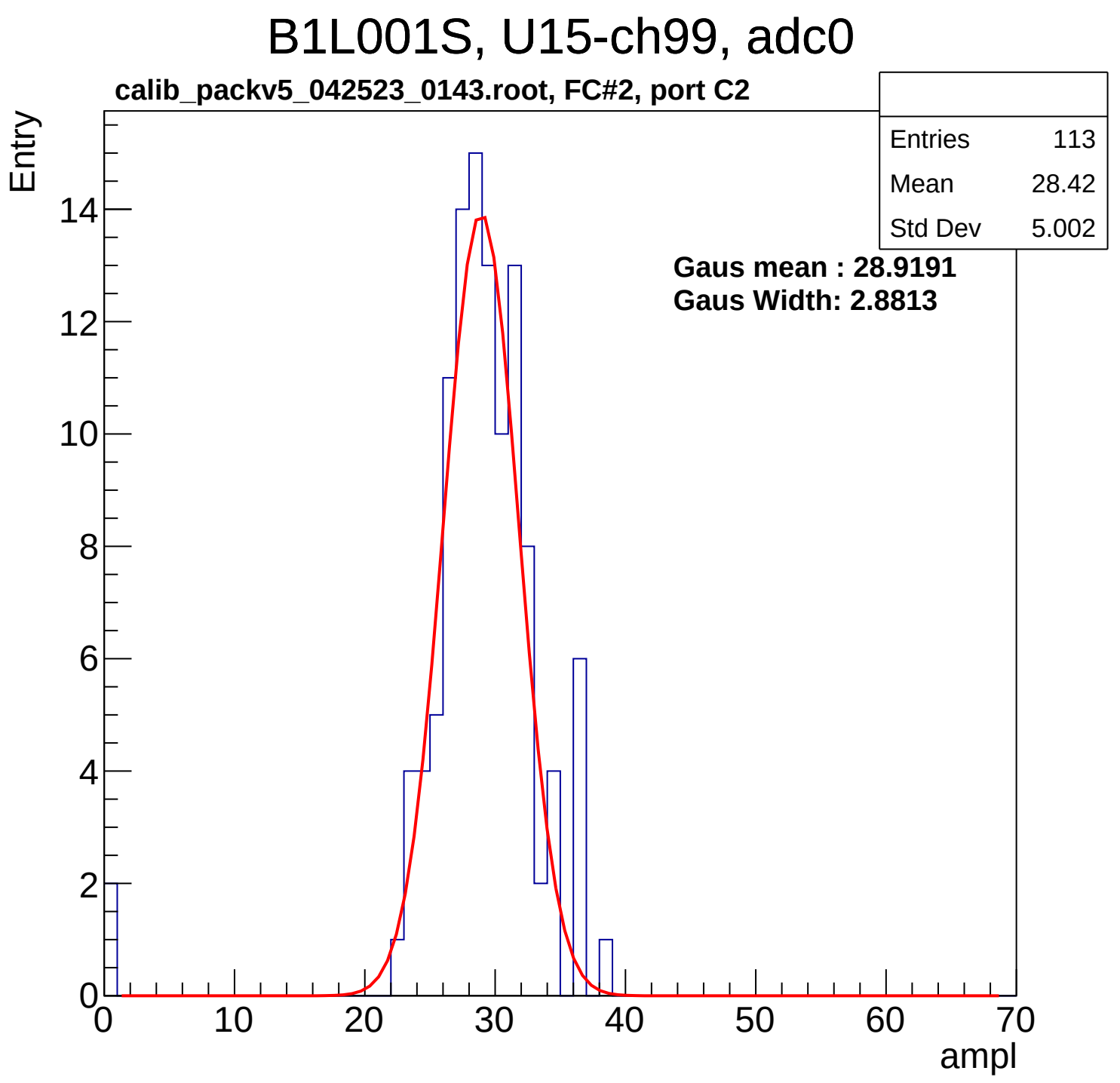
B1L001S, U15-ch99, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 28.42
Std Dev 5.002
Gaus mean : 28.9191
Gaus Width: 2.8813
Entry
ampl
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6
8
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60
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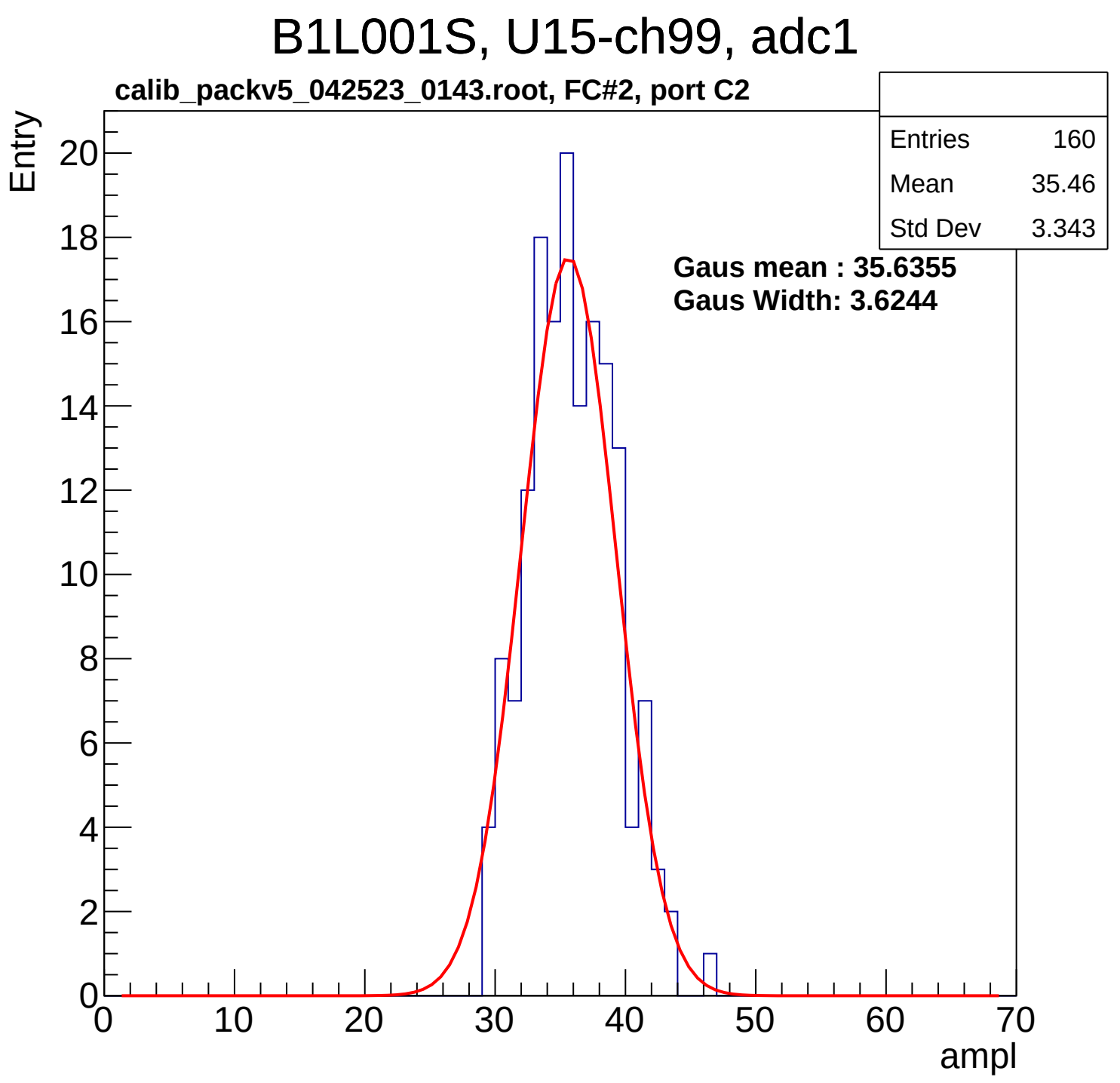
B1L001S, U15-ch99, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 35.46
Std Dev 3.343
Gaus mean : 35.6355
Gaus Width: 3.6244
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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40
50
60
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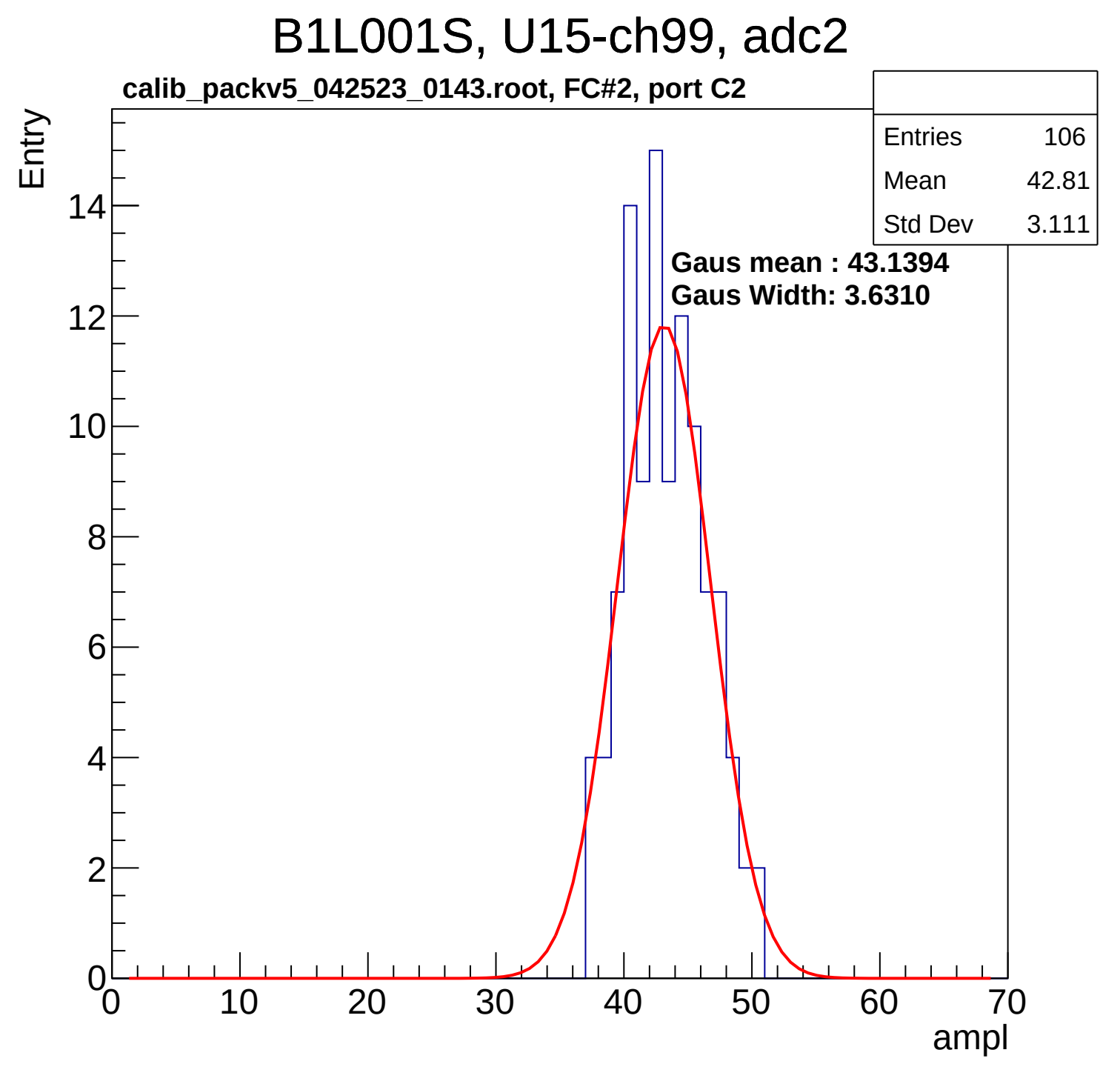
B1L001S, U15-ch99, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 42.81
Std Dev 3.111
Gaus mean : 43.1394
Gaus Width: 3.6310
Entry
ampl
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8
10
12
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0
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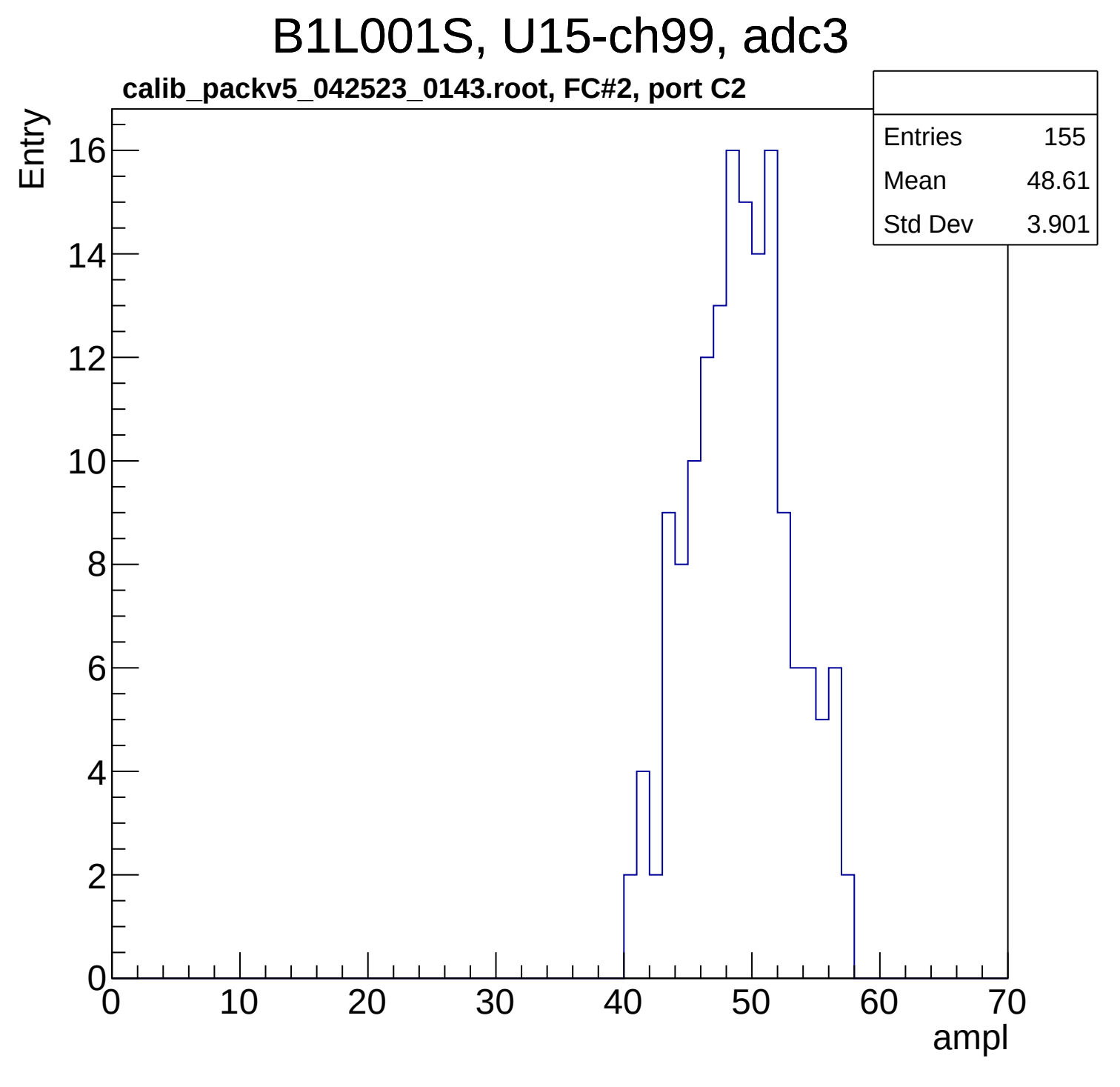
B1L001S, U15-ch99, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 48.61
Std Dev 3.901
Entry
ampl
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6
8
10
12
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16
0
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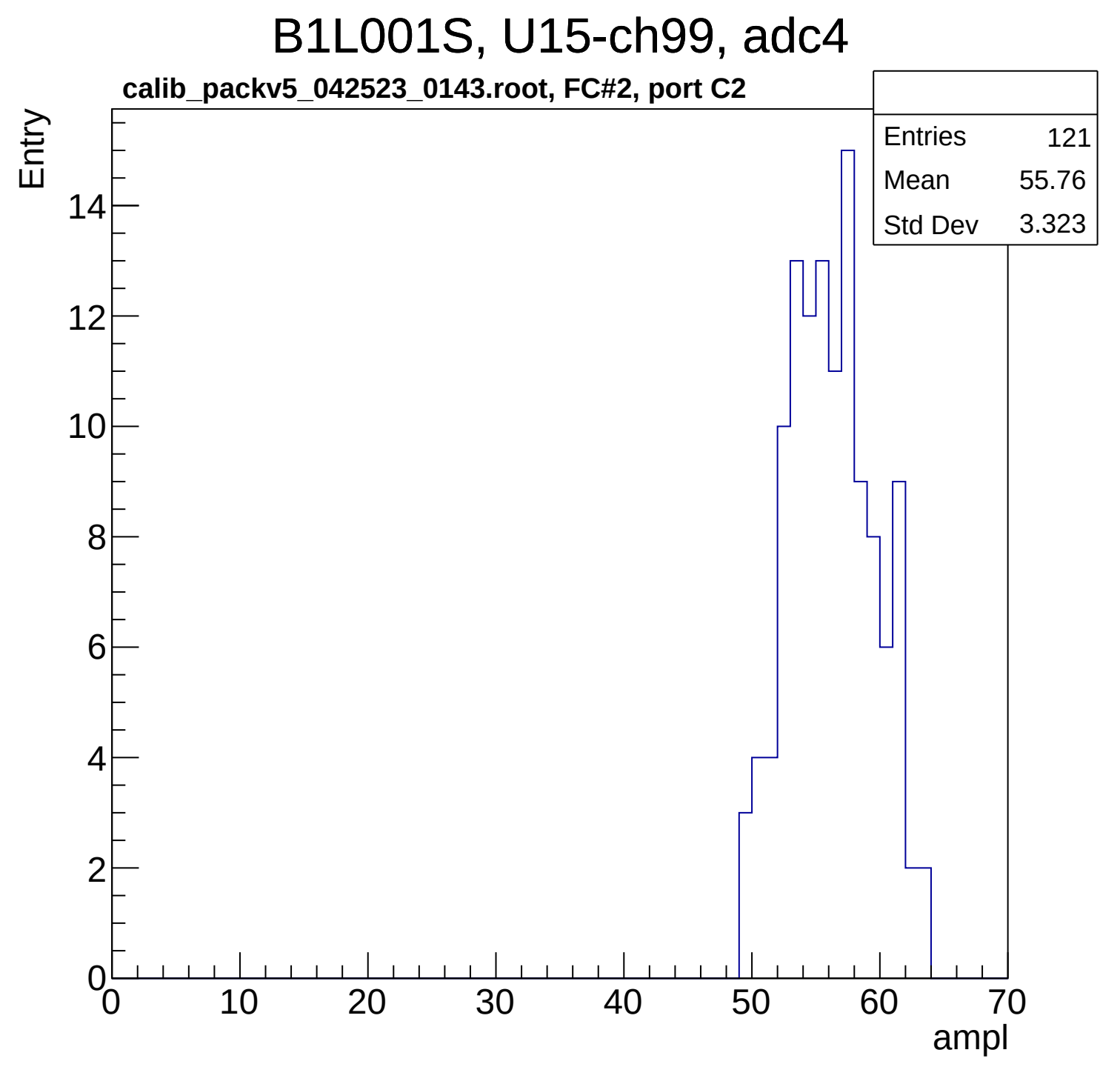
B1L001S, U15-ch99, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 55.76
Std Dev 3.323
Entry
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4
6
8
10
12
14
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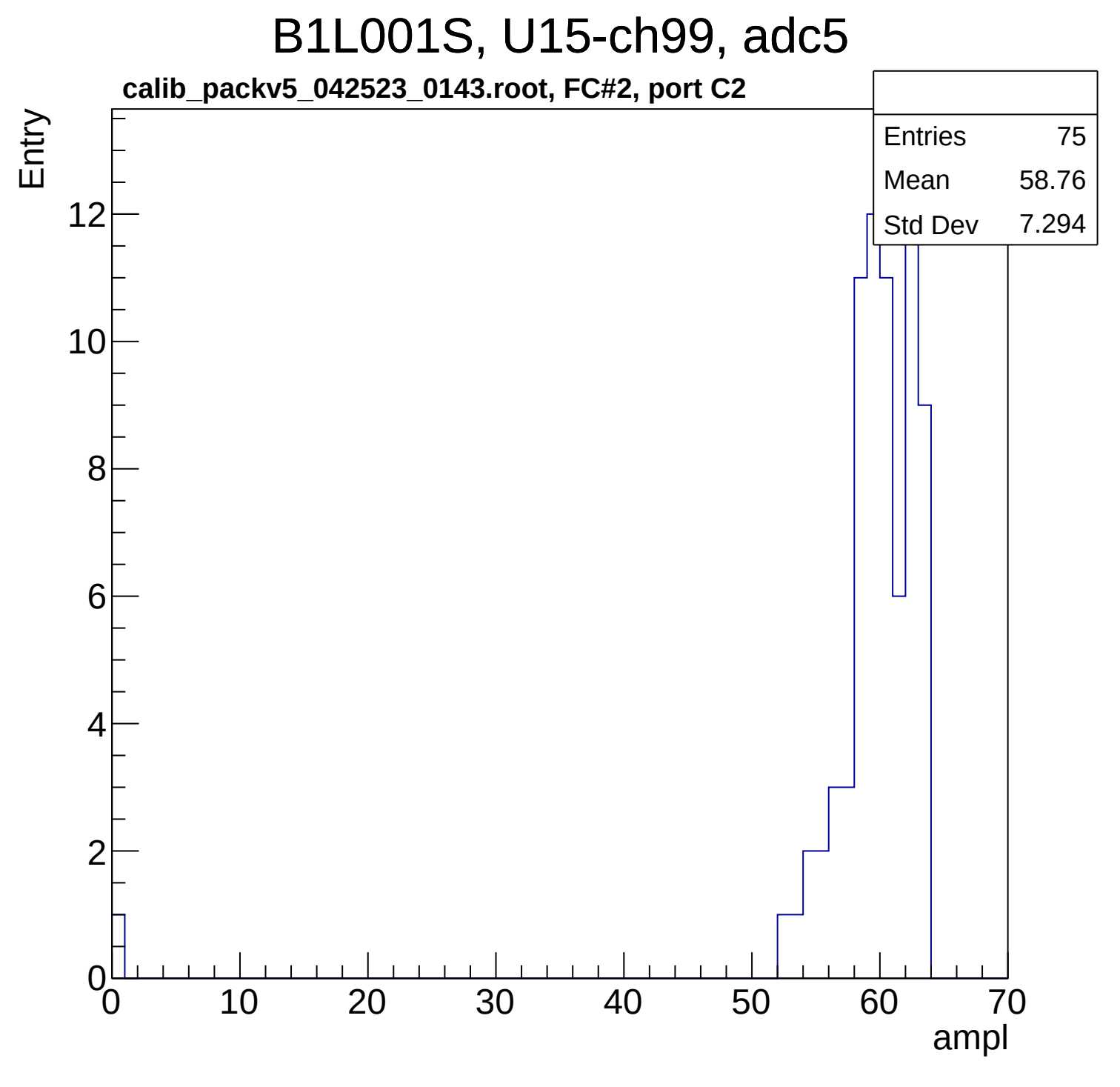
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch99, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 58.76
Std Dev 7.294
Entry
ampl
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6
8
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0
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60
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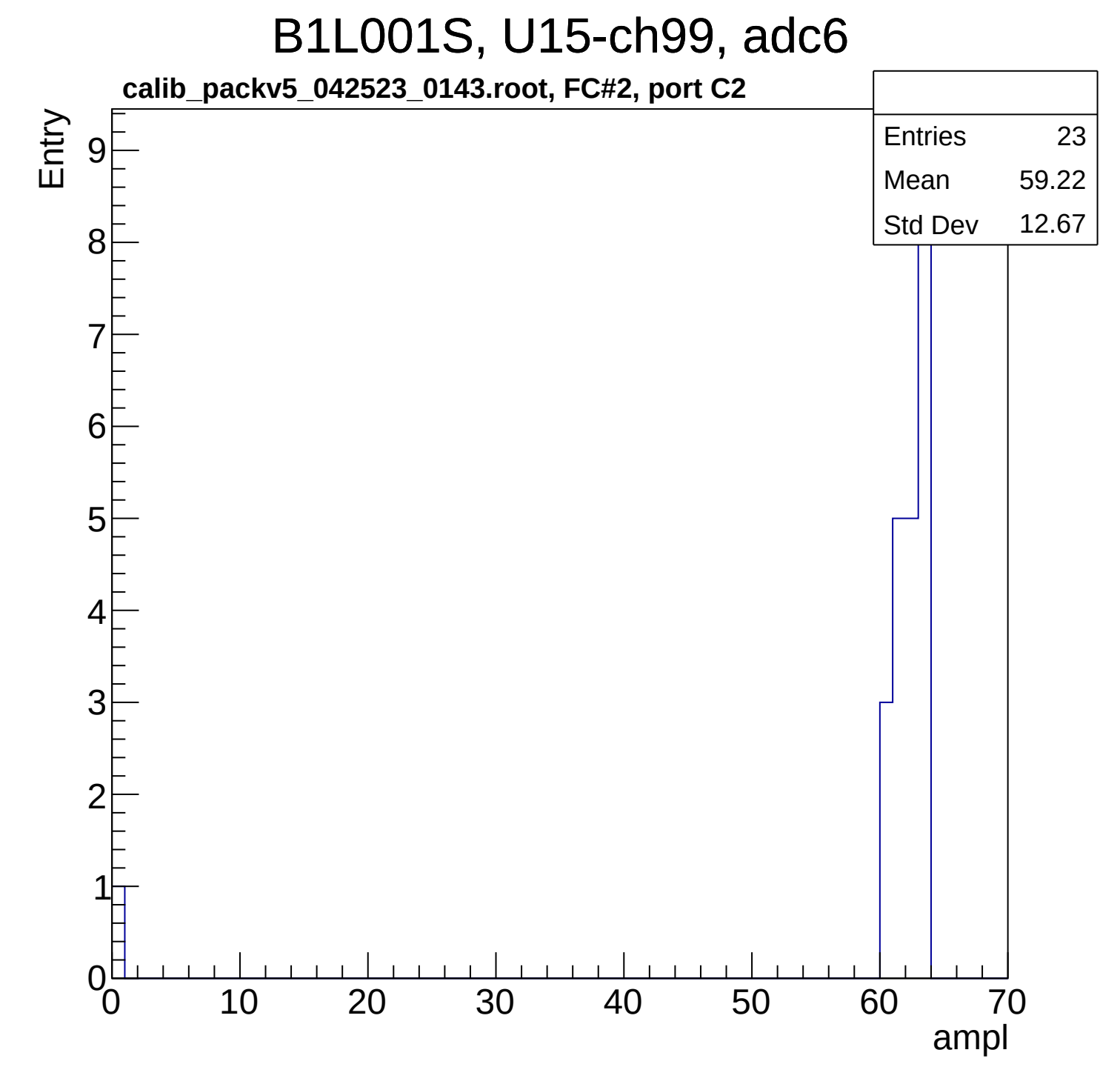
B1L001S, U15-ch99, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 23
Mean 59.22
Std Dev 12.67
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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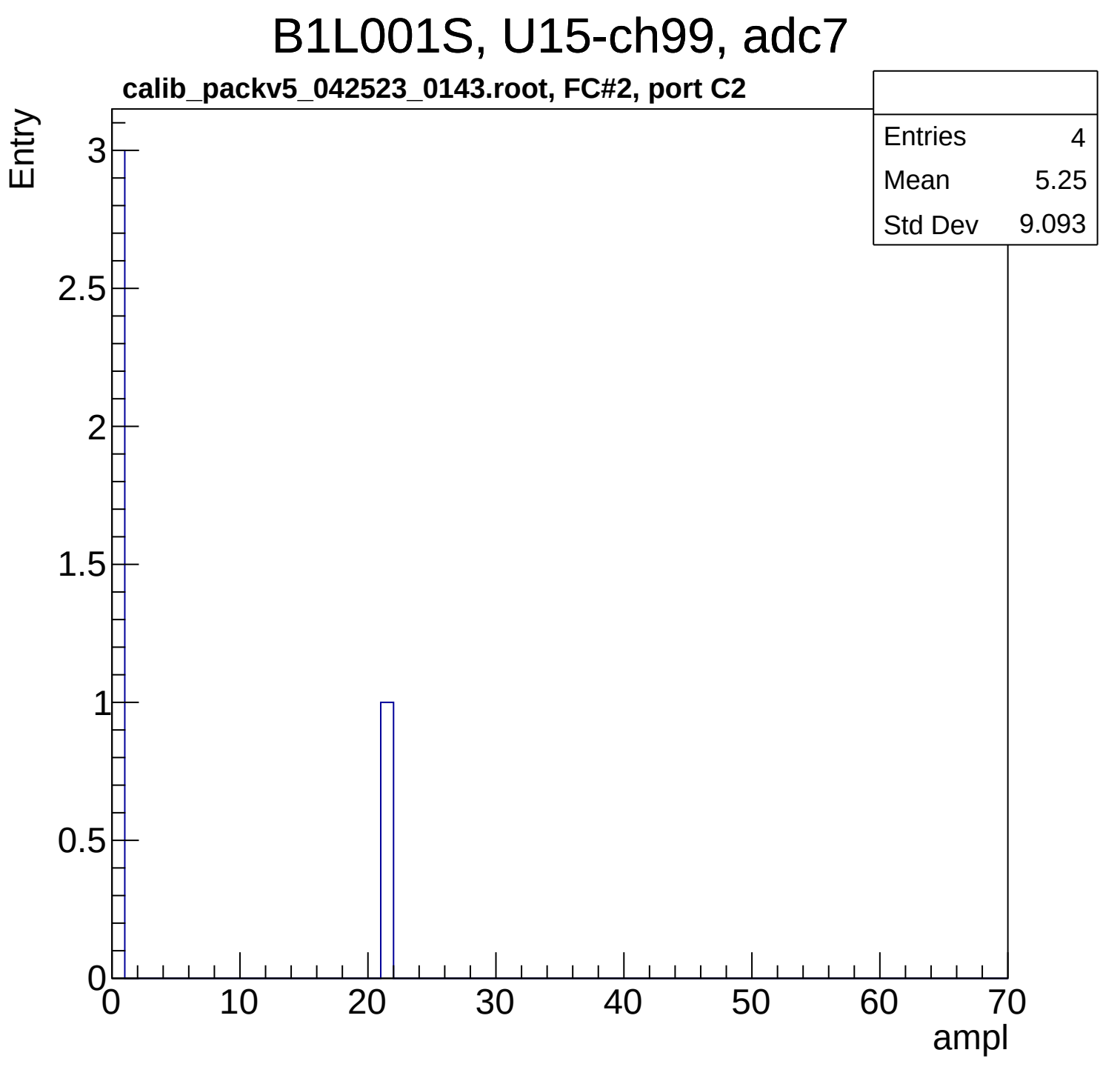
B1L001S, U15-ch99, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 5.25
Std Dev 9.093
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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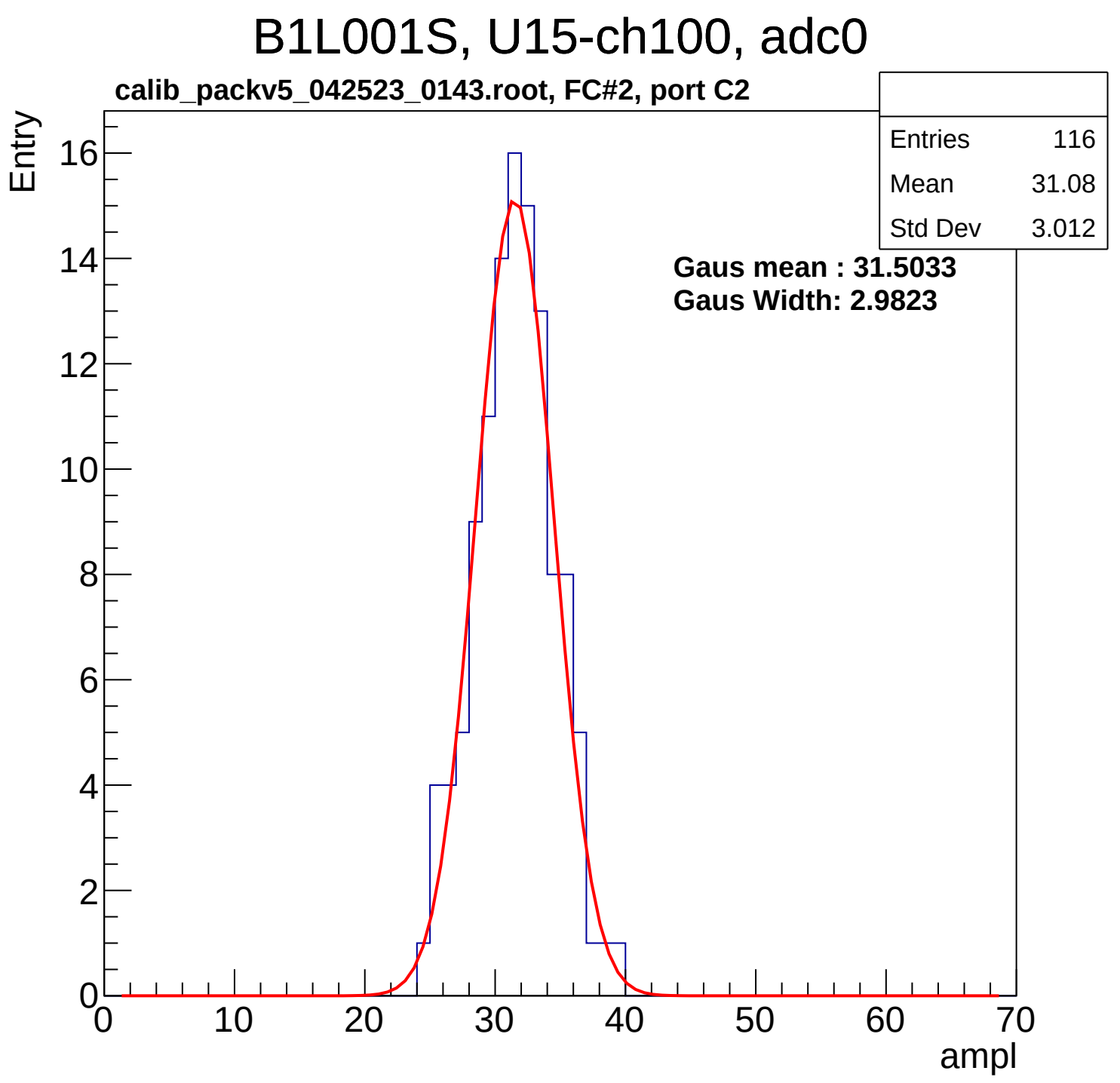
B1L001S, U15-ch100, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 31.08
Std Dev 3.012
Gaus mean : 31.5033
Gaus Width: 2.9823
Entry
ampl
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6
8
10
12
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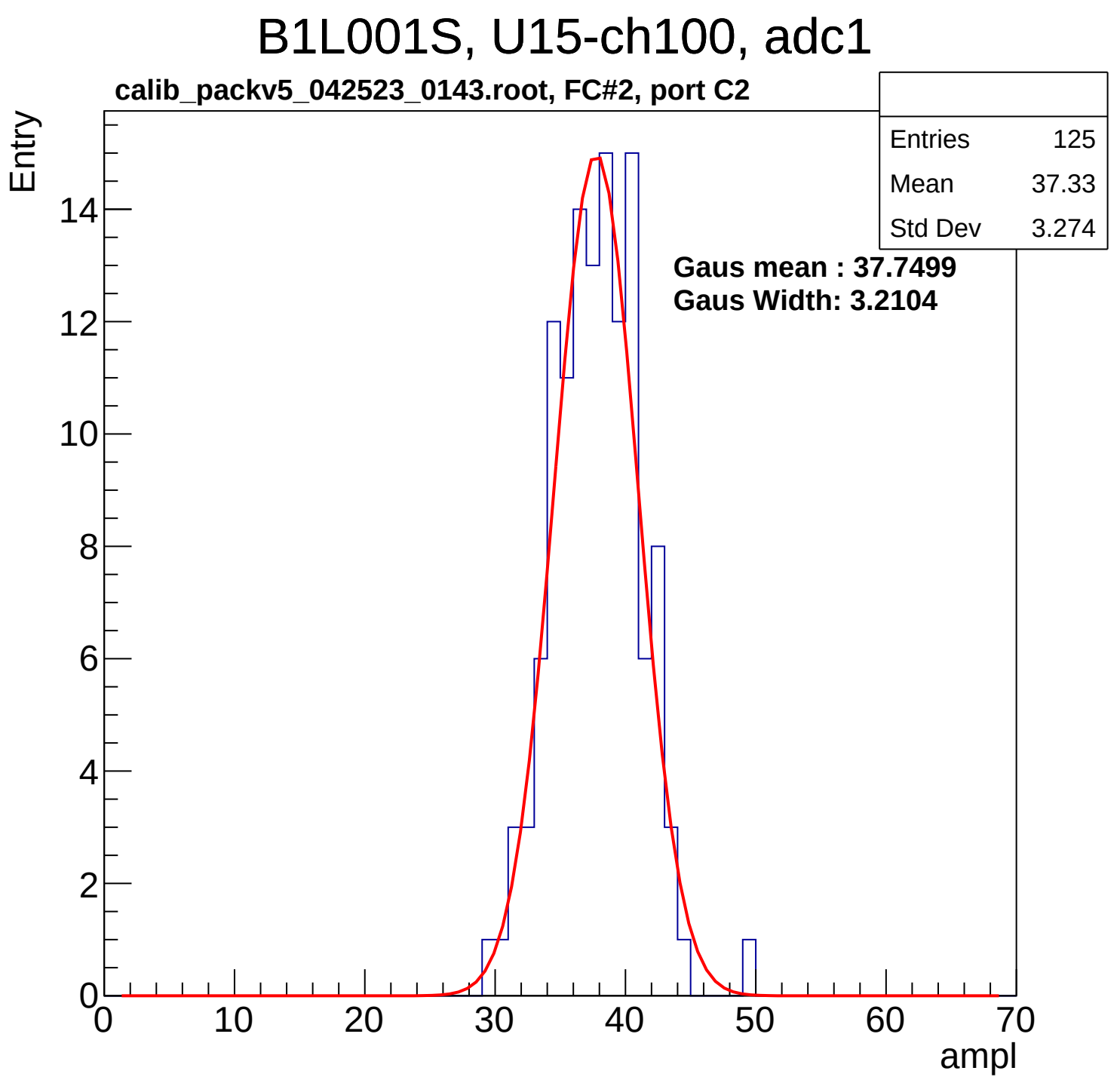
B1L001S, U15-ch100, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 37.33
Std Dev 3.274
Gaus mean : 37.7499
Gaus Width: 3.2104
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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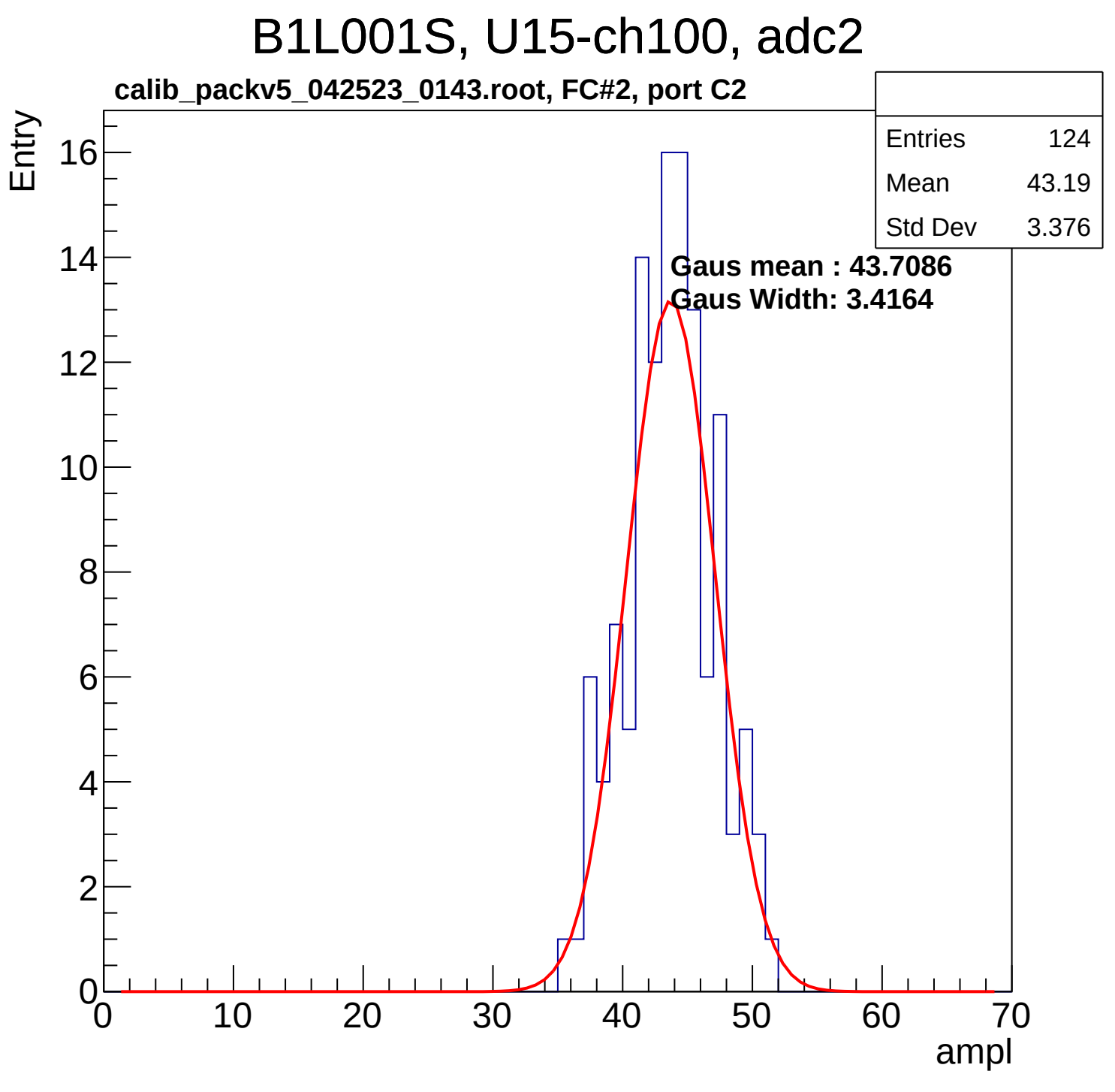
B1L001S, U15-ch100, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 43.19
Std Dev 3.376
Gaus mean : 43.7086
Gaus Width: 3.4164
Entry
ampl
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2
4
6
8
10
12
14
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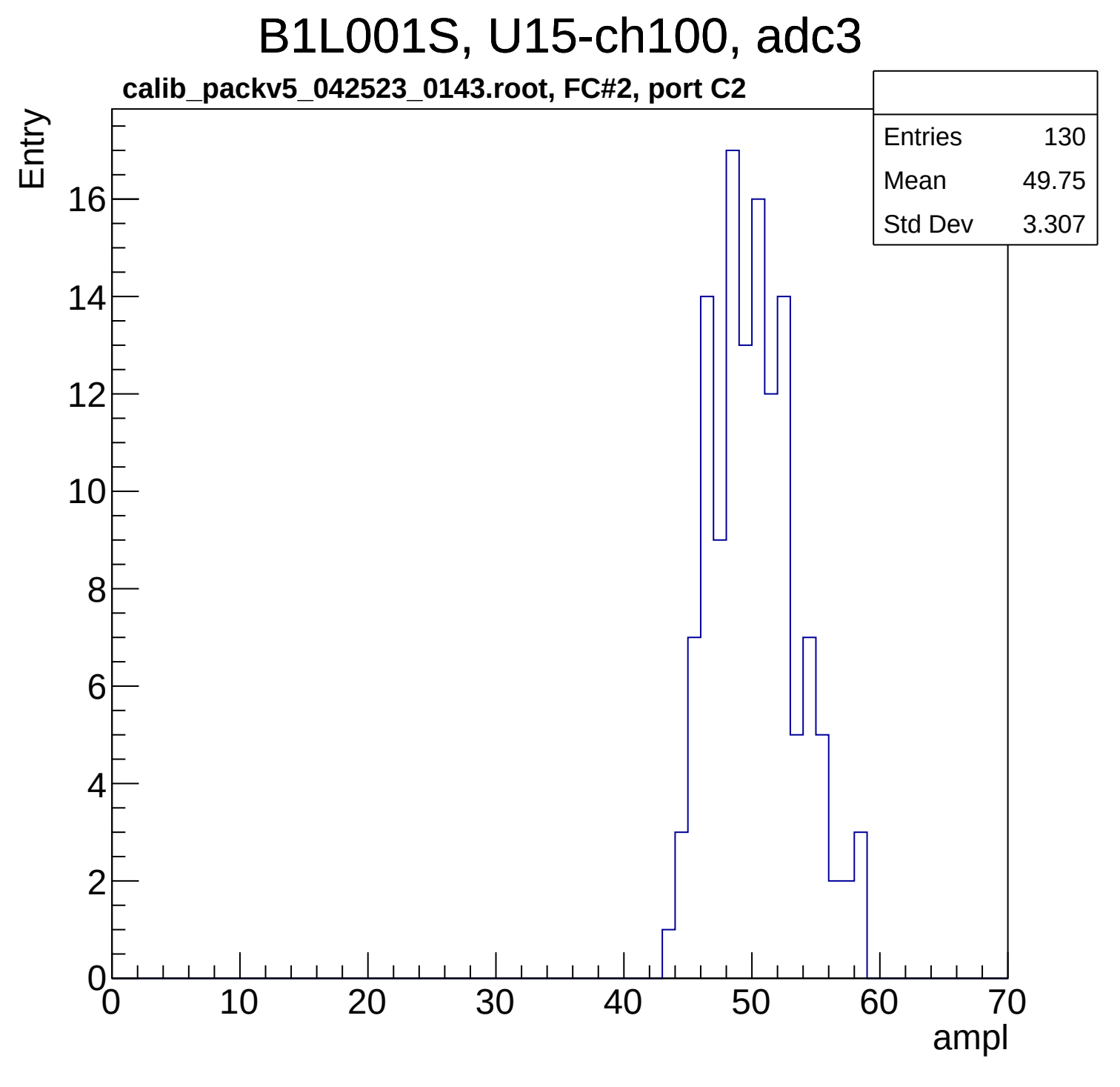
B1L001S, U15-ch100, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 49.75
Std Dev 3.307
Entry
ampl
0
2
4
6
8
10
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14
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0
10
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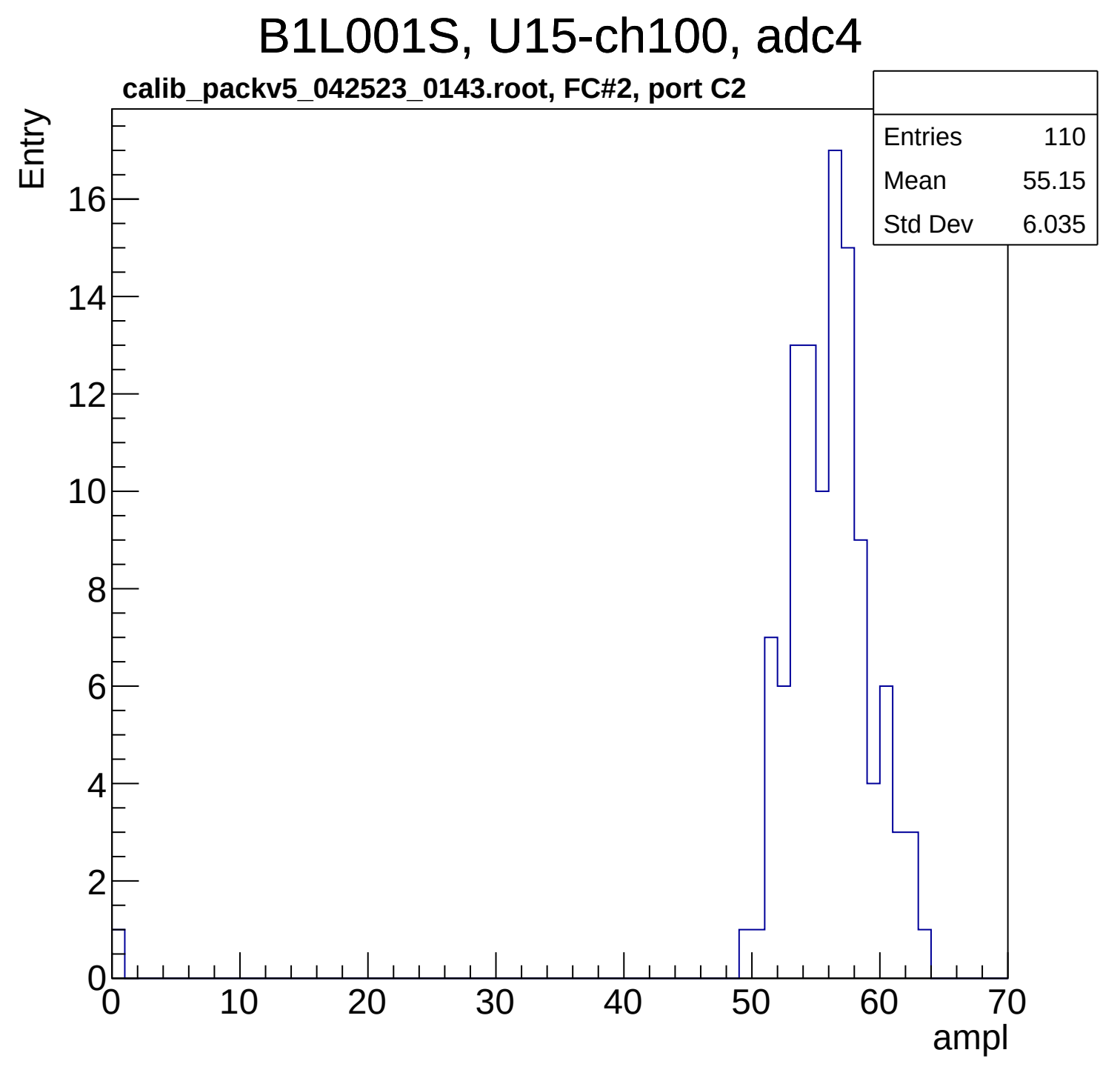
40
50
60
70


B1L001S, U15-ch100, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 55.15
Std Dev 6.035
Entry
ampl
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6
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10
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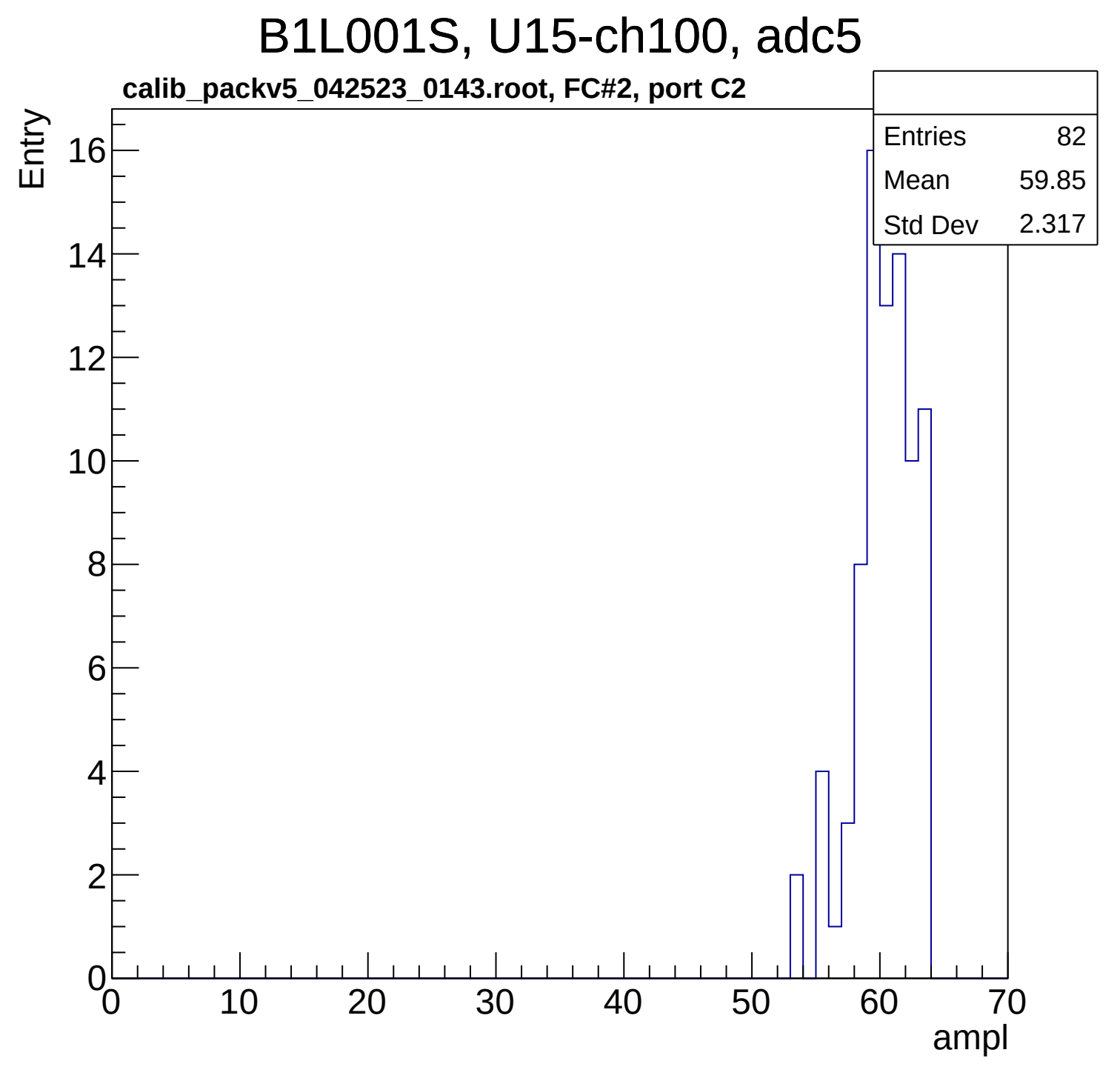
B1L001S, U15-ch100, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 59.85
Std Dev 2.317
Entry
ampl
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4
6
8
10
12
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16
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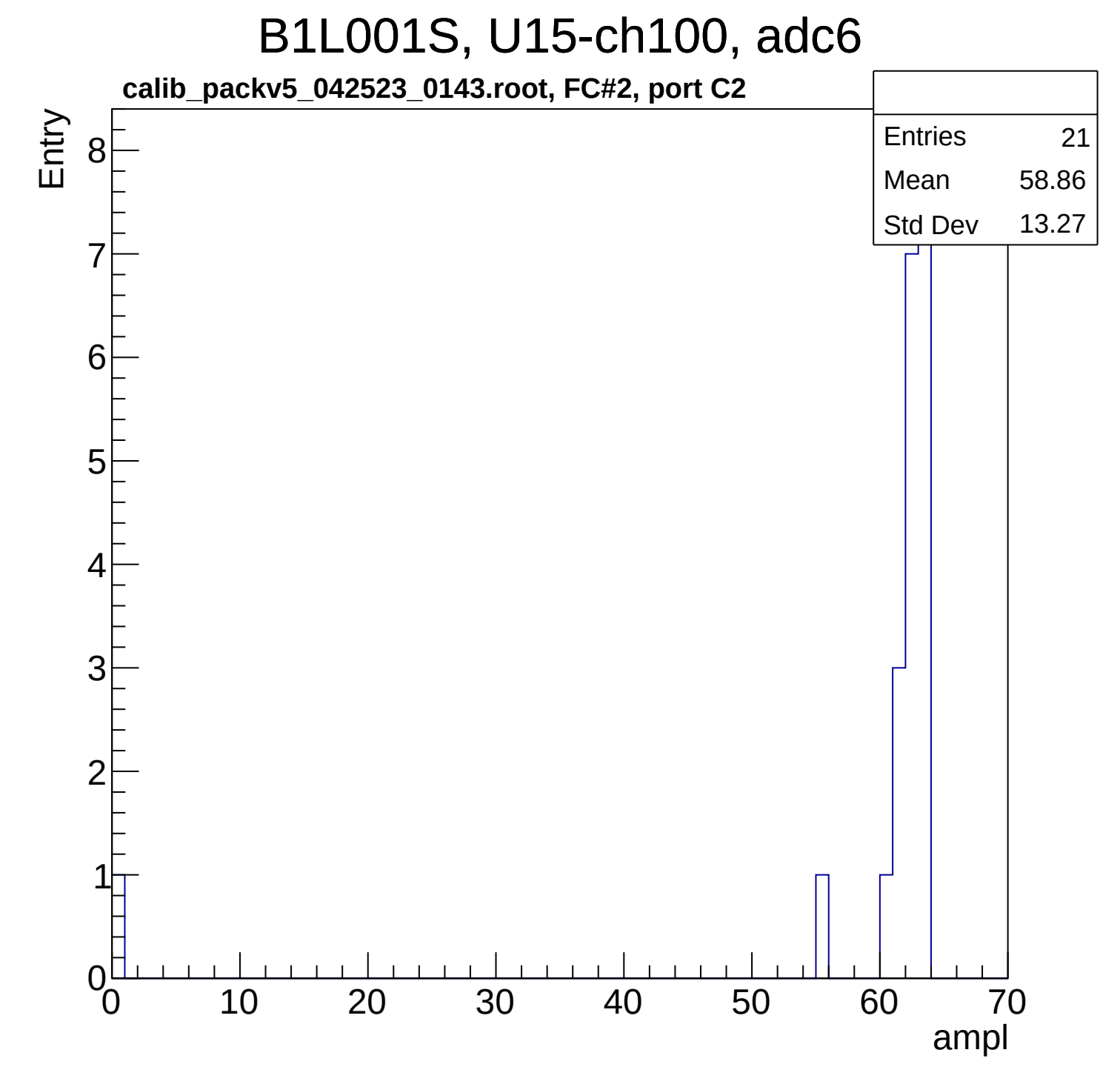
B1L001S, U15-ch100, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 21
Mean 58.86
Std Dev 13.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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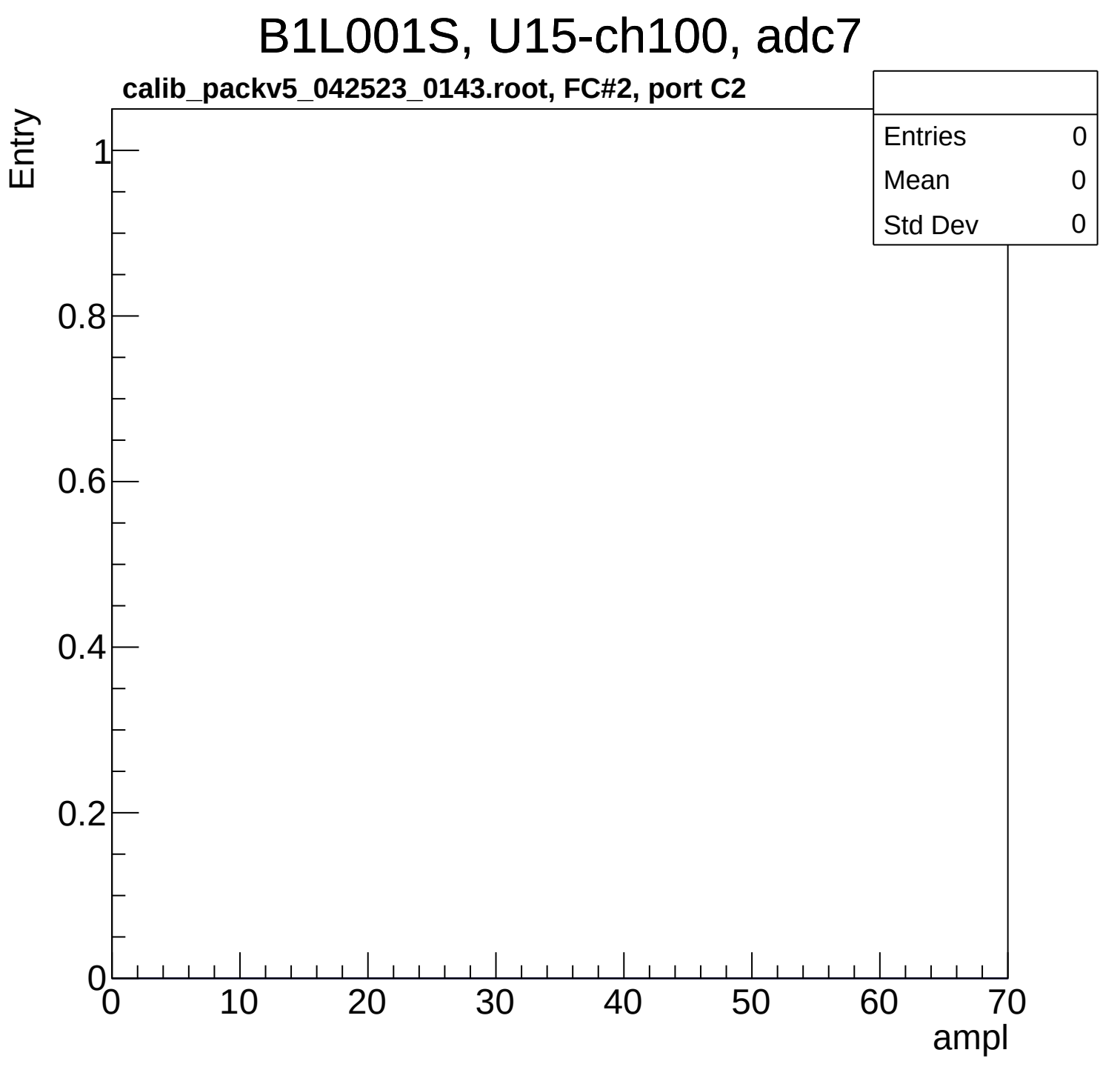
B1L001S, U15-ch100, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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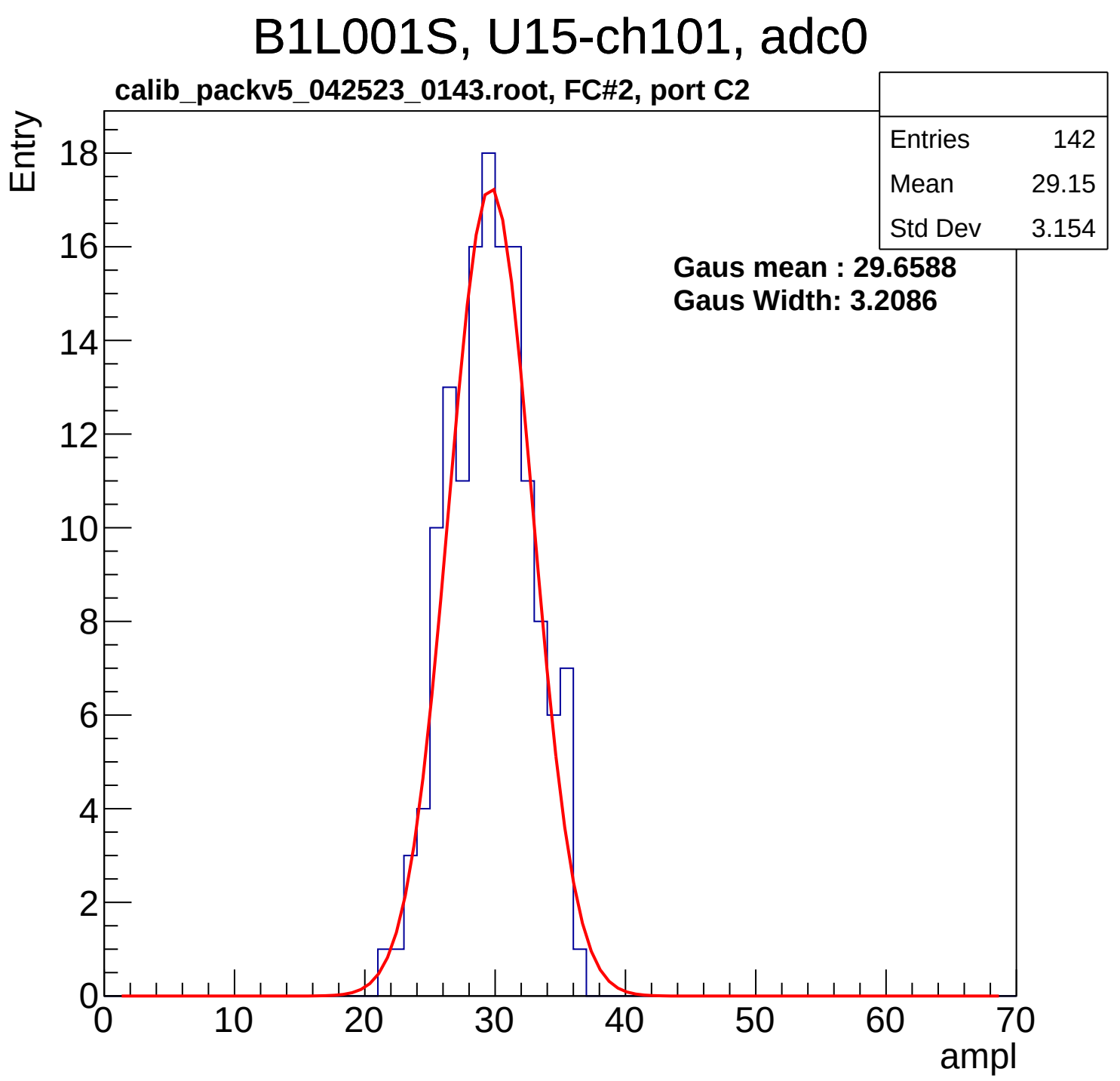
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch101, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 29.15
Std Dev 3.154
Gaus mean : 29.6588
Gaus Width: 3.2086
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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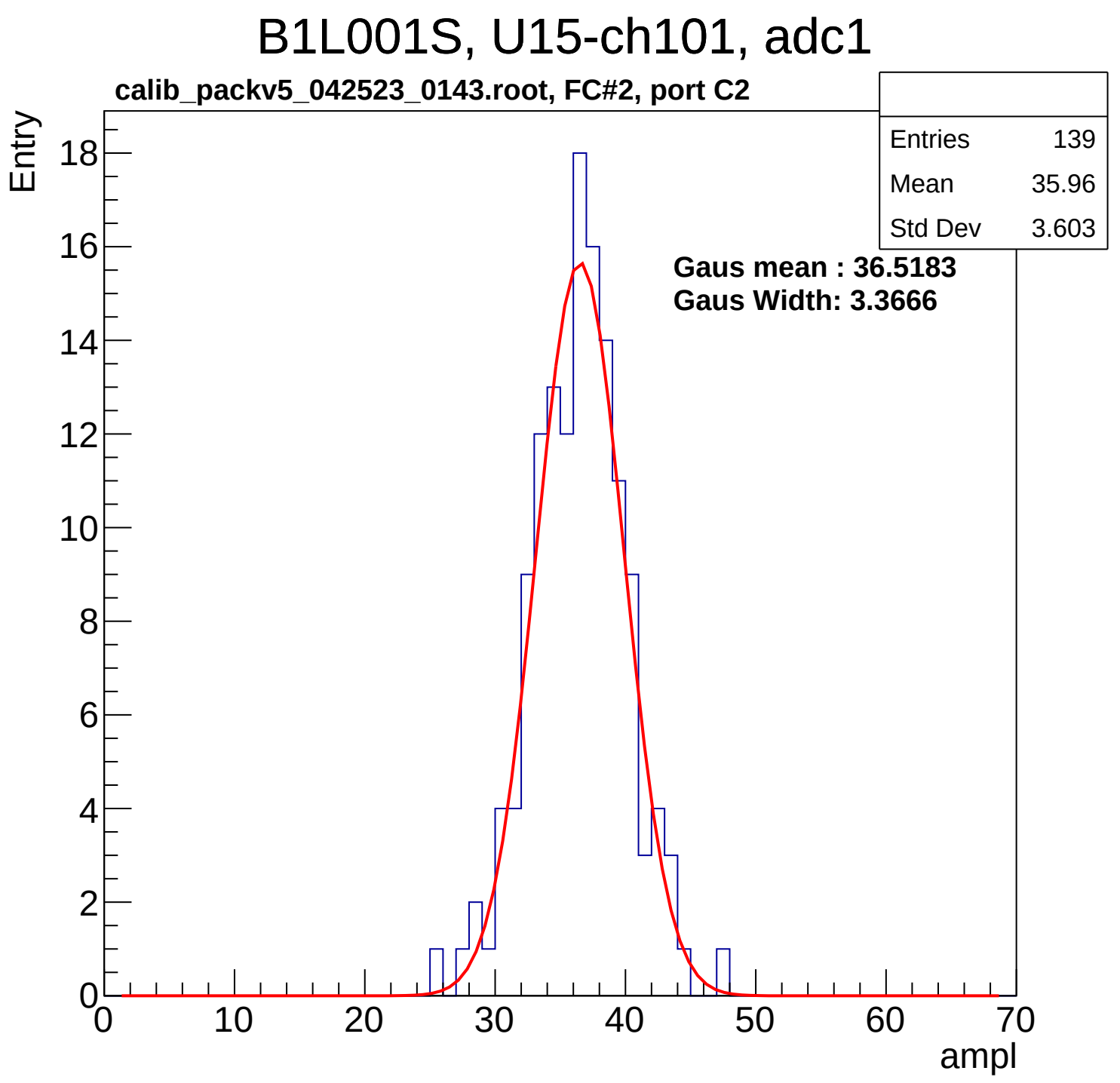
B1L001S, U15-ch101, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 35.96
Std Dev 3.603
Gaus mean : 36.5183
Gaus Width: 3.3666
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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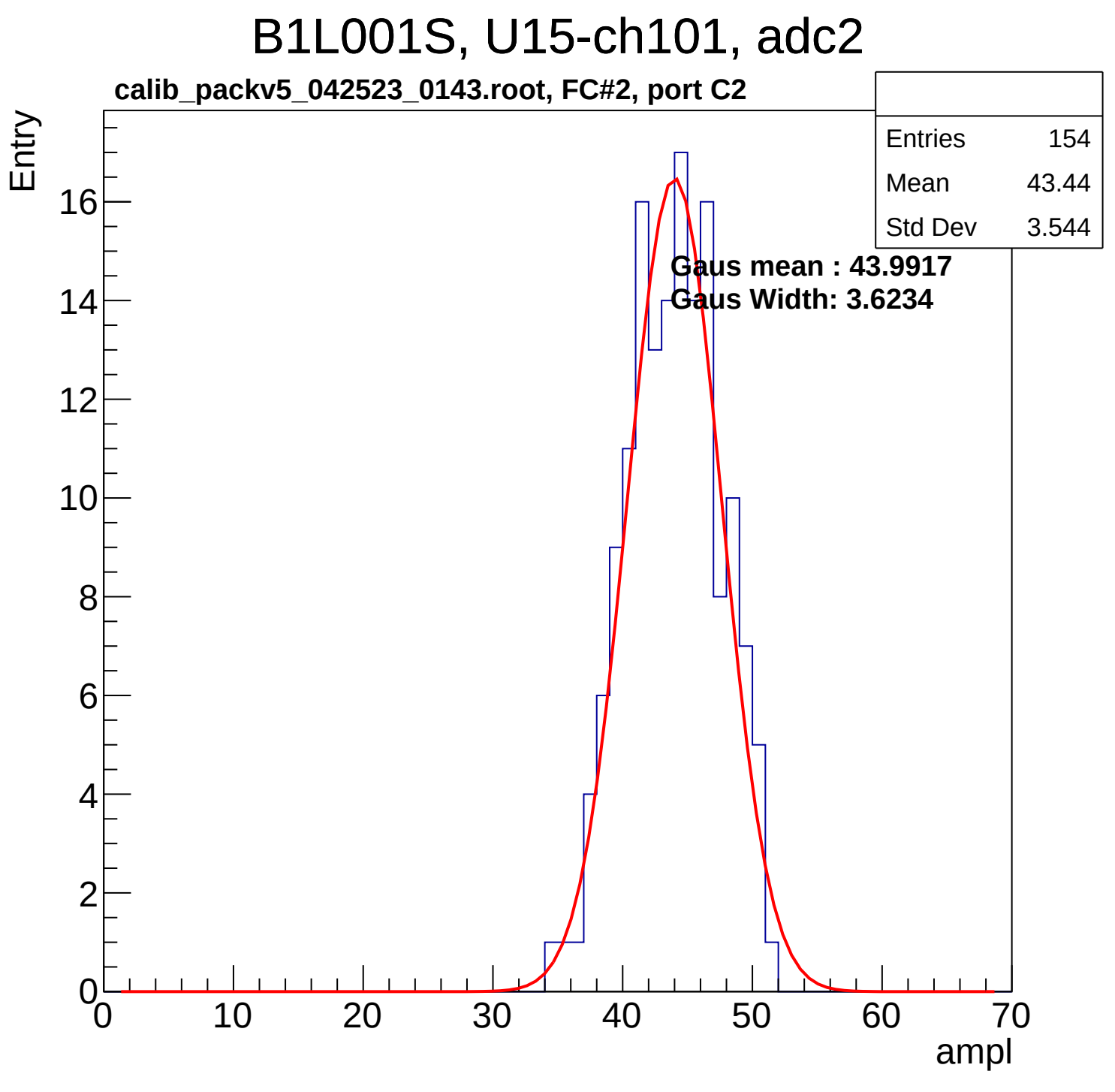
B1L001S, U15-ch101, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 43.44
Std Dev 3.544
Gaus mean : 43.9917
Gaus Width: 3.6234
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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30
40
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60
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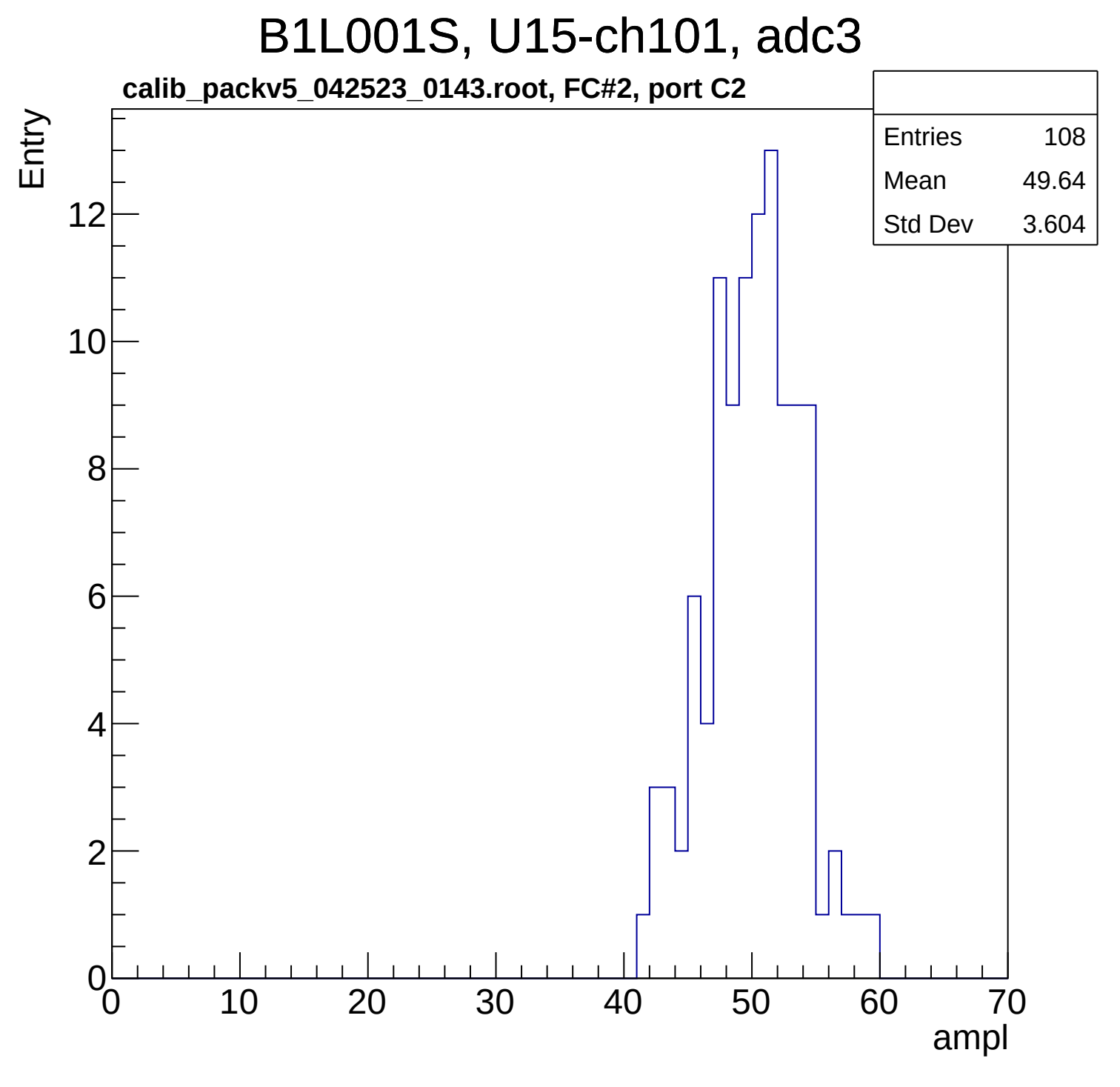
B1L001S, U15-ch101, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 49.64
Std Dev 3.604
Entry
ampl
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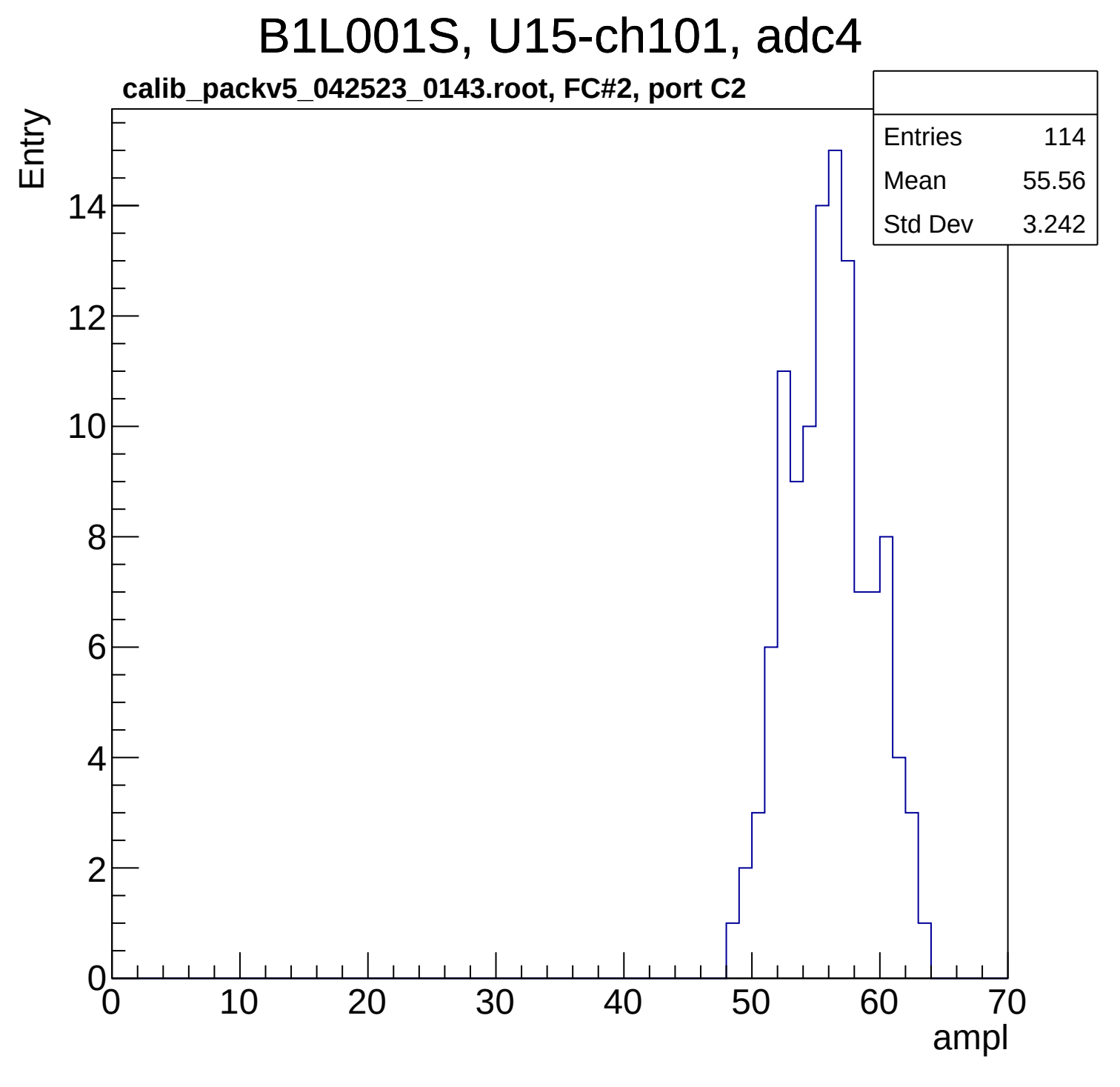
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch101, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 55.56
Std Dev 3.242
Entry
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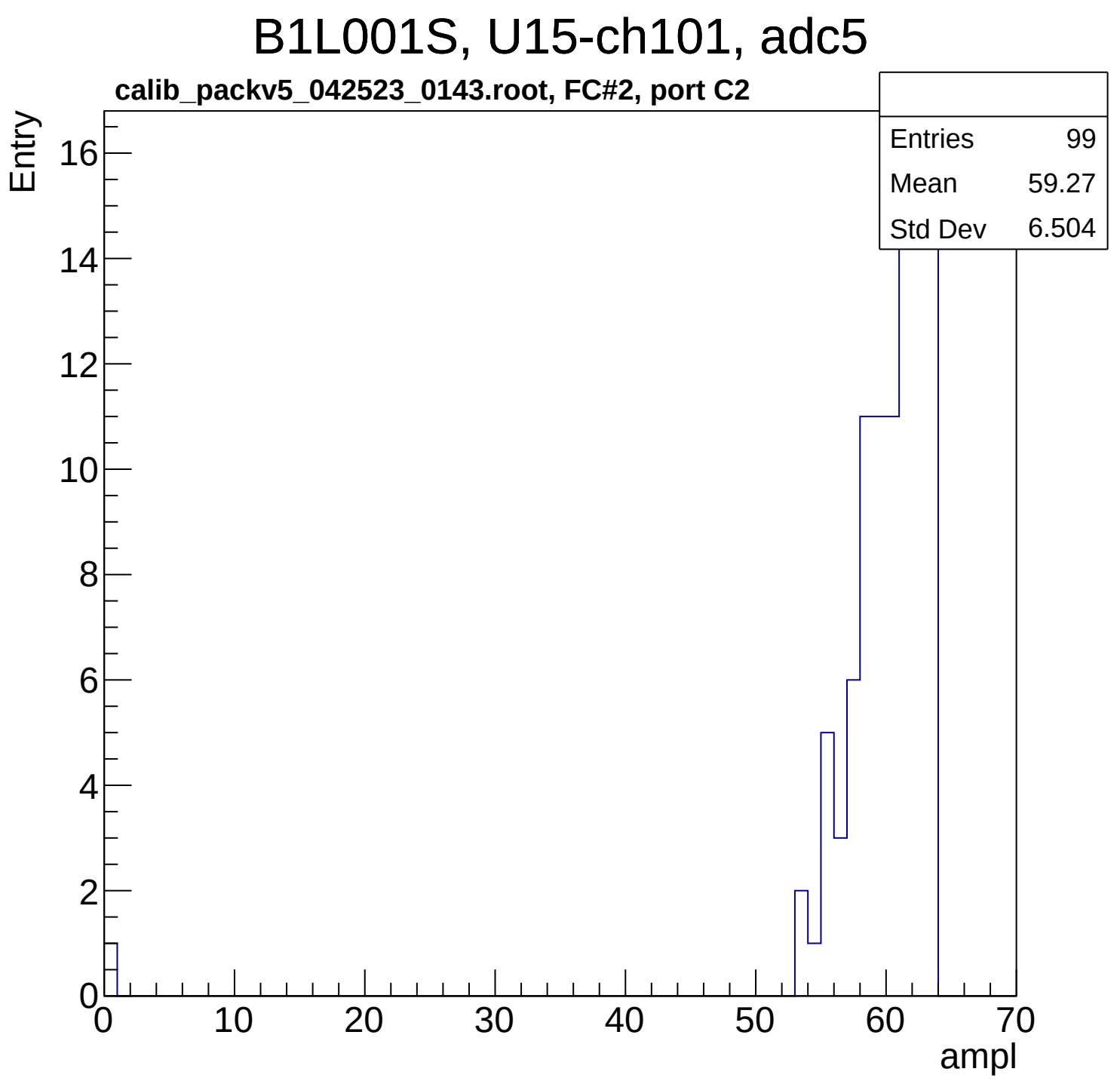
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch101, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 59.27
Std Dev 6.504
Entry
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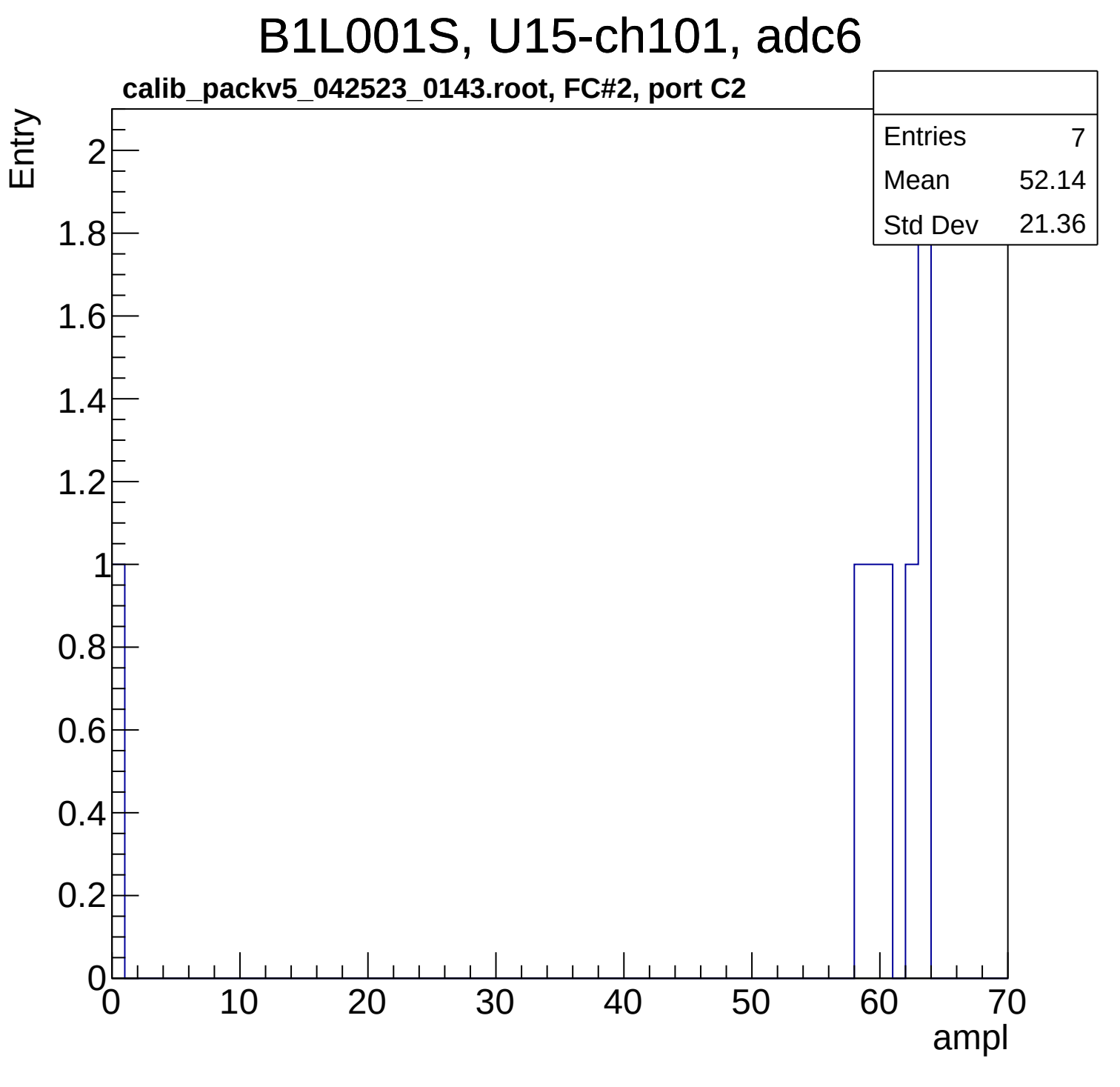
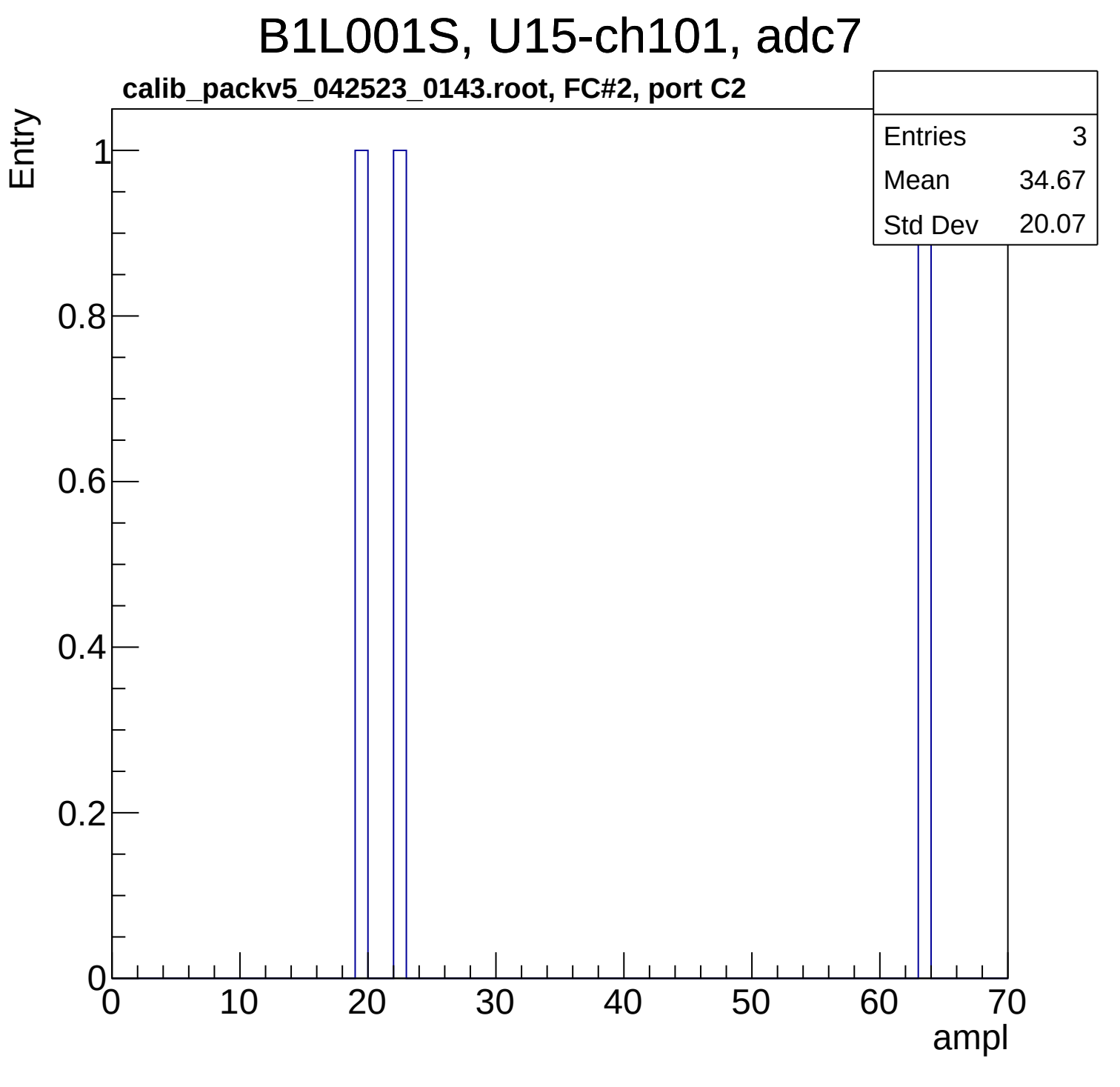
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch101, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 52.14
Std Dev 21.36
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch101, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 34.67
Std Dev 20.07
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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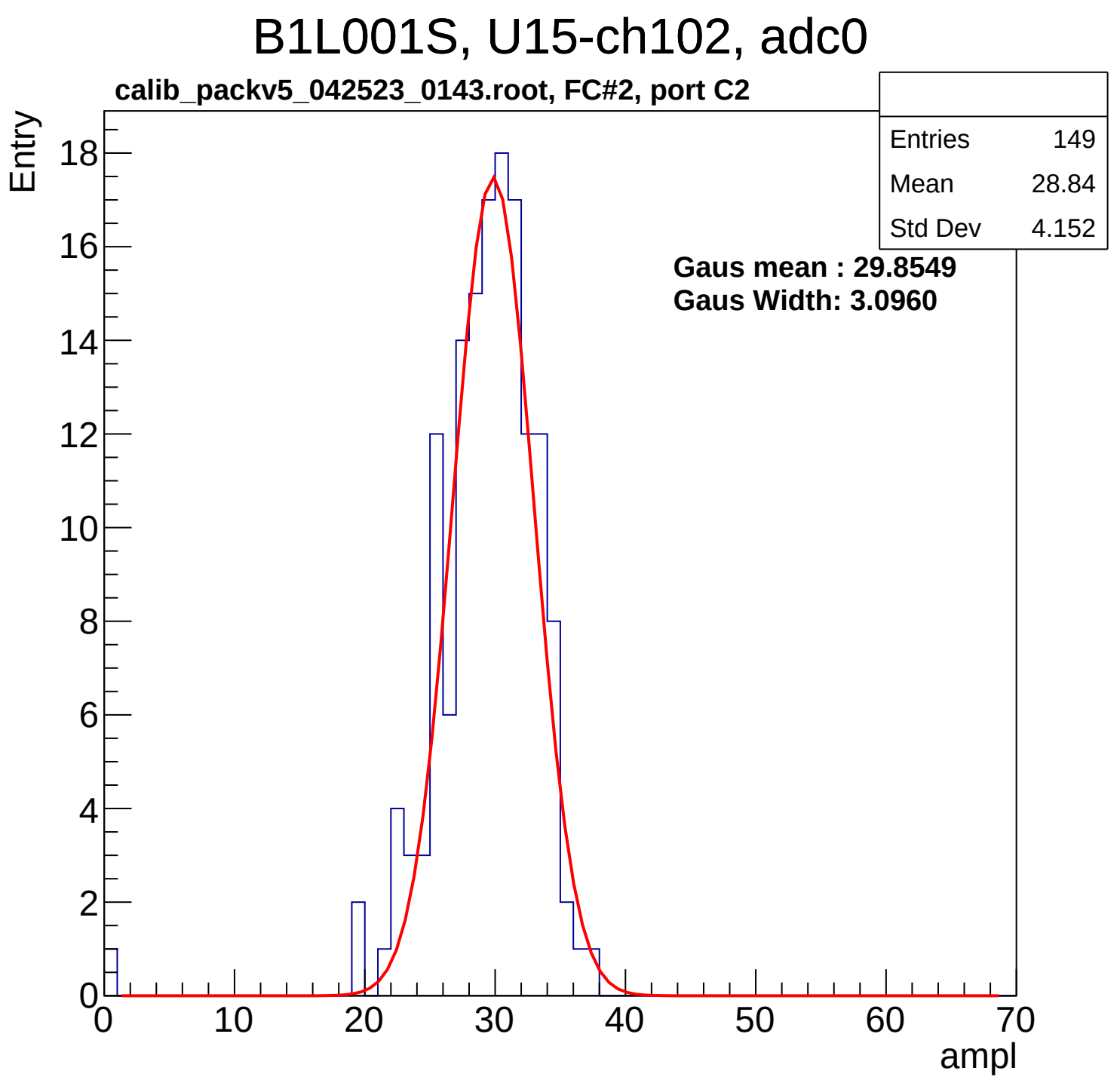
B1L001S, U15-ch102, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 28.84
Std Dev 4.152
Gaus mean : 29.8549
Gaus Width: 3.0960
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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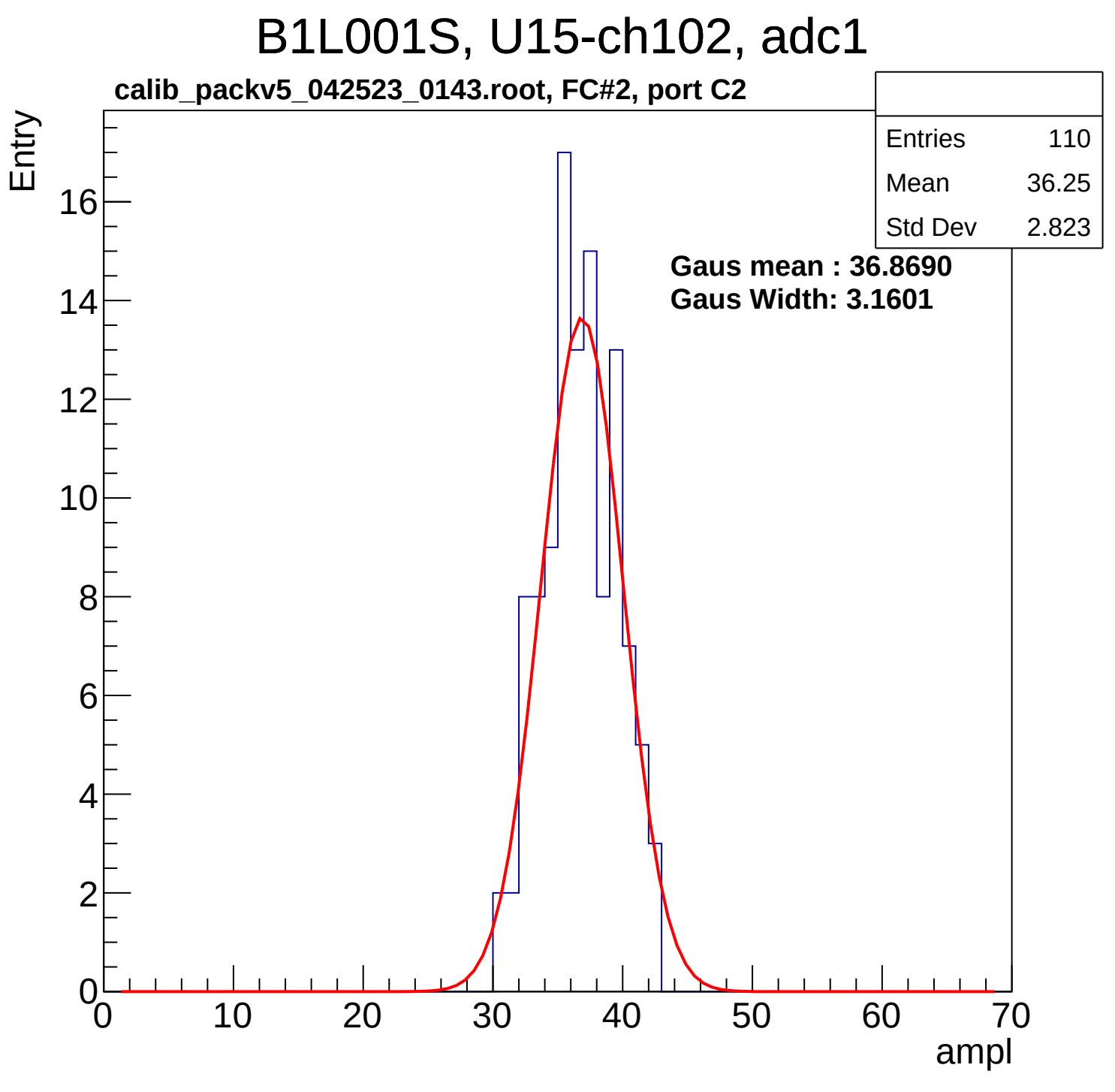
B1L001S, U15-ch102, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 36.25
Std Dev 2.823
Gaus mean : 36.8690
Gaus Width: 3.1601
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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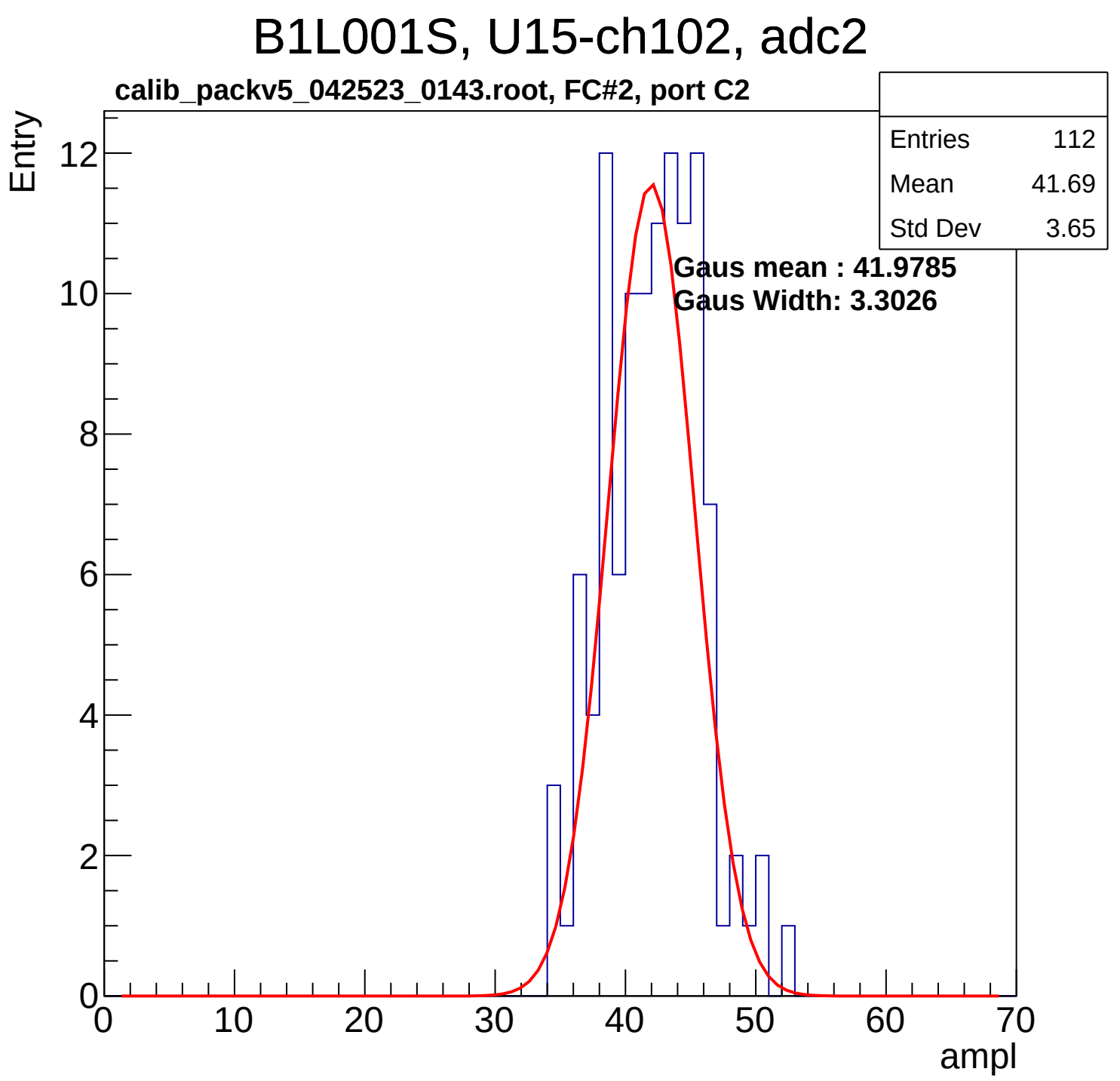
B1L001S, U15-ch102, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 41.69
Std Dev 3.65
Gaus mean : 41.9785
Gaus Width: 3.3026
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
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60
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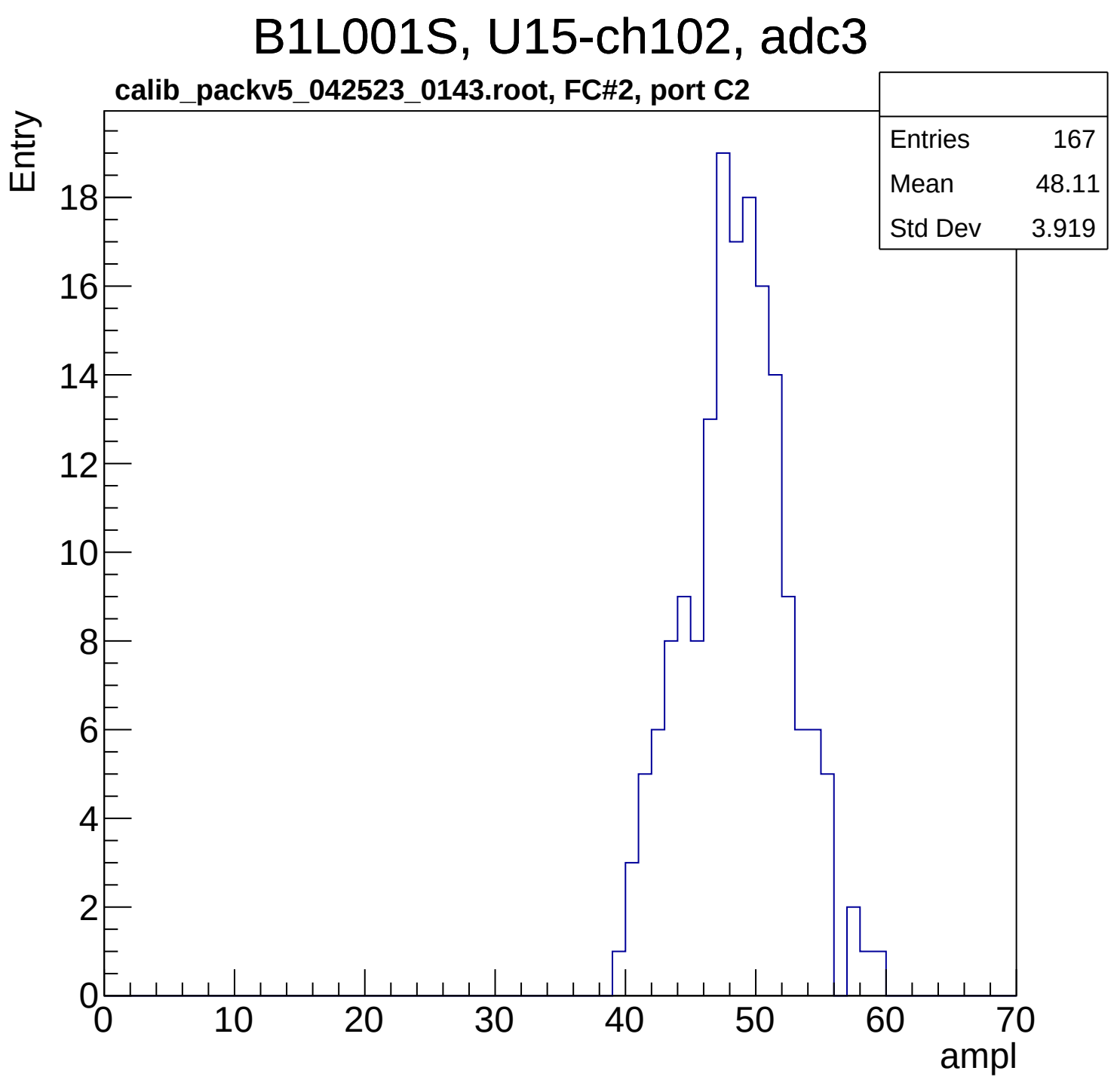
B1L001S, U15-ch102, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 48.11
Std Dev 3.919
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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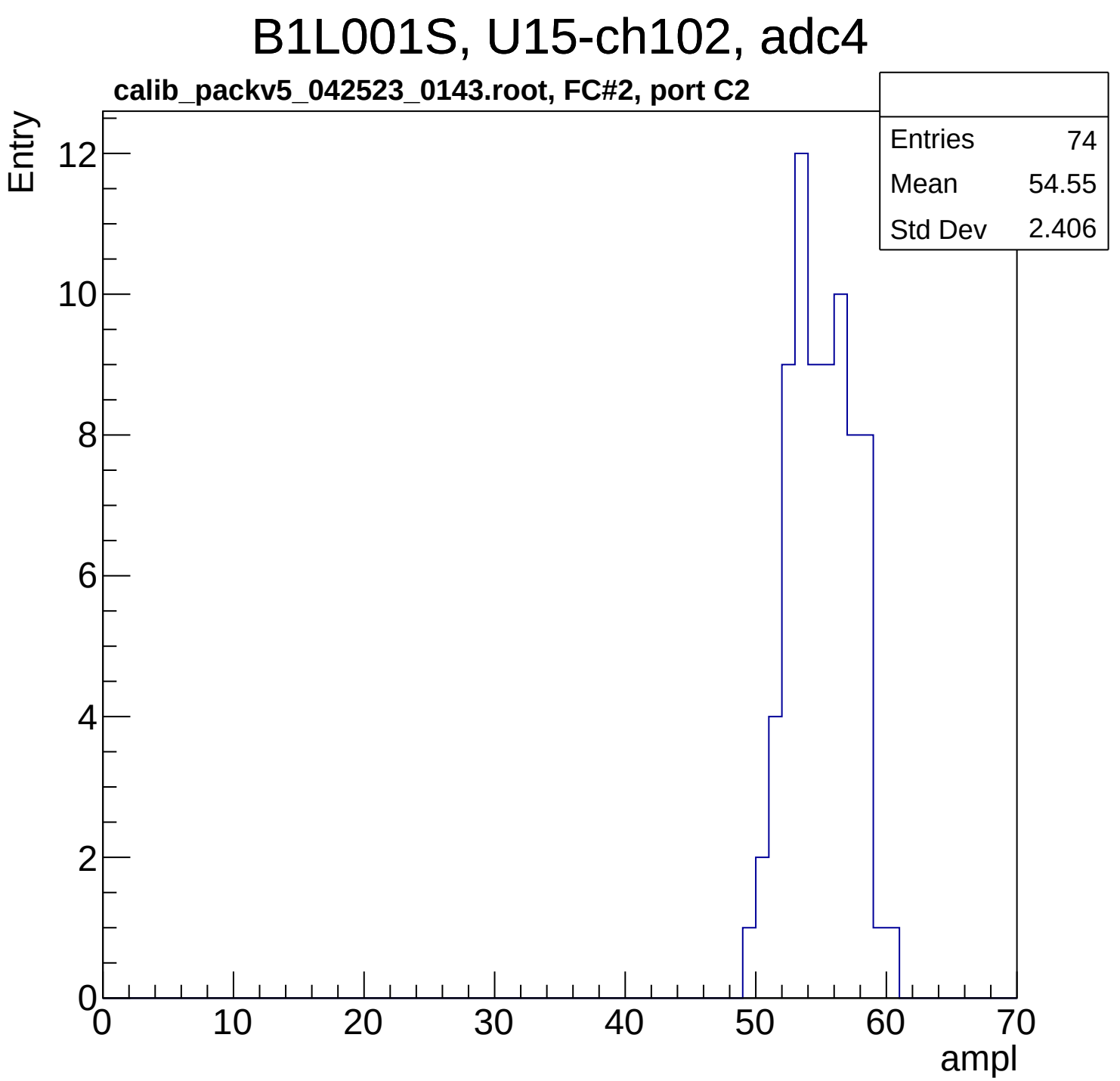
B1L001S, U15-ch102, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 54.55
Std Dev 2.406
Entry
ampl
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2
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6
8
10
12
0
10
20
30
40
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60
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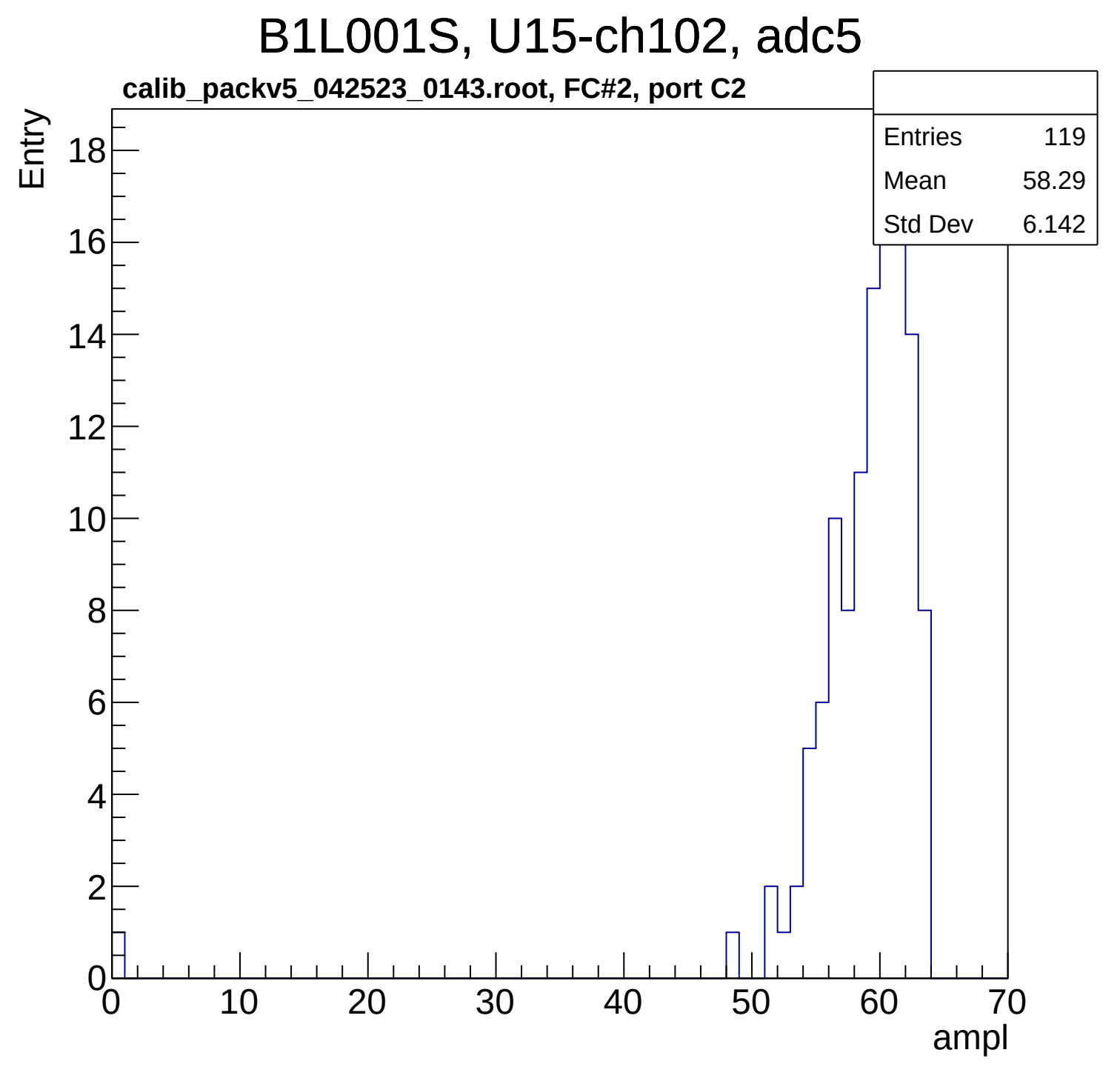
B1L001S, U15-ch102, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 58.29
Std Dev 6.142
Entry
ampl
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4
6
8
10
12
14
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0
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20
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40
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60
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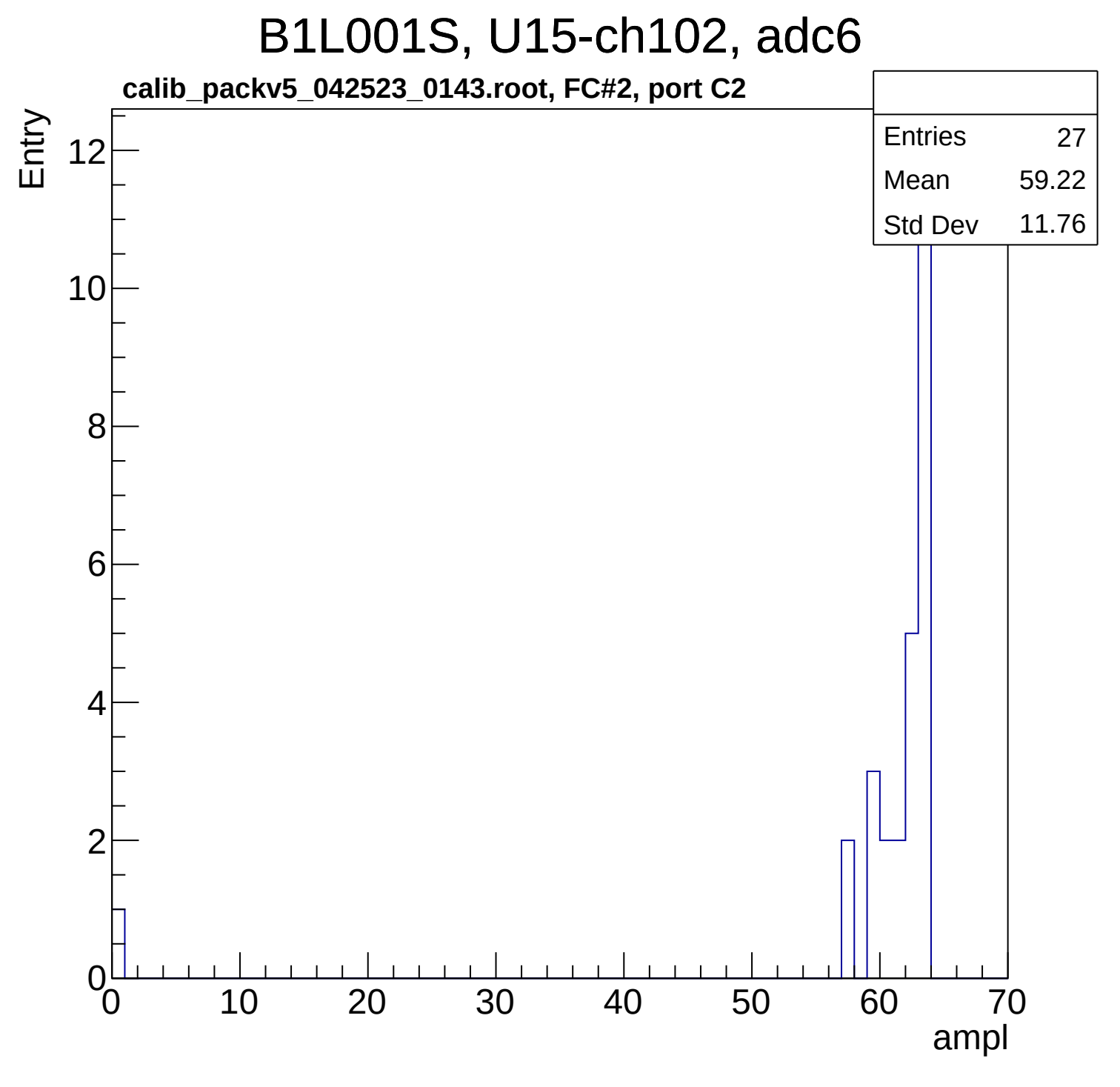
B1L001S, U15-ch102, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 27
Mean 59.22
Std Dev 11.76
Entry
0
2
4
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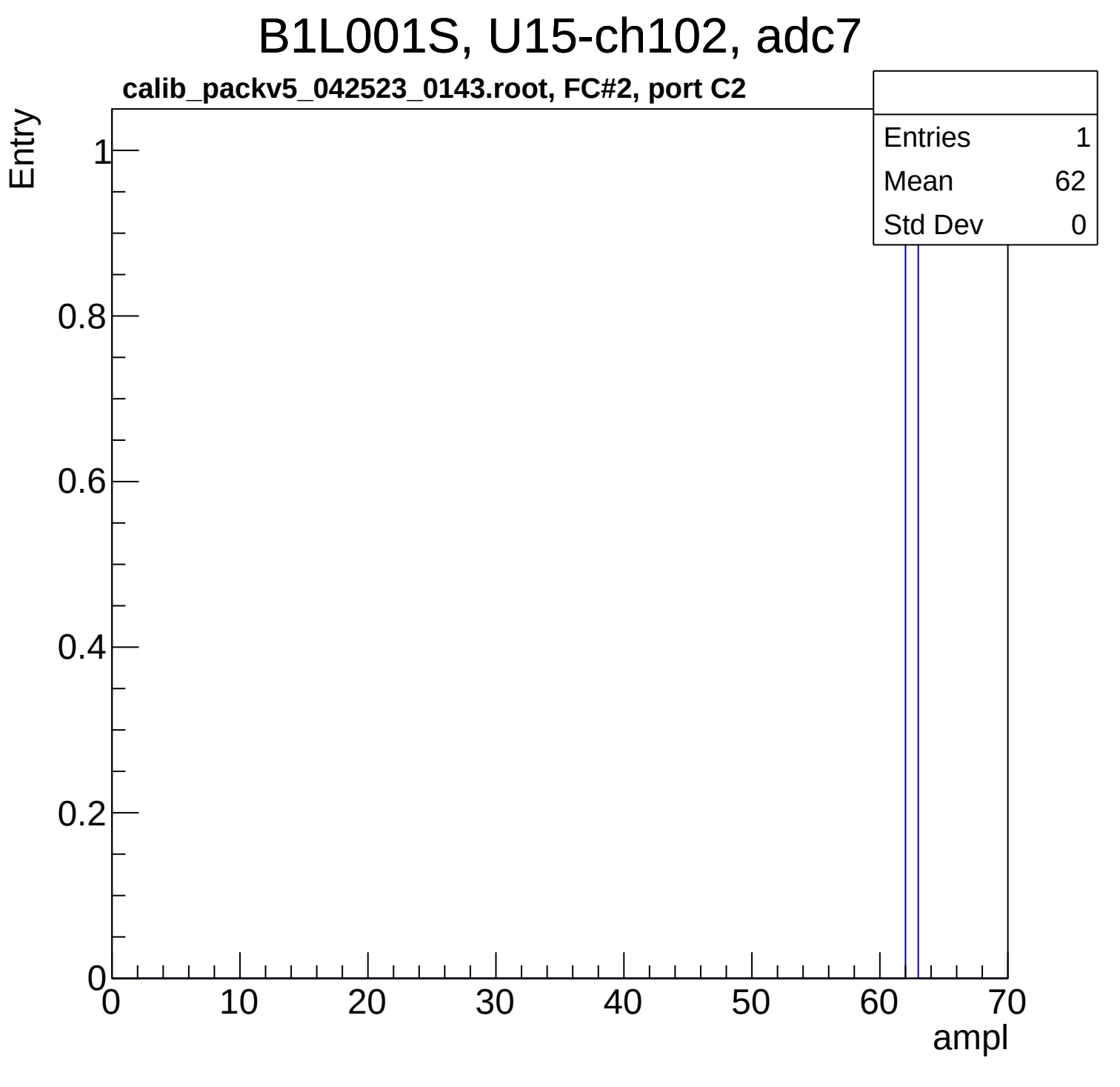
60
70
ampl


B1L001S, U15-ch102, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
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10
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30
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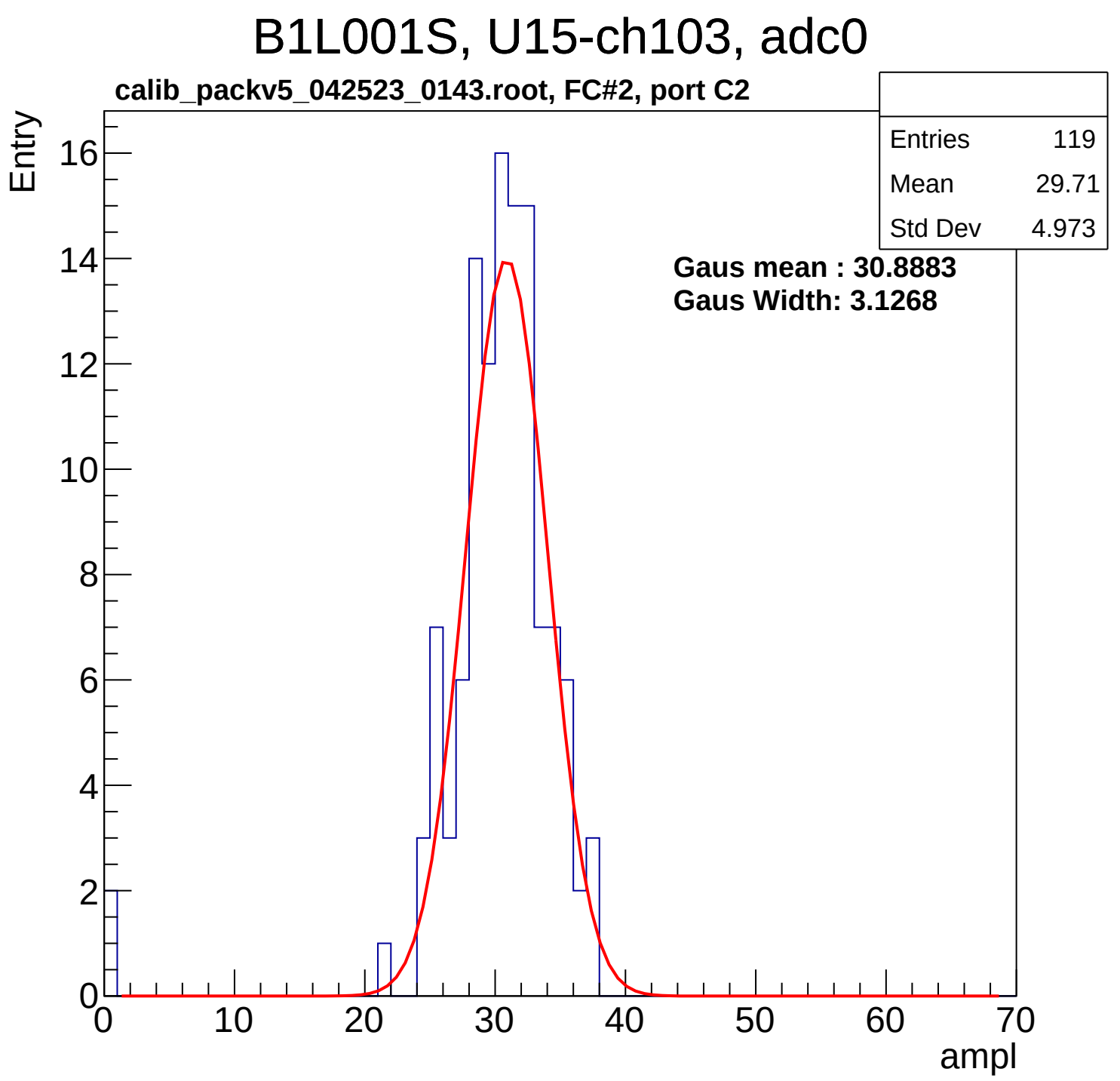
50
60
70
ampl


B1L001S, U15-ch103, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 29.71
Std Dev 4.973
Gaus mean : 30.8883
Gaus Width: 3.1268
Entry
ampl
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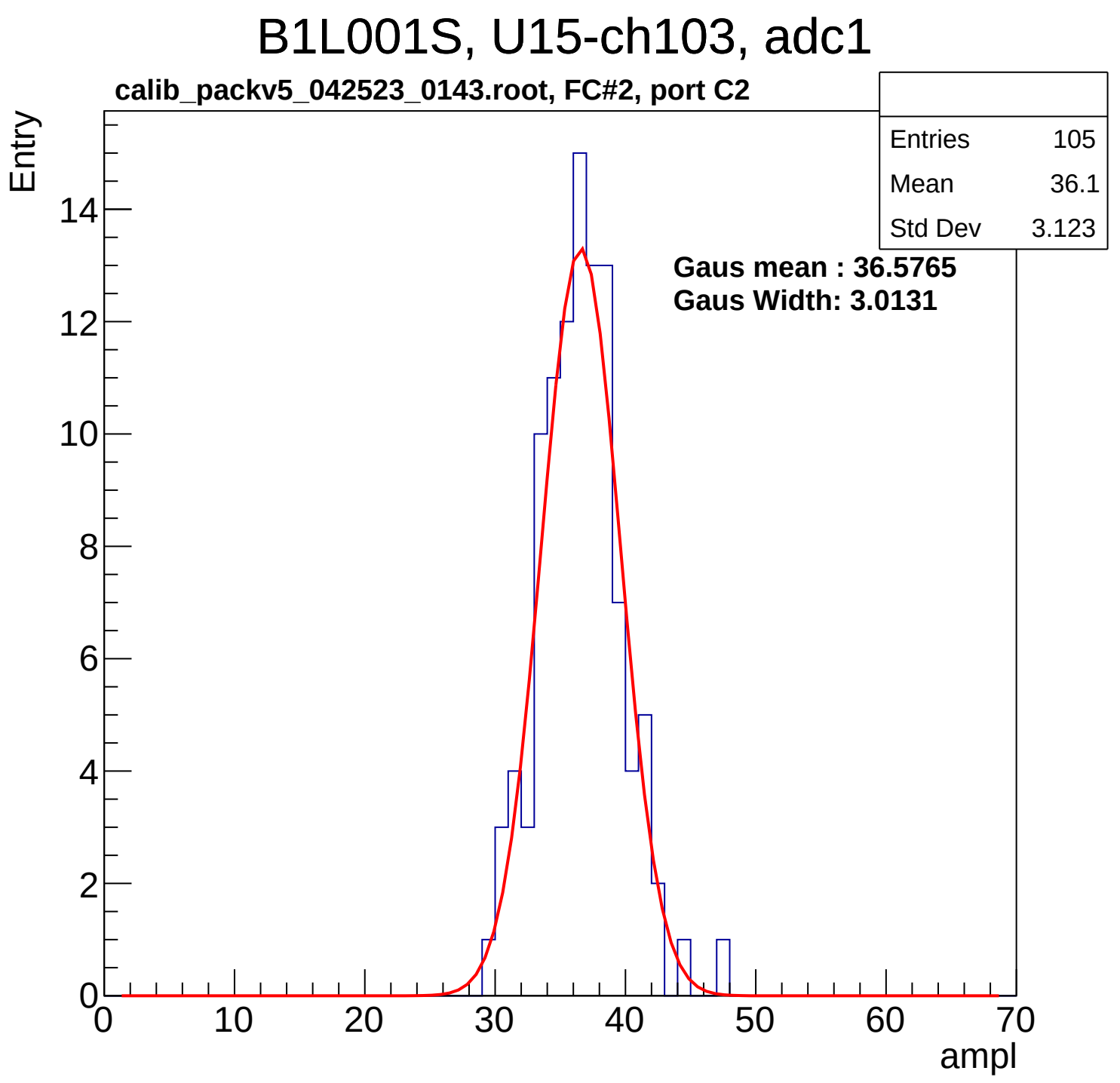
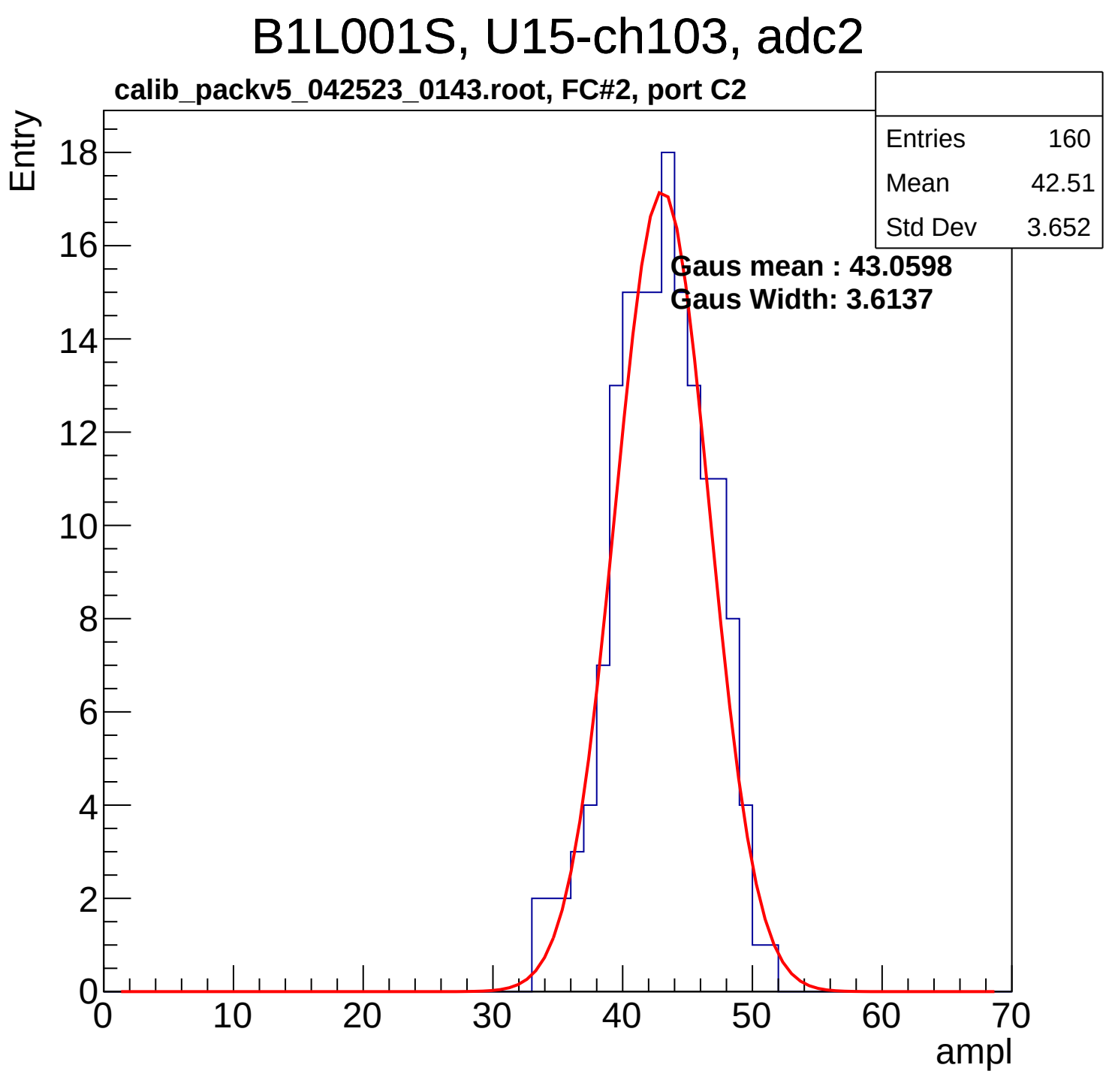
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch103, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 36.1
Std Dev 3.123
Gaus mean : 36.5765
Gaus Width: 3.0131
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch103, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 42.51
Std Dev 3.652
Gaus mean : 43.0598
Gaus Width: 3.6137
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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60
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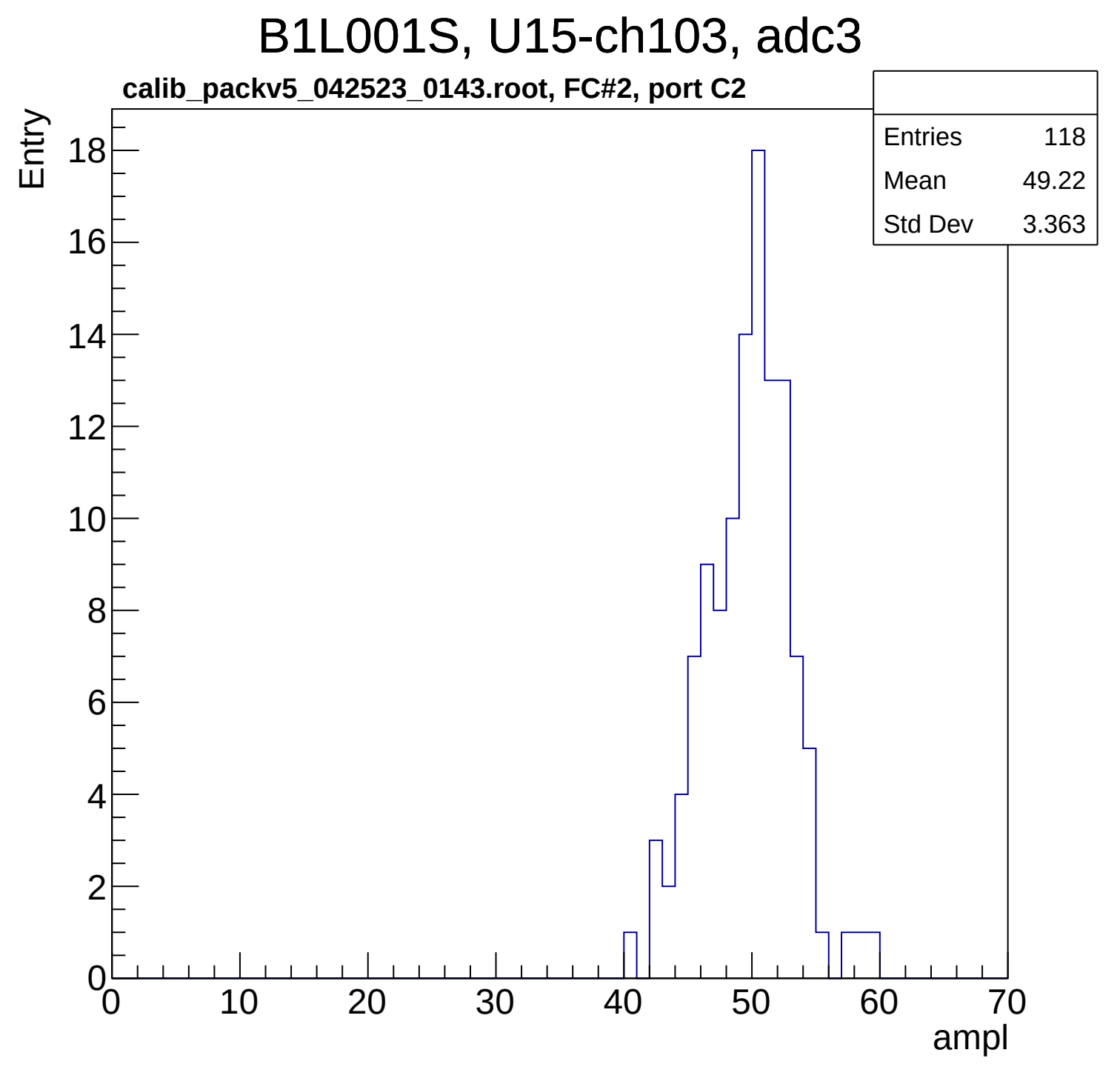
B1L001S, U15-ch103, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 49.22
Std Dev 3.363
Entry
ampl
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6
8
10
12
14
16
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60
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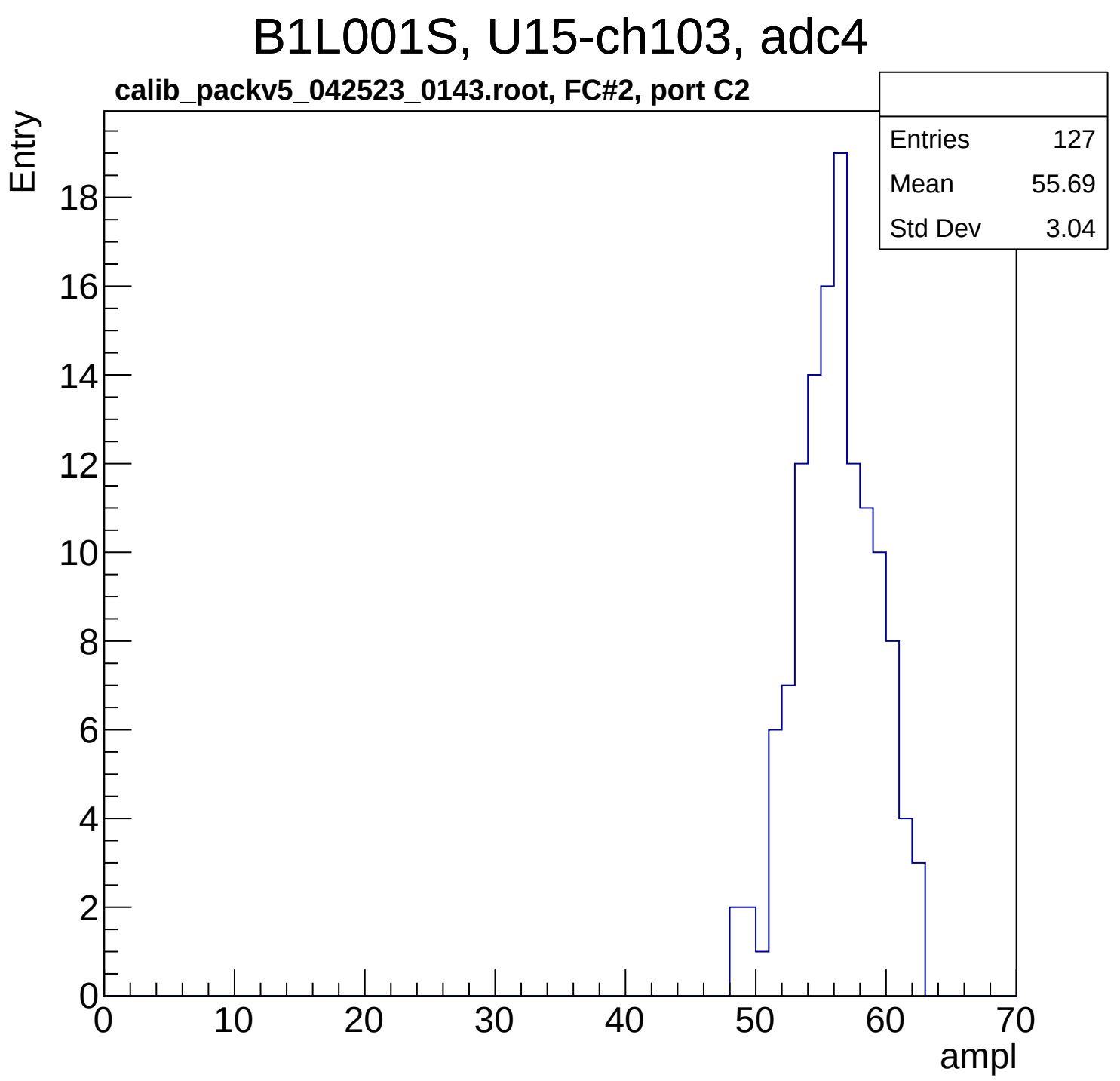
B1L001S, U15-ch103, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 55.69
Std Dev 3.04
Entry
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6
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10
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10
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30
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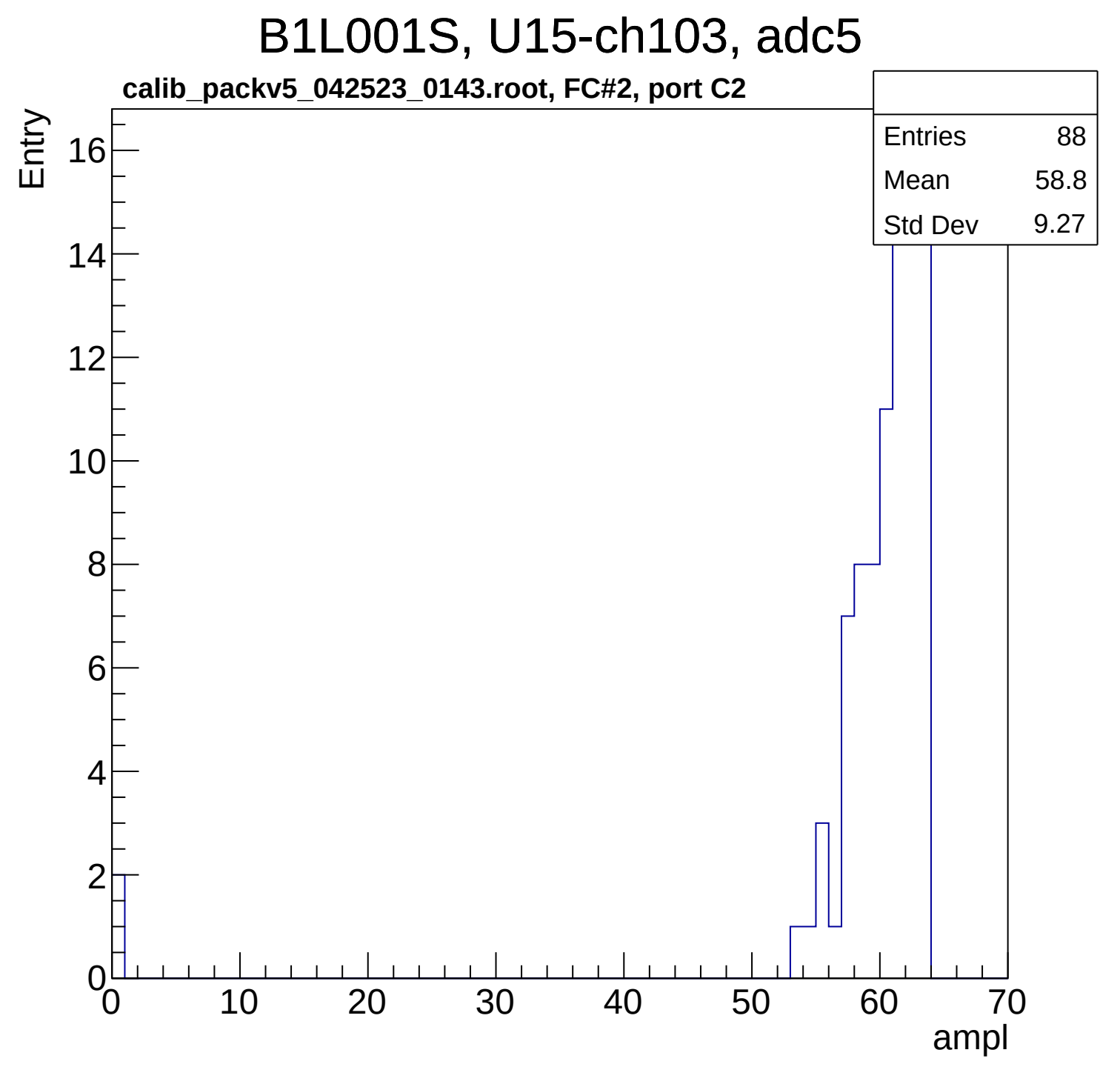
50
60
70
ampl


B1L001S, U15-ch103, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 58.8
Std Dev 9.27
Entry
ampl
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6
8
10
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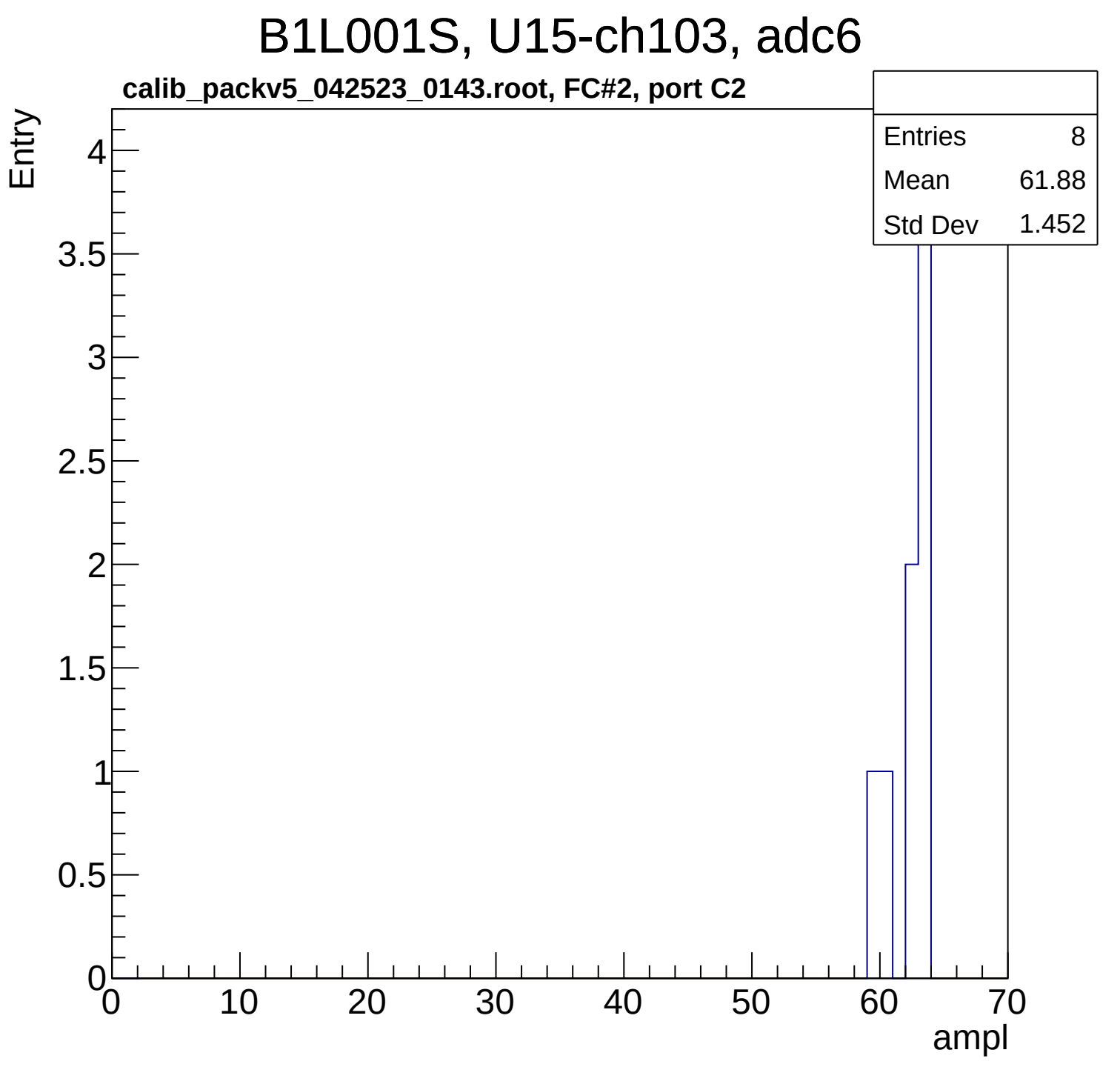
B1L001S, U15-ch103, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.88
Std Dev 1.452
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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60
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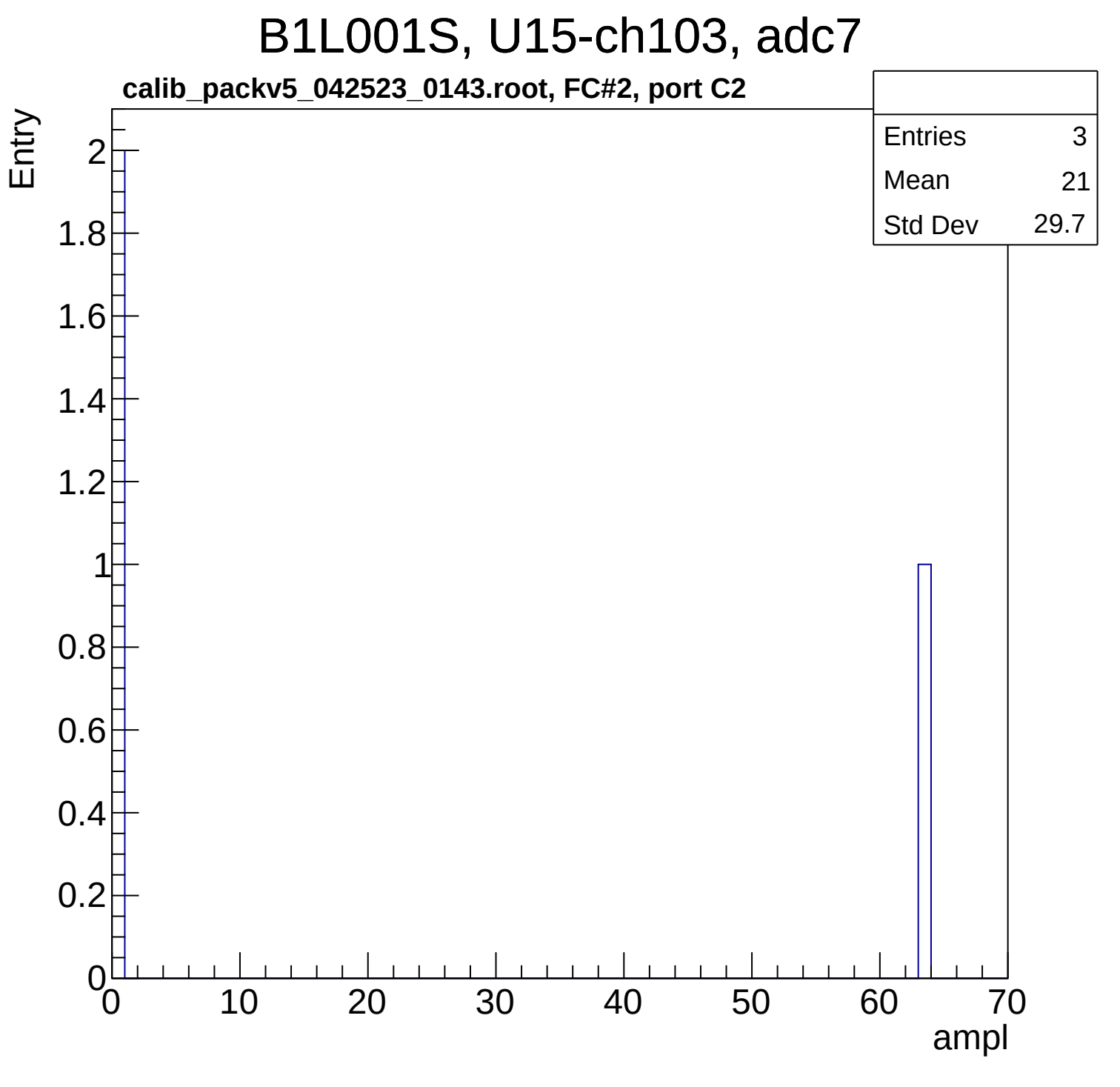
B1L001S, U15-ch103, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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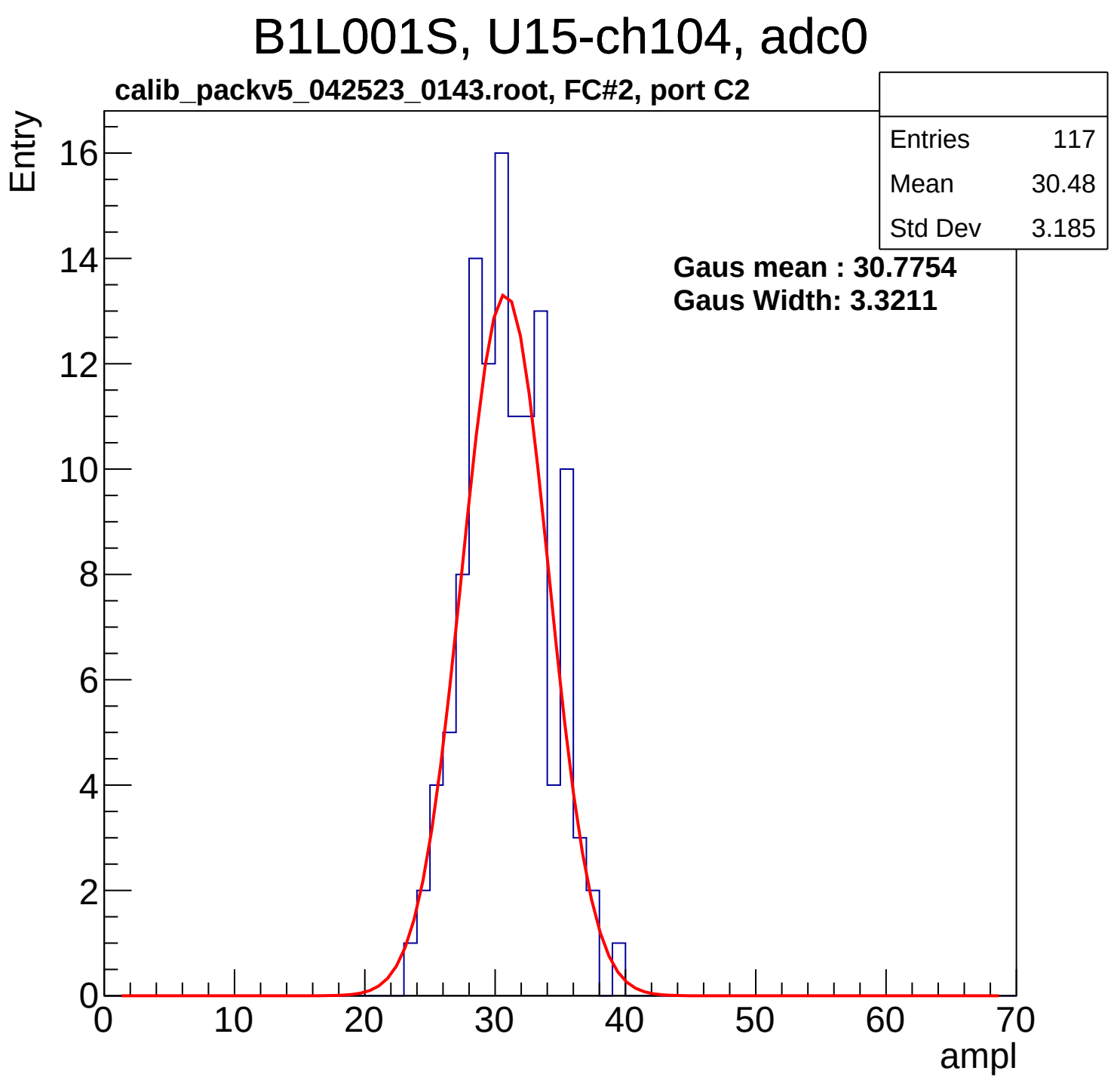
B1L001S, U15-ch104, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 30.48
Std Dev 3.185
Gaus mean : 30.7754
Gaus Width: 3.3211
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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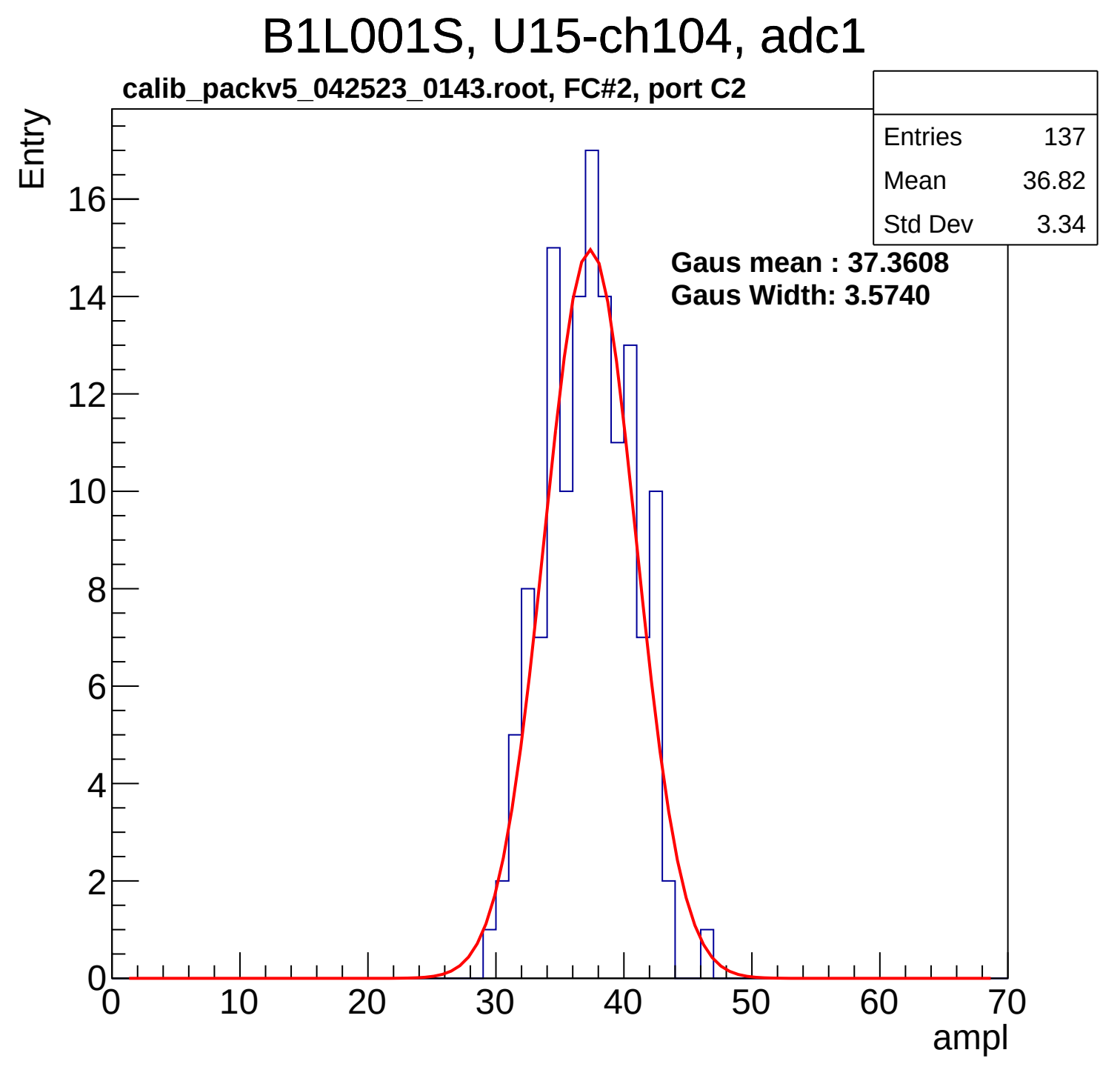
B1L001S, U15-ch104, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 36.82
Std Dev 3.34
Gaus mean : 37.3608
Gaus Width: 3.5740
Entry
ampl
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2
4
6
8
10
12
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0
10
20
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40
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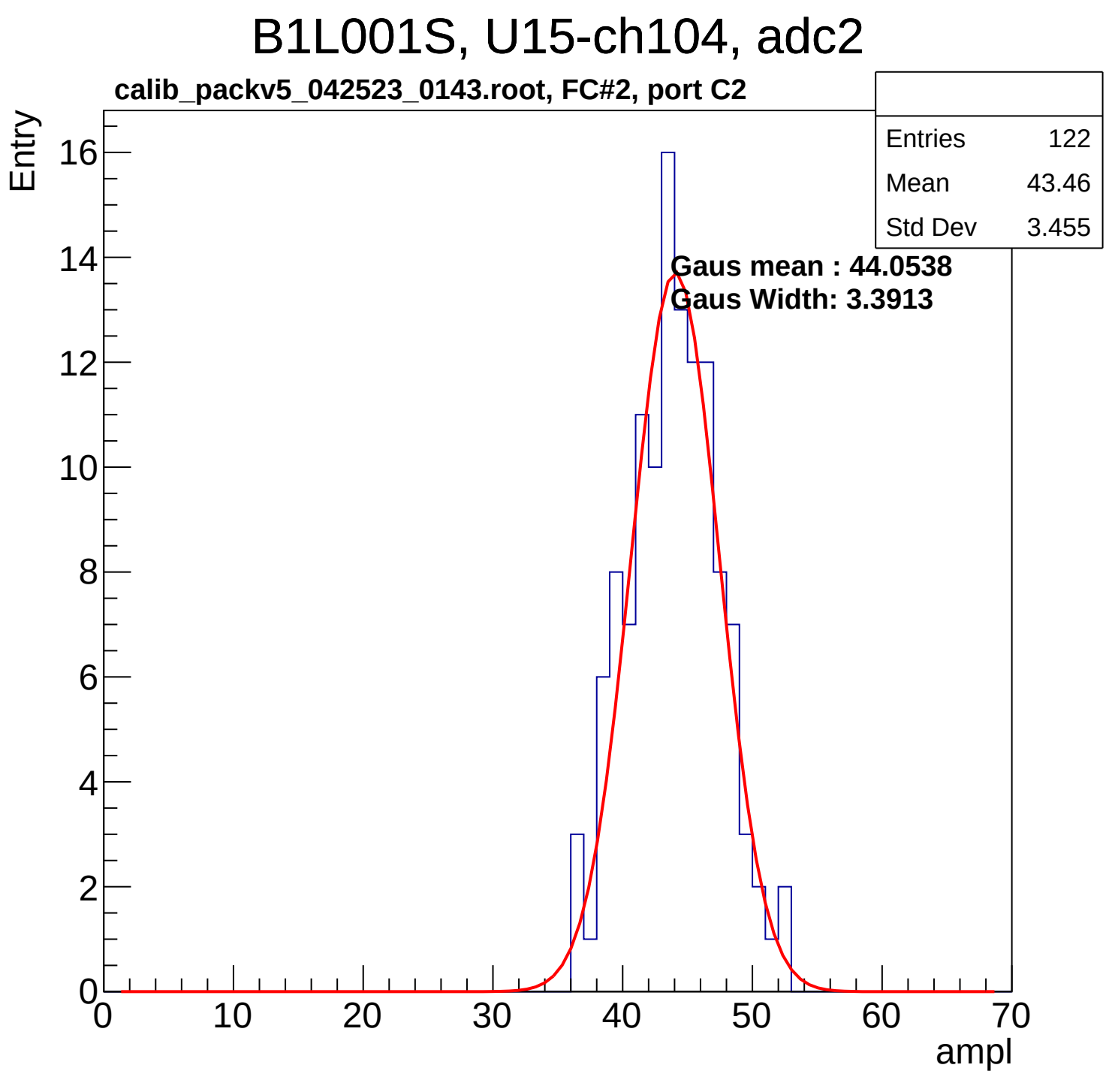
B1L001S, U15-ch104, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 43.46
Std Dev 3.455
Gaus mean : 44.0538
Gaus Width: 3.3913
Entry
ampl
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2
4
6
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10
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0
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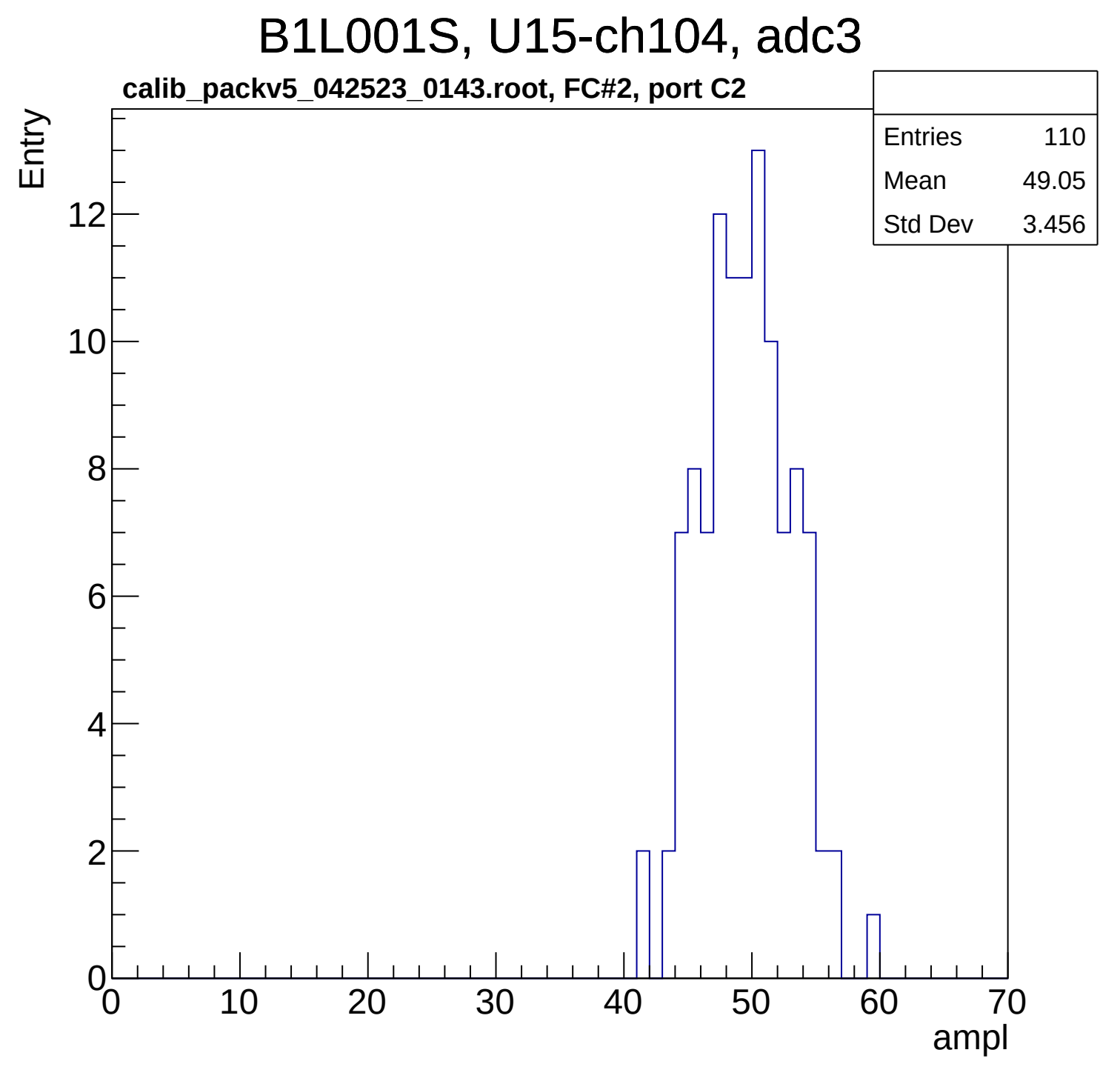
B1L001S, U15-ch104, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.05
Std Dev 3.456
Entry
ampl
0
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0
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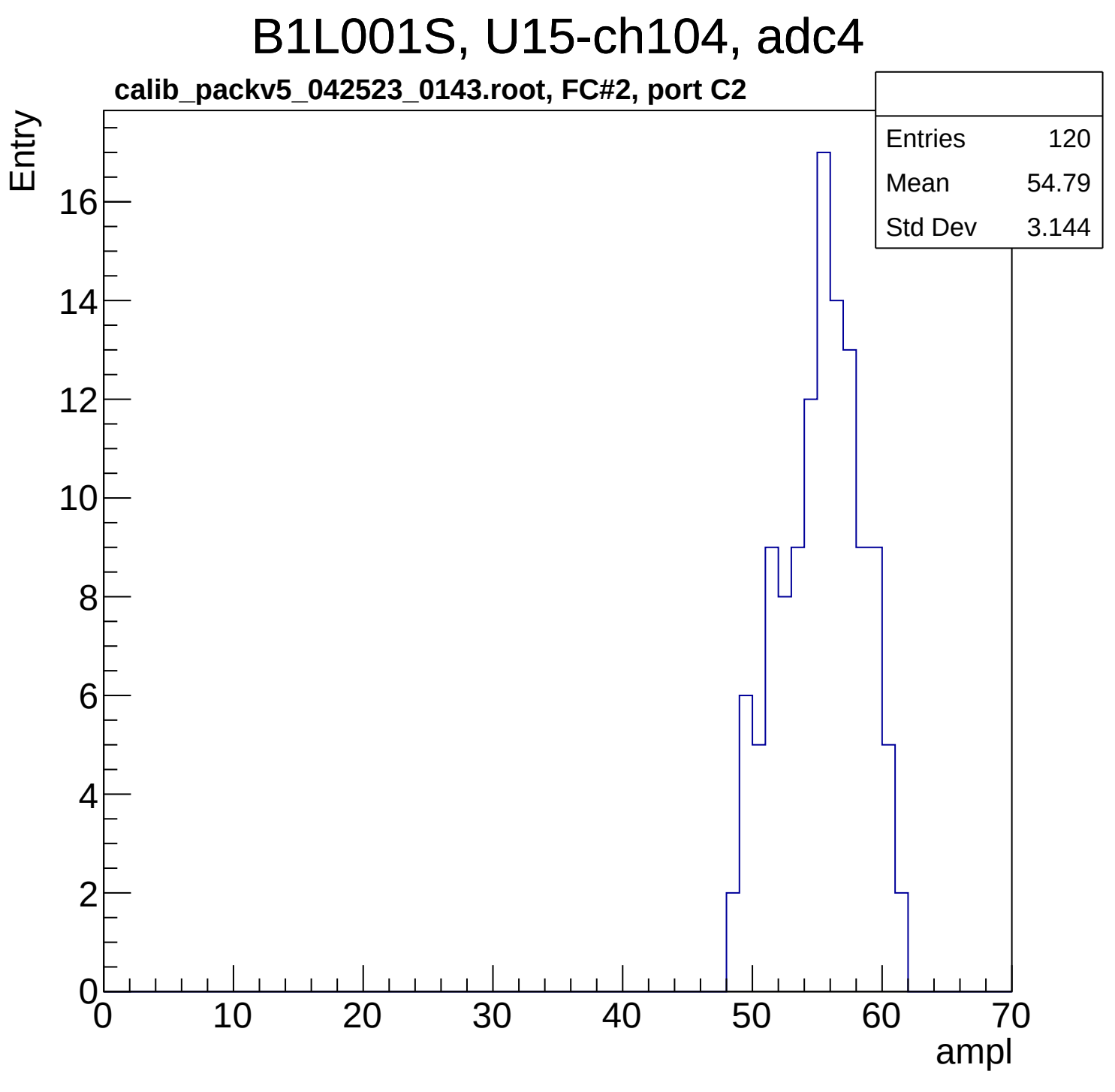
B1L001S, U15-ch104, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 54.79
Std Dev 3.144
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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60
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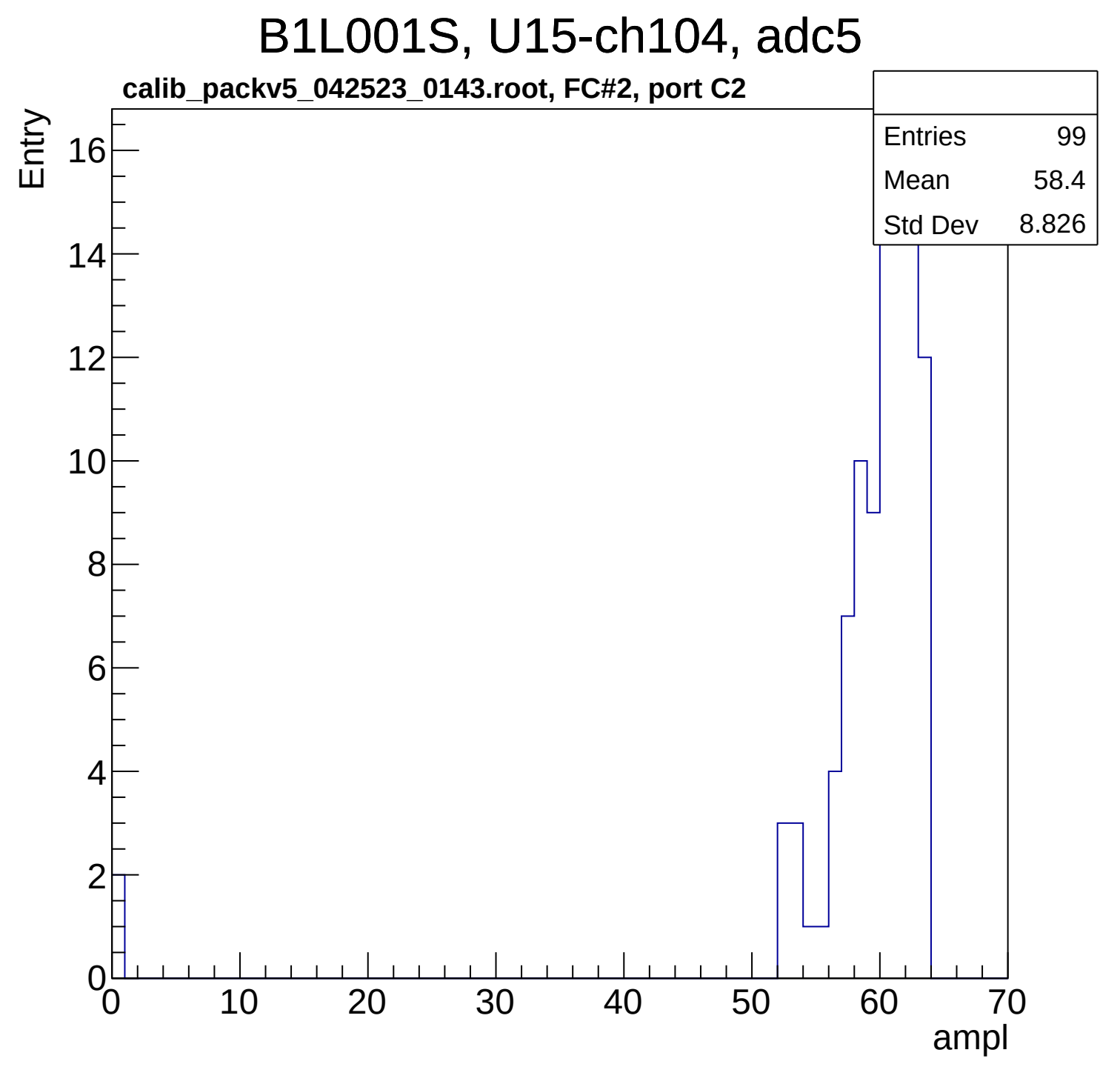
B1L001S, U15-ch104, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 58.4
Std Dev 8.826
Entry
ampl
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2
4
6
8
10
12
14
16
0
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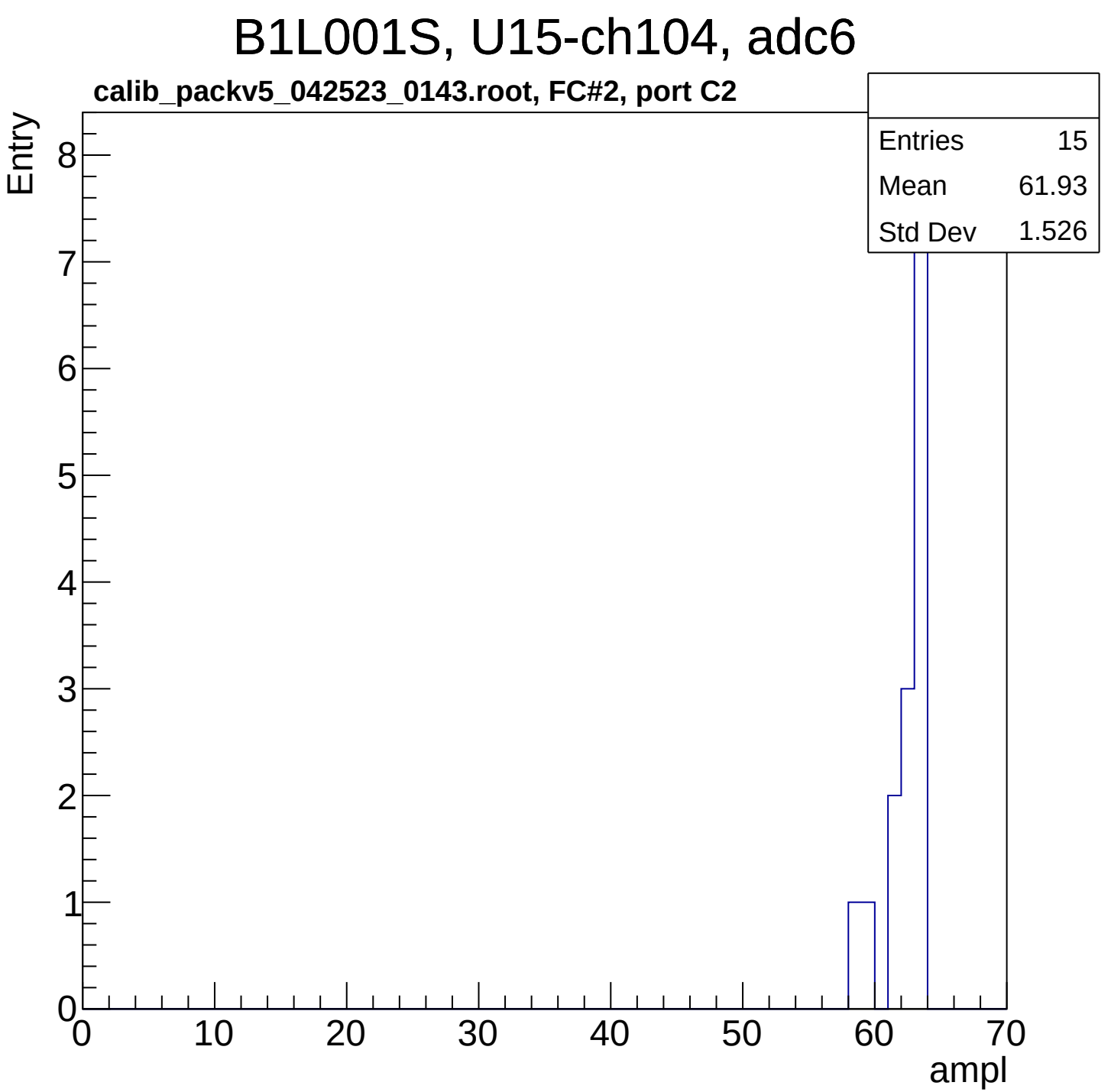
B1L001S, U15-ch104, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 61.93
Std Dev 1.526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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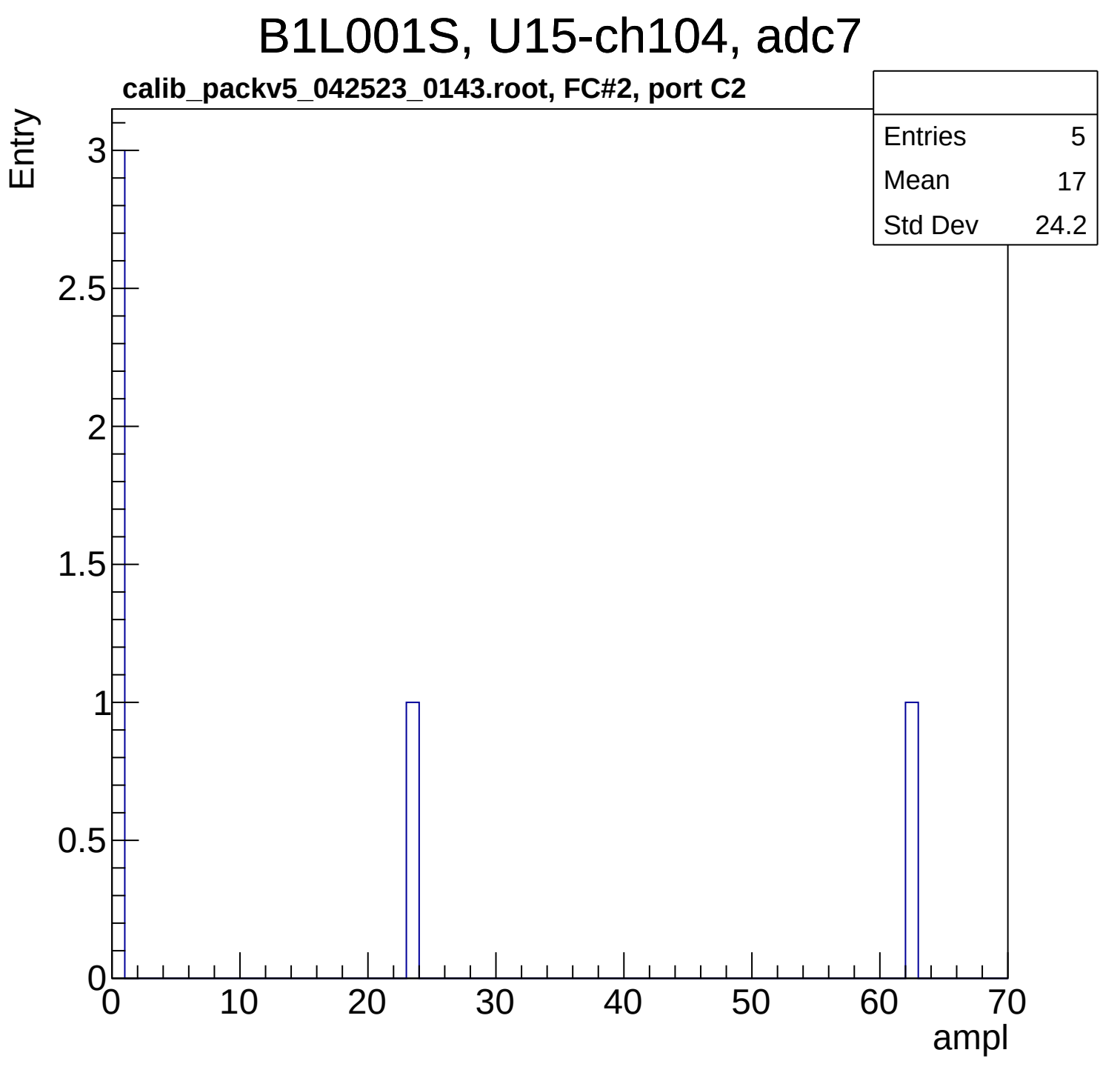
B1L001S, U15-ch104, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 17
Std Dev 24.2
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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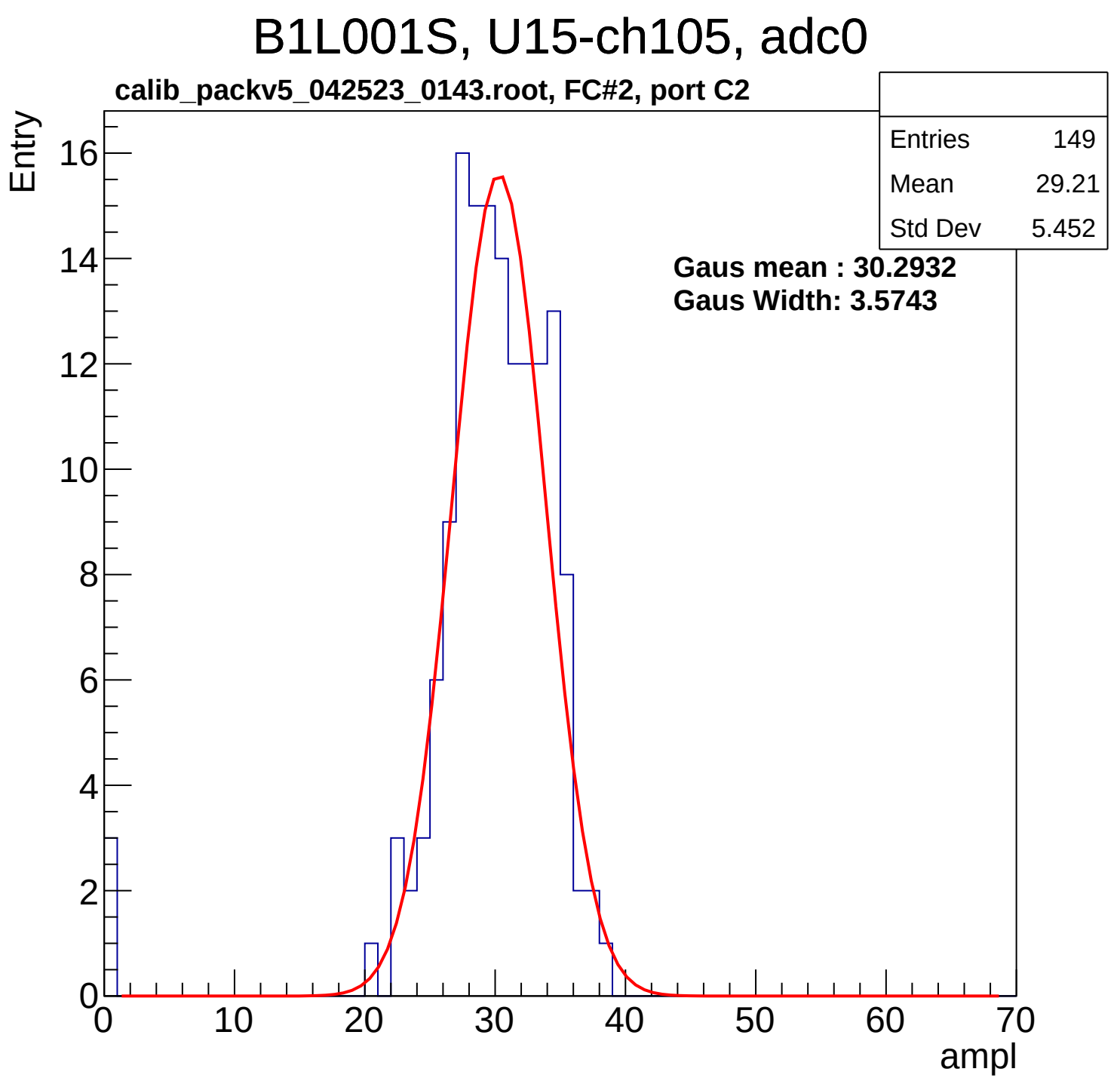
B1L001S, U15-ch105, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 29.21
Std Dev 5.452
Gaus mean : 30.2932
Gaus Width: 3.5743
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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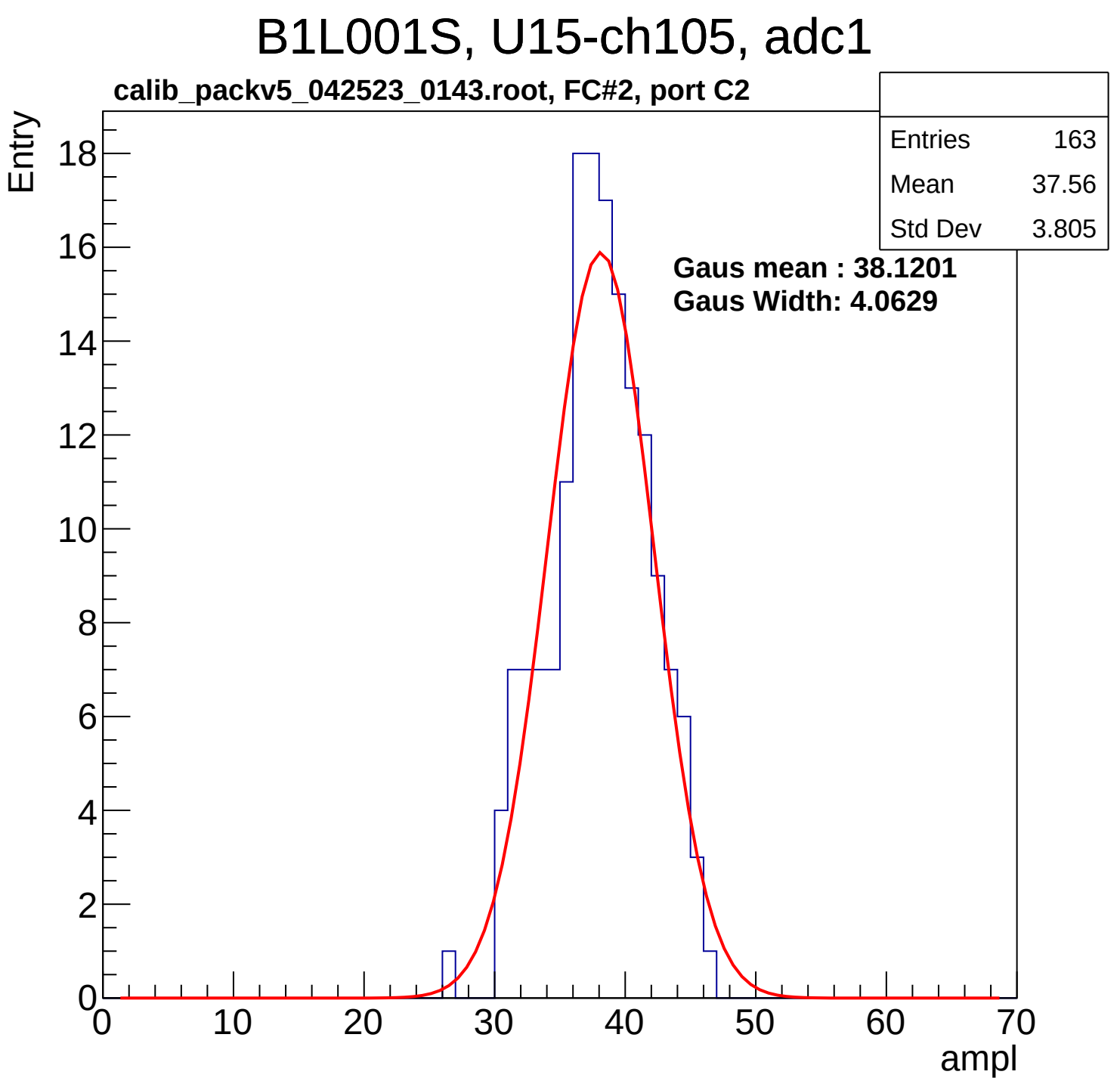
B1L001S, U15-ch105, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 37.56
Std Dev 3.805
Gaus mean : 38.1201
Gaus Width: 4.0629
Entry
ampl
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6
8
10
12
14
16
18
0
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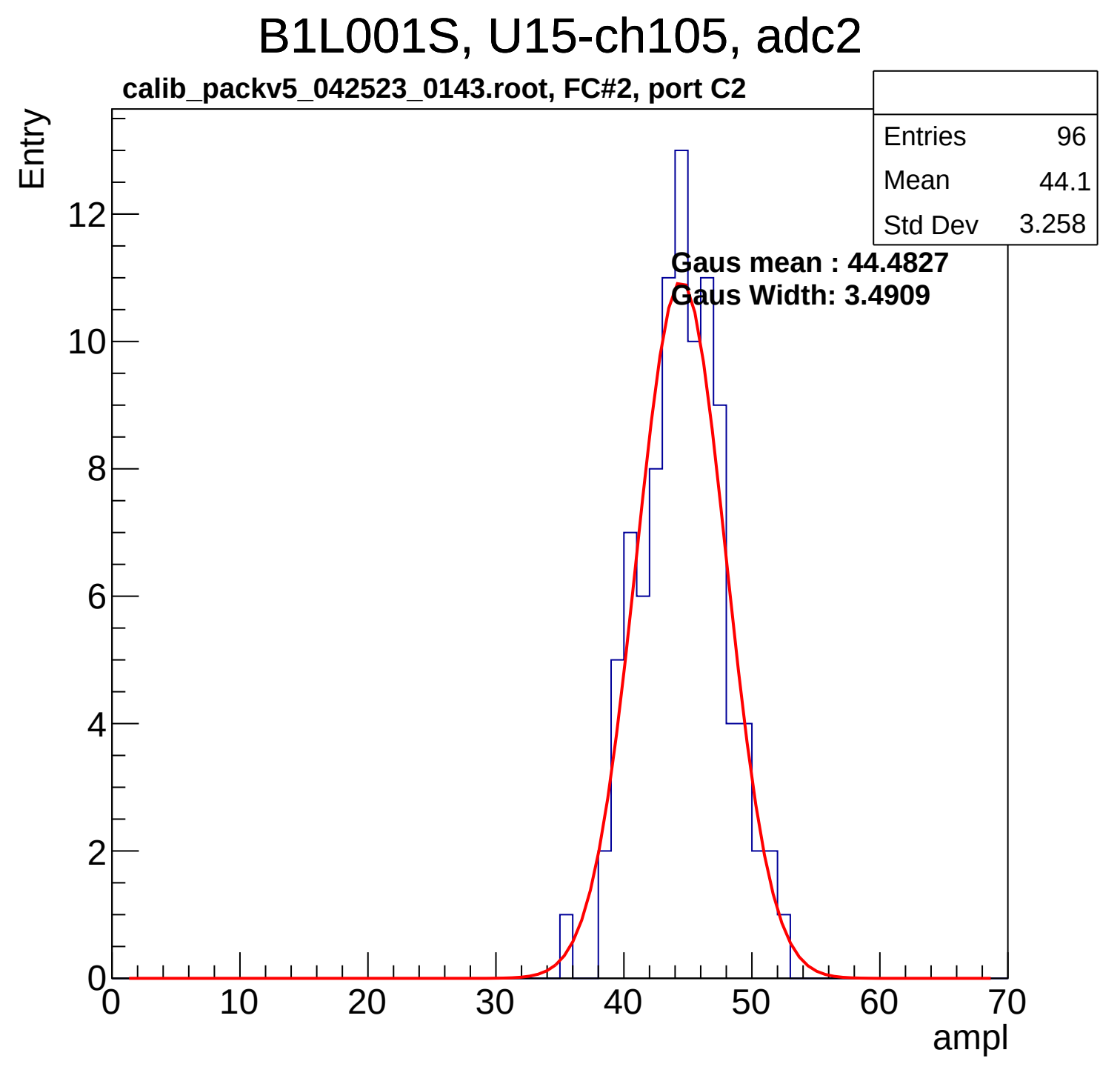
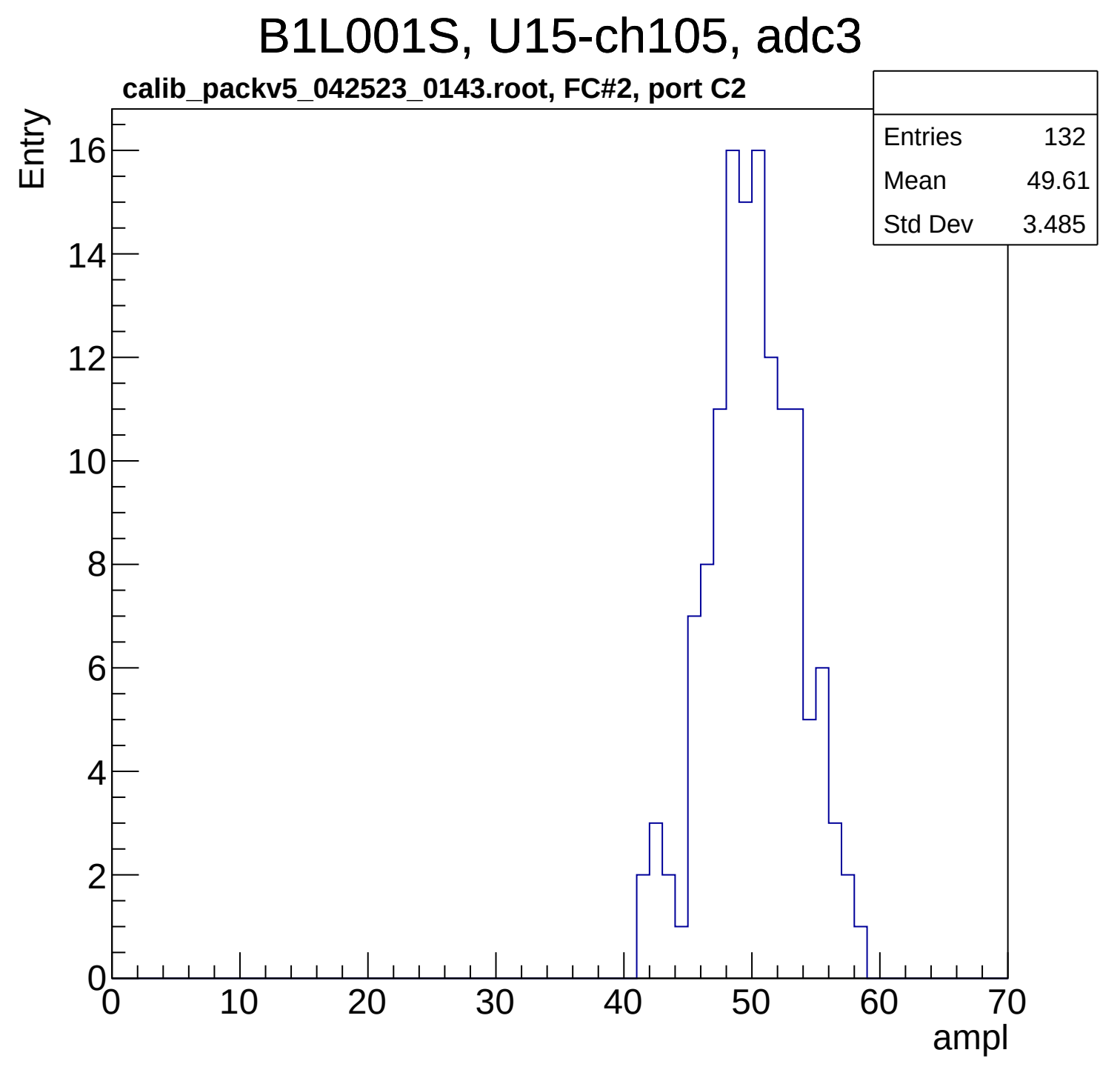
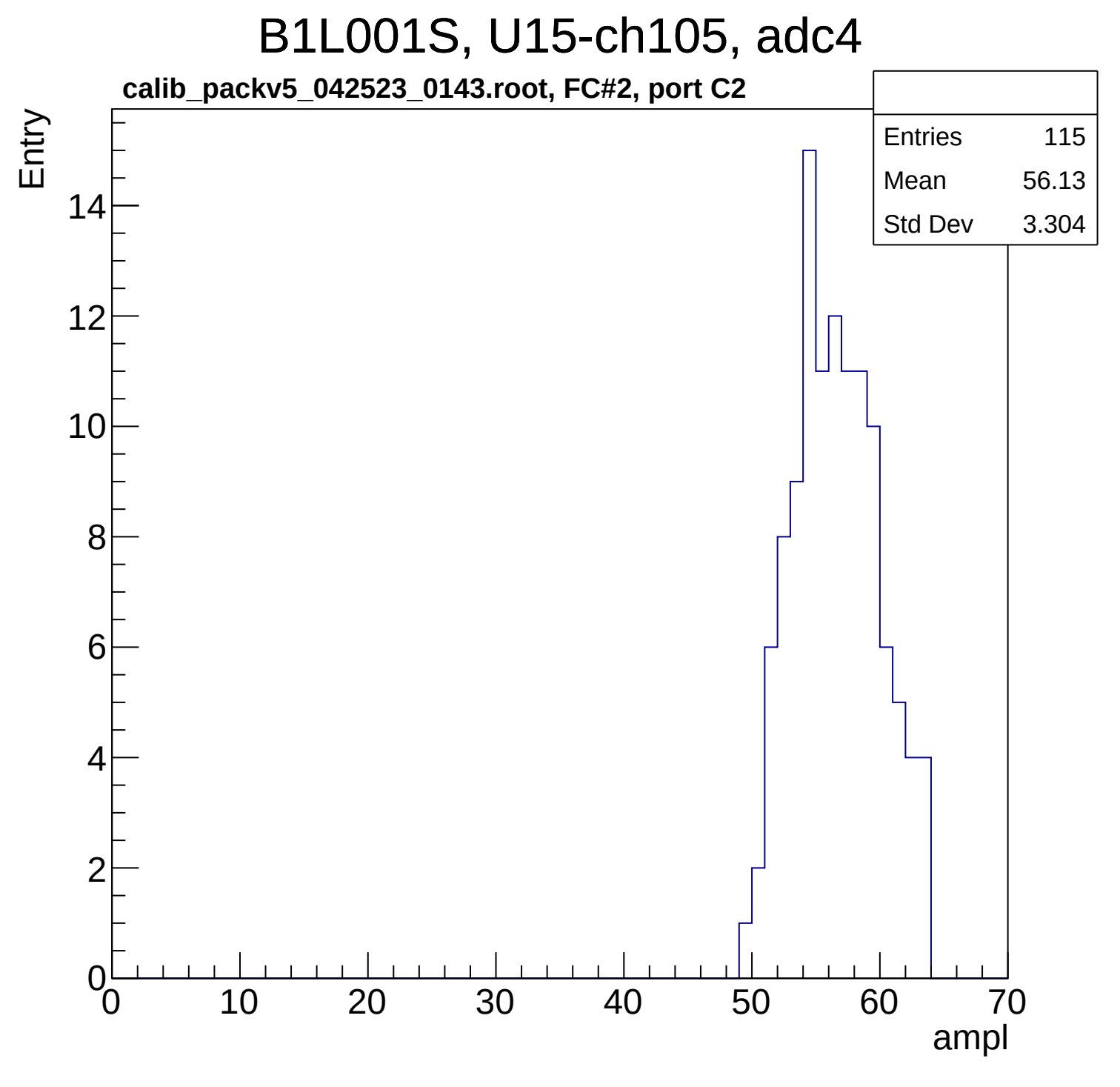

B1L001S, U15-ch105, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 44.1
Std Dev 3.258
Gaus mean : 44.4827
Gaus Width: 3.4909
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch105, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 49.61
Std Dev 3.485
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch105, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 56.13
Std Dev 3.304
Entry
0
2
4
6
8
10
12
14
0
10
20
30
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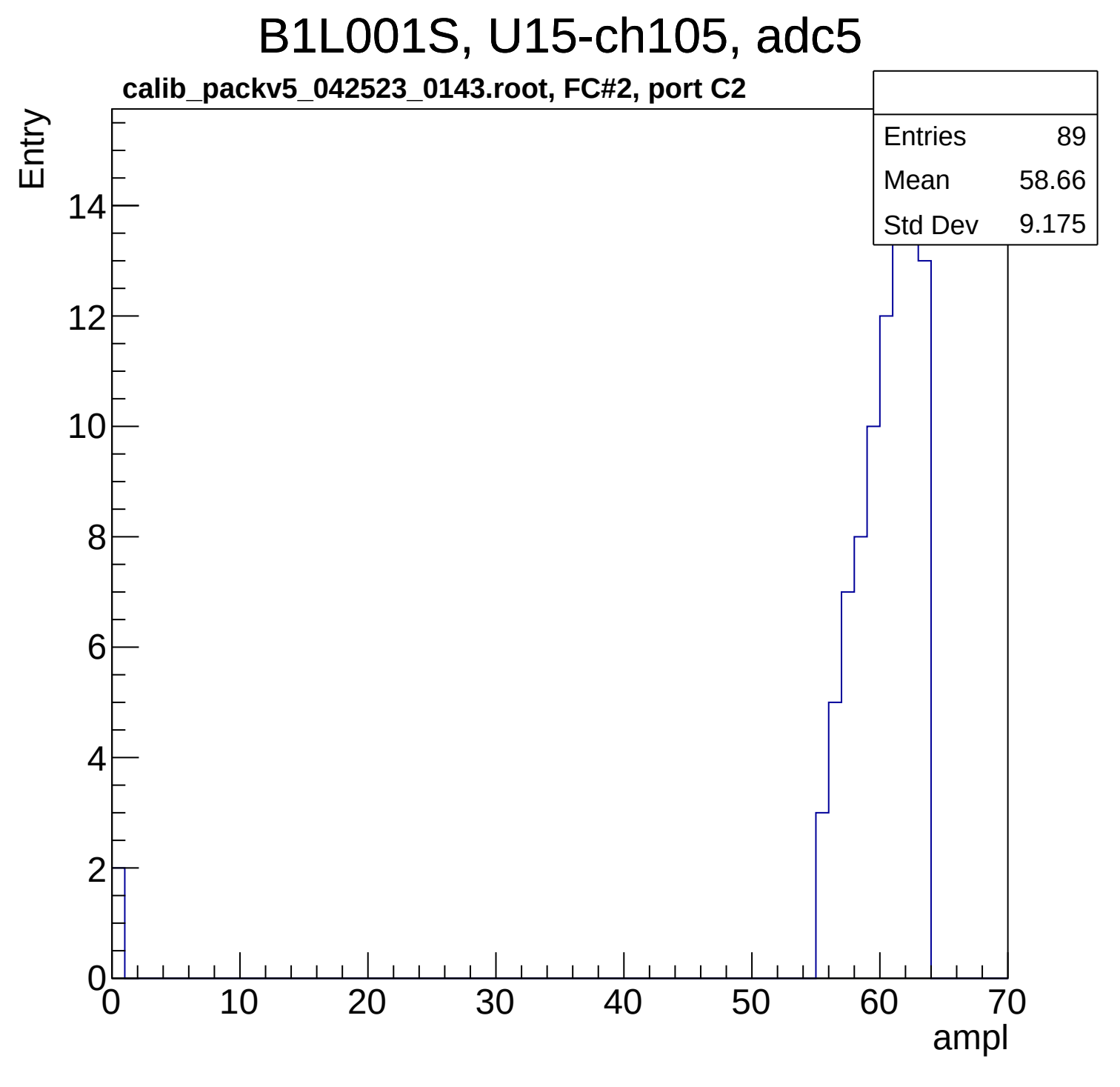
50
60
70
ampl


B1L001S, U15-ch105, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 58.66
Std Dev 9.175
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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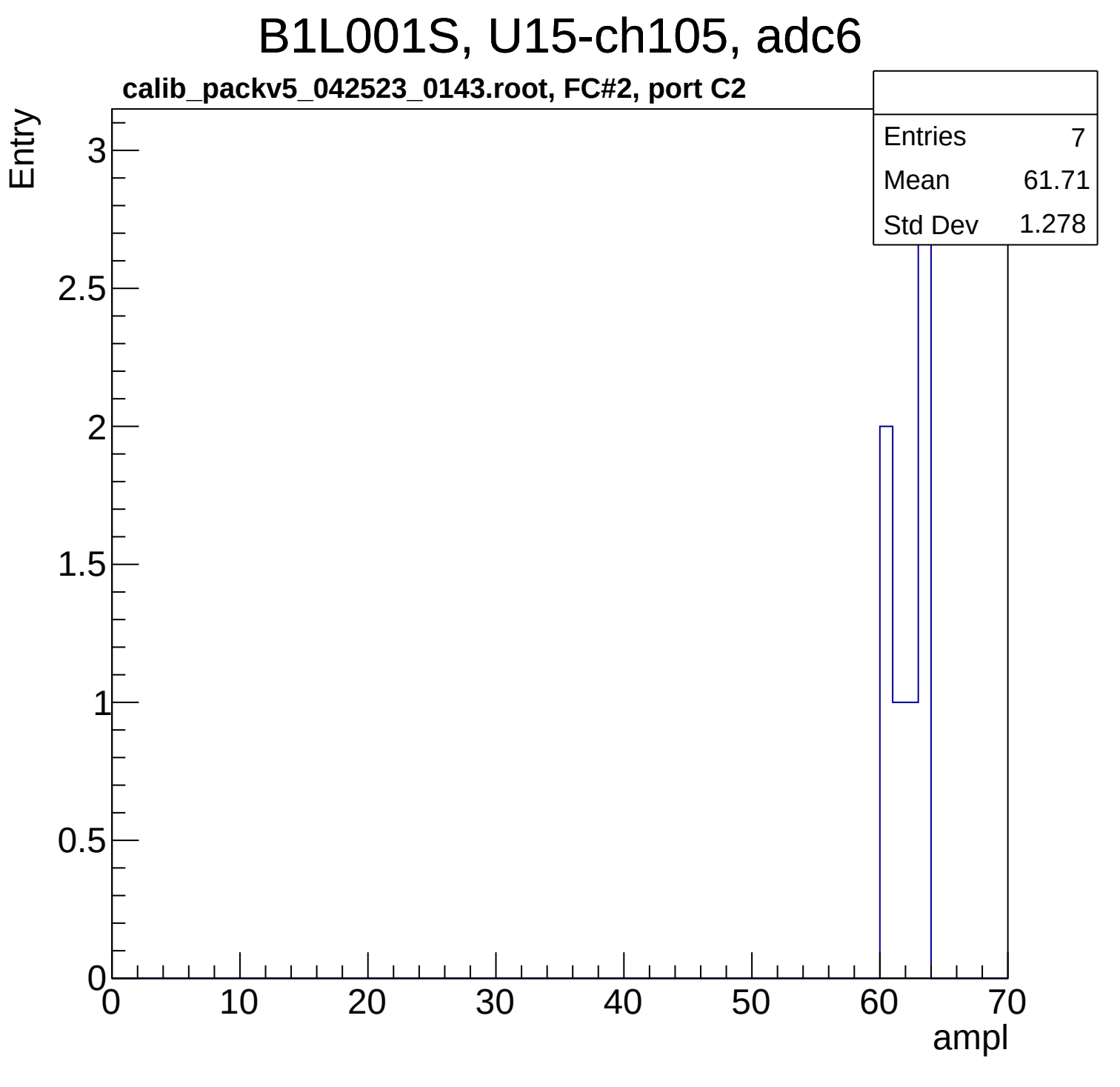
B1L001S, U15-ch105, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 61.71
Std Dev 1.278
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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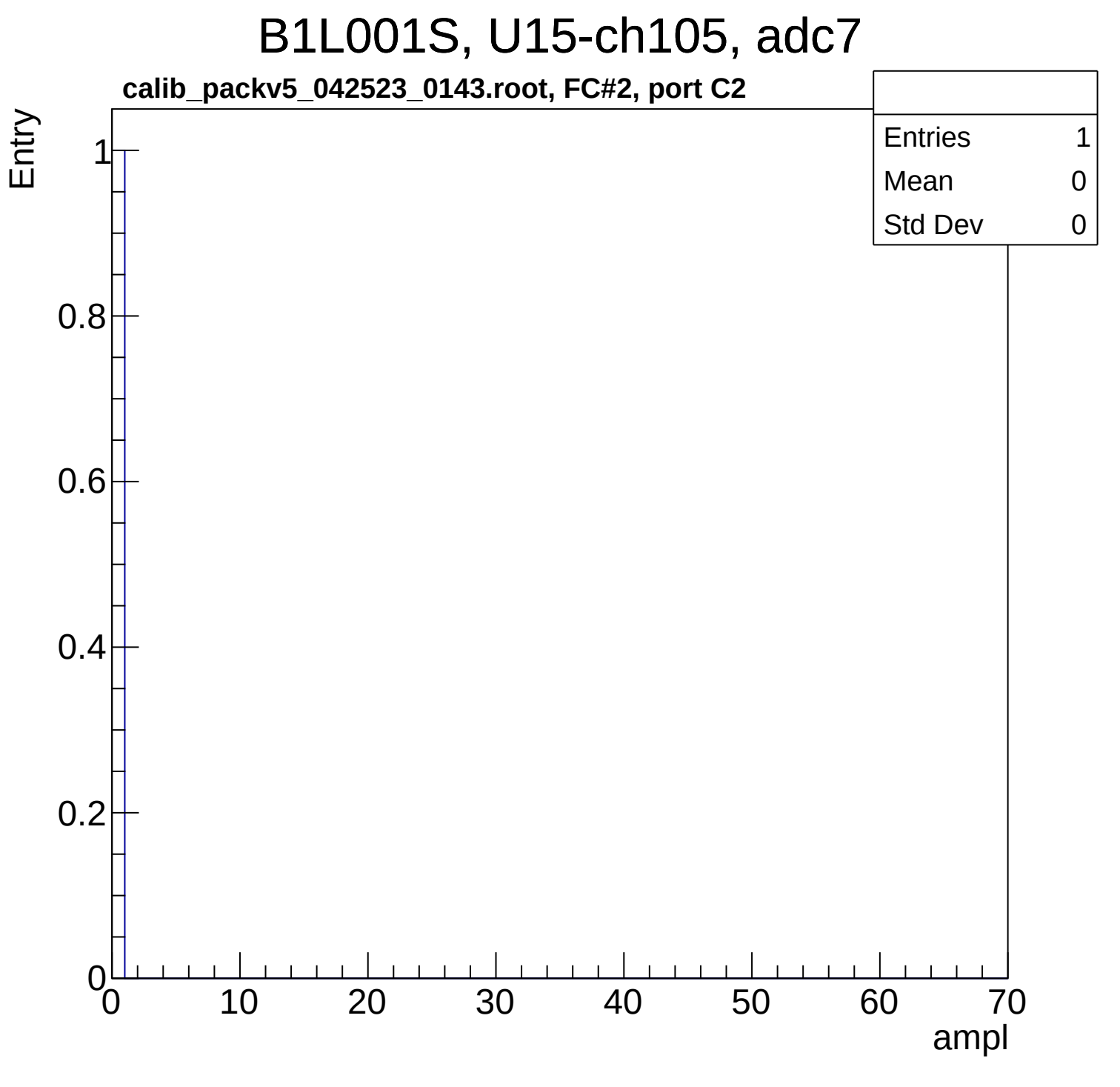
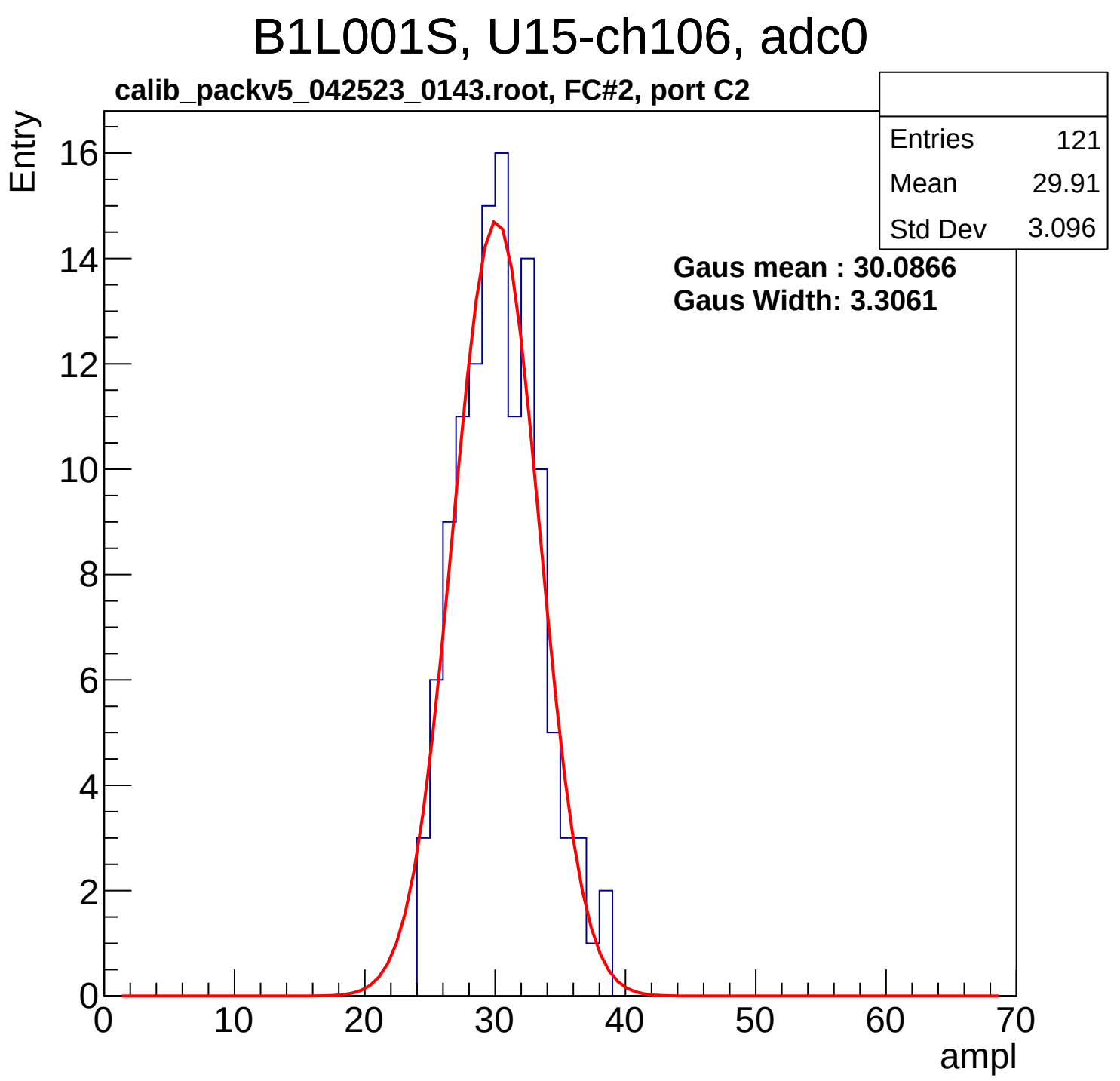

B1L001S, U15-ch105, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch106, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 29.91
Std Dev 3.096
Gaus mean : 30.0866
Gaus Width: 3.3061
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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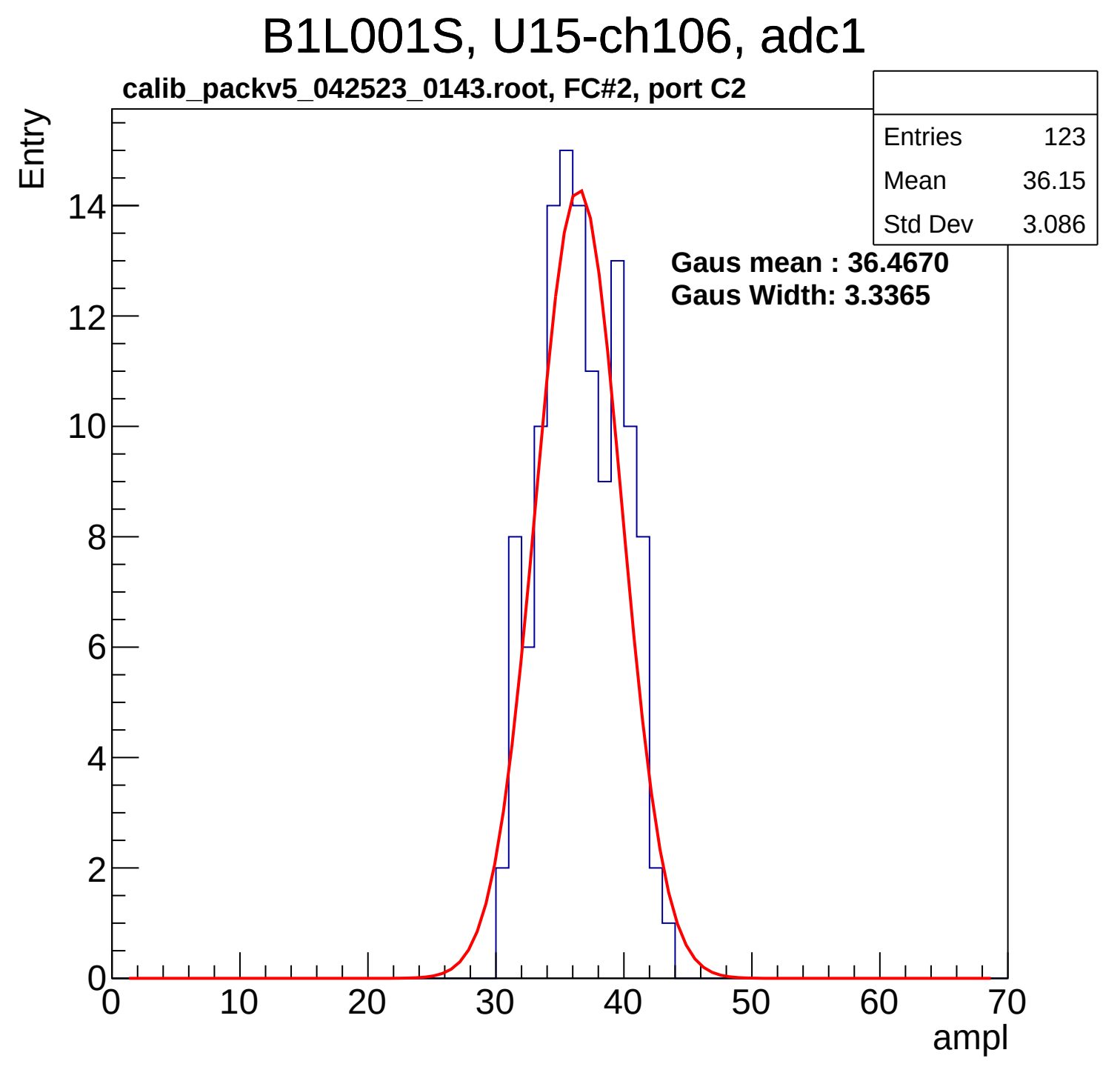
B1L001S, U15-ch106, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 36.15
Std Dev 3.086
Gaus mean : 36.4670
Gaus Width: 3.3365
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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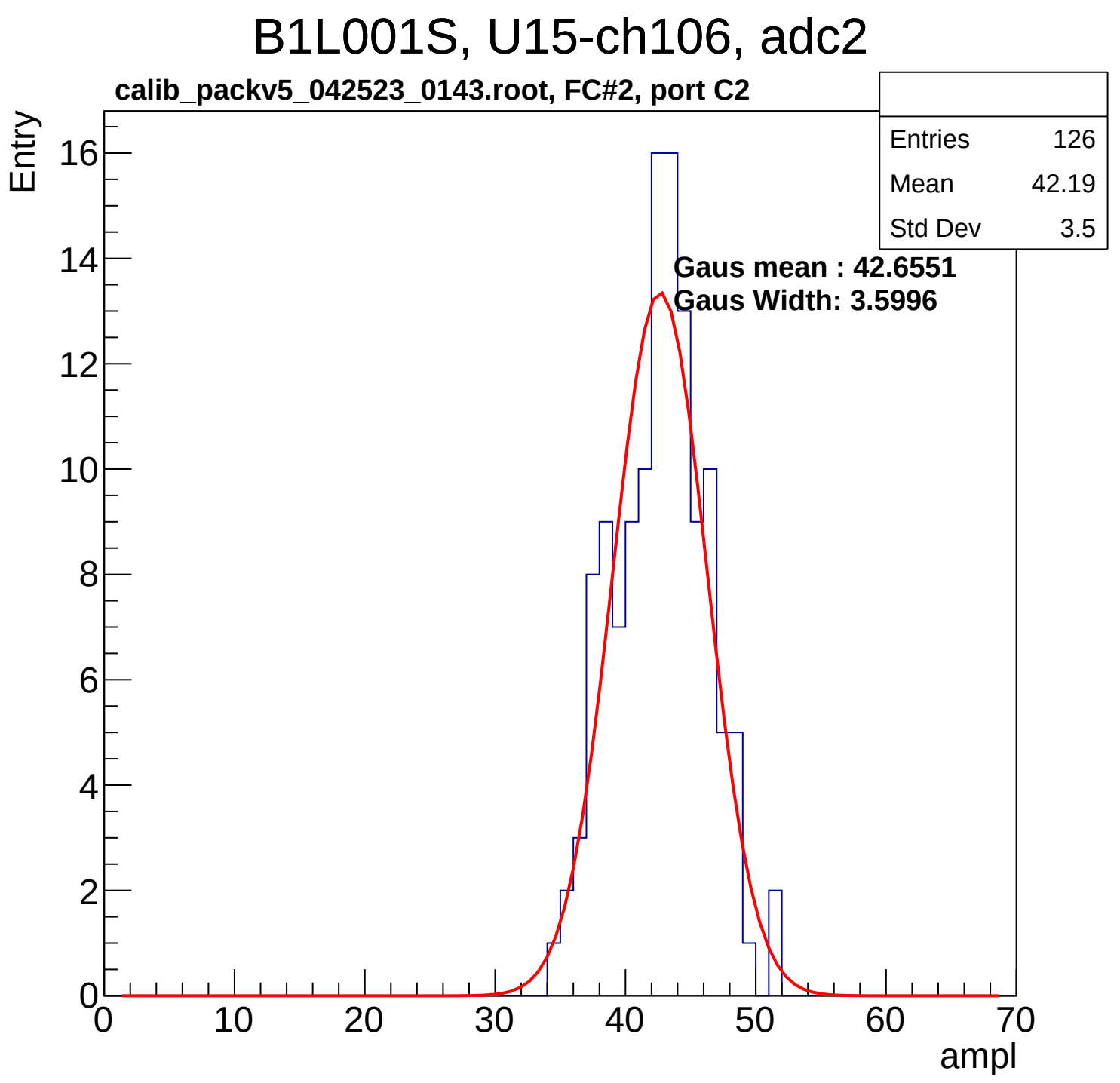
B1L001S, U15-ch106, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 42.19
Std Dev 3.5
Gaus mean : 42.6551
Gaus Width: 3.5996
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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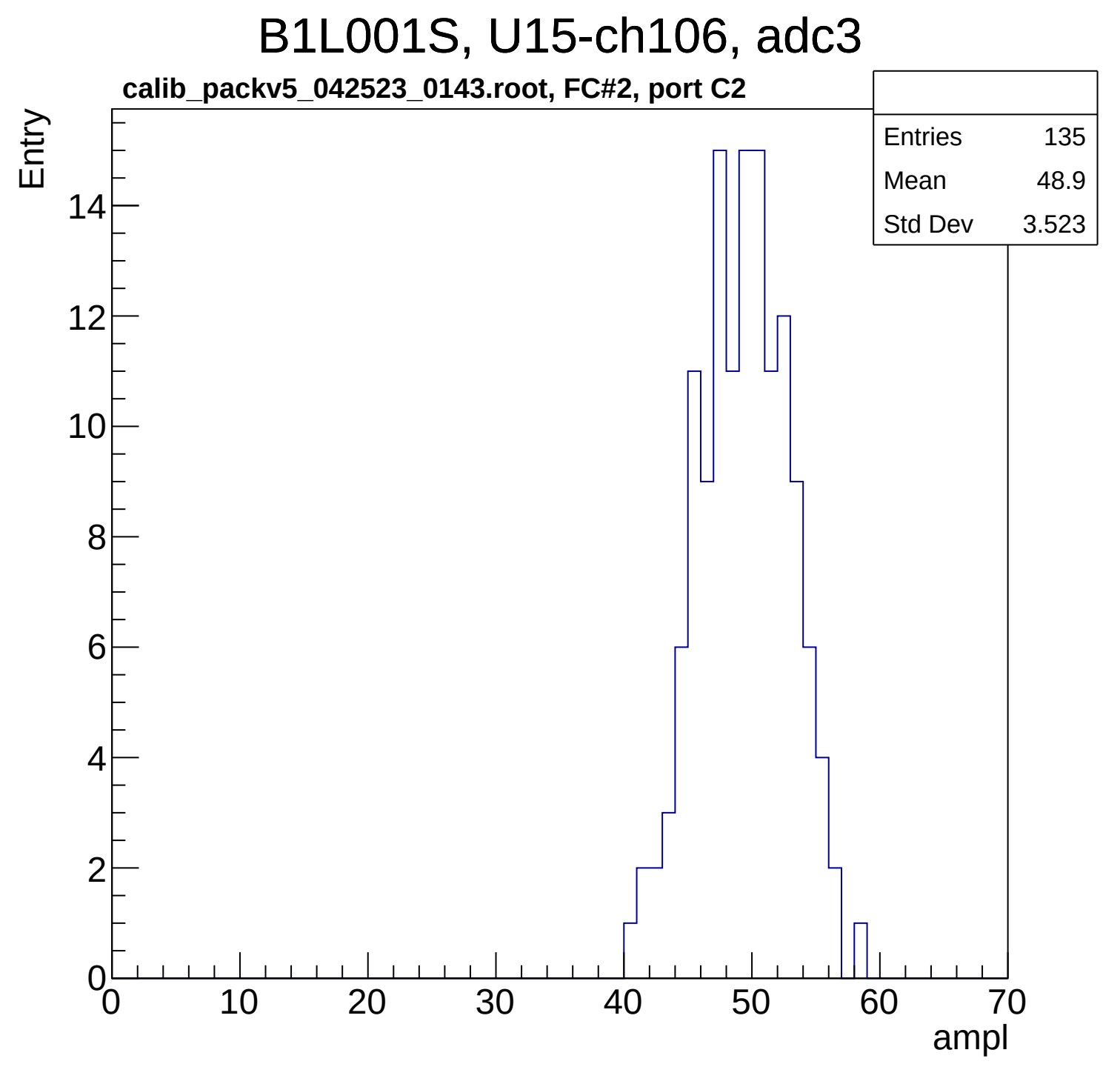
B1L001S, U15-ch106, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 48.9
Std Dev 3.523
Entry
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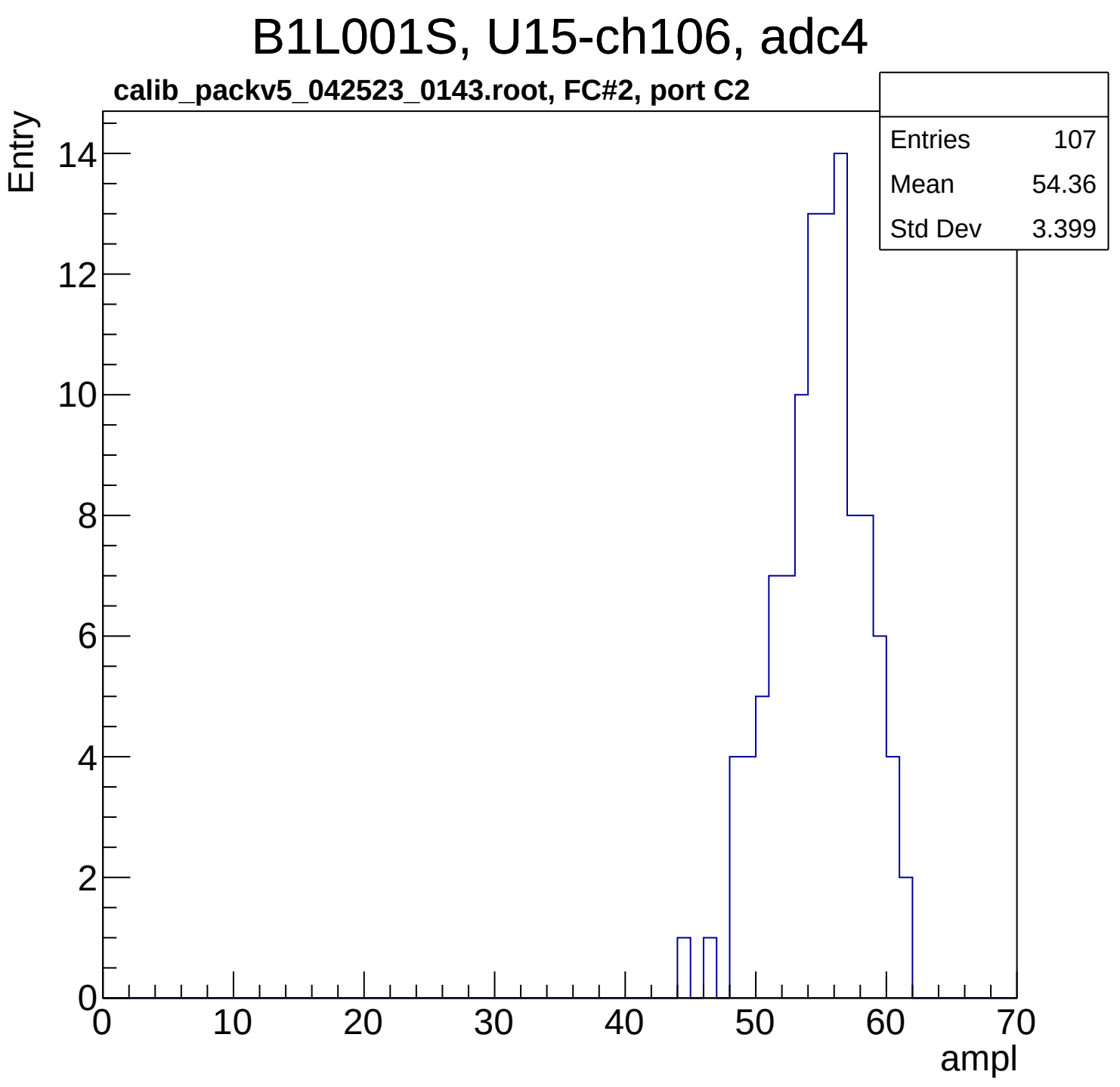
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch106, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 54.36
Std Dev 3.399
Entry
ampl
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8
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0
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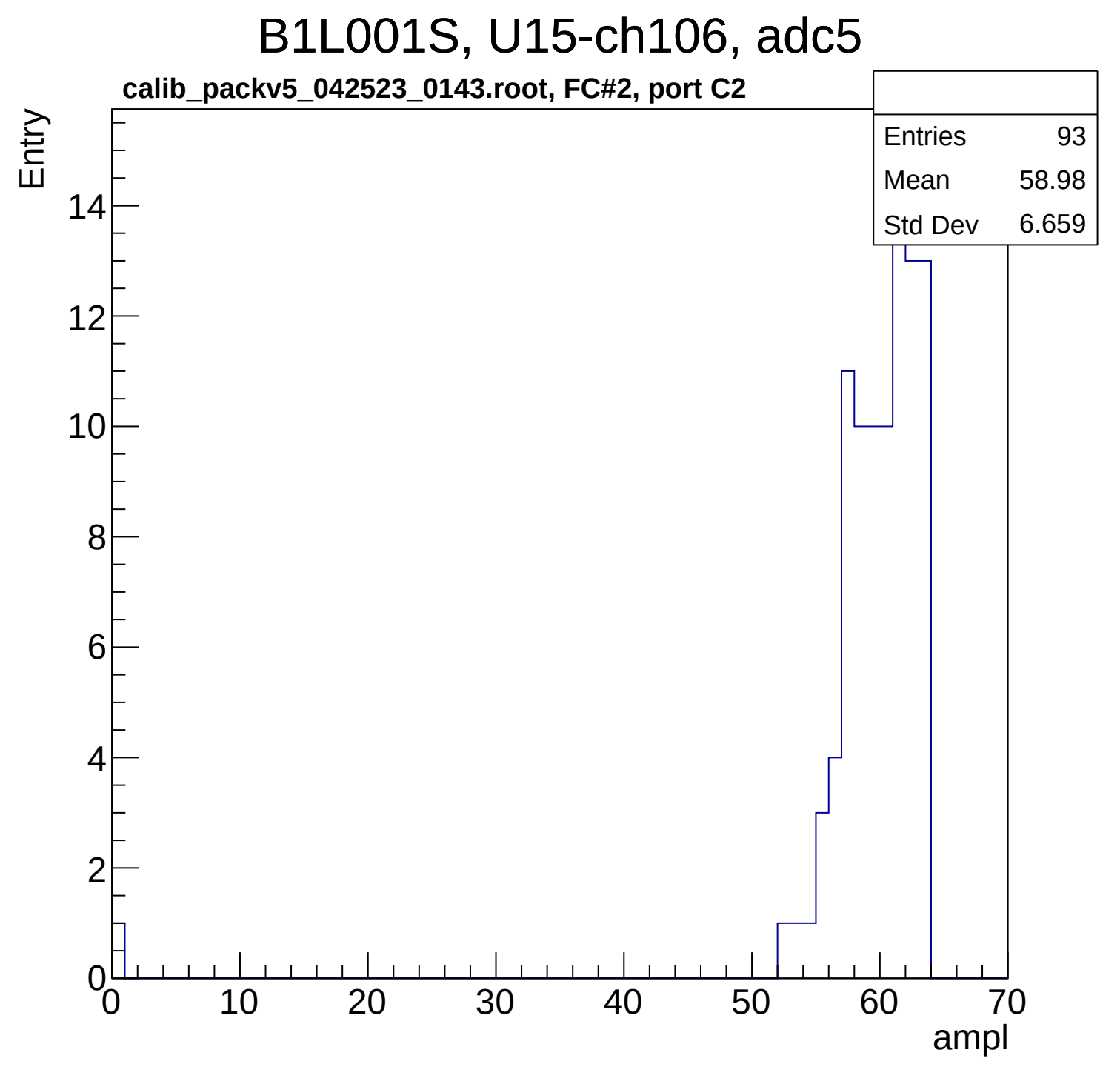
B1L001S, U15-ch106, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 58.98
Std Dev 6.659
Entry
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6
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14
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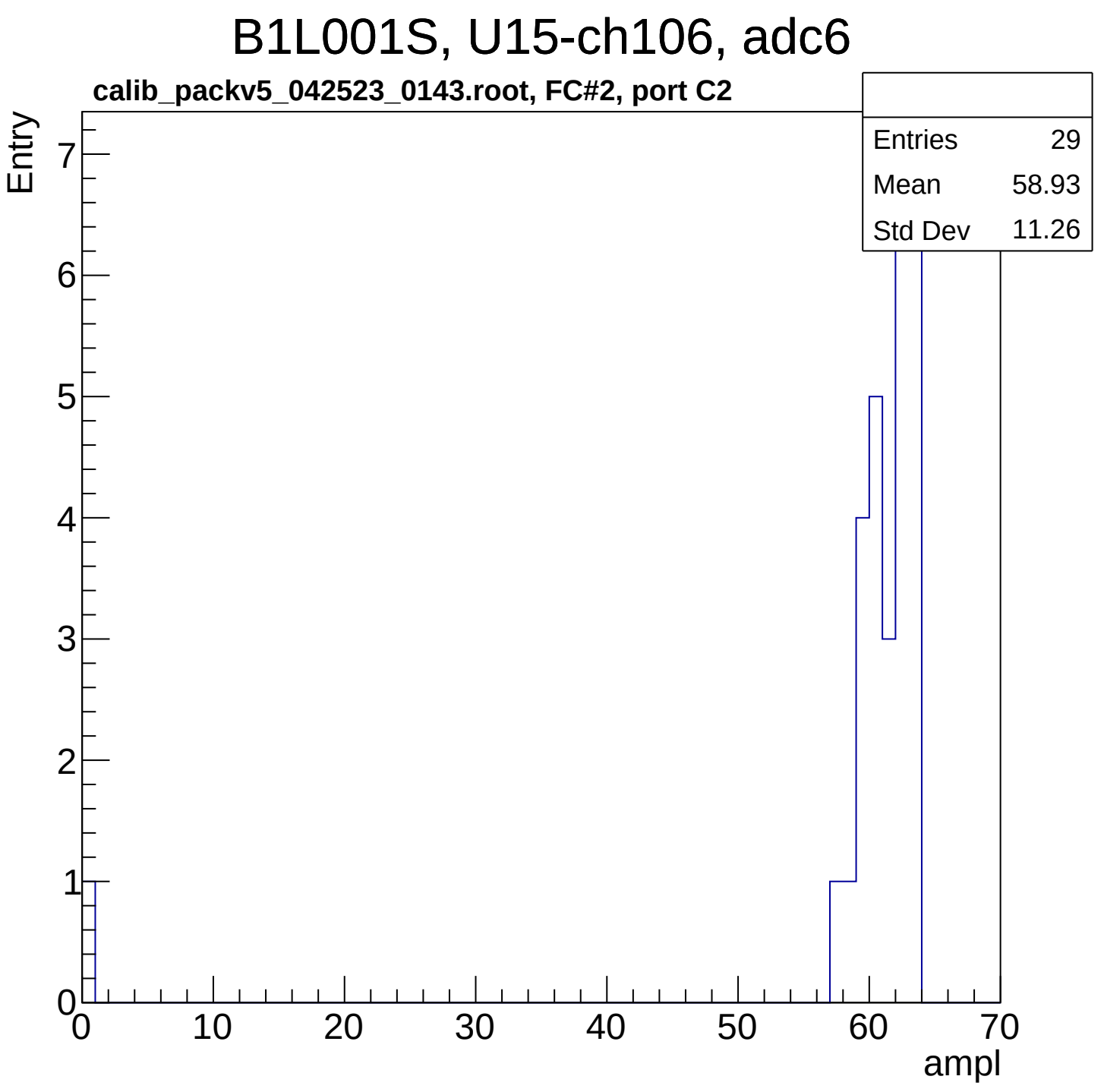
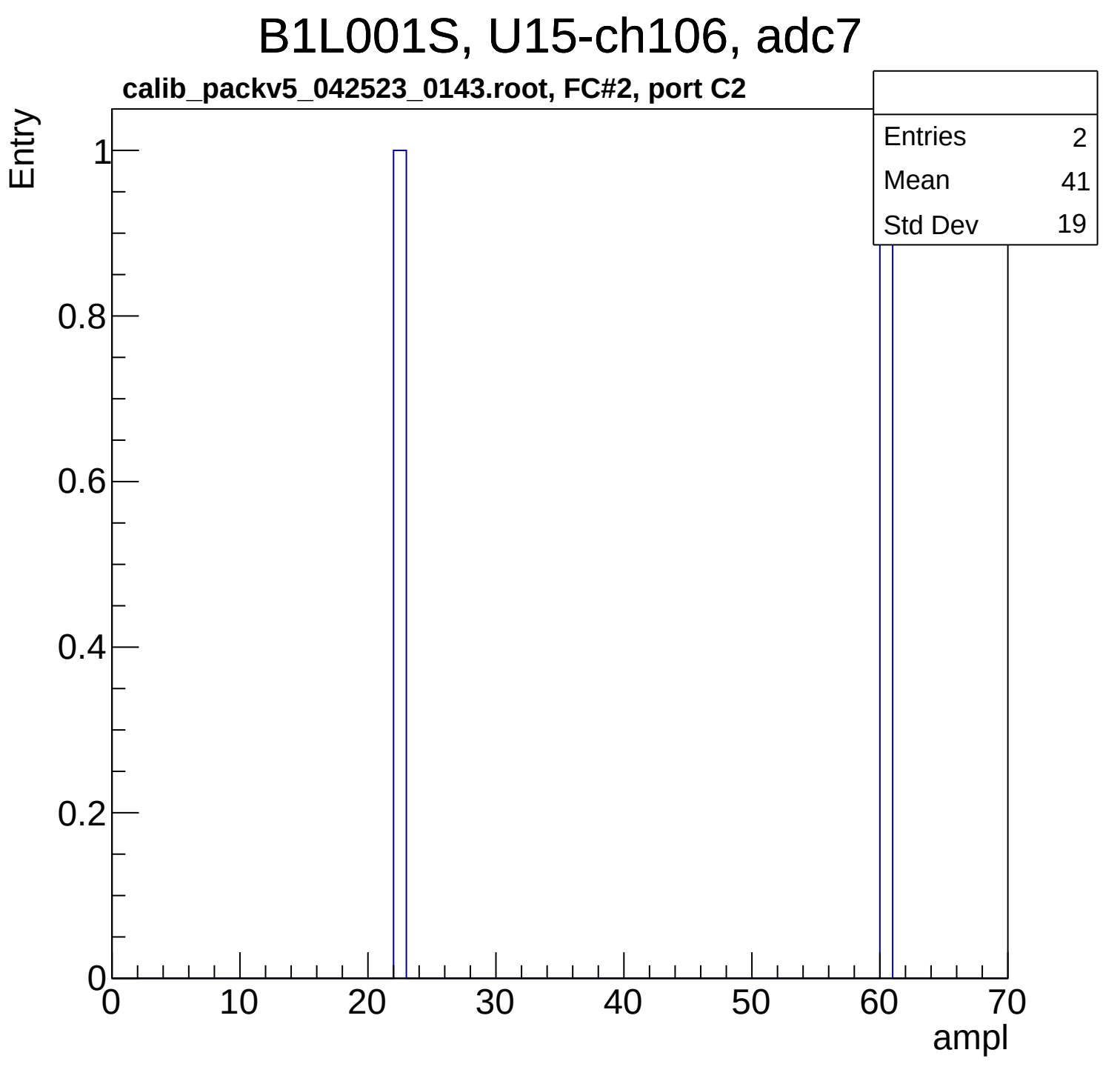
70
ampl


B1L001S, U15-ch106, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 29
Mean 58.93
Std Dev 11.26
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch106, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 41
Std Dev 19
Entry
ampl
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0.4
0.6
0.8
1
0
10
20
30
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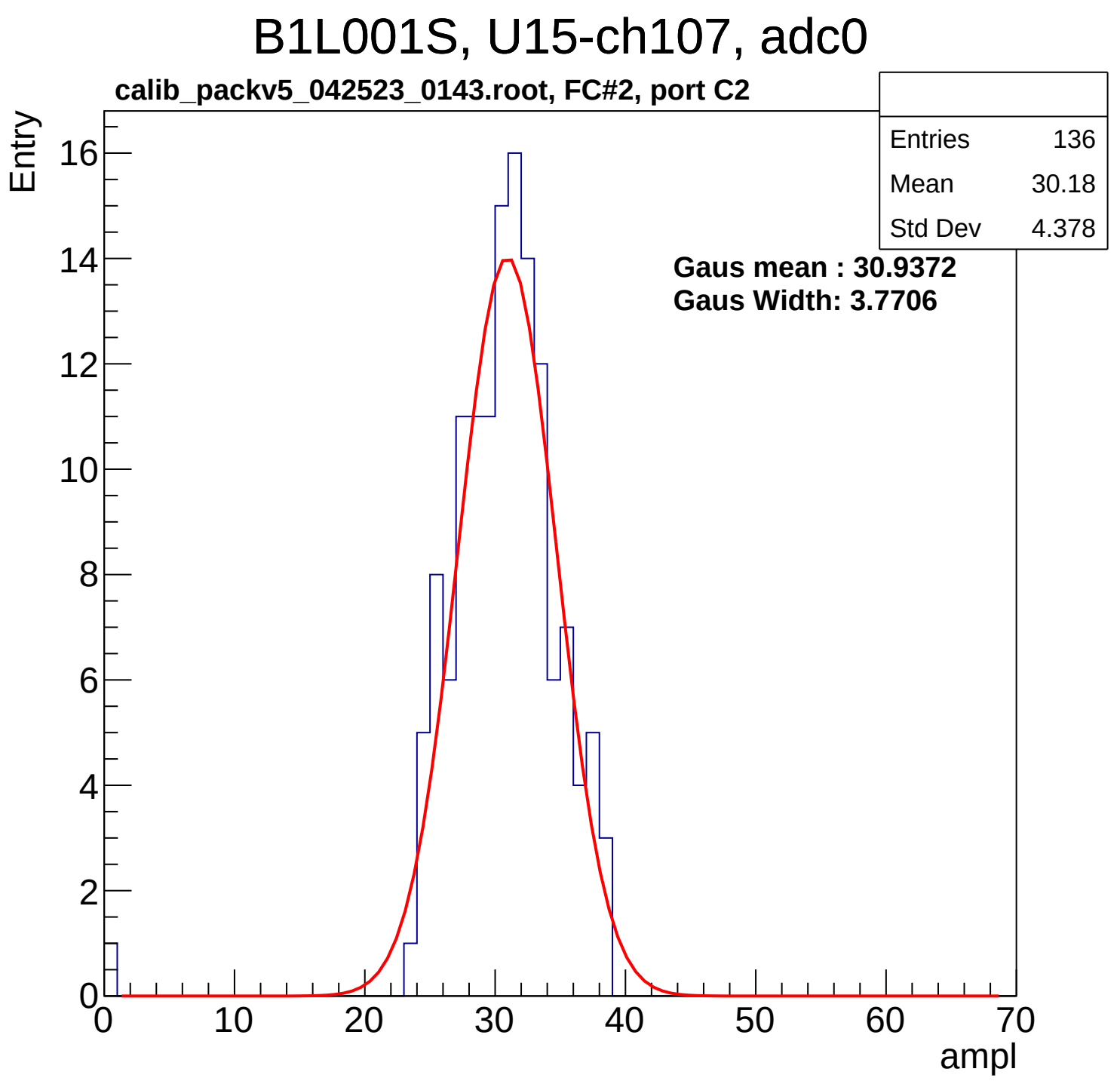
50
60
70


B1L001S, U15-ch107, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 30.18
Std Dev 4.378
Gaus mean : 30.9372
Gaus Width: 3.7706
Entry
ampl
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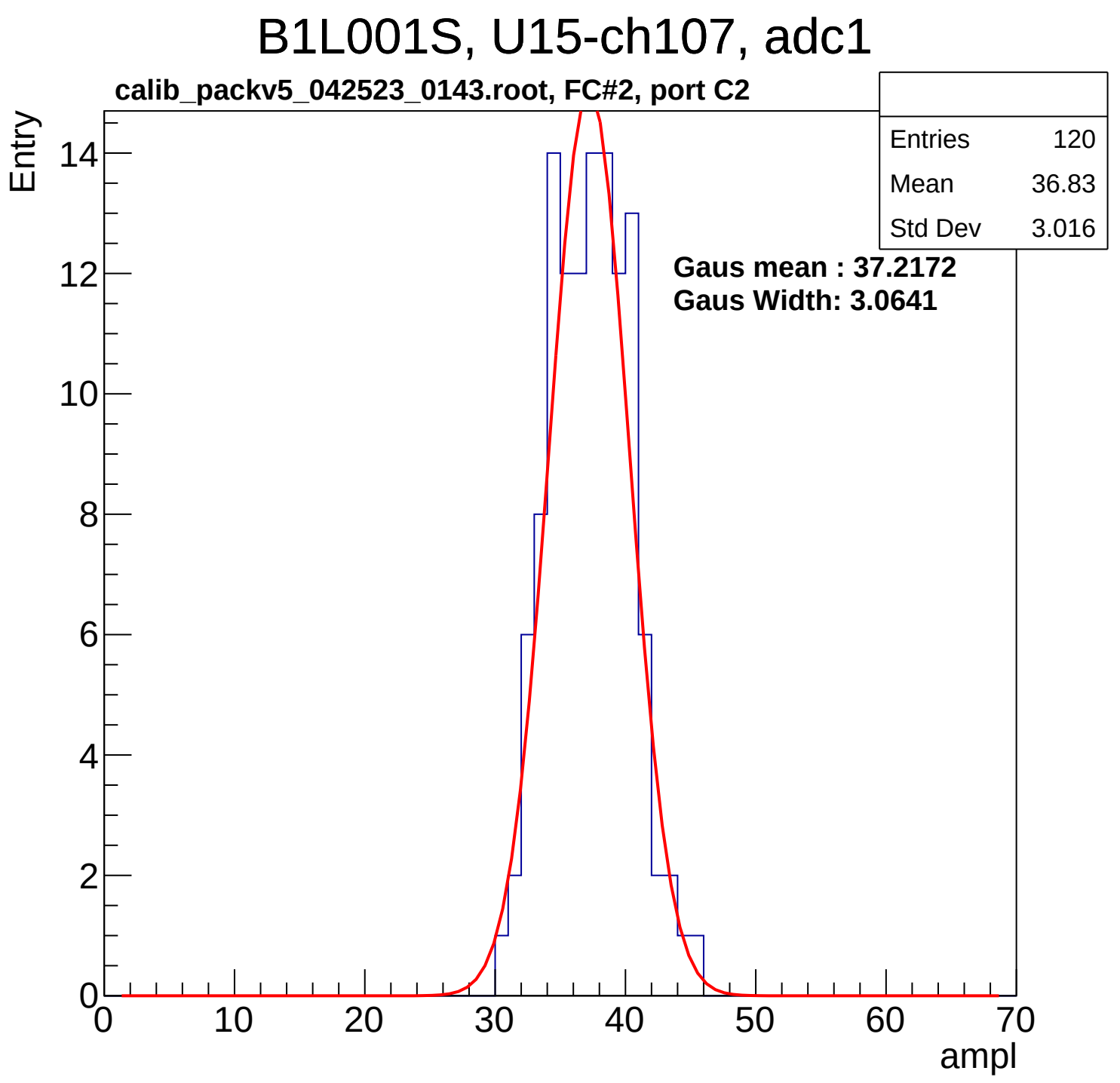
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch107, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 36.83
Std Dev 3.016
Gaus mean : 37.2172
Gaus Width: 3.0641
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
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60
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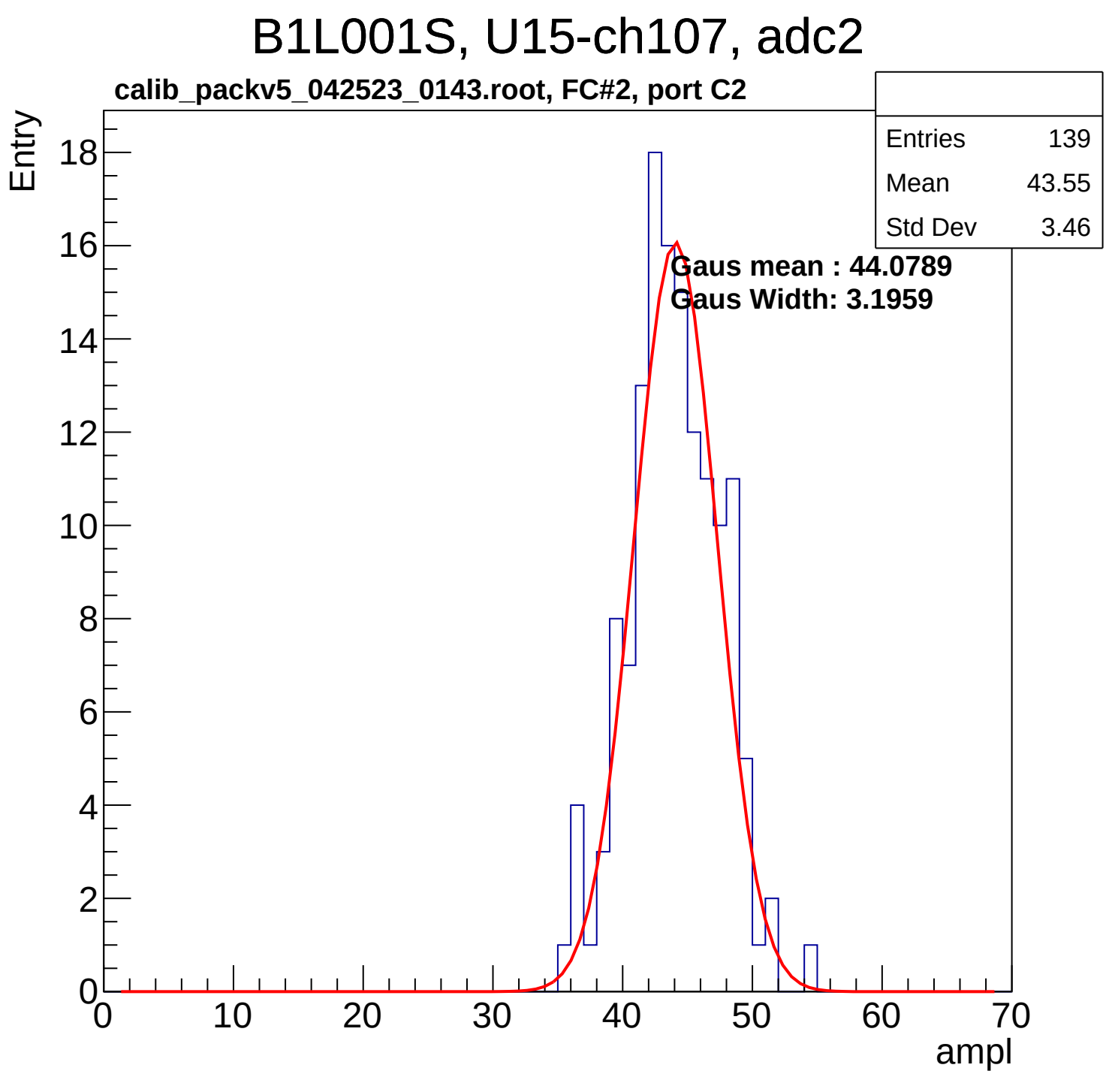
B1L001S, U15-ch107, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 43.55
Std Dev 3.46
Gaus mean : 44.0789
Gaus Width: 3.1959
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
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60
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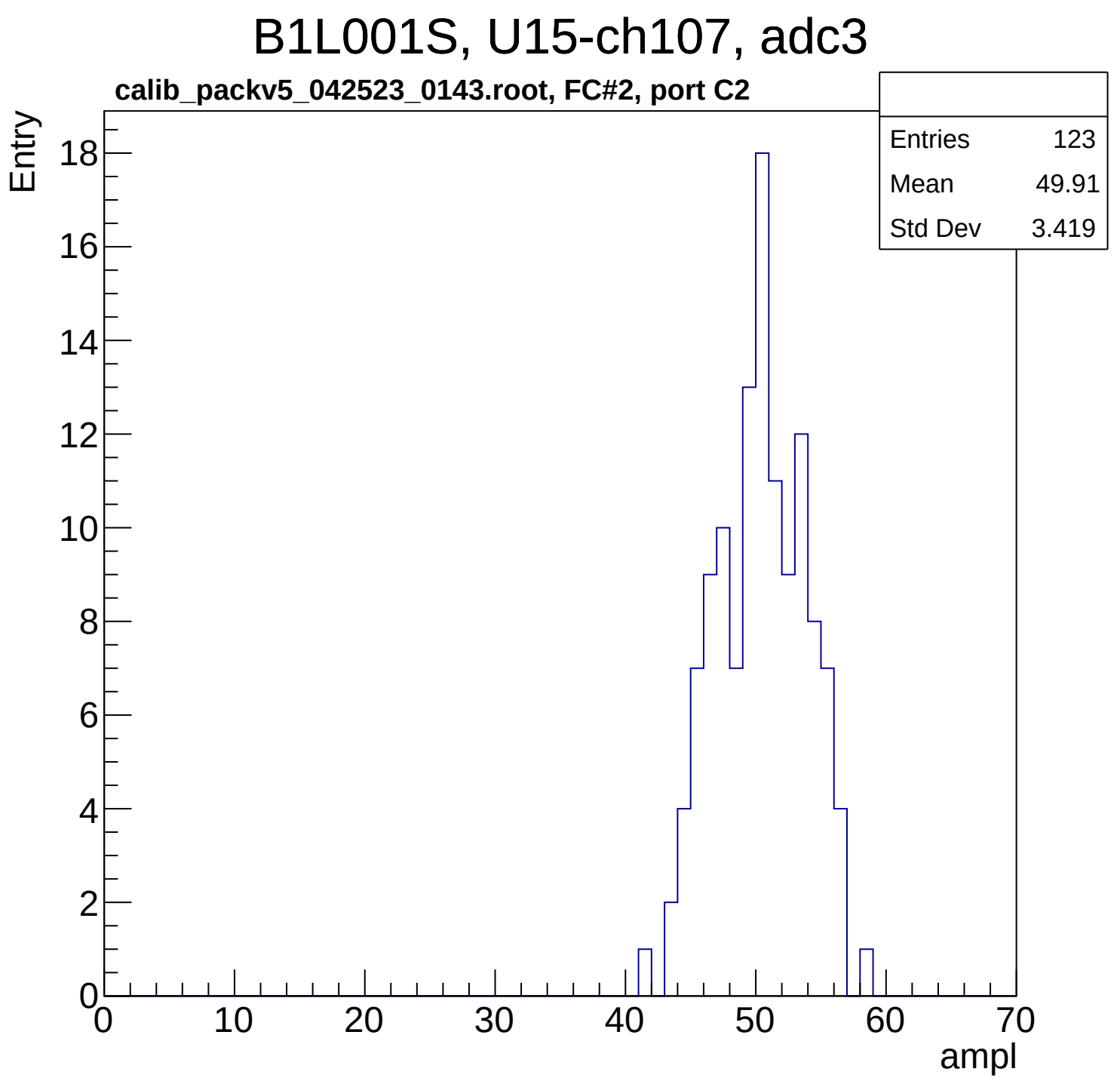
B1L001S, U15-ch107, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 49.91
Std Dev 3.419
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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60
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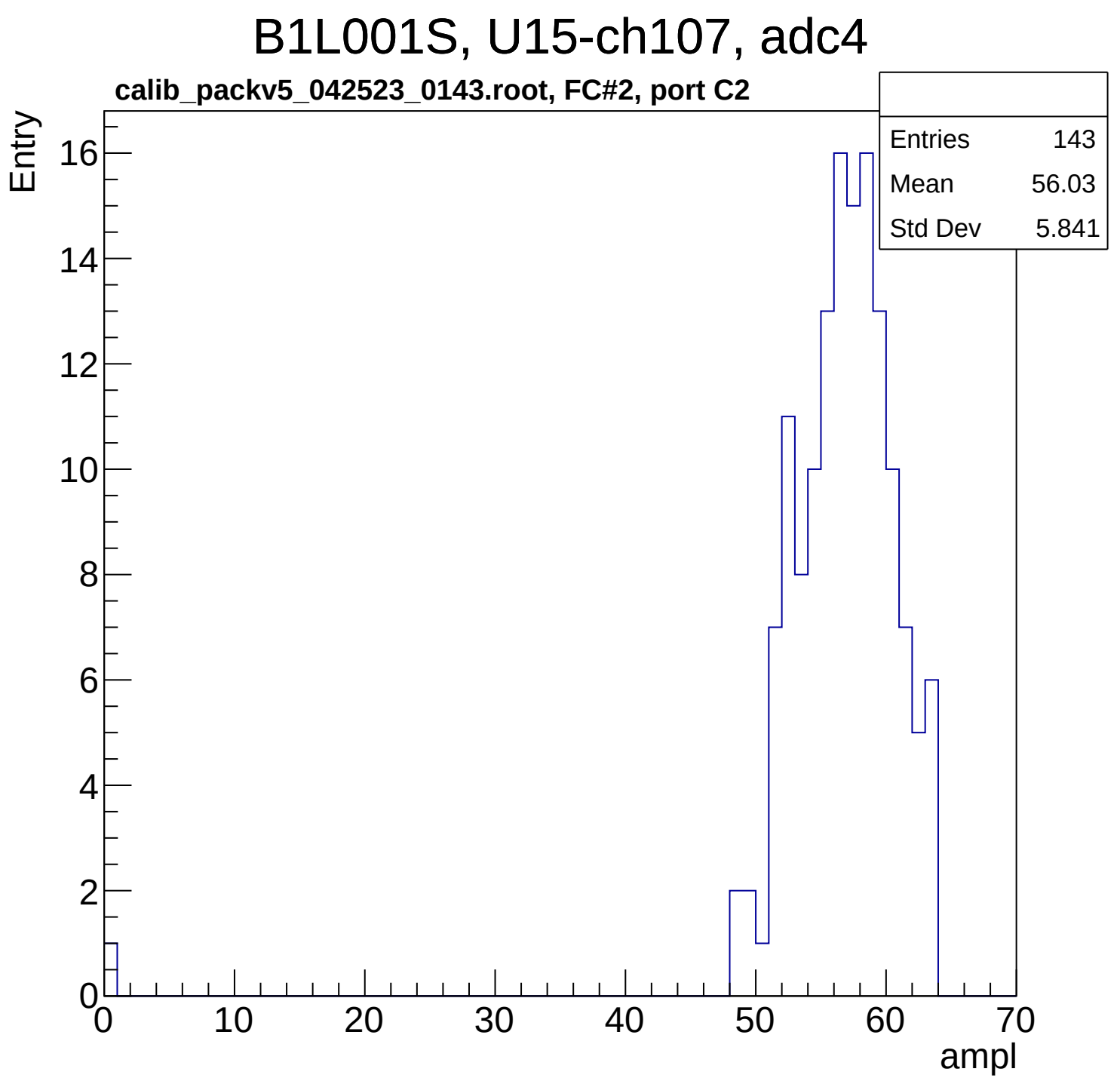
B1L001S, U15-ch107, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 56.03
Std Dev 5.841
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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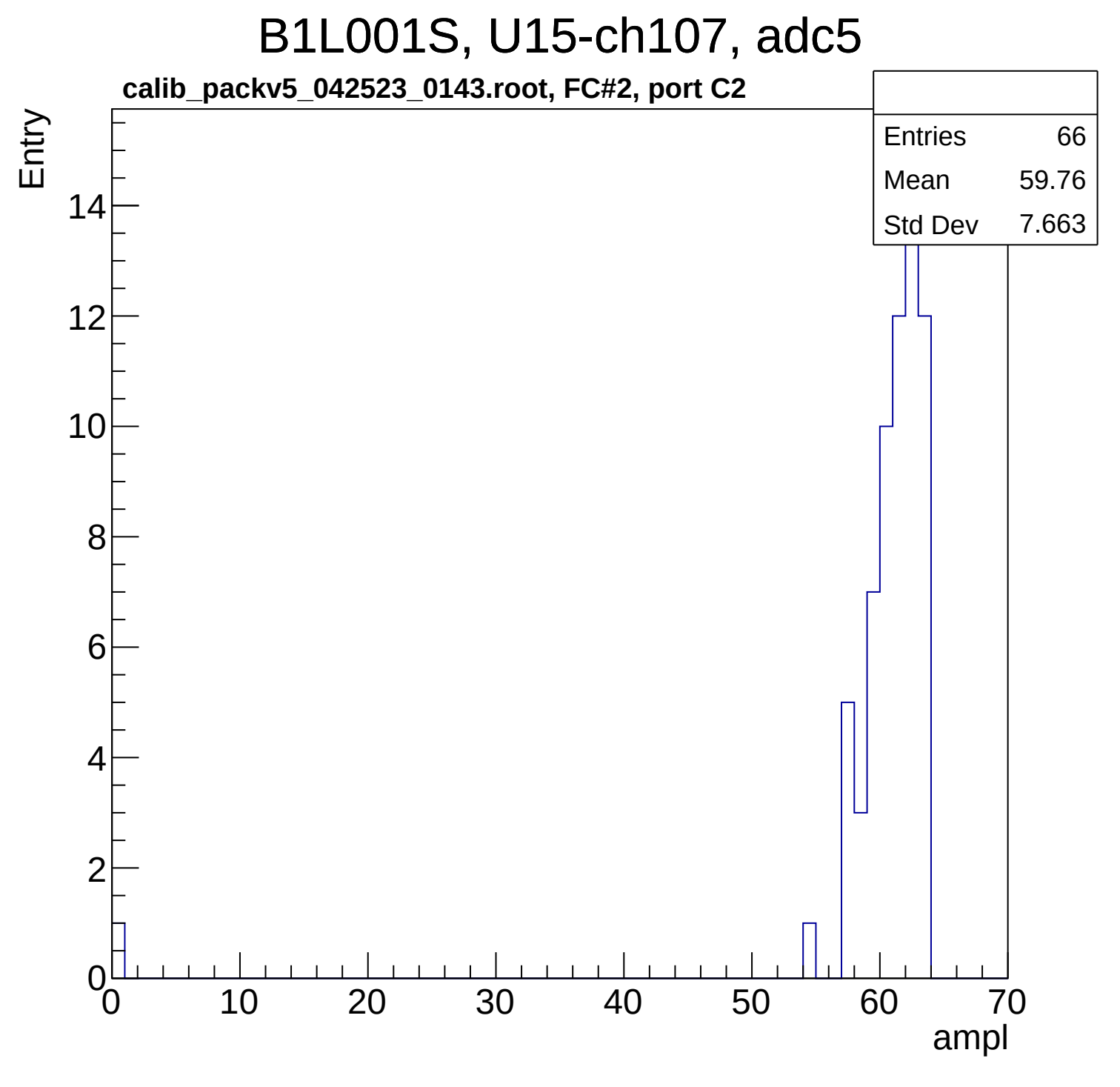
B1L001S, U15-ch107, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 66
Mean 59.76
Std Dev 7.663
Entry
ampl
0
2
4
6
8
10
12
14
0
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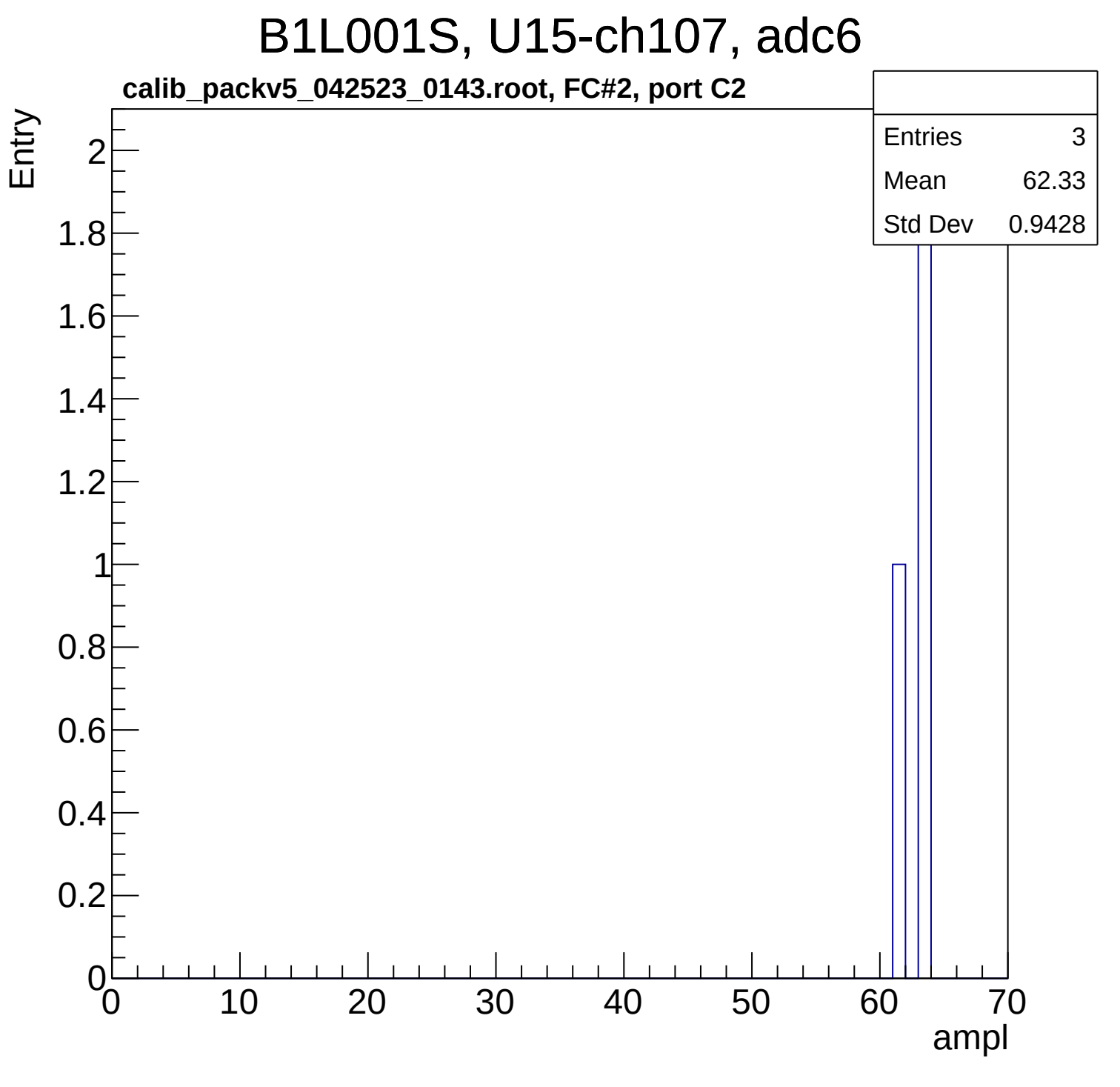
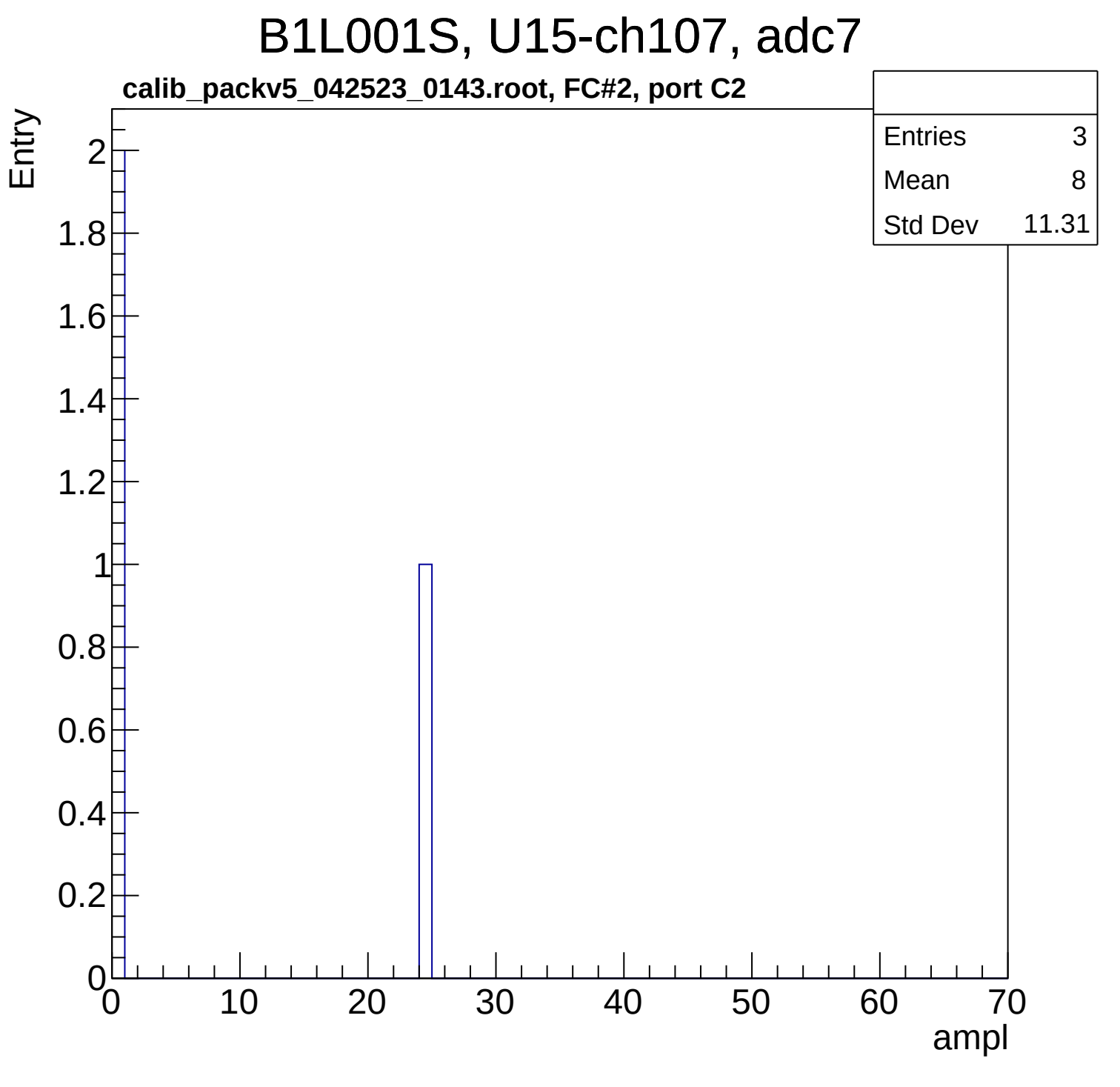
40
50
60
70


B1L001S, U15-ch107, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch107, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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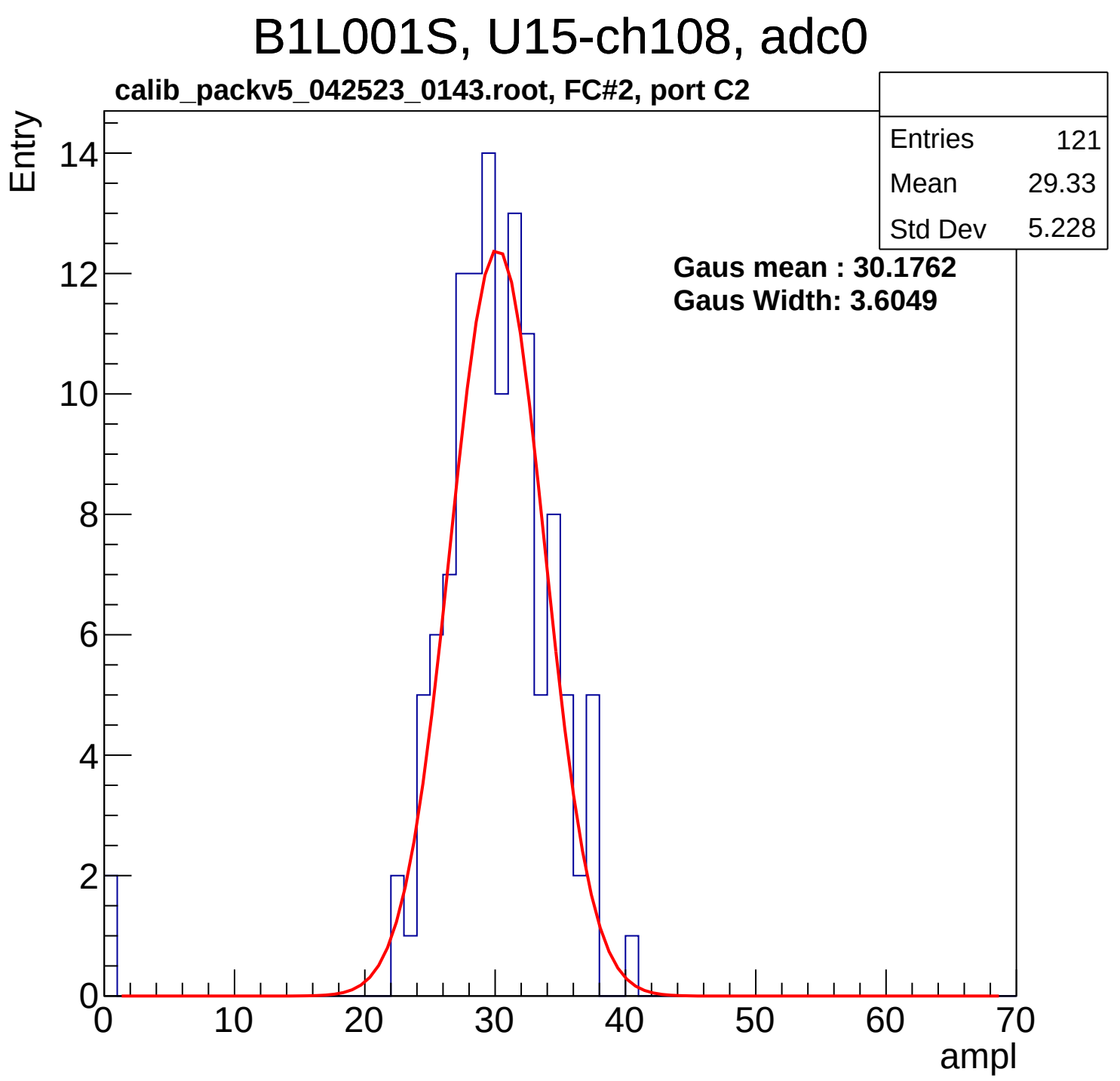
B1L001S, U15-ch108, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 29.33
Std Dev 5.228
Gaus mean : 30.1762
Gaus Width: 3.6049
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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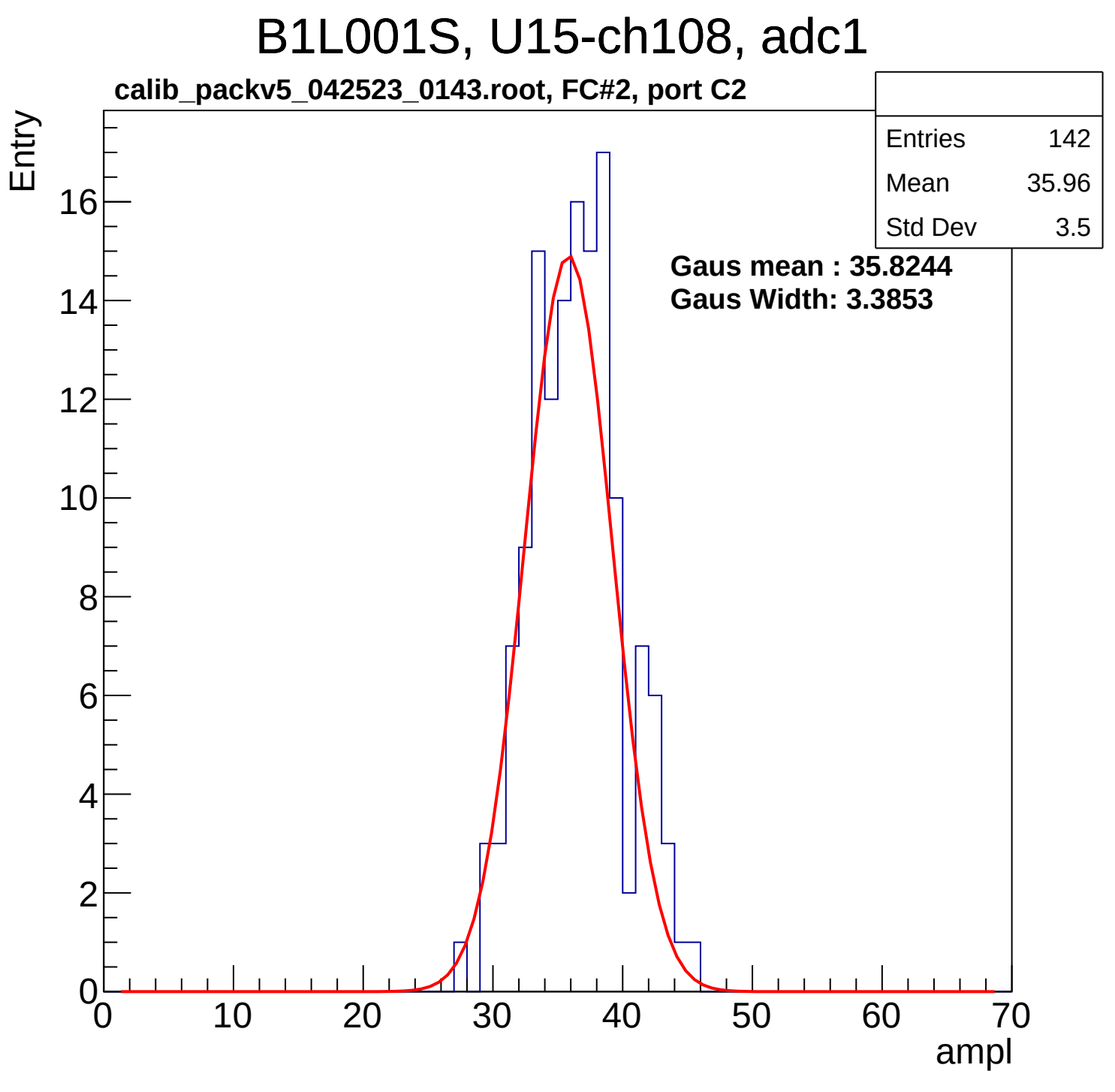
B1L001S, U15-ch108, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 35.96
Std Dev 3.5
Gaus mean : 35.8244
Gaus Width: 3.3853
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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60
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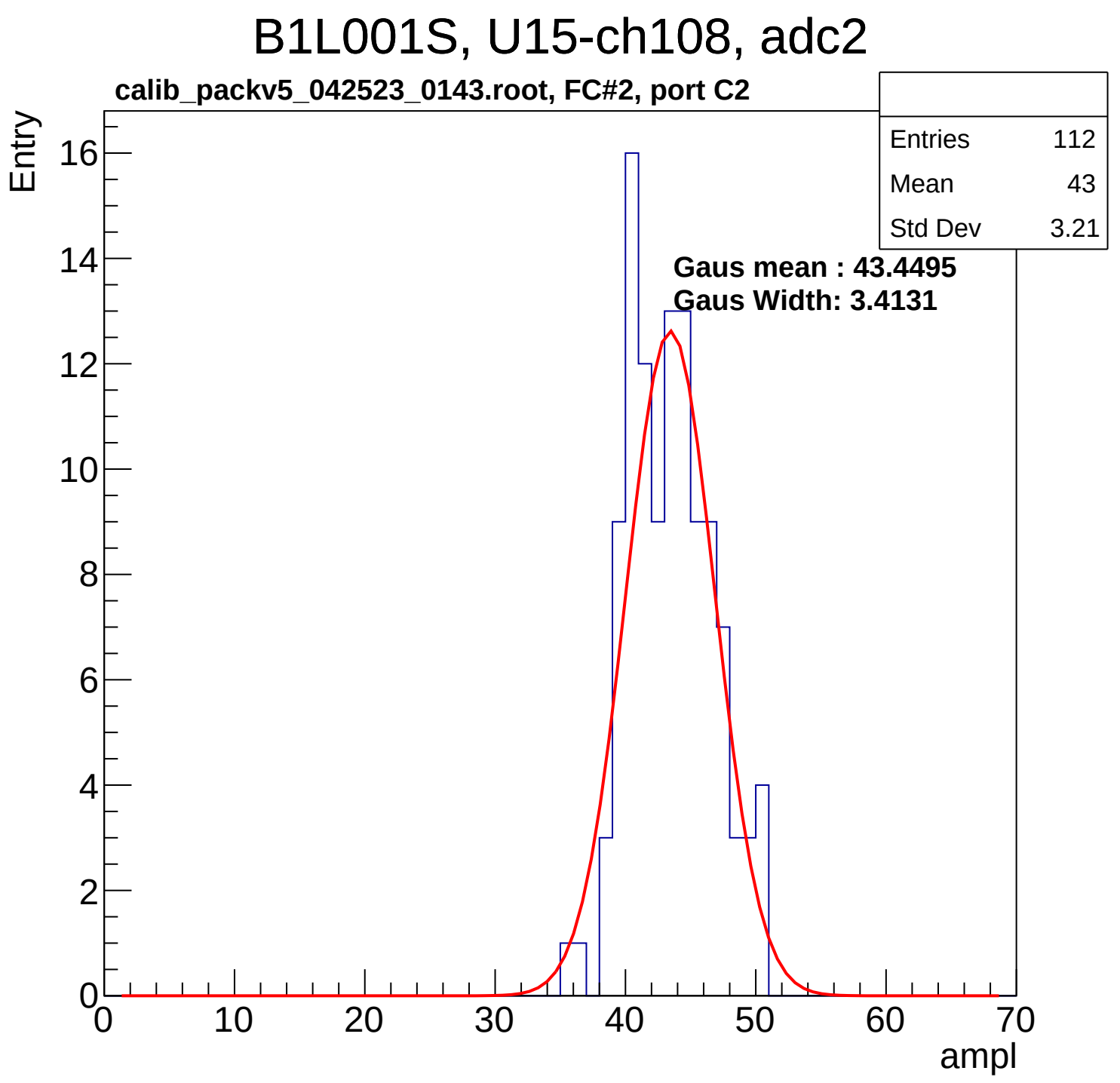
B1L001S, U15-ch108, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43
Std Dev 3.21
Gaus mean : 43.4495
Gaus Width: 3.4131
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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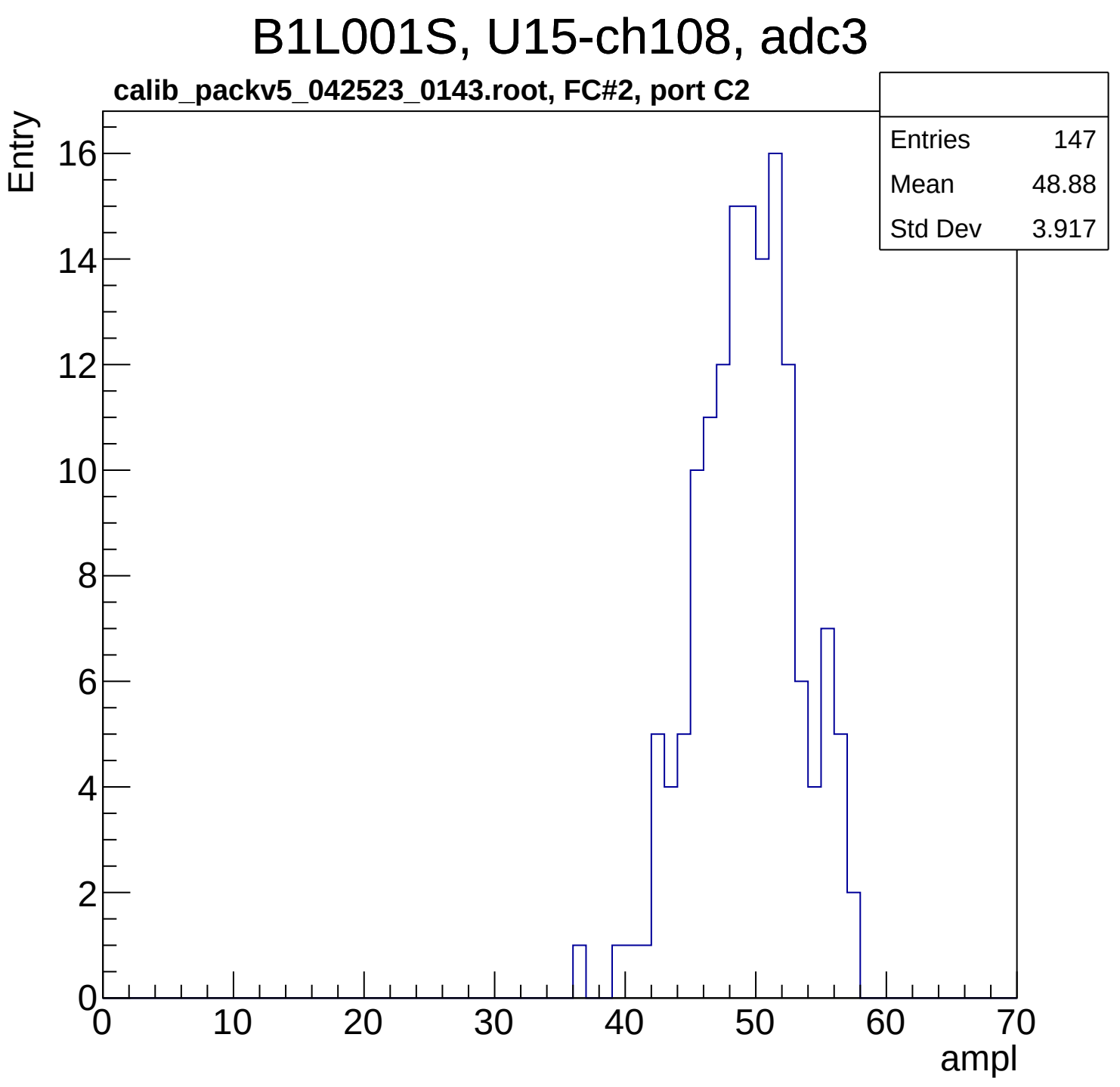
B1L001S, U15-ch108, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 48.88
Std Dev 3.917
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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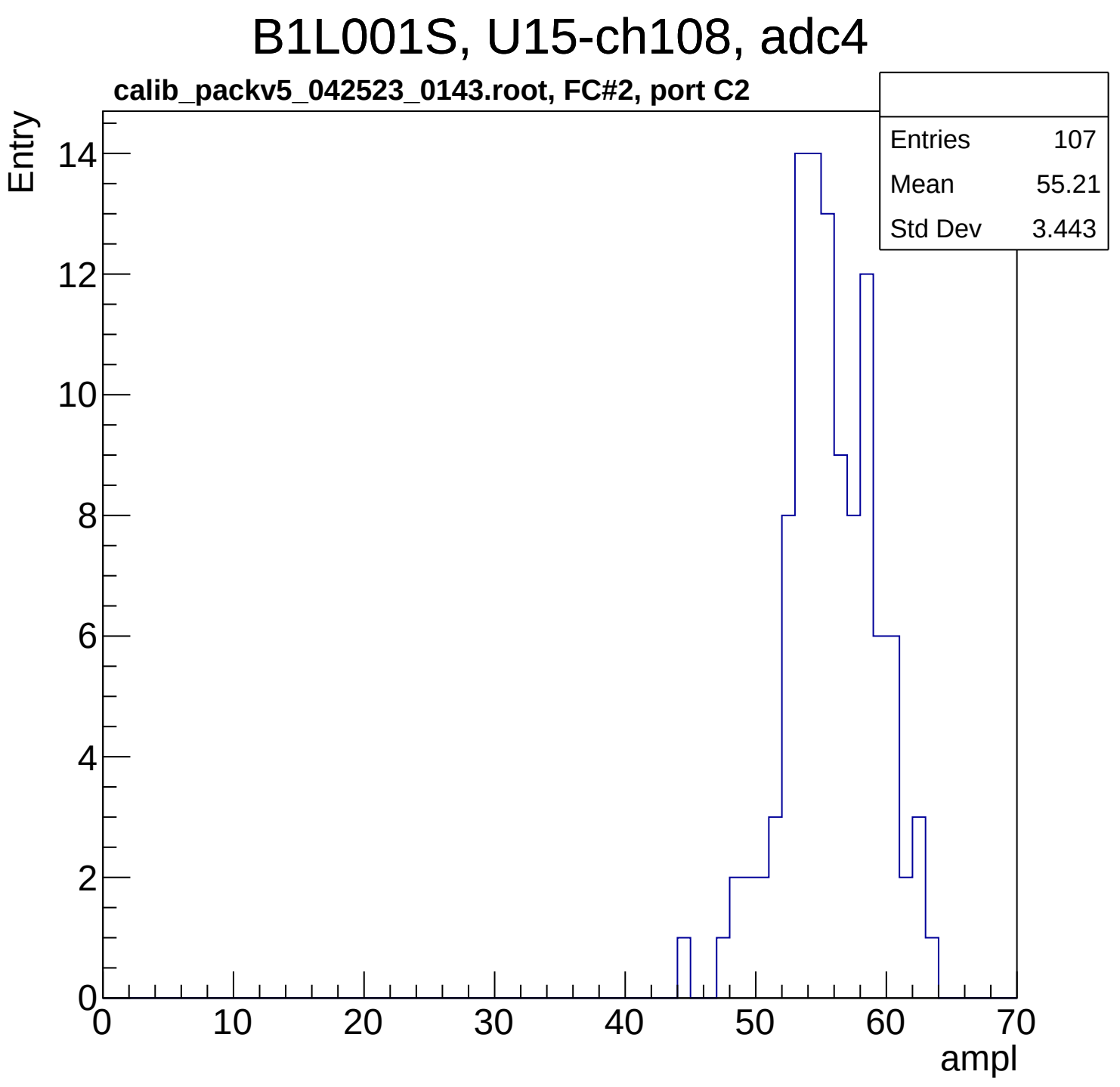
B1L001S, U15-ch108, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 55.21
Std Dev 3.443
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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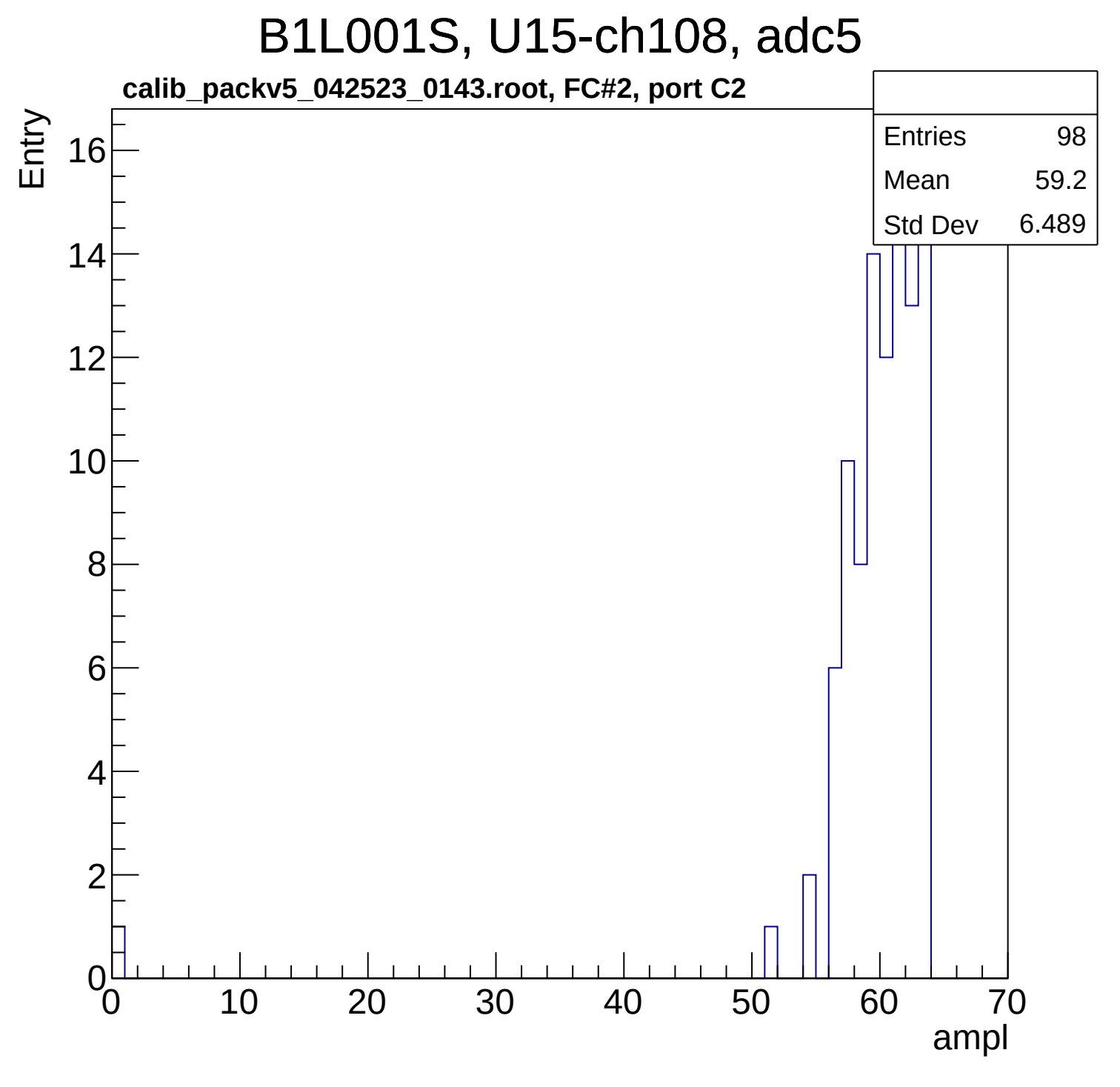
B1L001S, U15-ch108, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 59.2
Std Dev 6.489
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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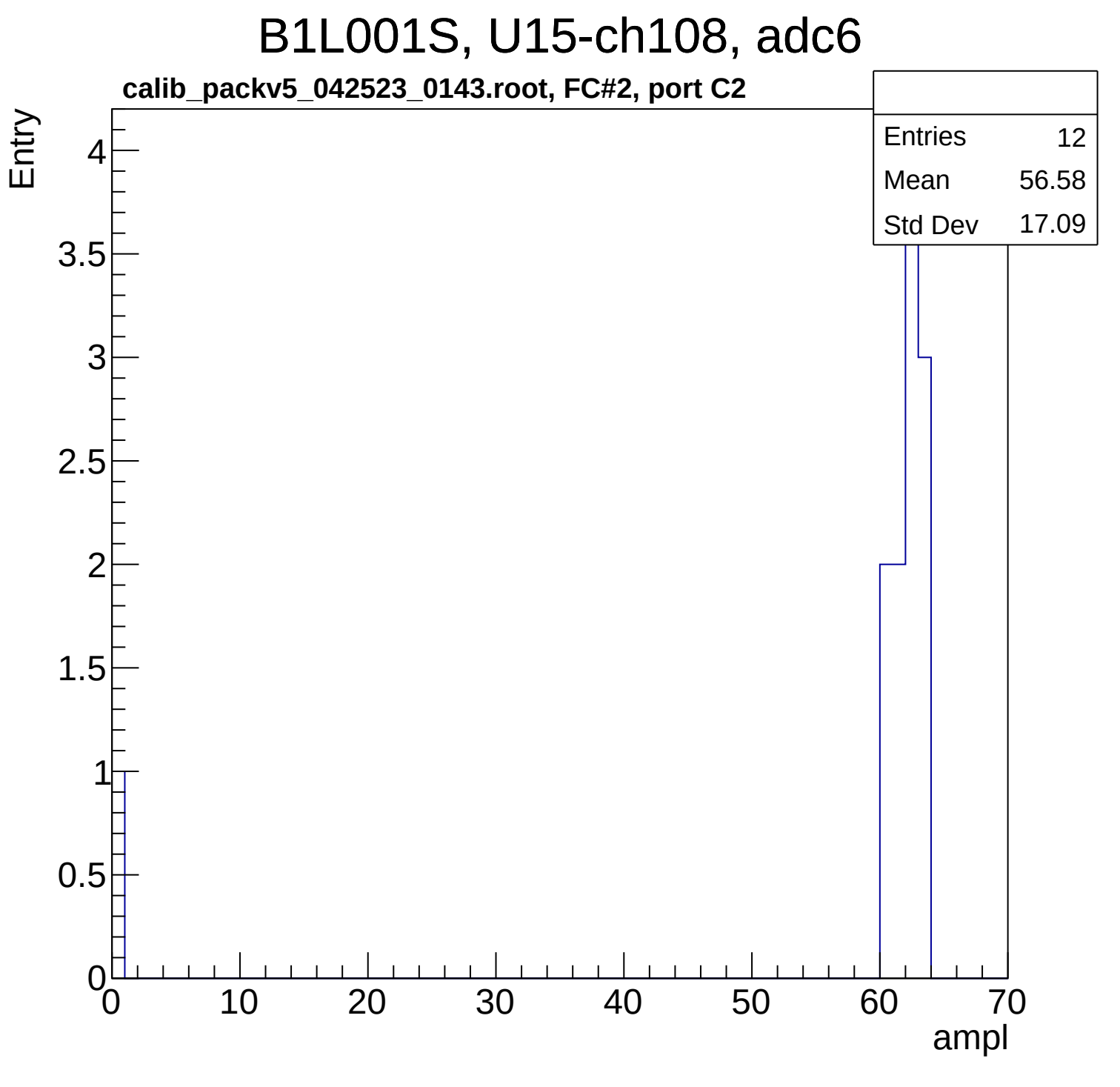
B1L001S, U15-ch108, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 56.58
Std Dev 17.09
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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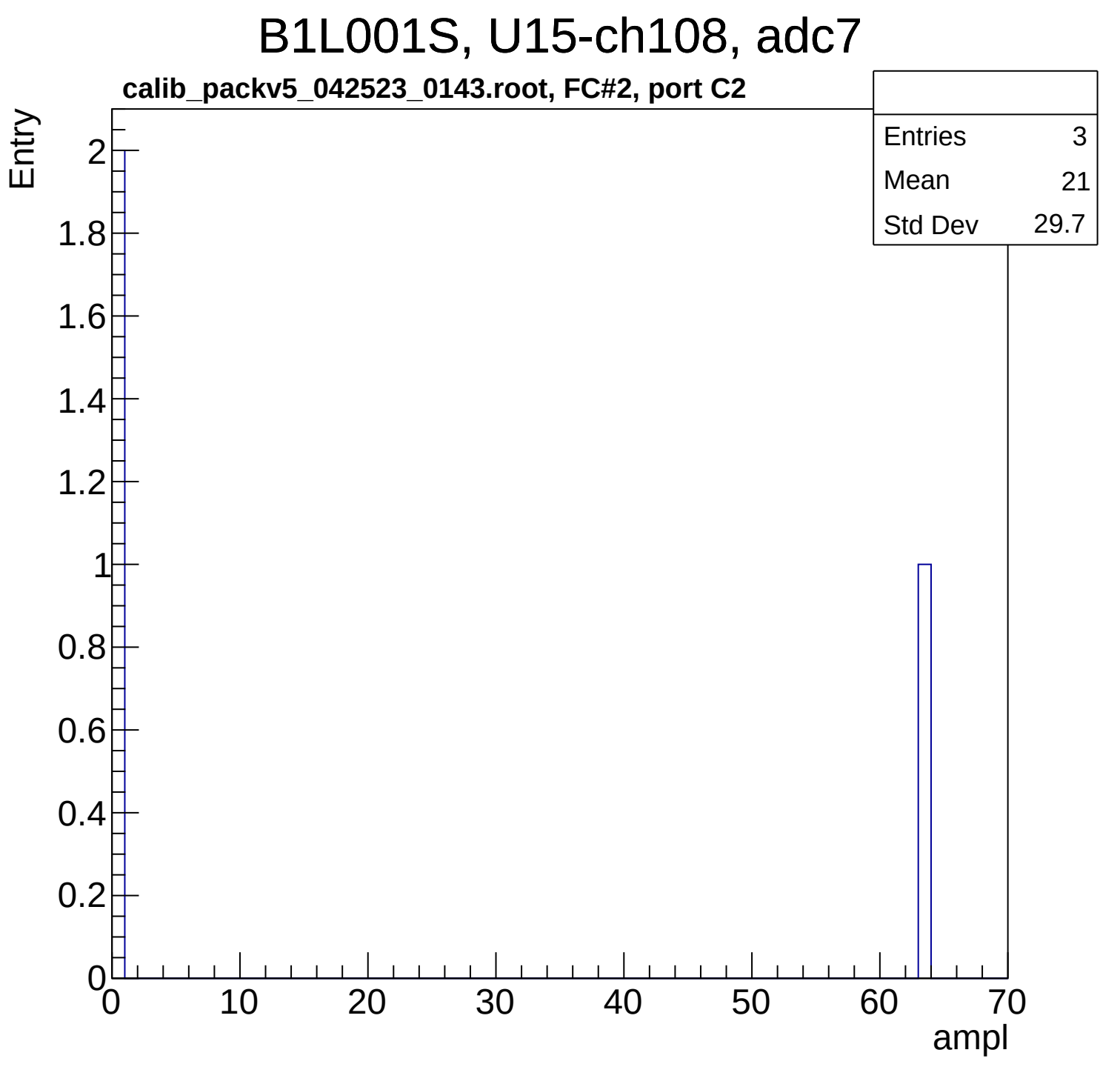
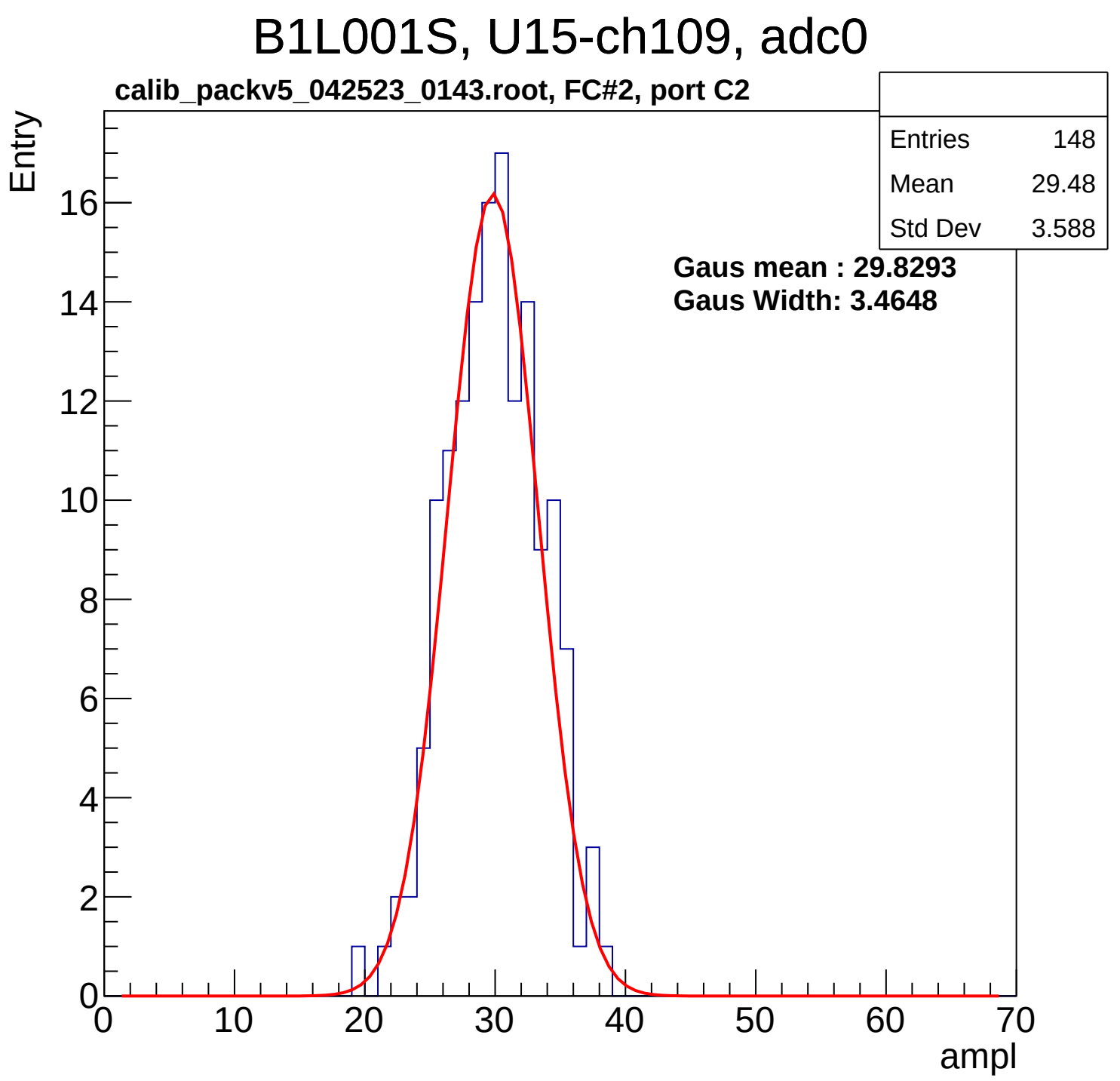

B1L001S, U15-ch108, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U15-ch109, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 29.48
Std Dev 3.588
Gaus mean : 29.8293
Gaus Width: 3.4648
Entry
ampl
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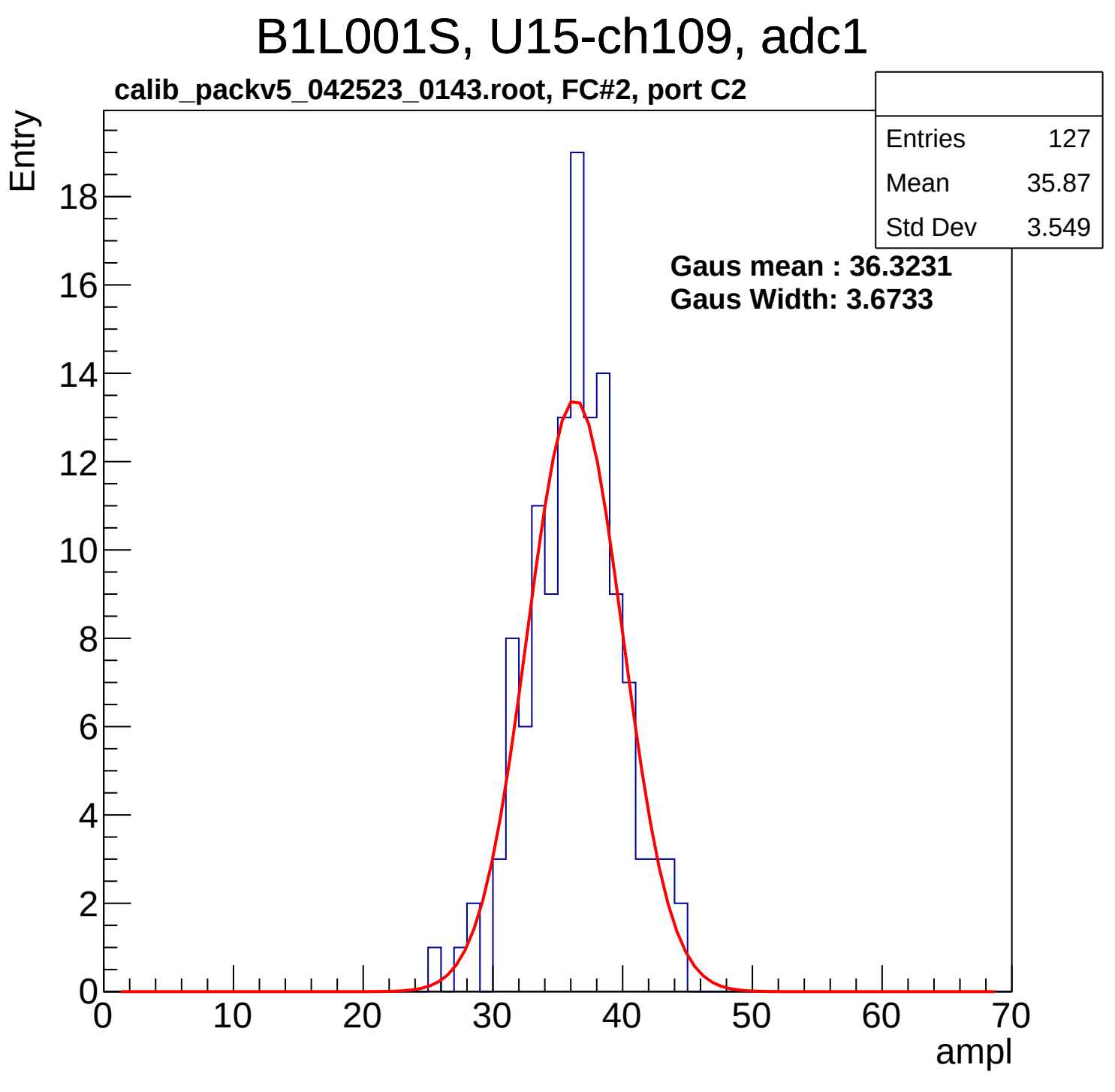
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch109, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 35.87
Std Dev 3.549
Gaus mean : 36.3231
Gaus Width: 3.6733
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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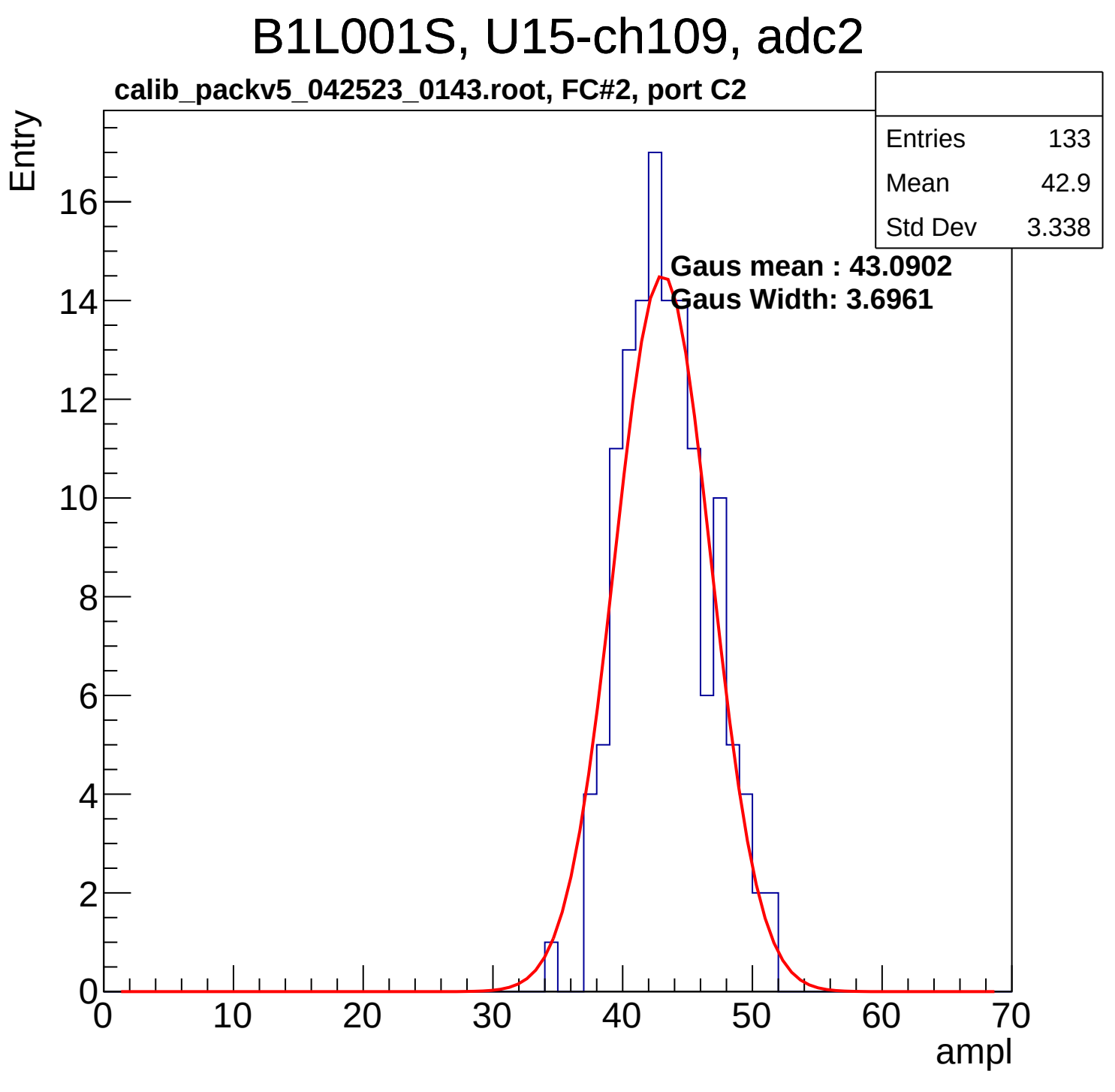
B1L001S, U15-ch109, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 42.9
Std Dev 3.338
Gaus mean : 43.0902
Gaus Width: 3.6961
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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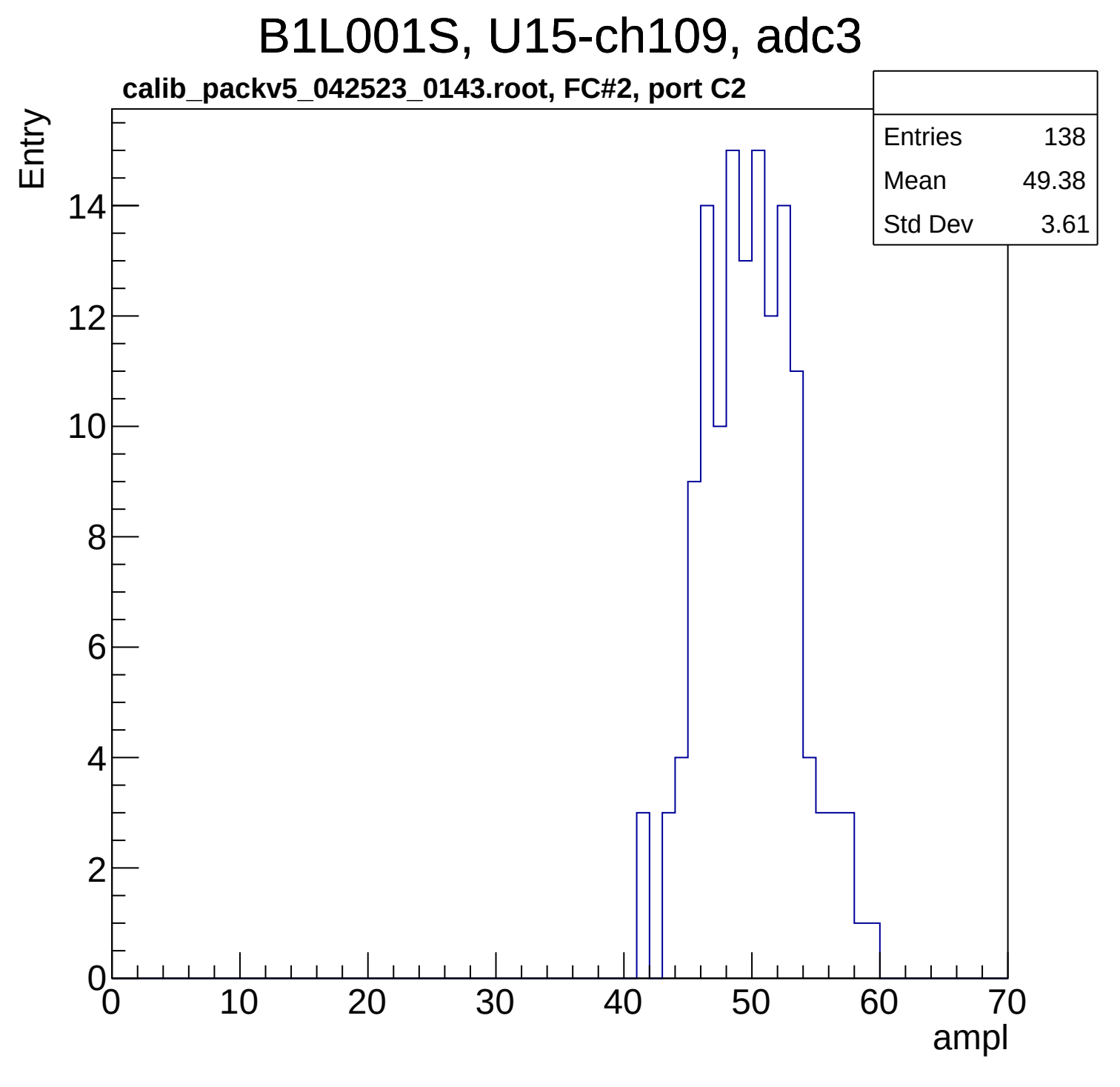
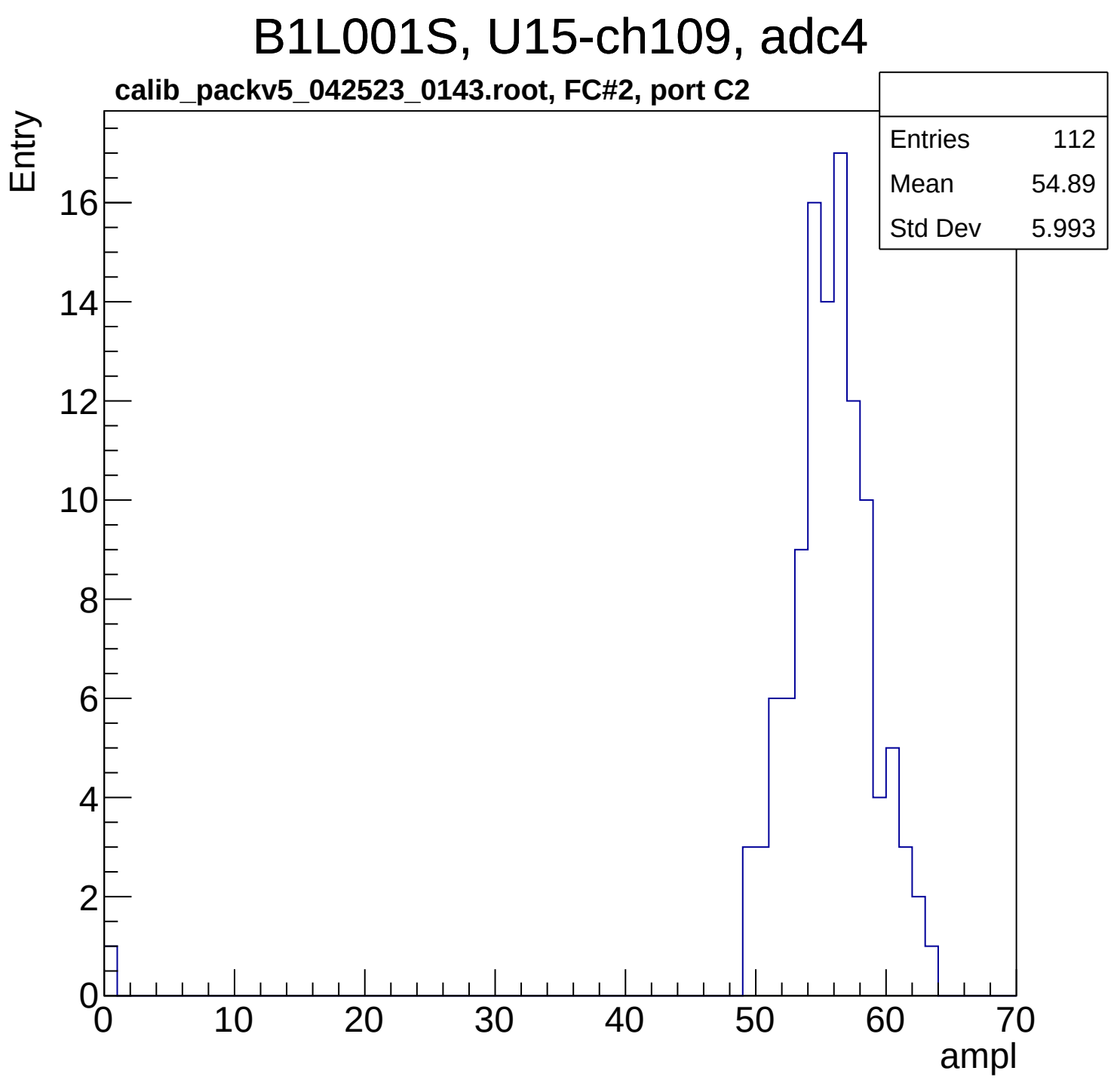

B1L001S, U15-ch109, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 49.38
Std Dev 3.61
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch109, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 54.89
Std Dev 5.993
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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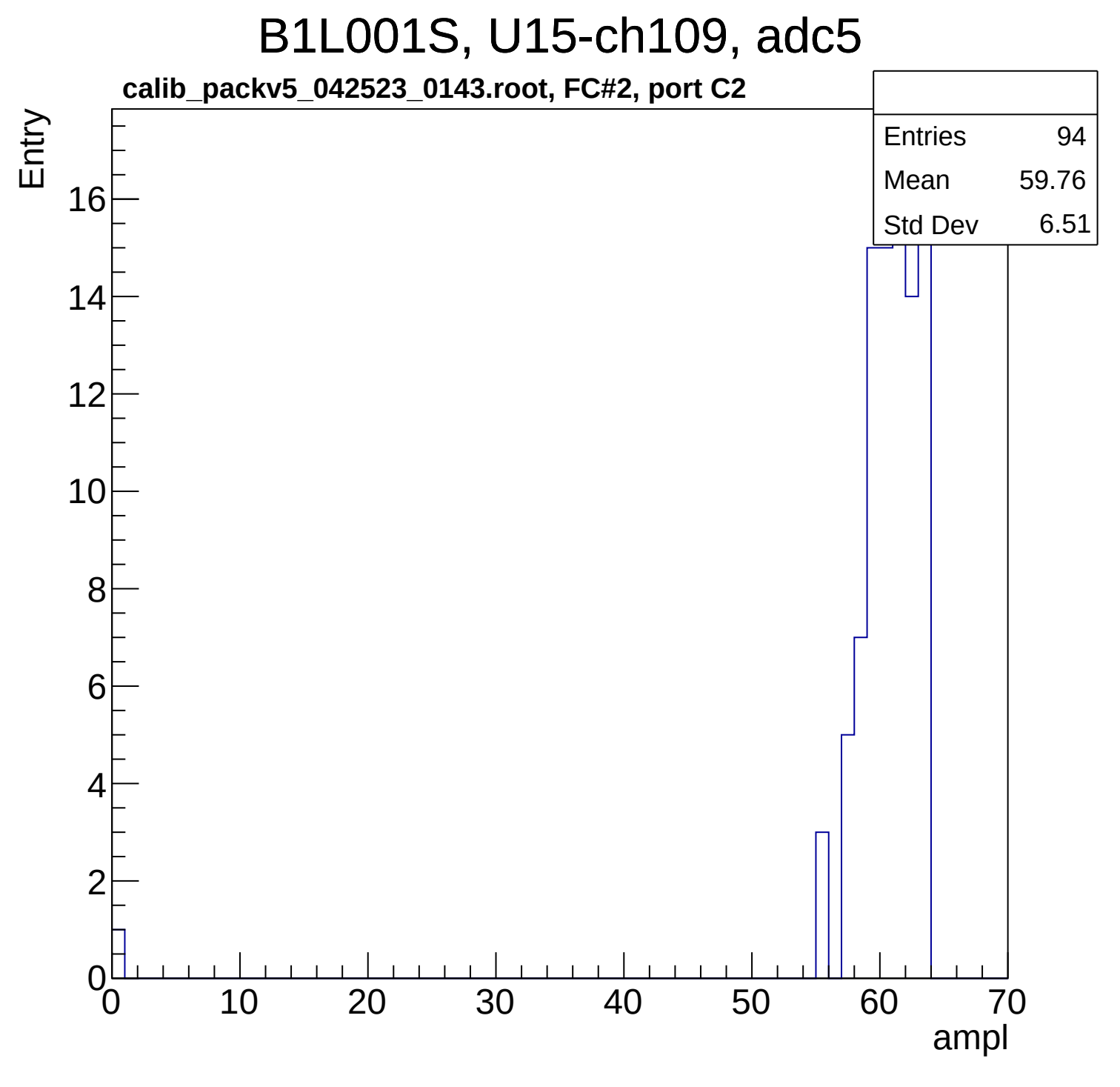
B1L001S, U15-ch109, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.76
Std Dev 6.51
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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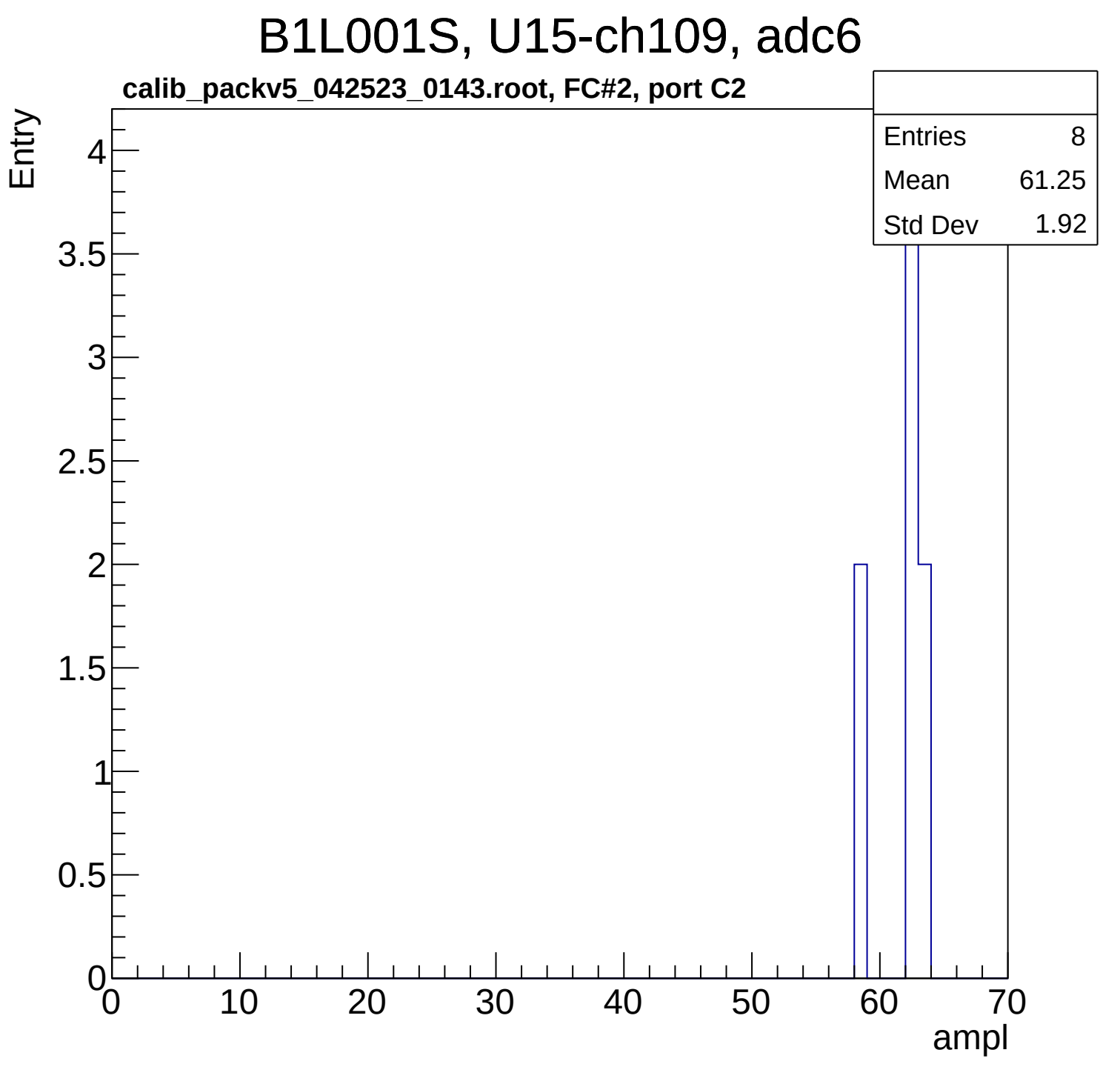
B1L001S, U15-ch109, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.25
Std Dev 1.92
Entry
ampl
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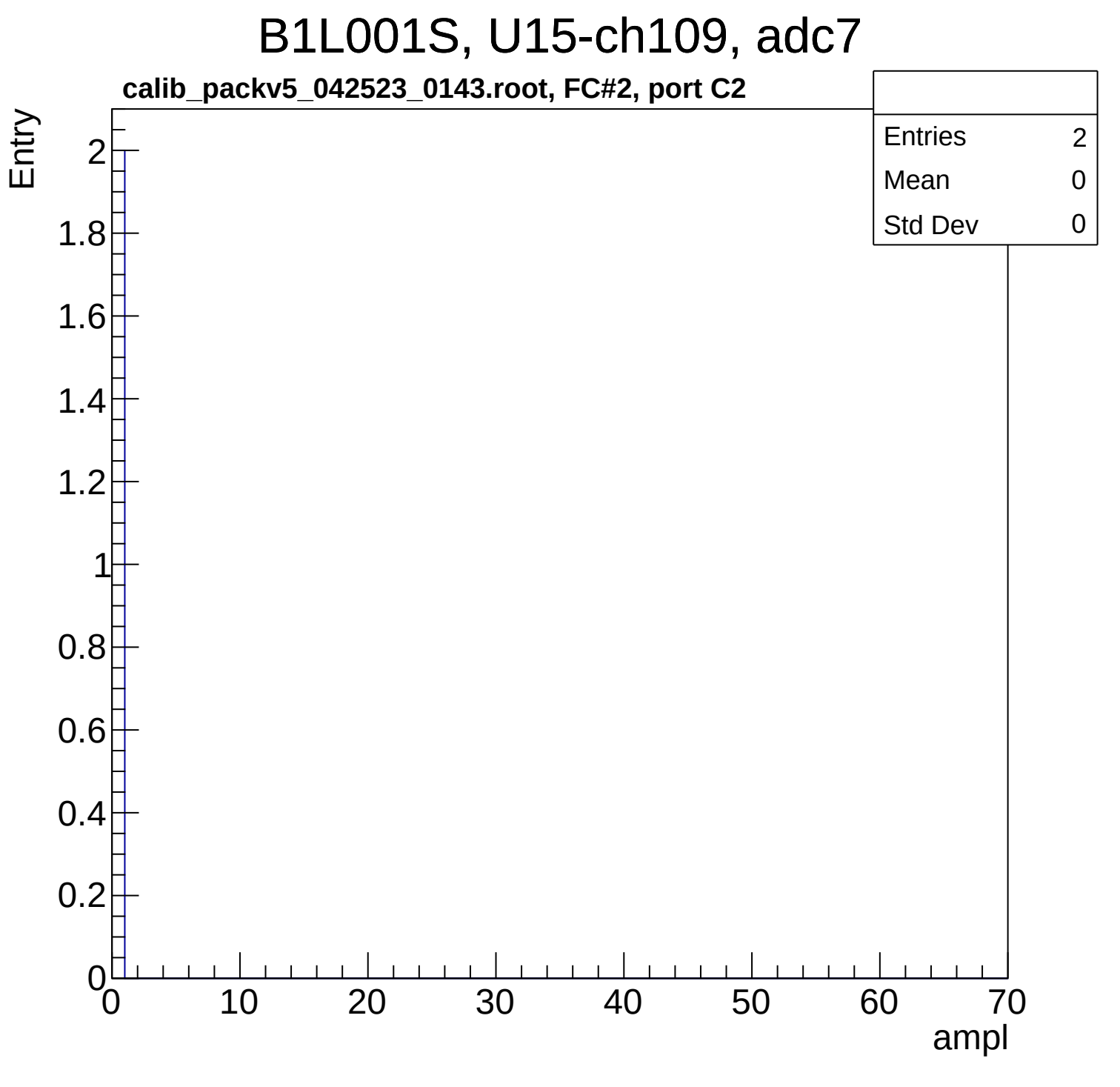
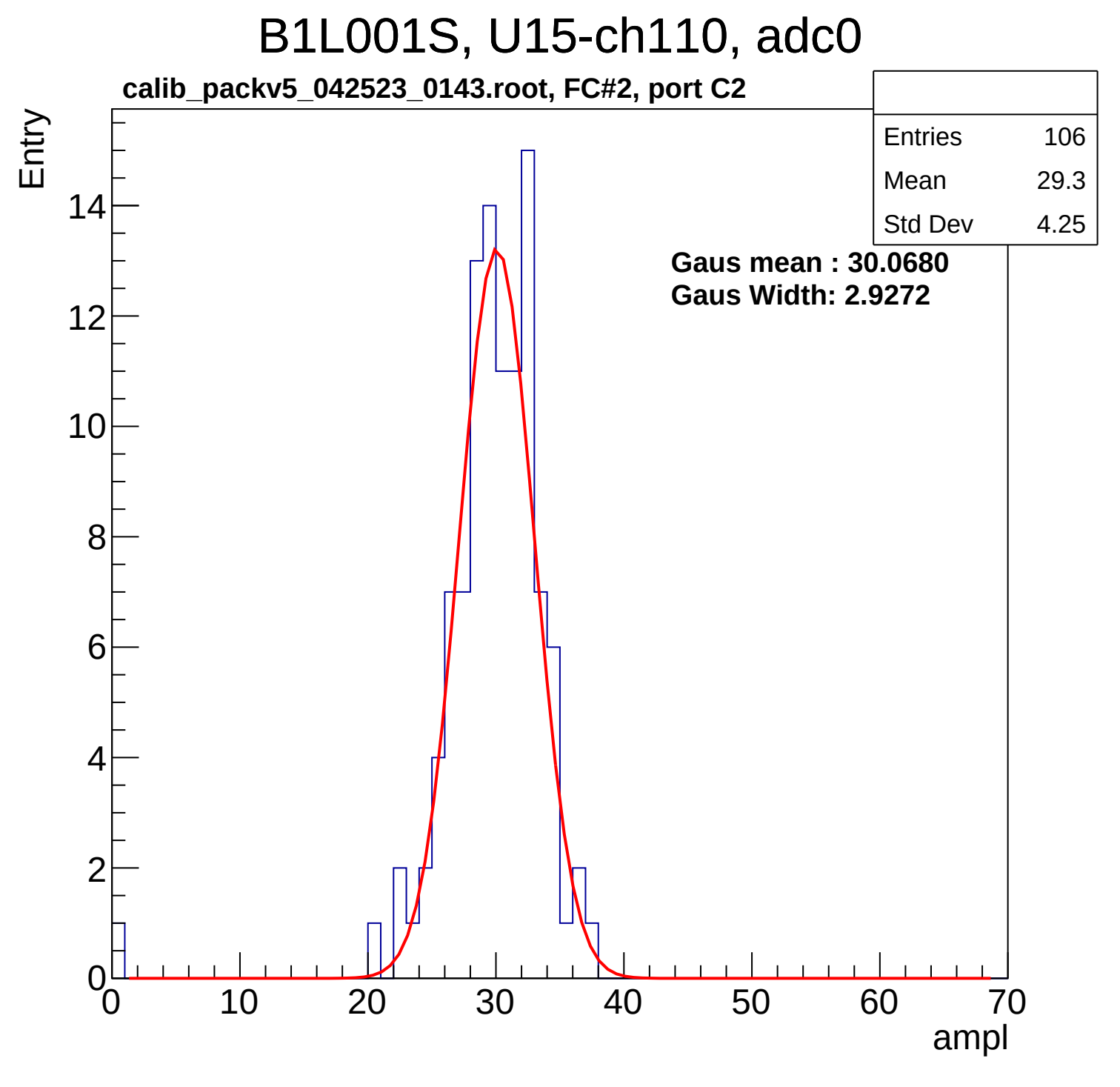
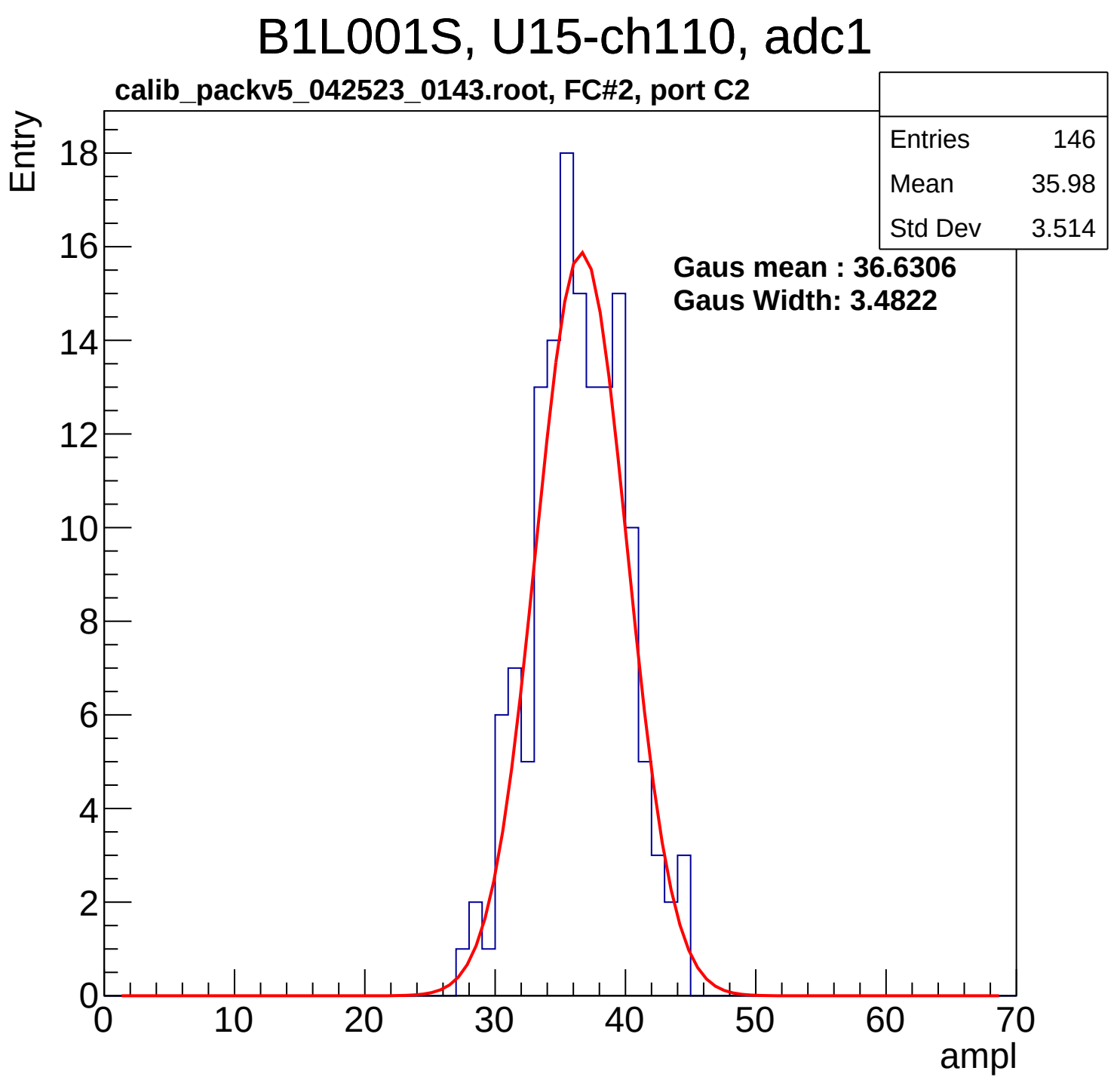
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch109, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch110, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 29.3
Std Dev 4.25
Gaus mean : 30.0680
Gaus Width: 2.9272
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch110, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 35.98
Std Dev 3.514
Gaus mean : 36.6306
Gaus Width: 3.4822
Entry
ampl
0
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4
6
8
10
12
14
16
18
0
10
20
30
40
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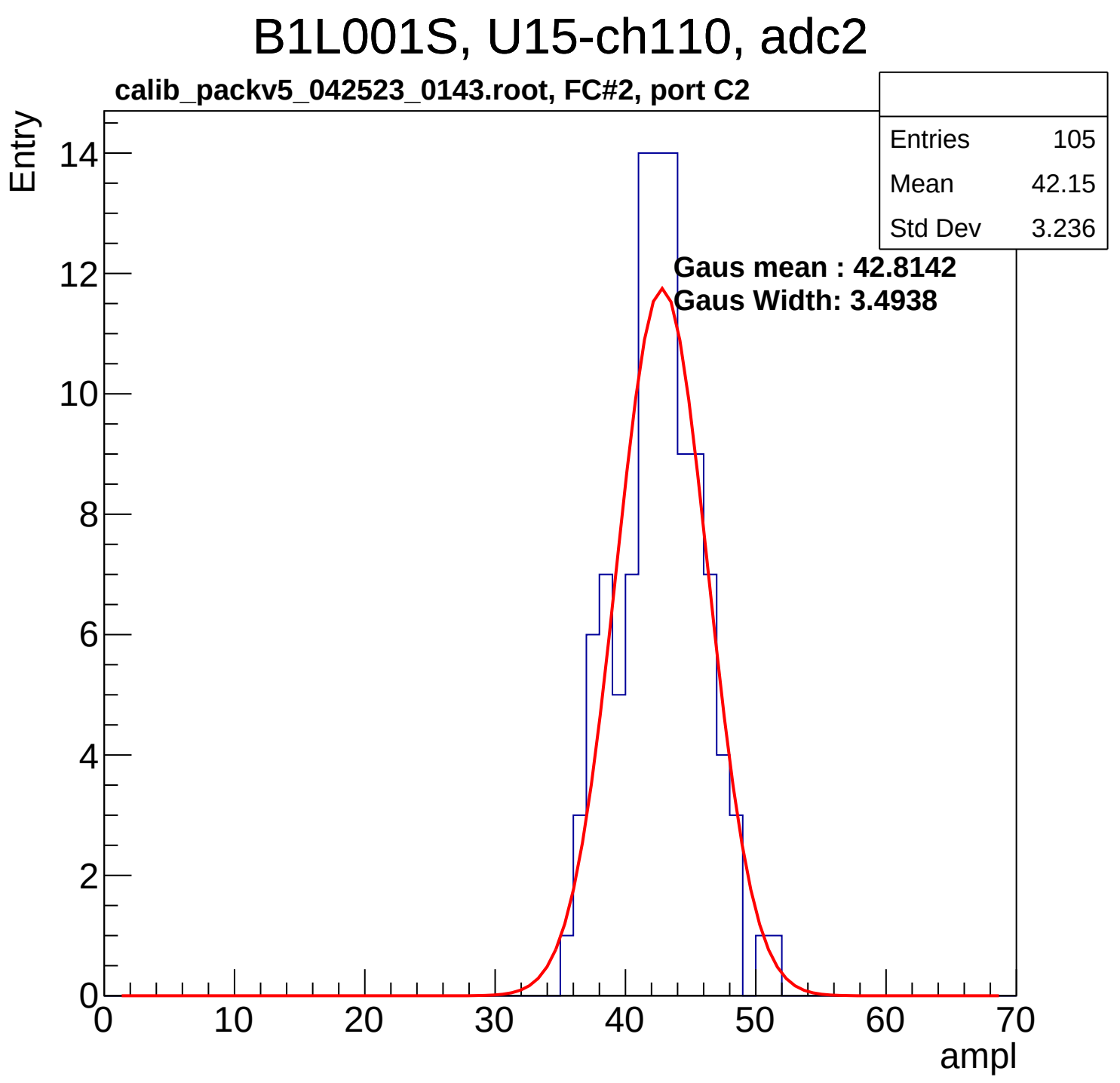
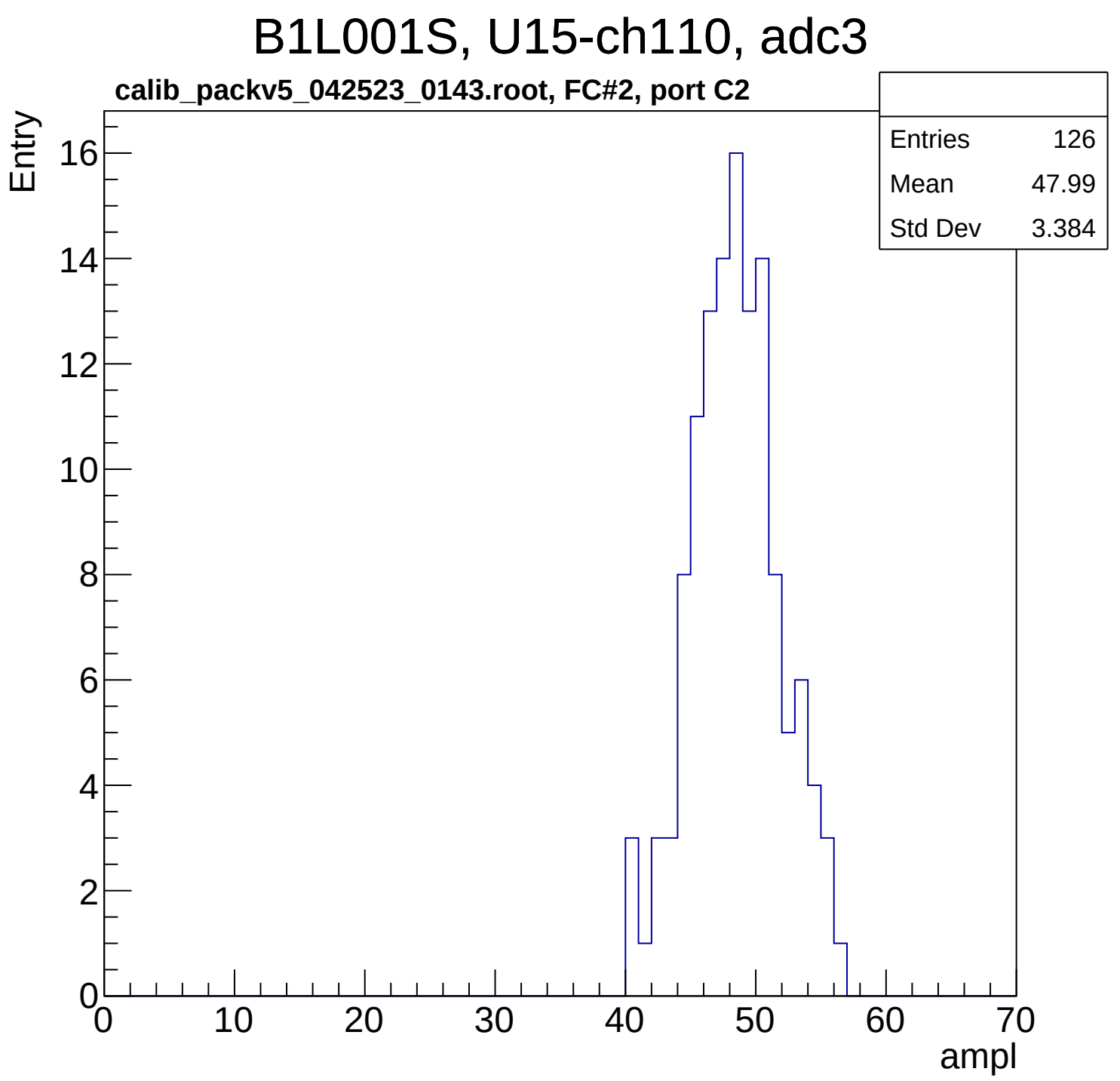
60
70


B1L001S, U15-ch110, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 42.15
Std Dev 3.236
Gaus mean : 42.8142
Gaus Width: 3.4938
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch110, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 47.99
Std Dev 3.384
Entry
ampl
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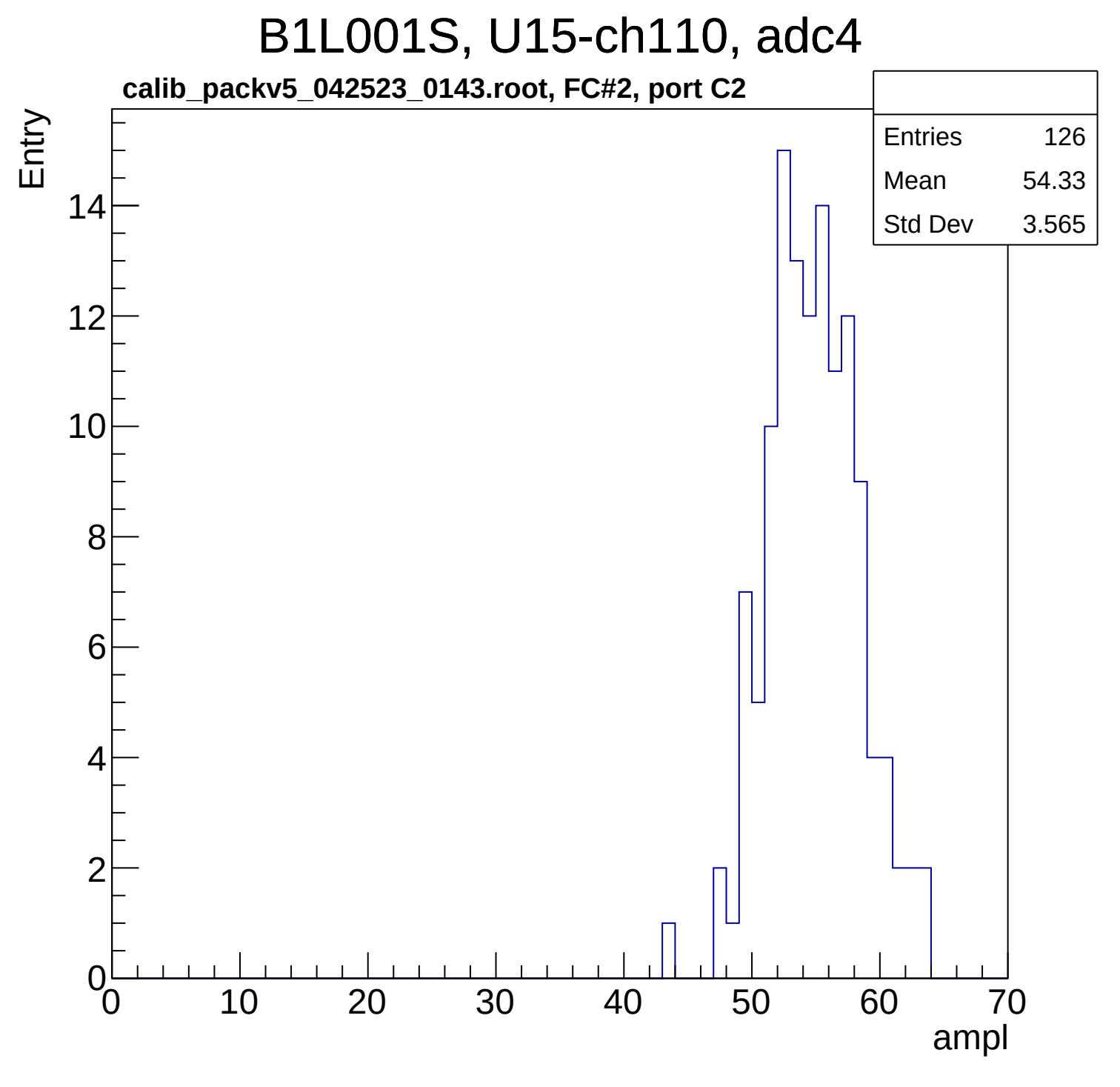
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch110, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 54.33
Std Dev 3.565
Entry
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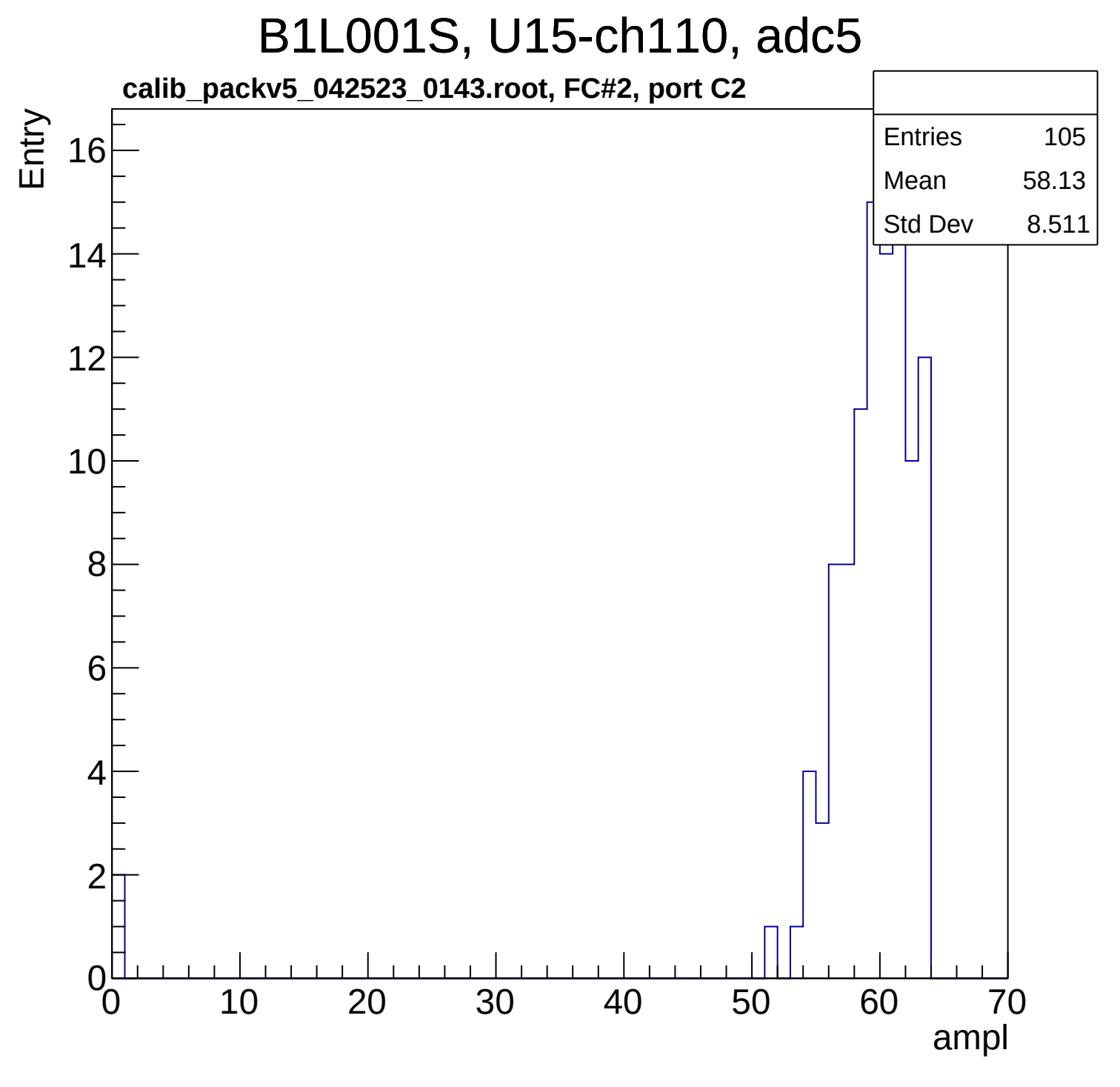
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch110, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 58.13
Std Dev 8.511
Entry
ampl
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6
8
10
12
14
16
0
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60
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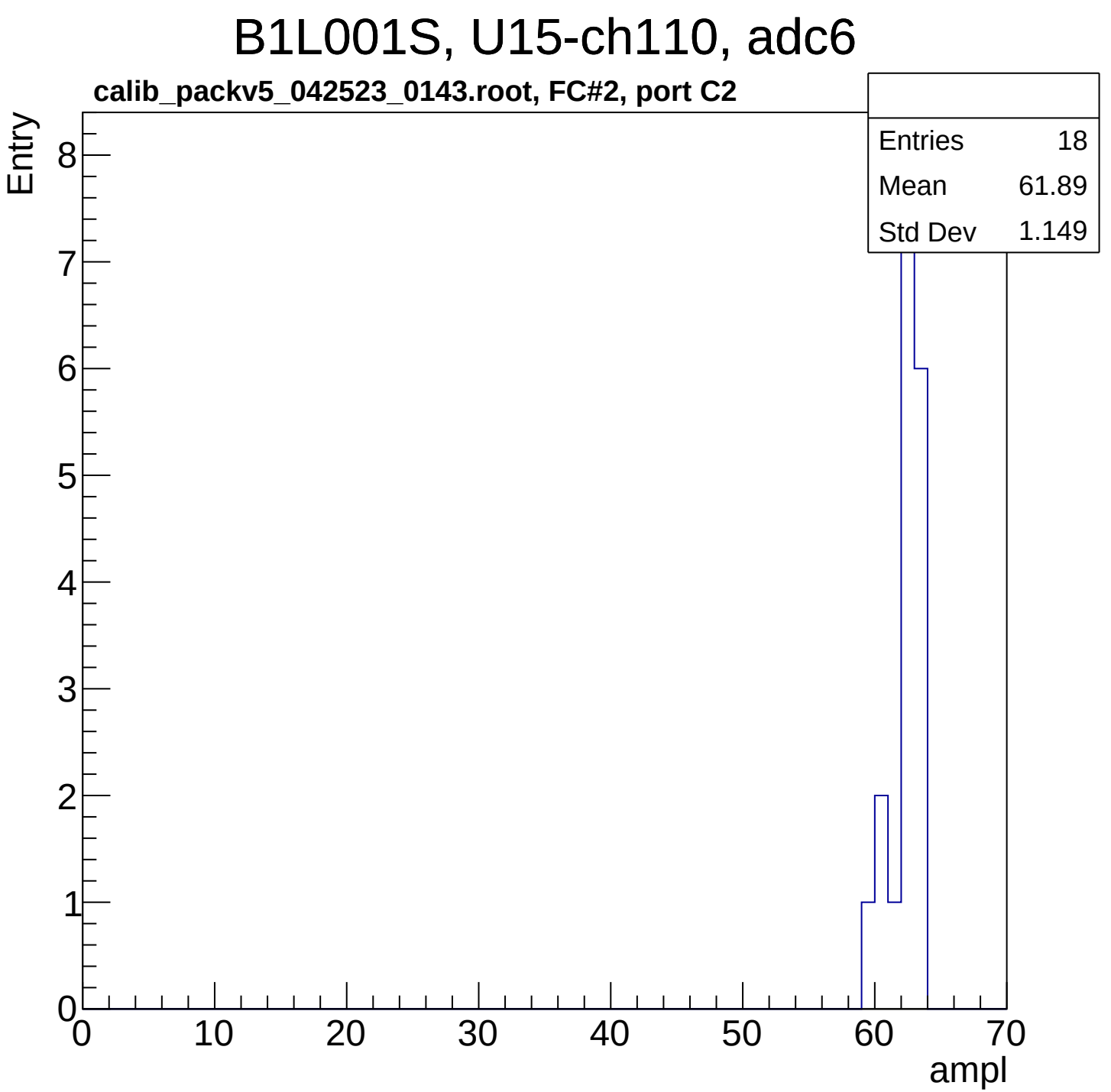
B1L001S, U15-ch110, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 61.89
Std Dev 1.149
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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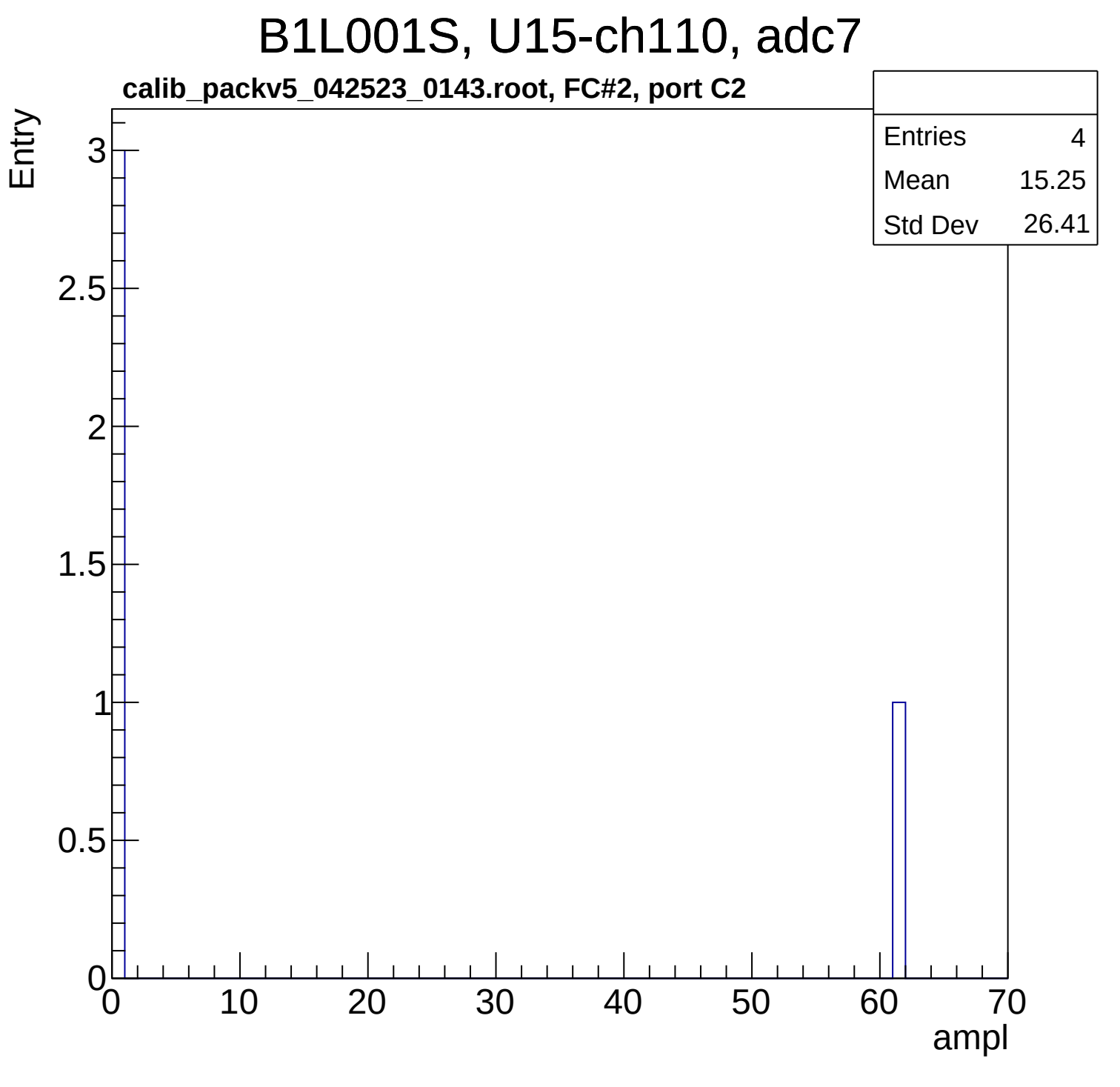
B1L001S, U15-ch110, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 15.25
Std Dev 26.41
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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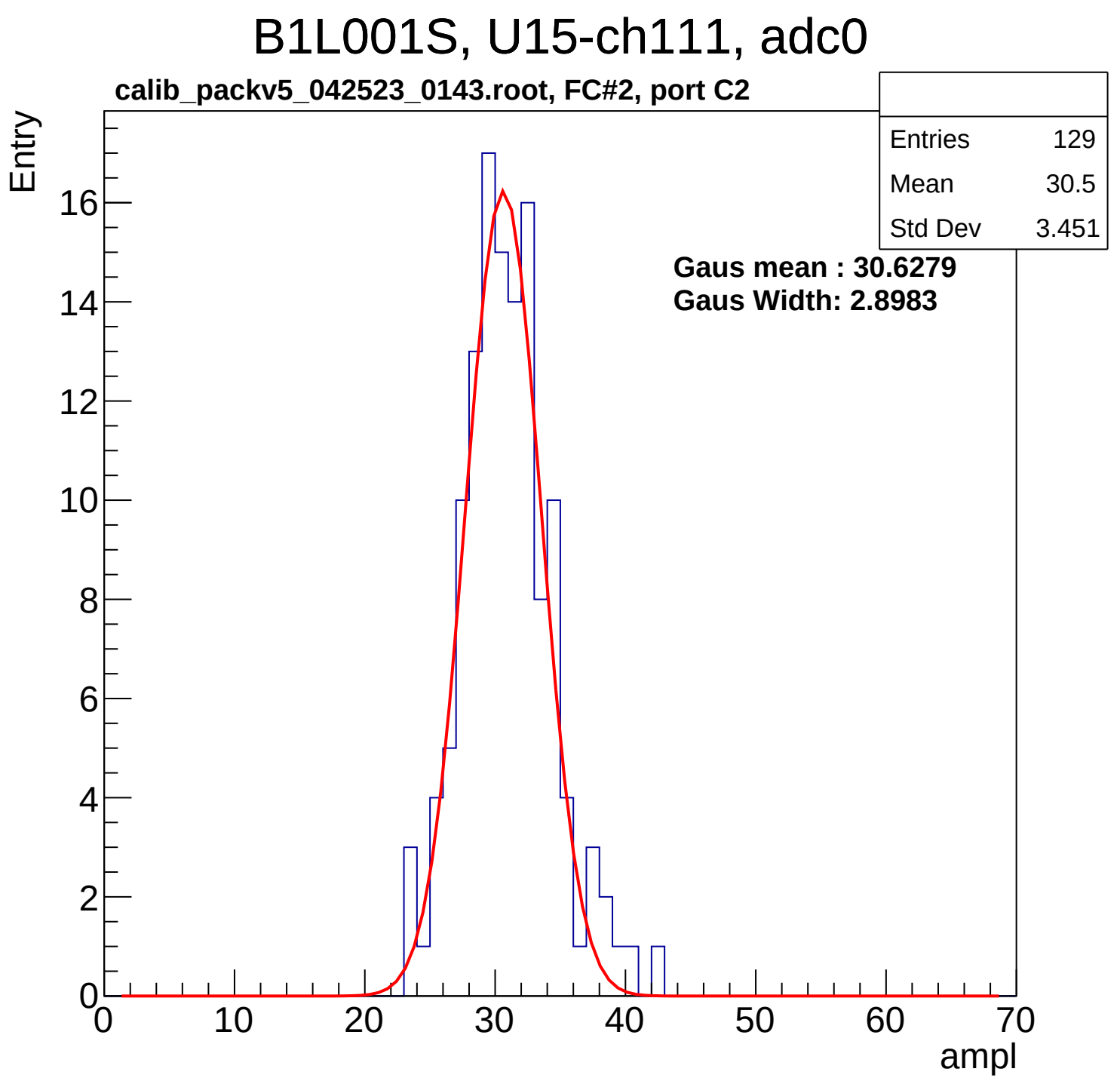
B1L001S, U15-ch111, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 30.5
Std Dev 3.451
Gaus mean : 30.6279
Gaus Width: 2.8983
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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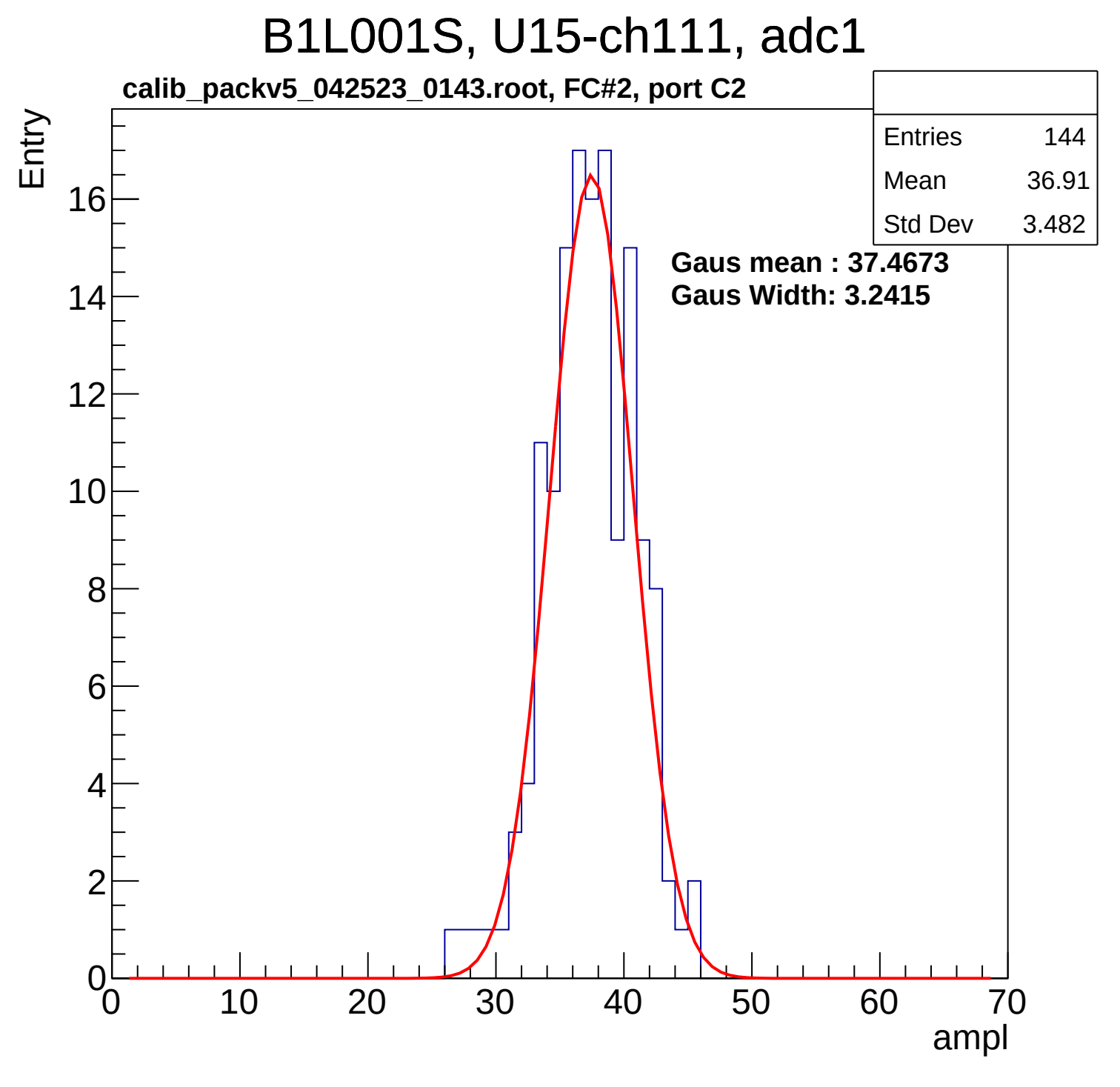
B1L001S, U15-ch111, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 36.91
Std Dev 3.482
Gaus mean : 37.4673
Gaus Width: 3.2415
Entry
ampl
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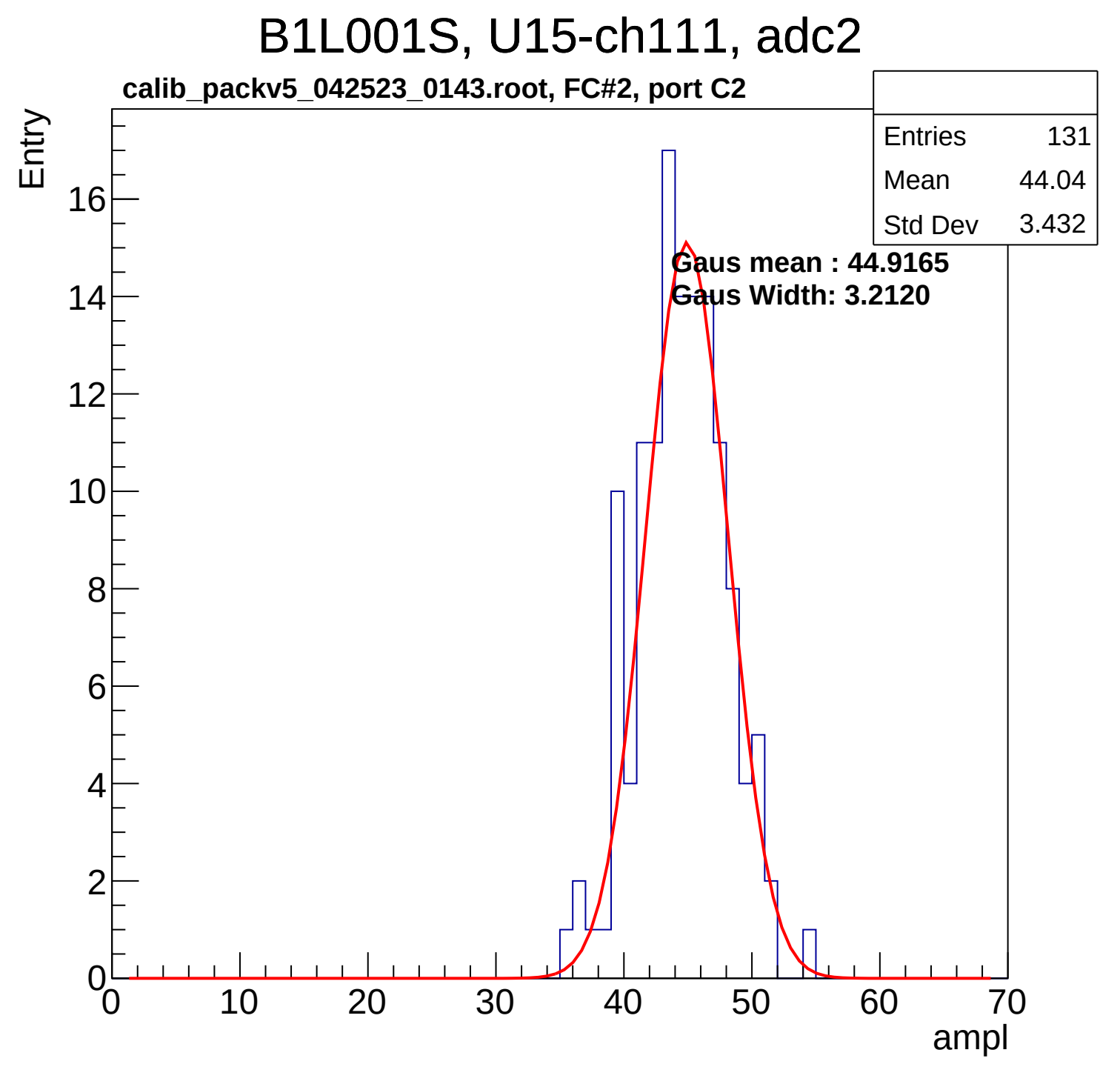
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch111, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 44.04
Std Dev 3.432
Gaus mean : 44.9165
Gaus Width: 3.2120
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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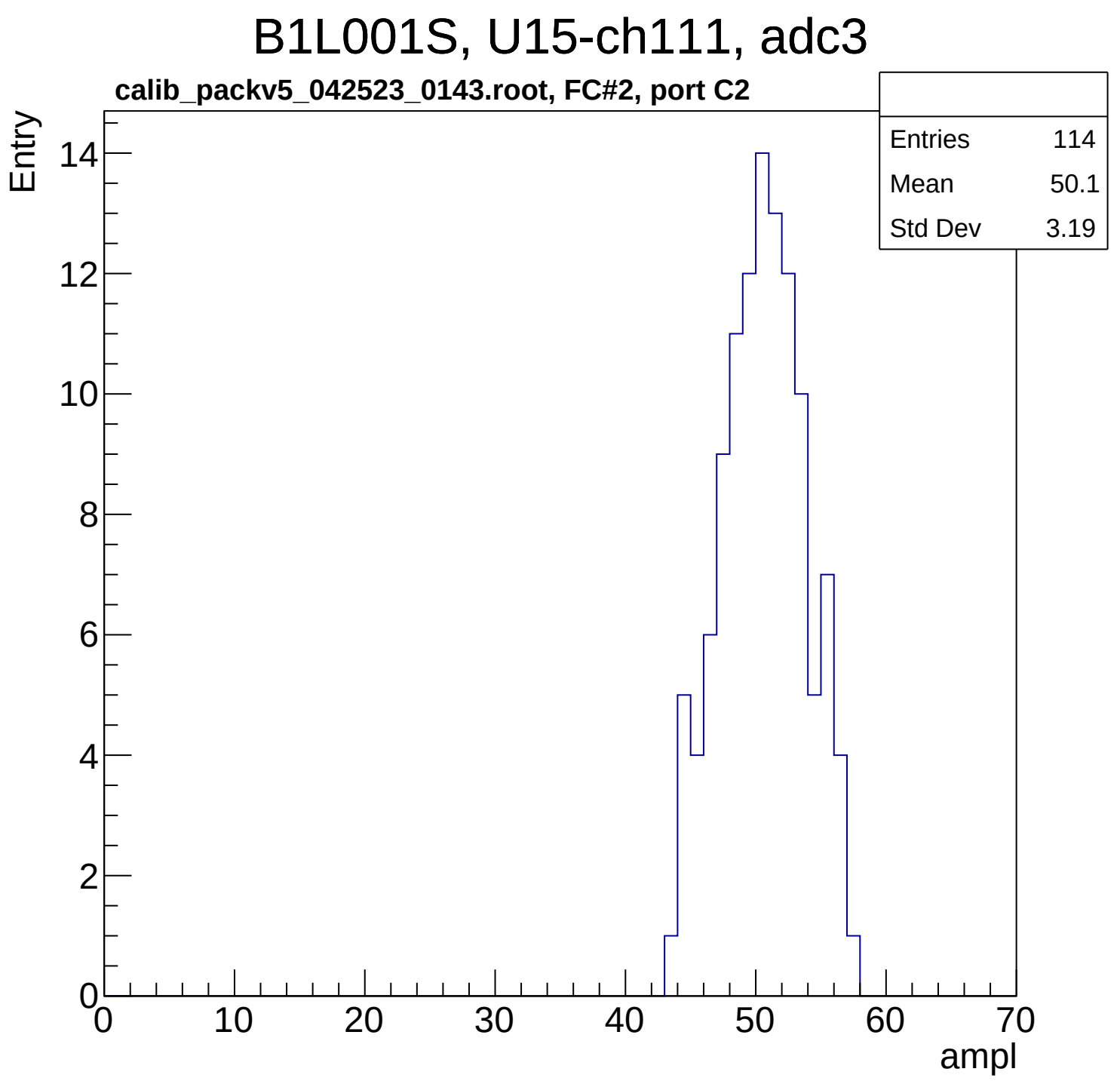
B1L001S, U15-ch111, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 50.1
Std Dev 3.19
Entry
ampl
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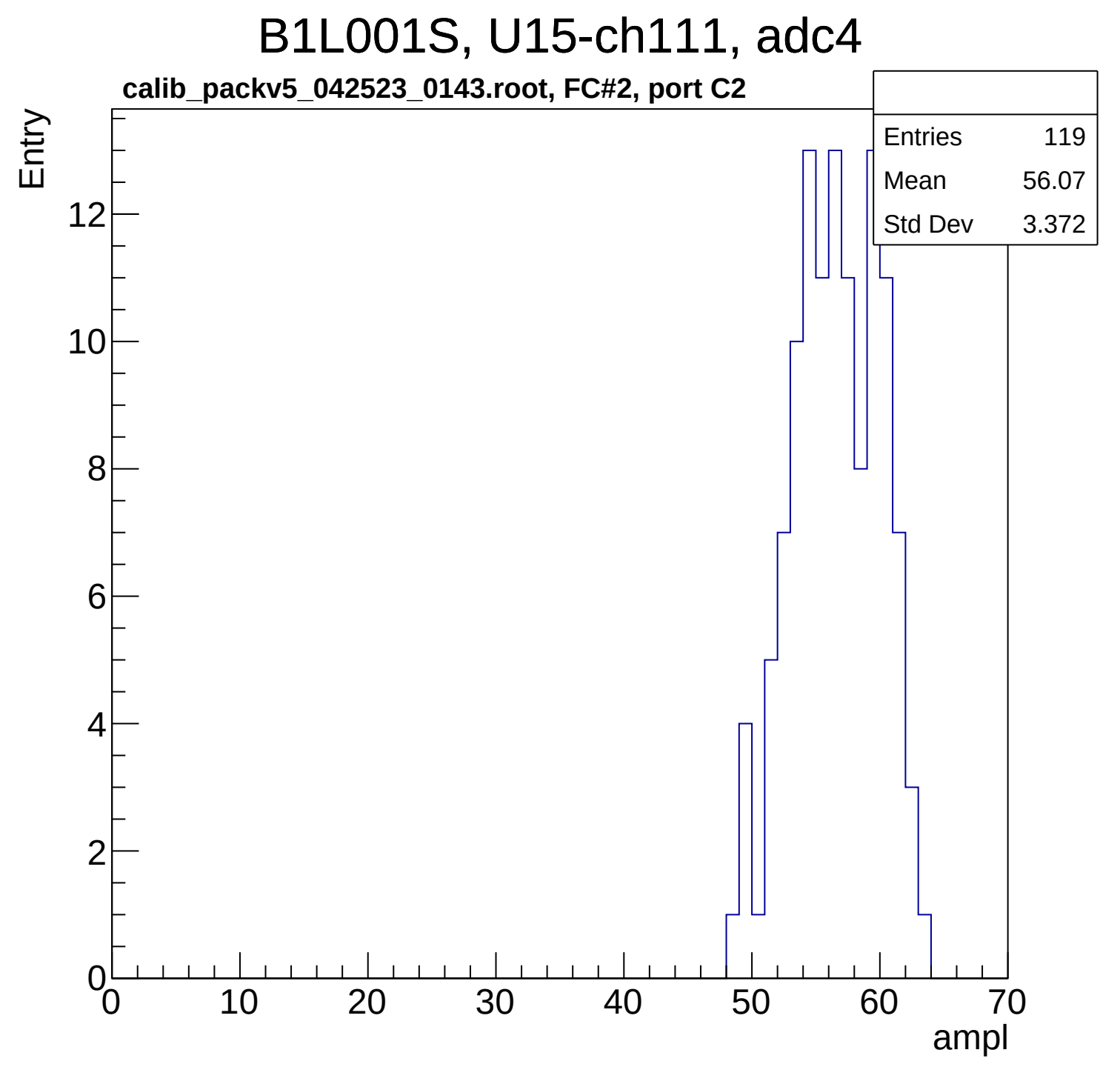
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch111, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 56.07
Std Dev 3.372
Entry
ampl
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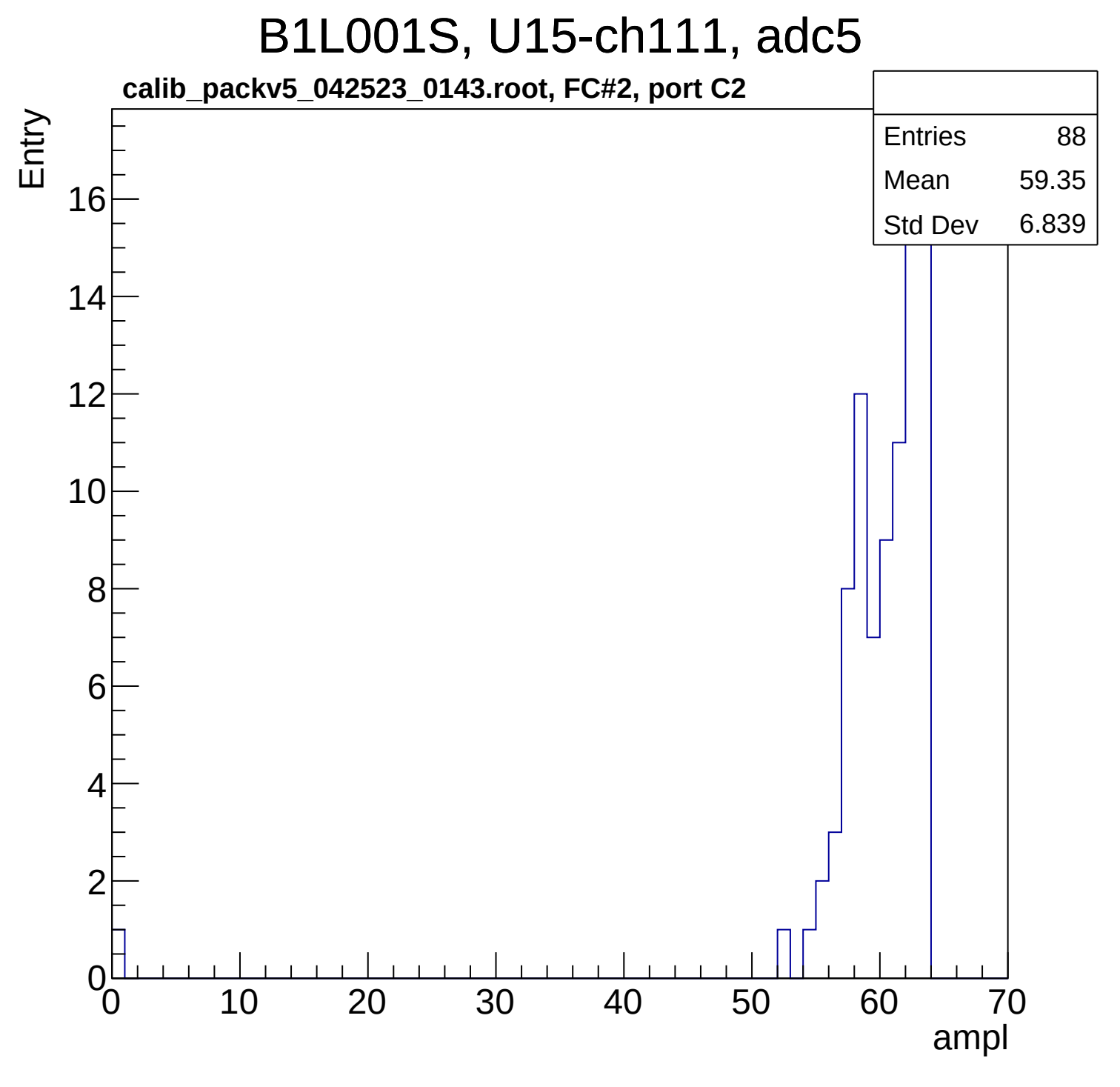
B1L001S, U15-ch111, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.35
Std Dev 6.839
Entry
ampl
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4
6
8
10
12
14
16
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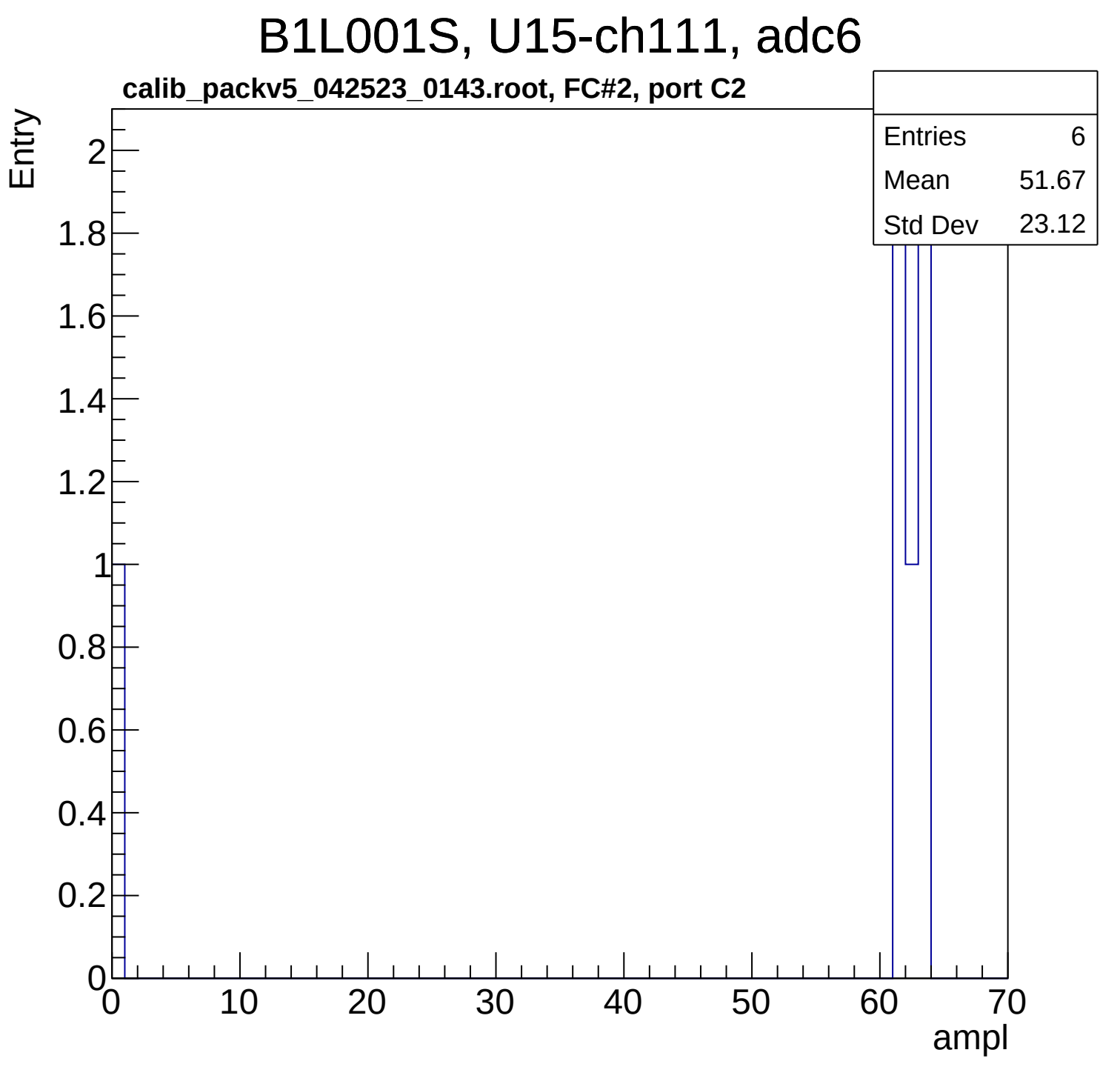
B1L001S, U15-ch111, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 51.67
Std Dev 23.12
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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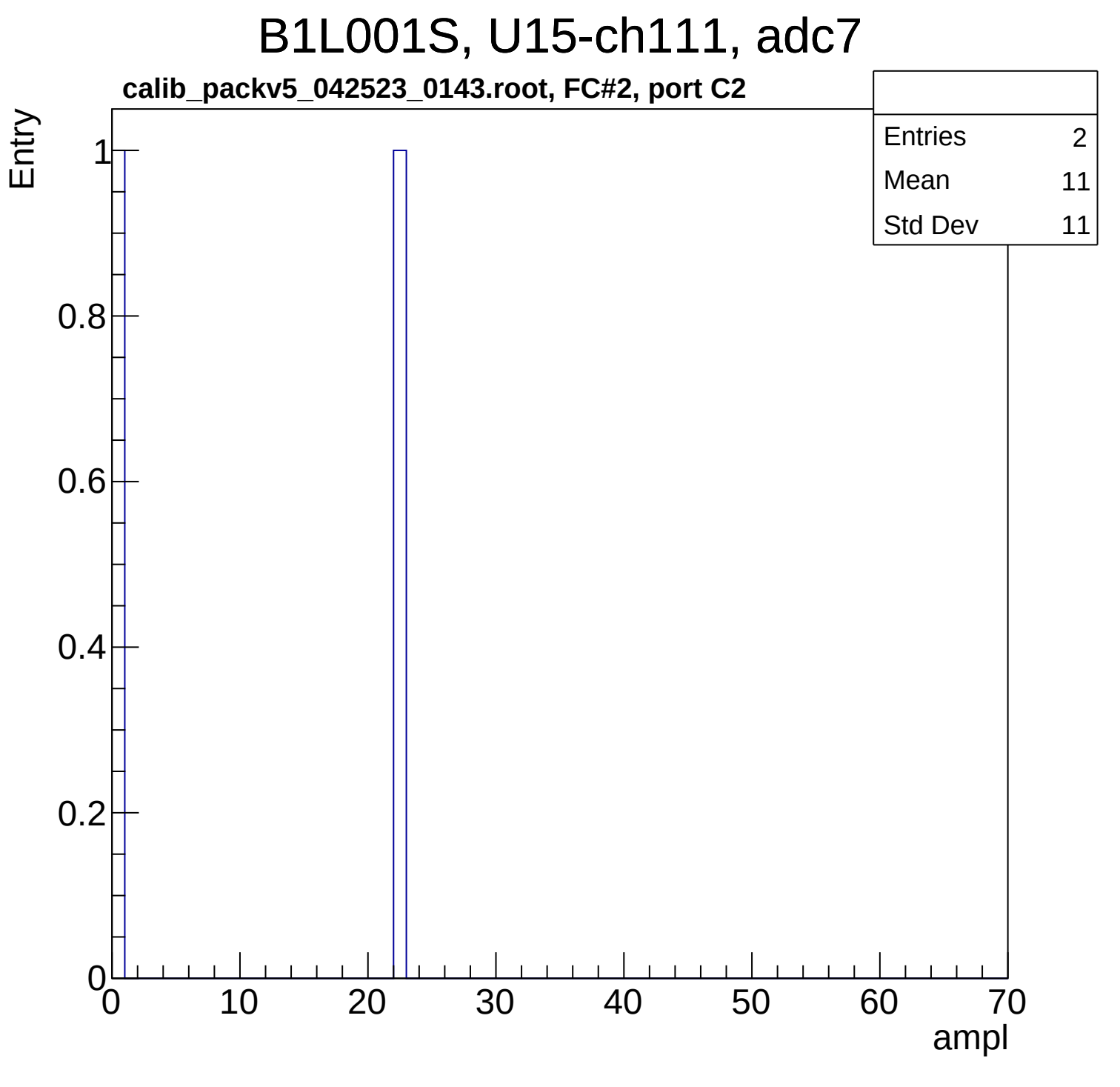
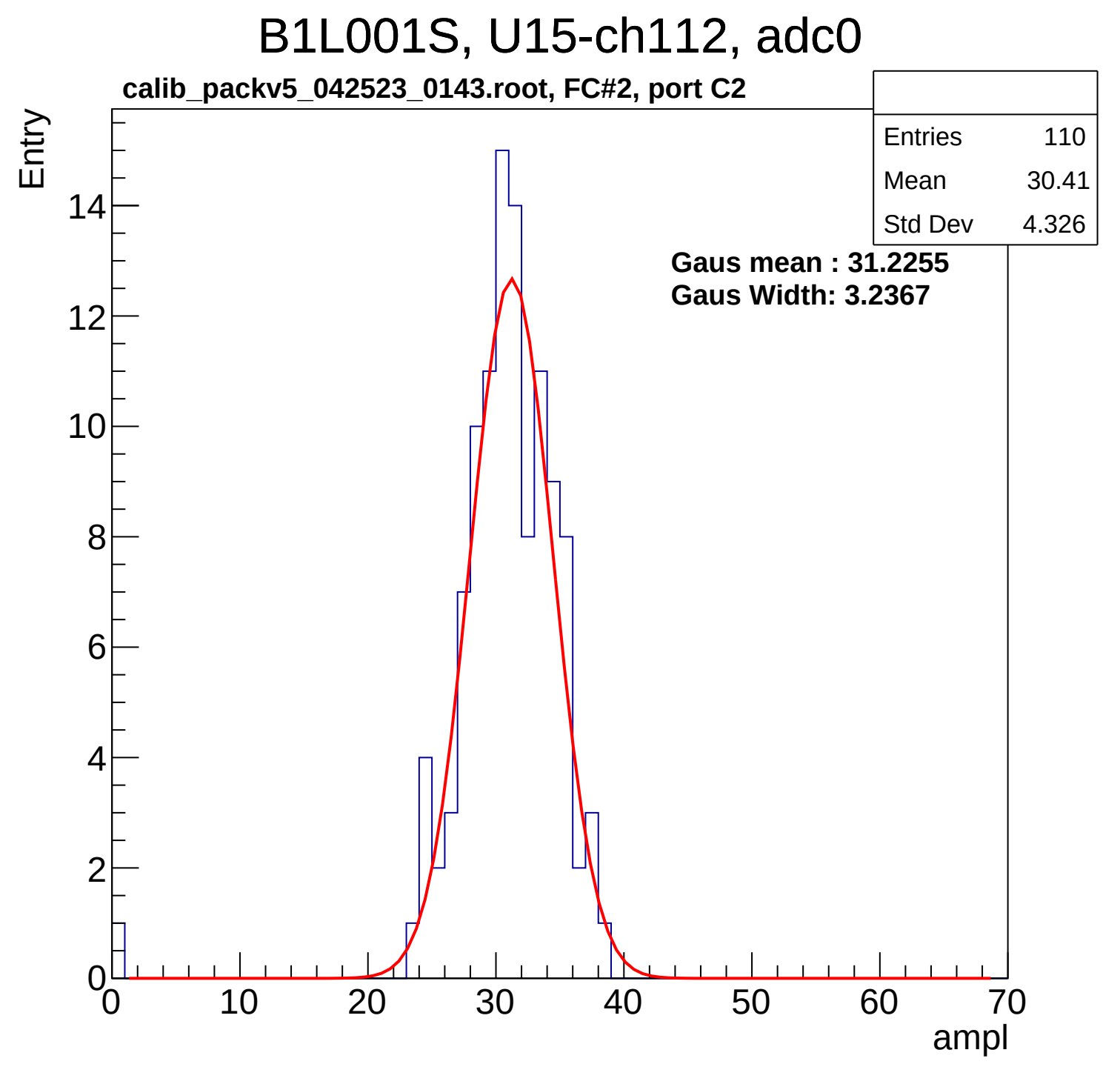
40
50
60
70


B1L001S, U15-ch111, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch112, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 30.41
Std Dev 4.326
Gaus mean : 31.2255
Gaus Width: 3.2367
Entry
ampl
0
2
4
6
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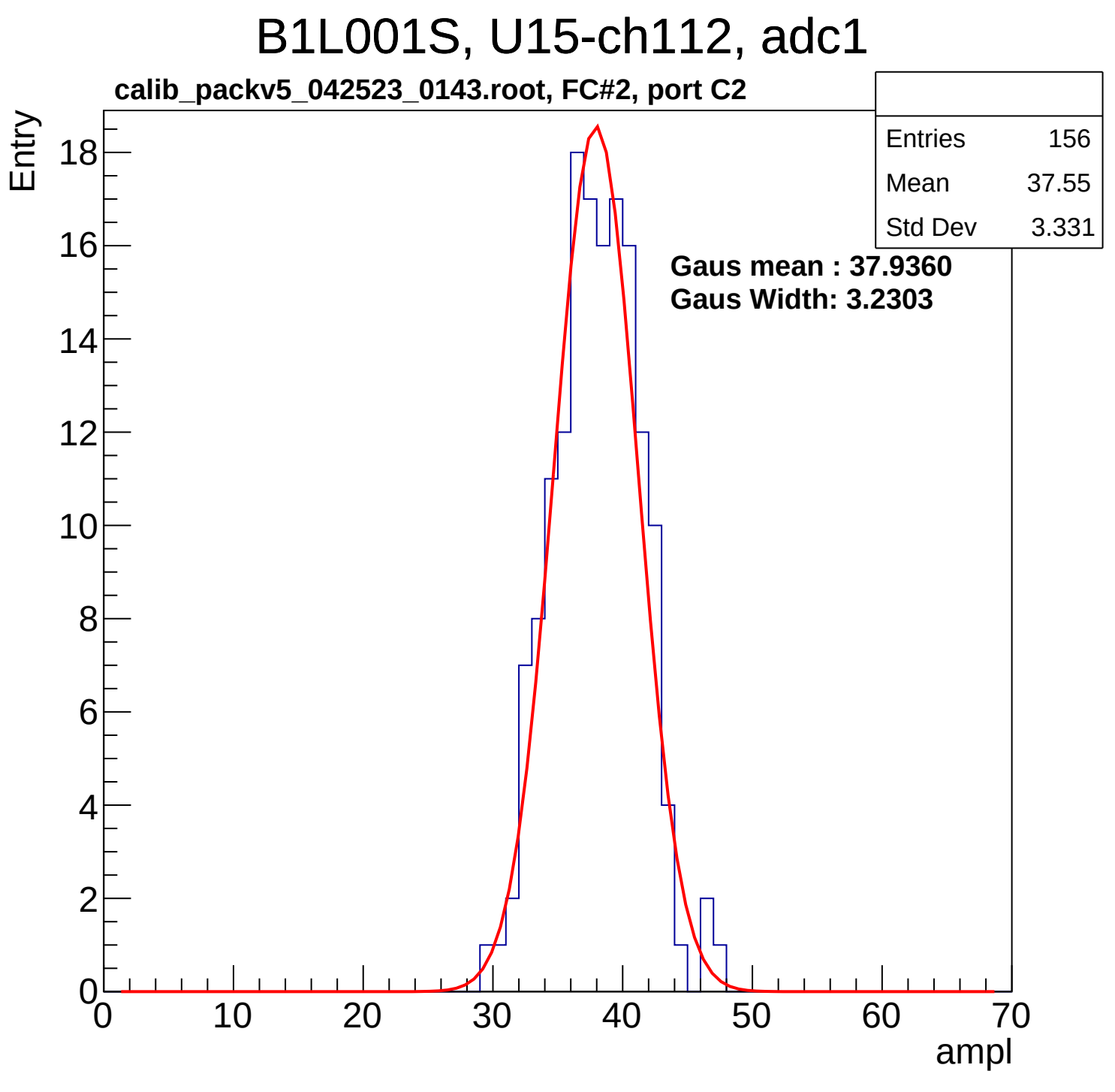
B1L001S, U15-ch112, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 37.55
Std Dev 3.331
Gaus mean : 37.9360
Gaus Width: 3.2303
Entry
ampl
0
2
4
6
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10
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16
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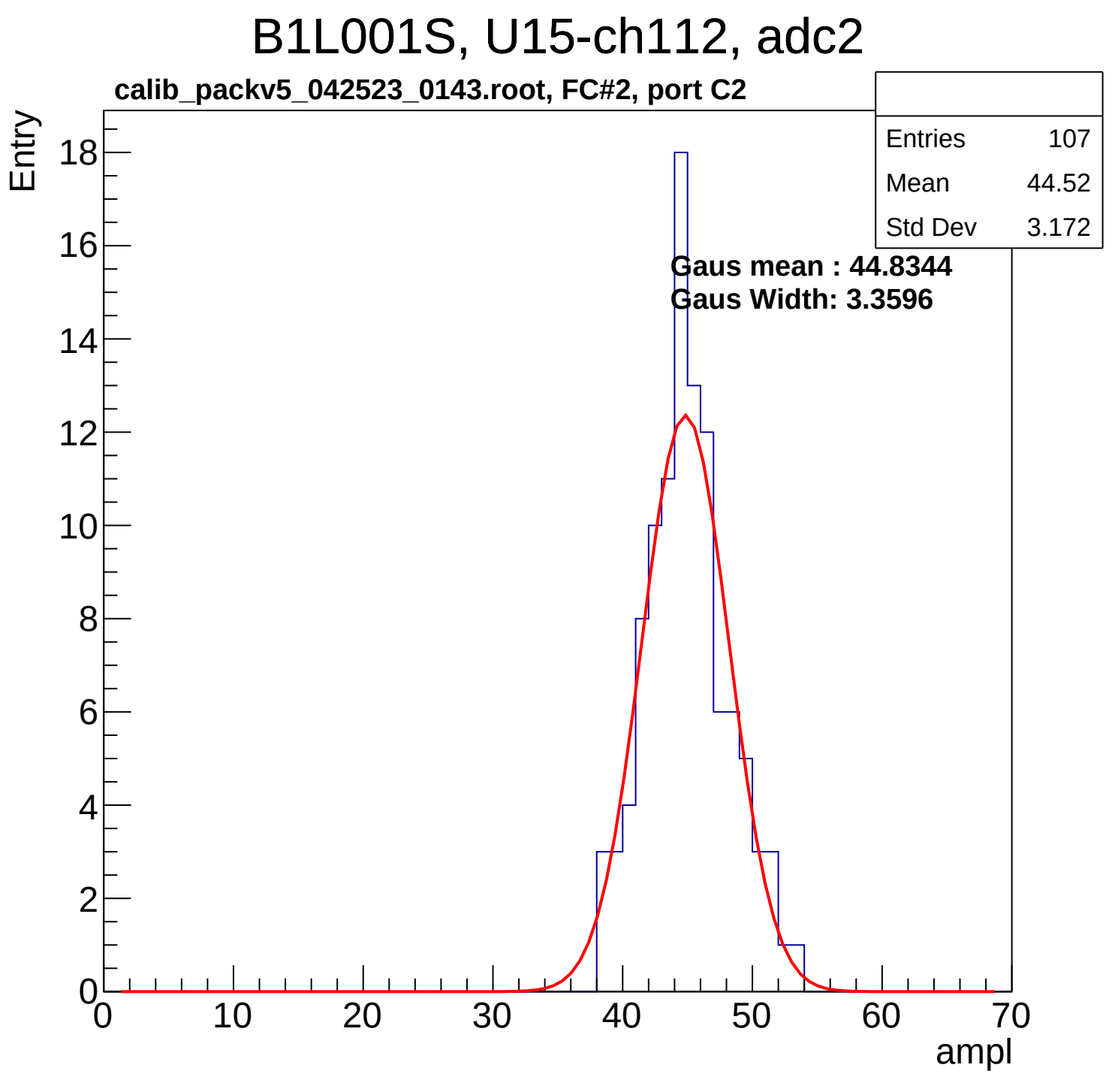
B1L001S, U15-ch112, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 44.52
Std Dev 3.172
Gaus mean : 44.8344
Gaus Width: 3.3596
Entry
ampl
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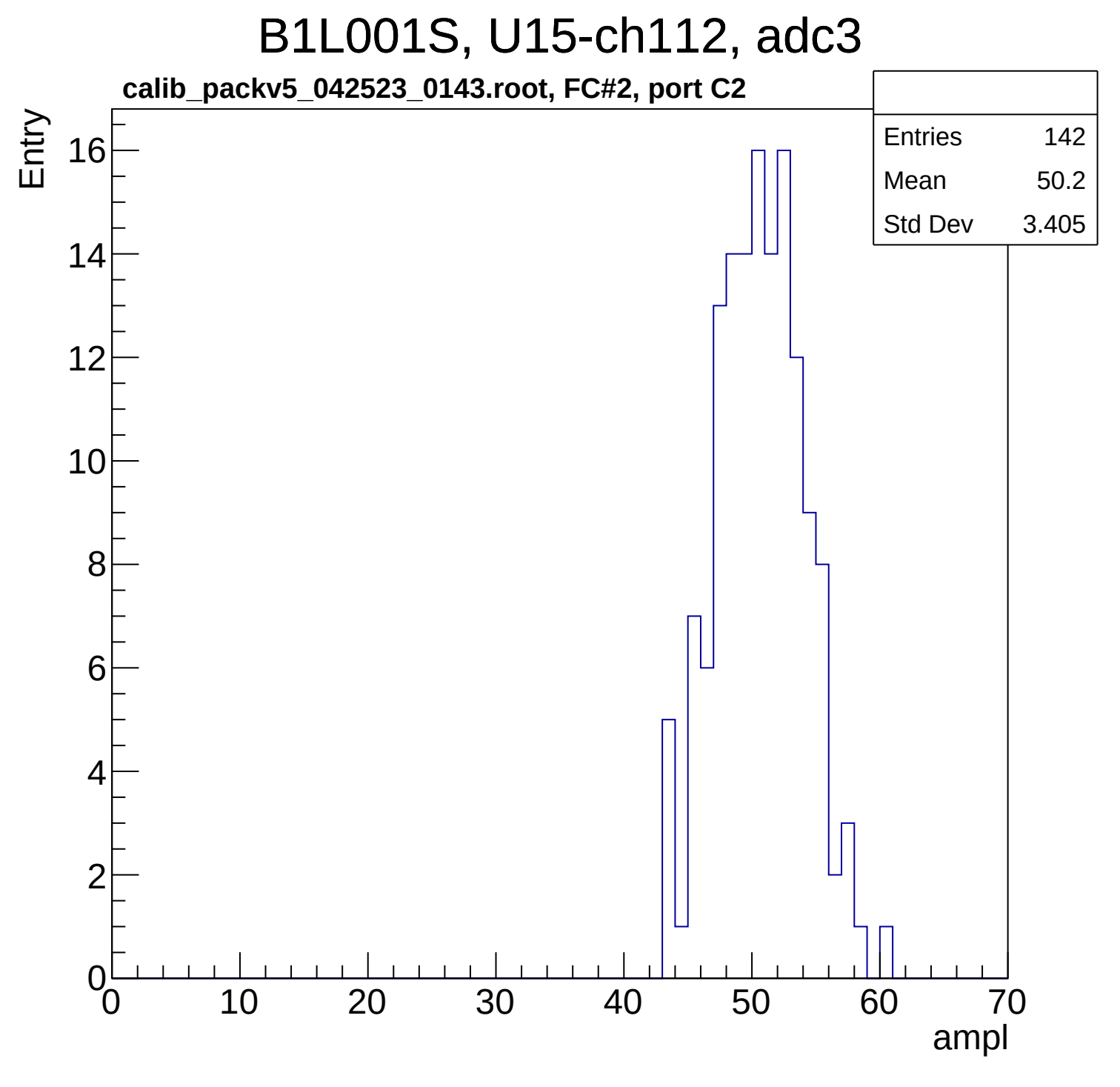
B1L001S, U15-ch112, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 50.2
Std Dev 3.405
Entry
ampl
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2
4
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8
10
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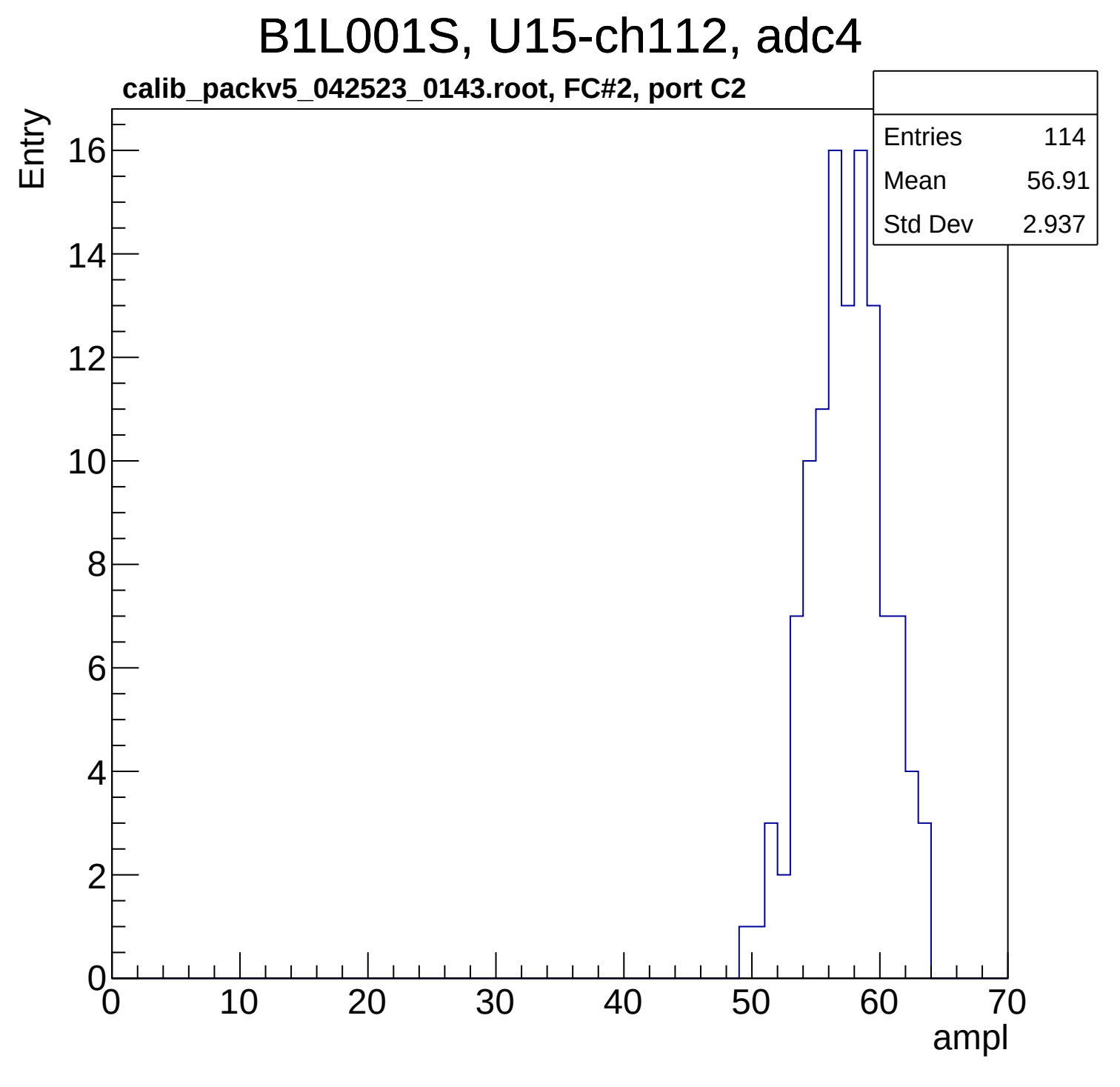
B1L001S, U15-ch112, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 56.91
Std Dev 2.937
Entry
ampl
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2
4
6
8
10
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14
16
0
10
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60
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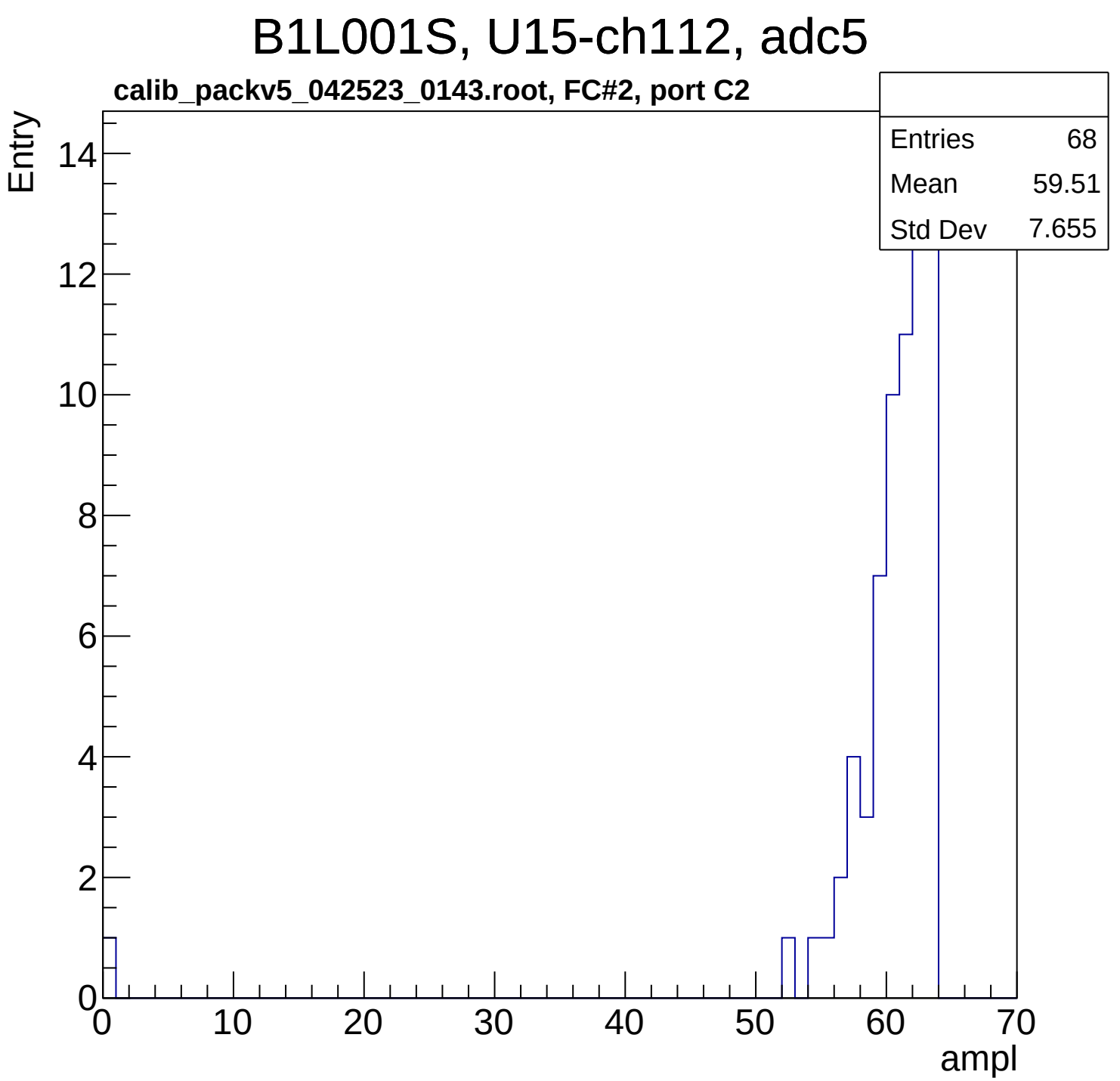
B1L001S, U15-ch112, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 68
Mean 59.51
Std Dev 7.655
Entry
ampl
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2
4
6
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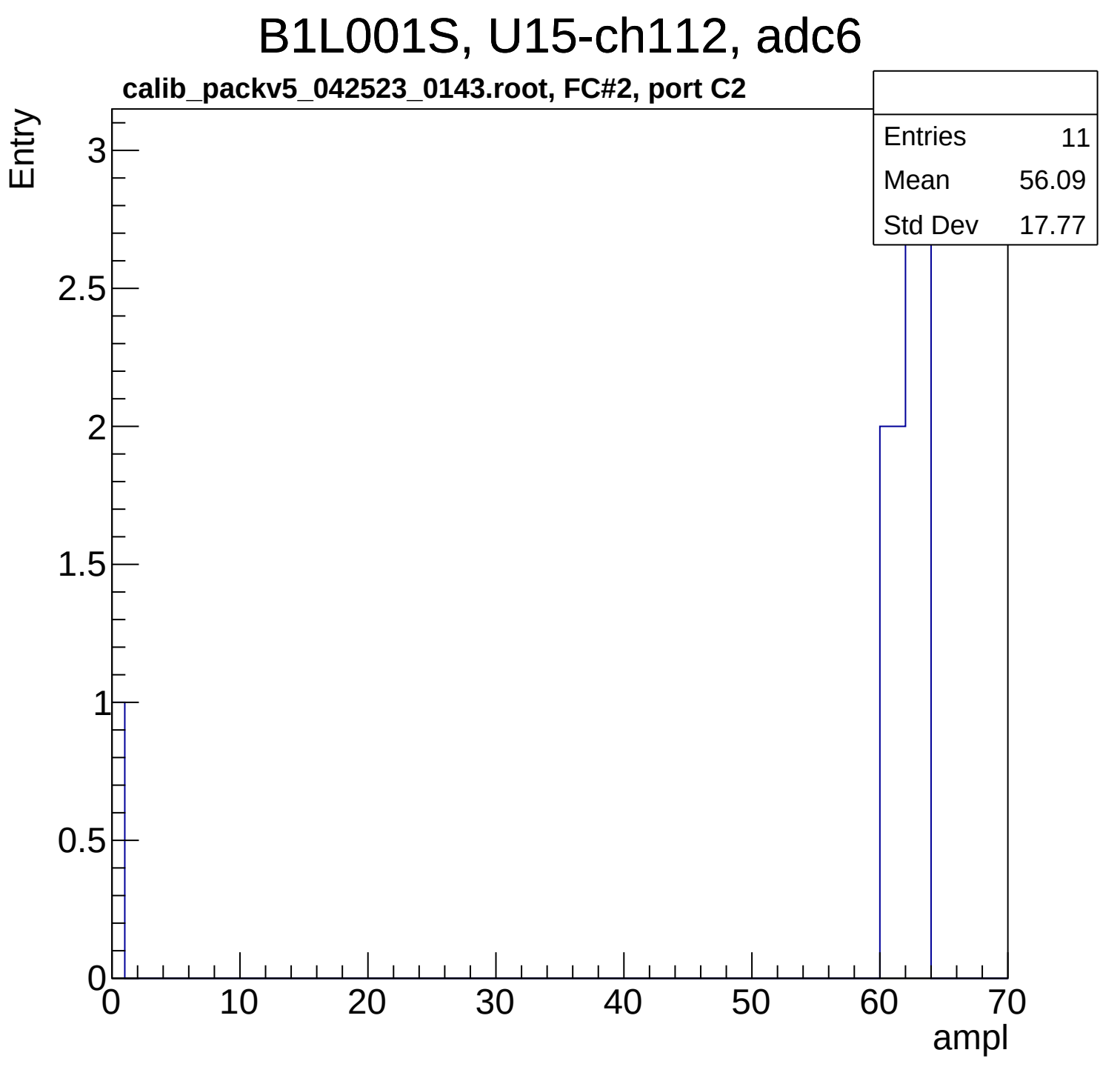
B1L001S, U15-ch112, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 56.09
Std Dev 17.77
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
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40
50
60
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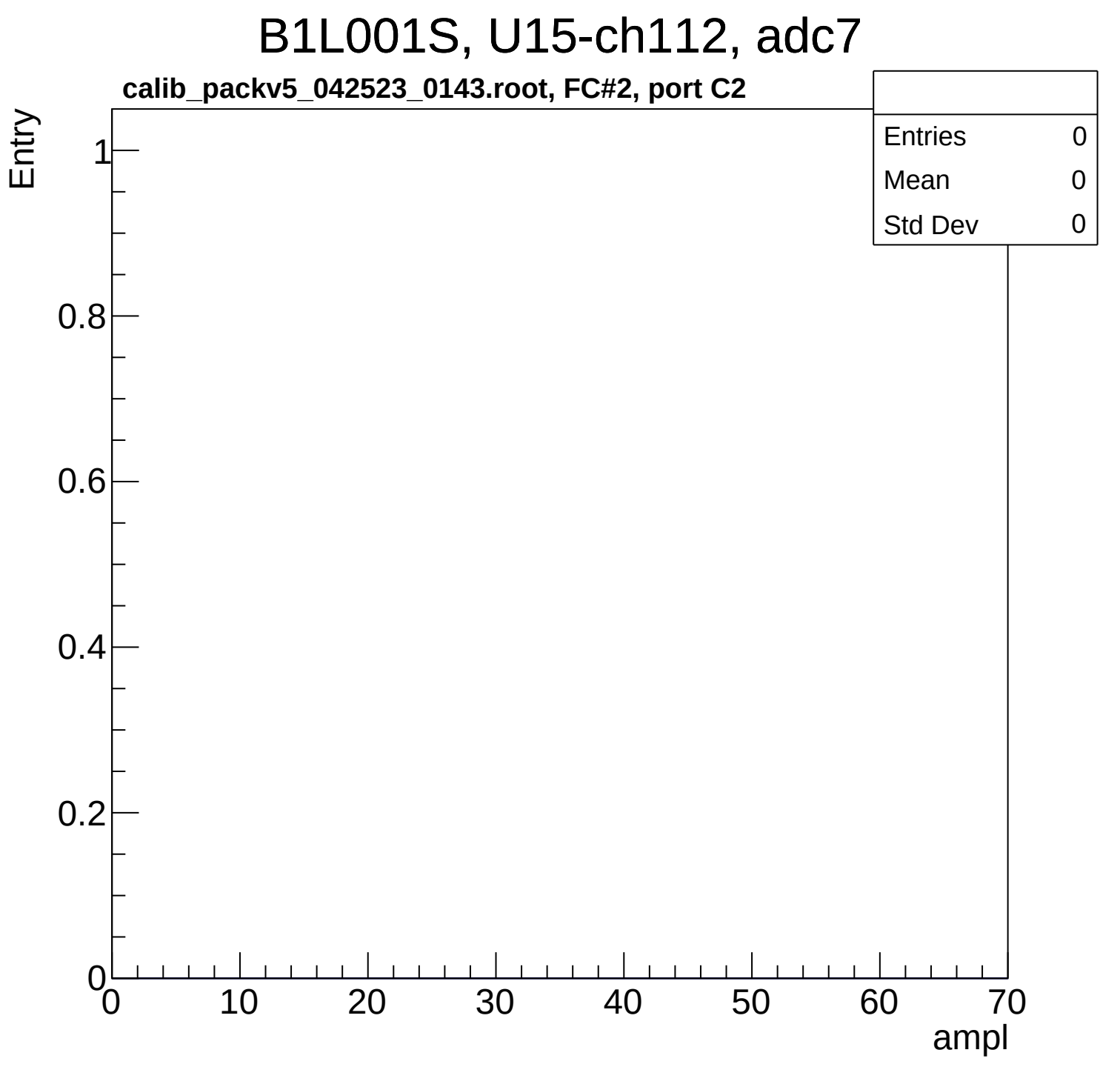
B1L001S, U15-ch112, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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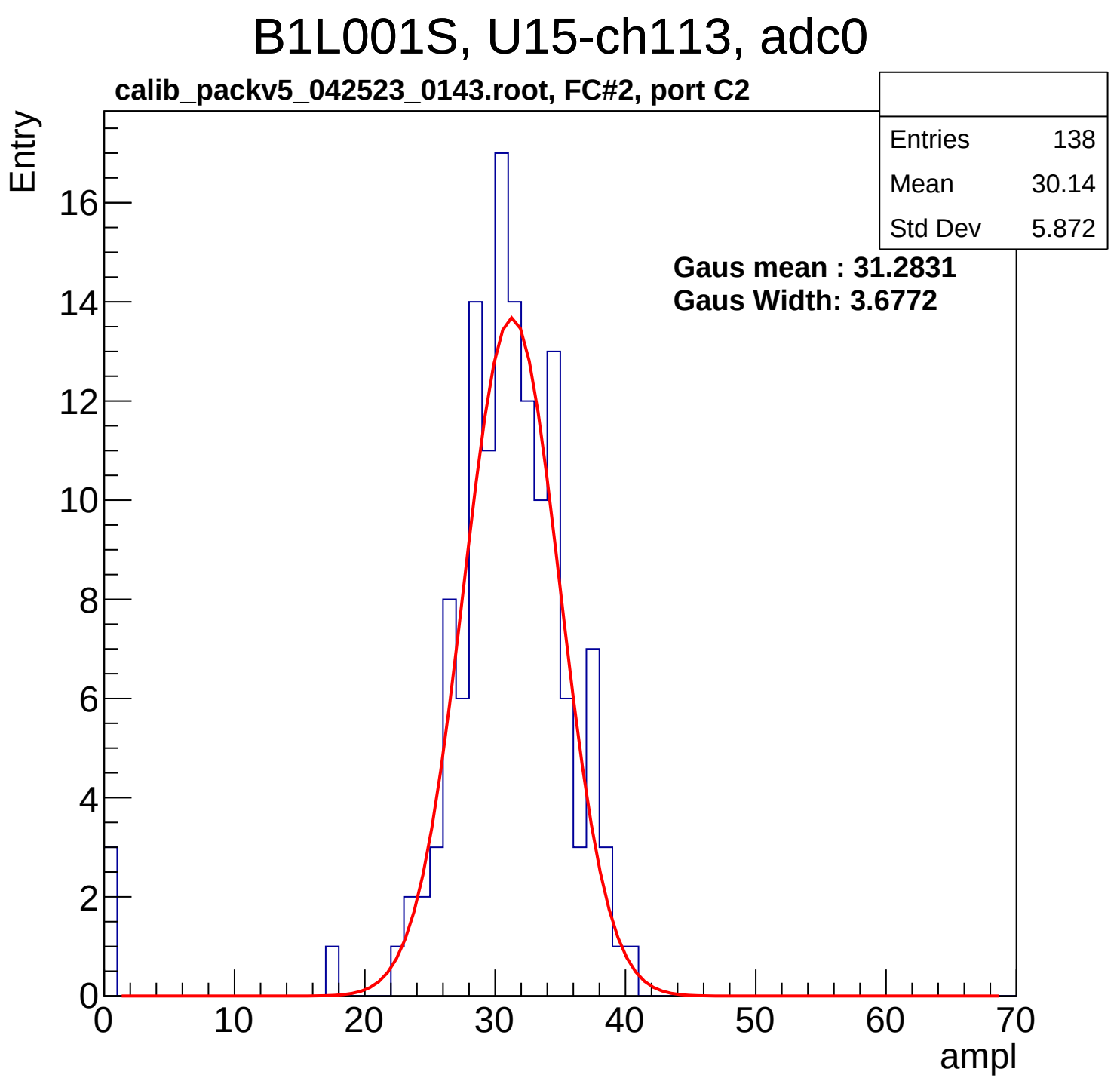
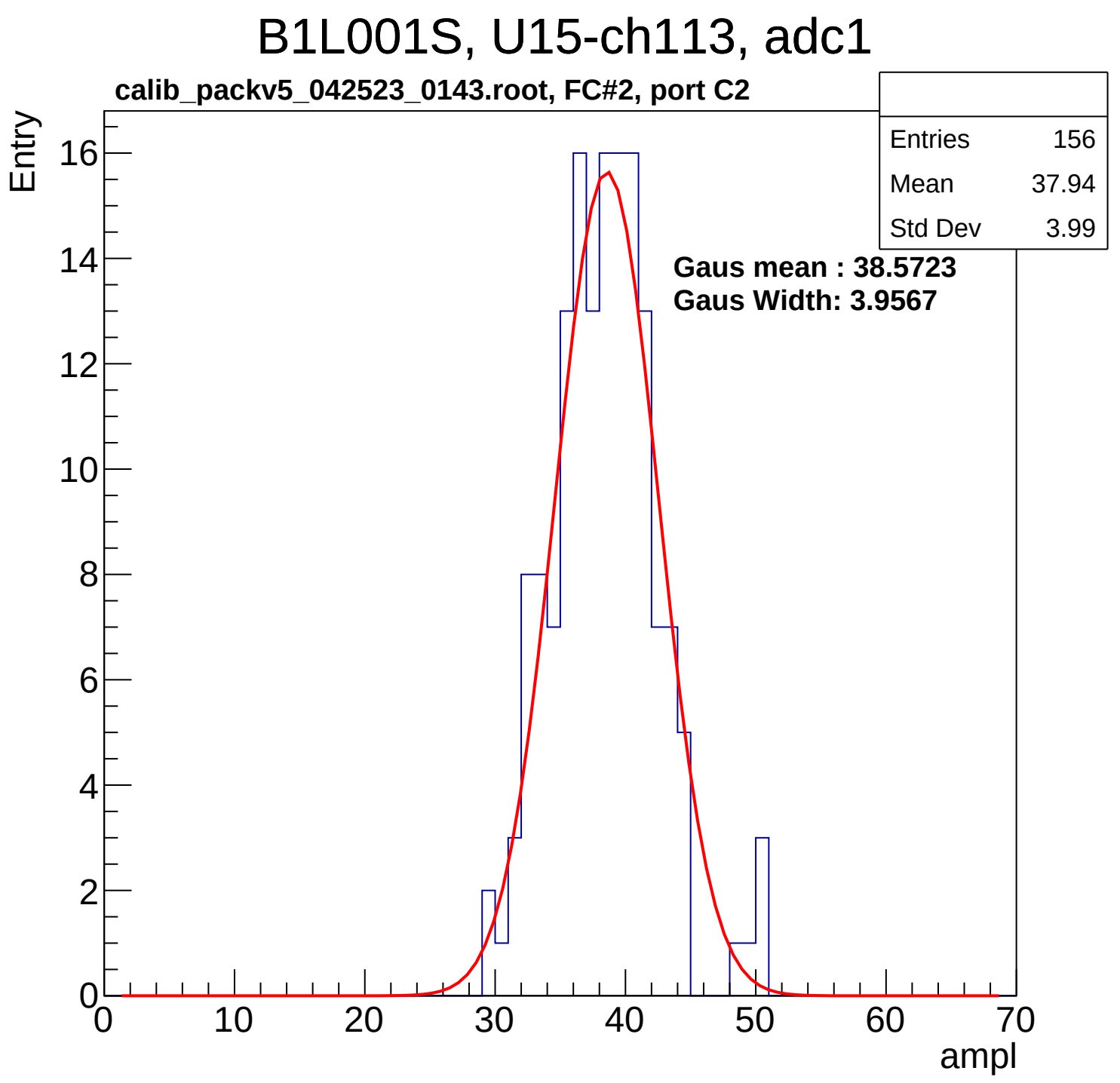
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch113, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 30.14
Std Dev 5.872
Gaus mean : 31.2831
Gaus Width: 3.6772
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch113, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 37.94
Std Dev 3.99
Gaus mean : 38.5723
Gaus Width: 3.9567
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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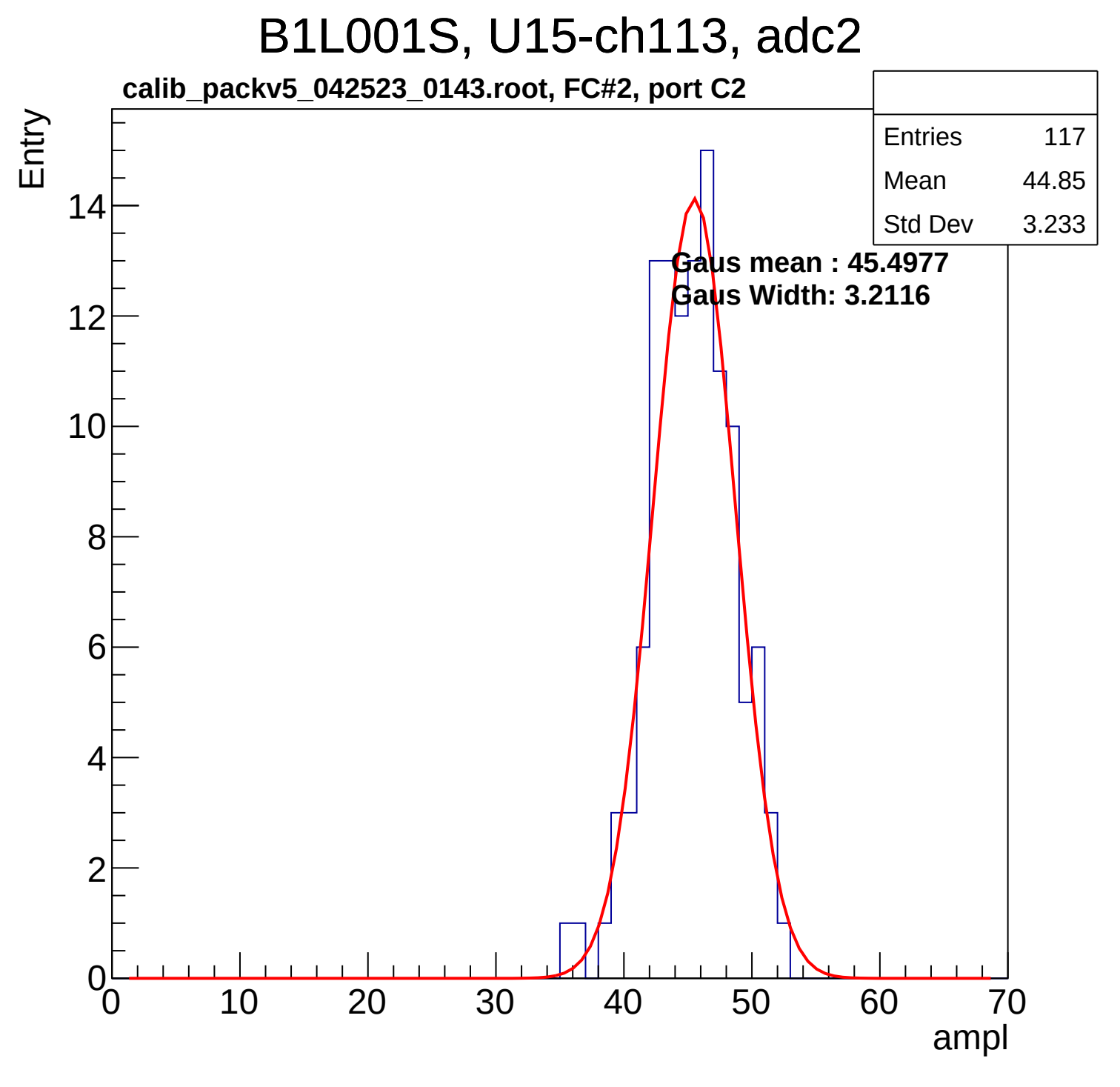
B1L001S, U15-ch113, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 44.85
Std Dev 3.233
Gaus mean : 45.4977
Gaus Width: 3.2116
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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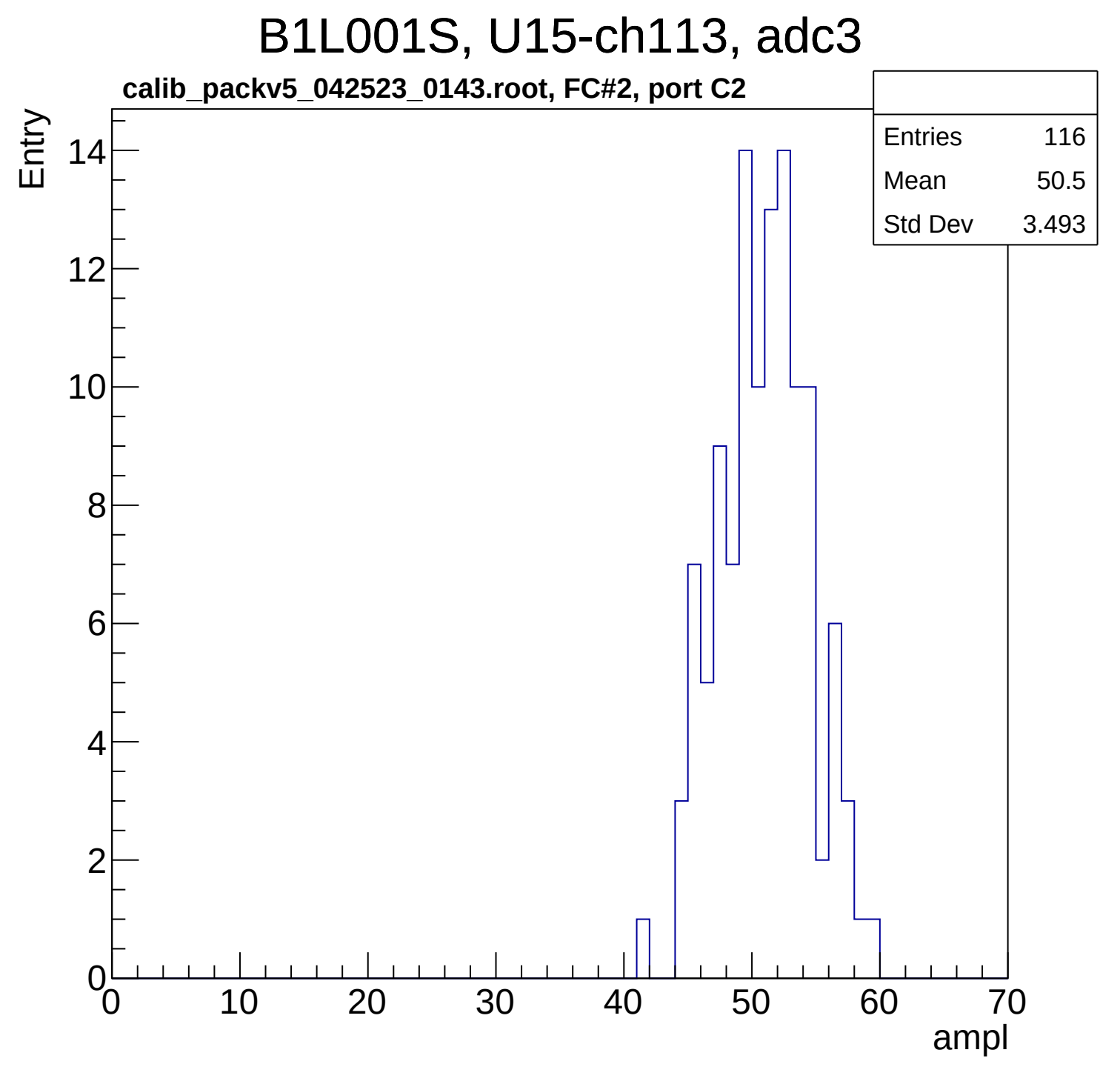
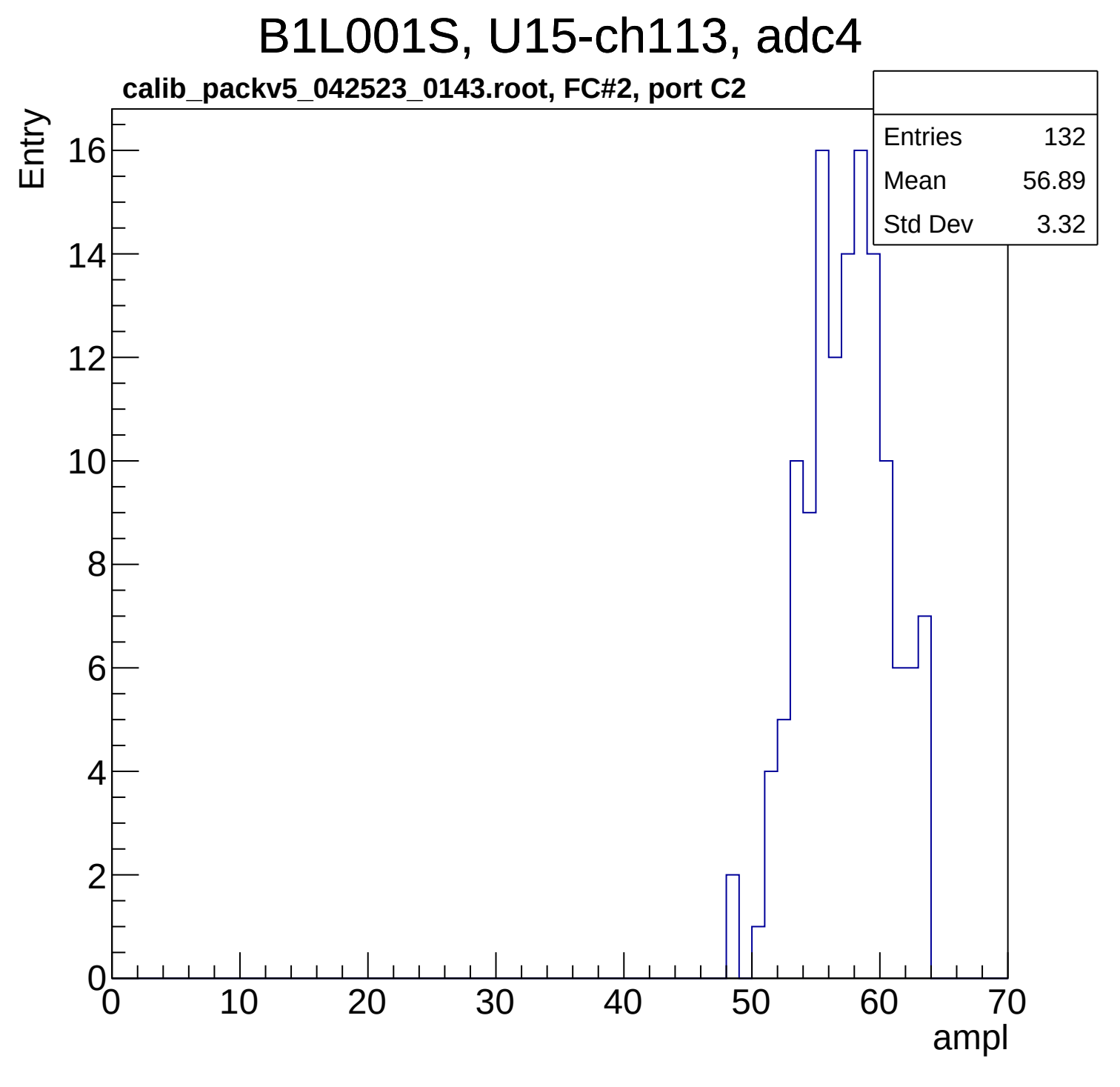

B1L001S, U15-ch113, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 50.5
Std Dev 3.493
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch113, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 56.89
Std Dev 3.32
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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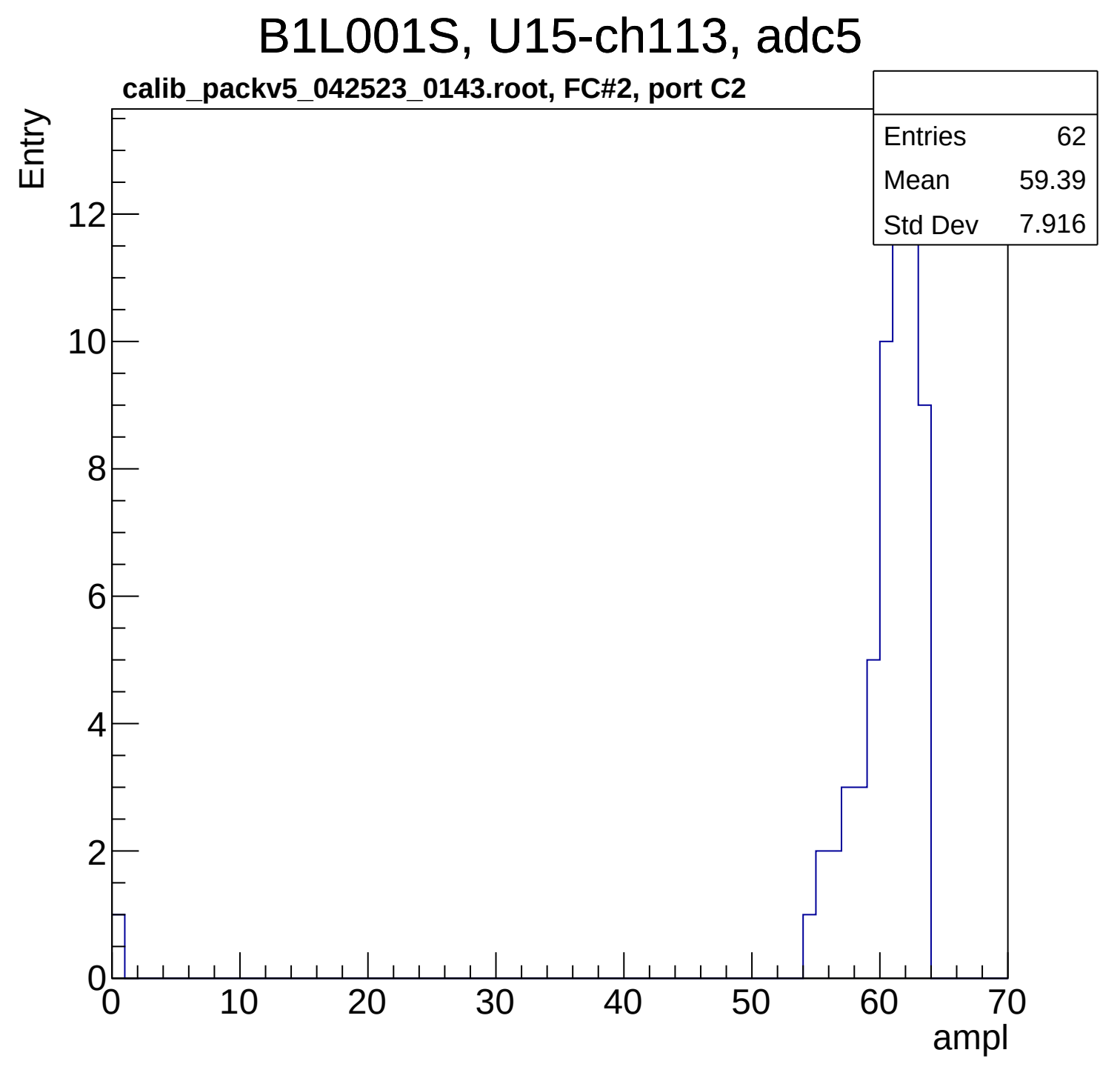
B1L001S, U15-ch113, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 62
Mean 59.39
Std Dev 7.916
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
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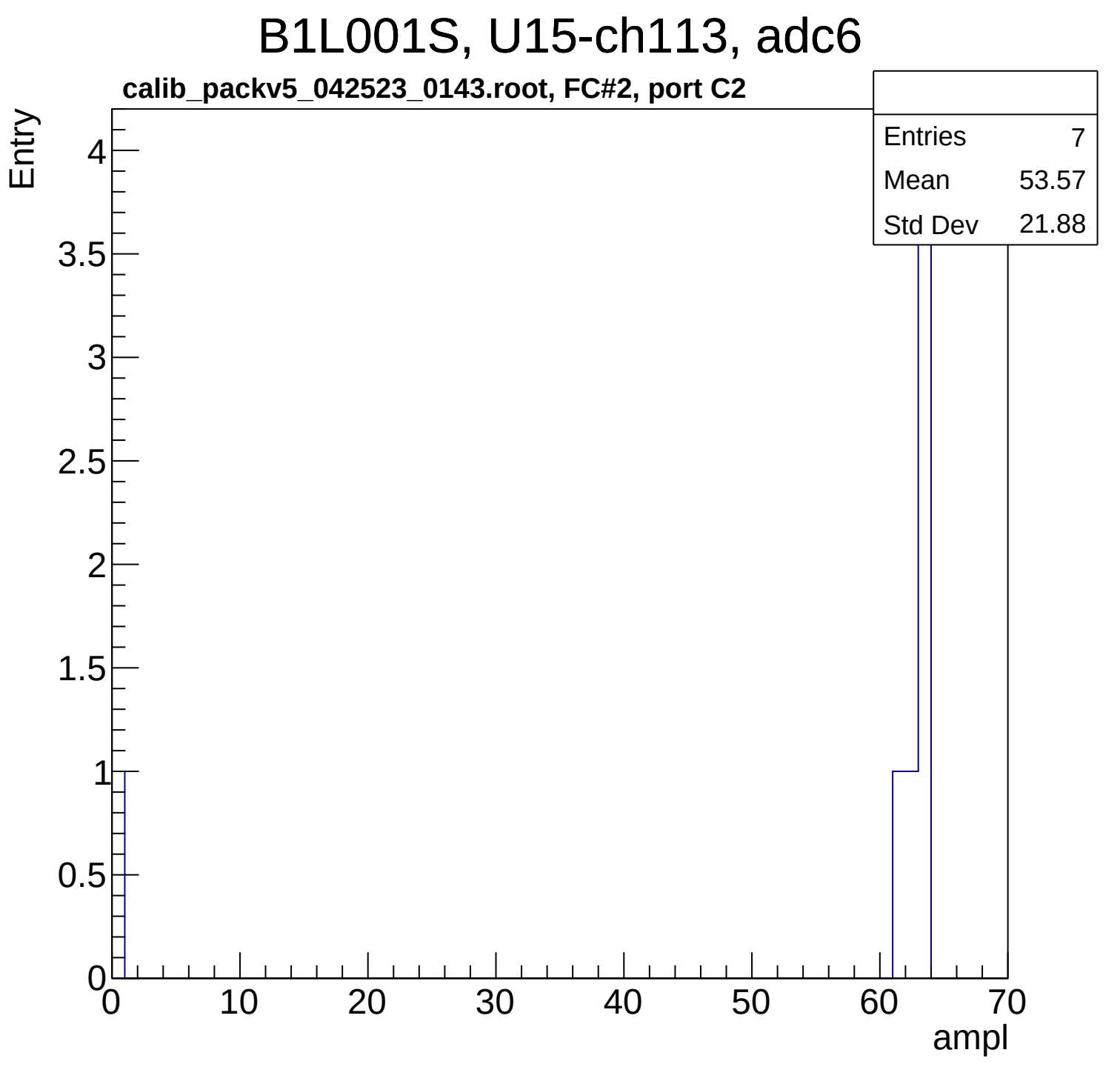
B1L001S, U15-ch113, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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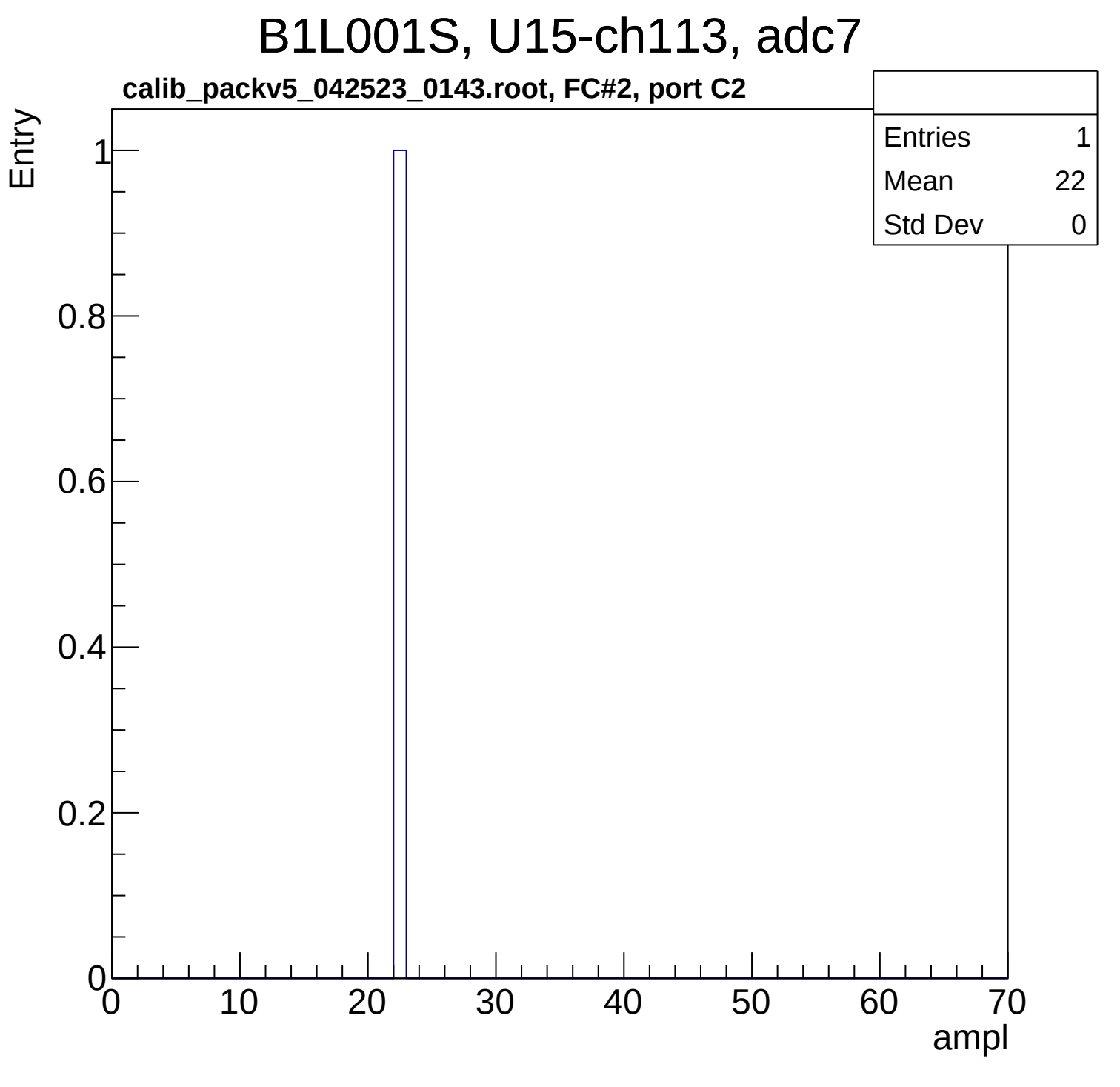
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U15-ch113, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
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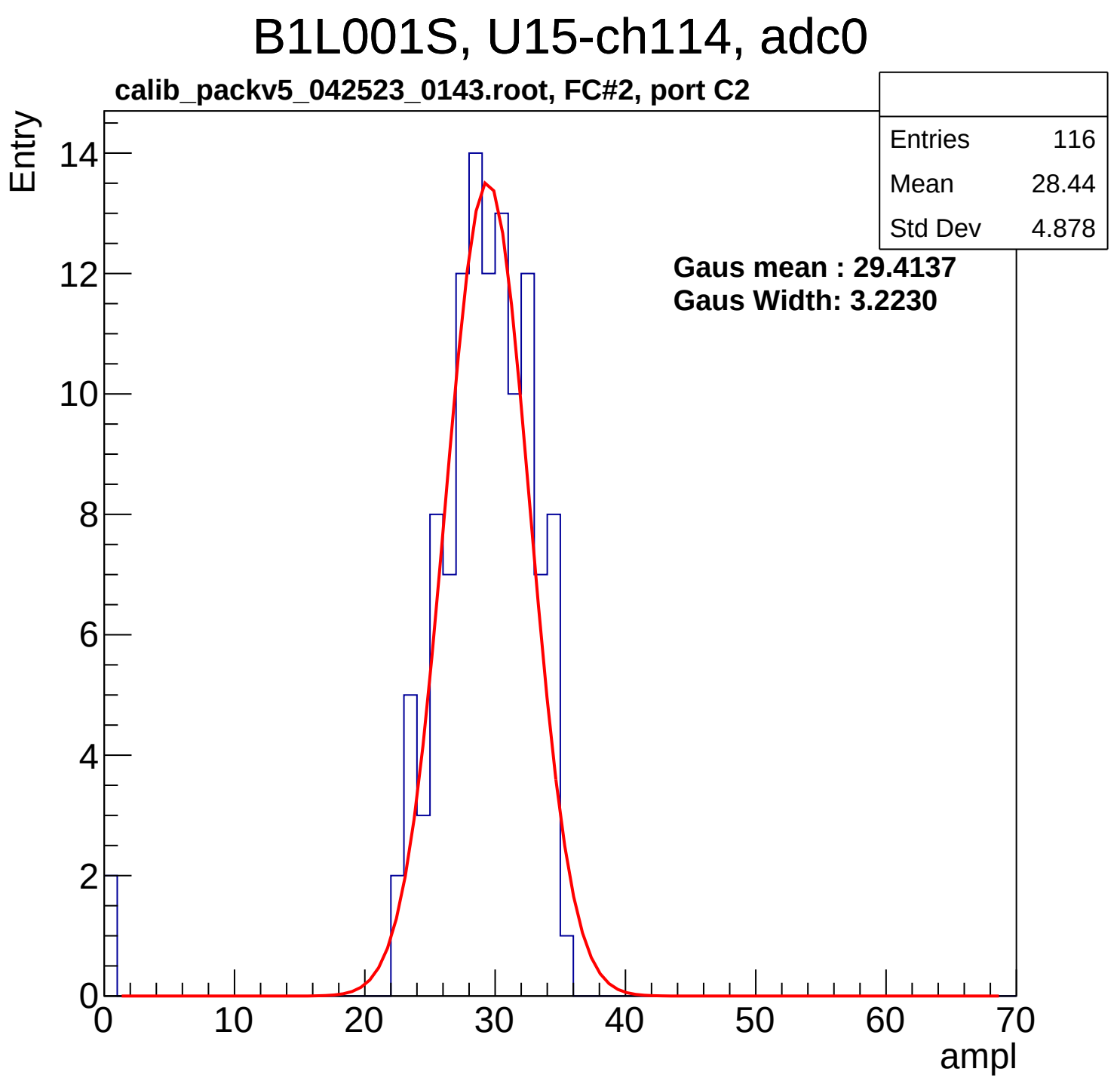
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch114, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 28.44
Std Dev 4.878
Gaus mean : 29.4137
Gaus Width: 3.2230
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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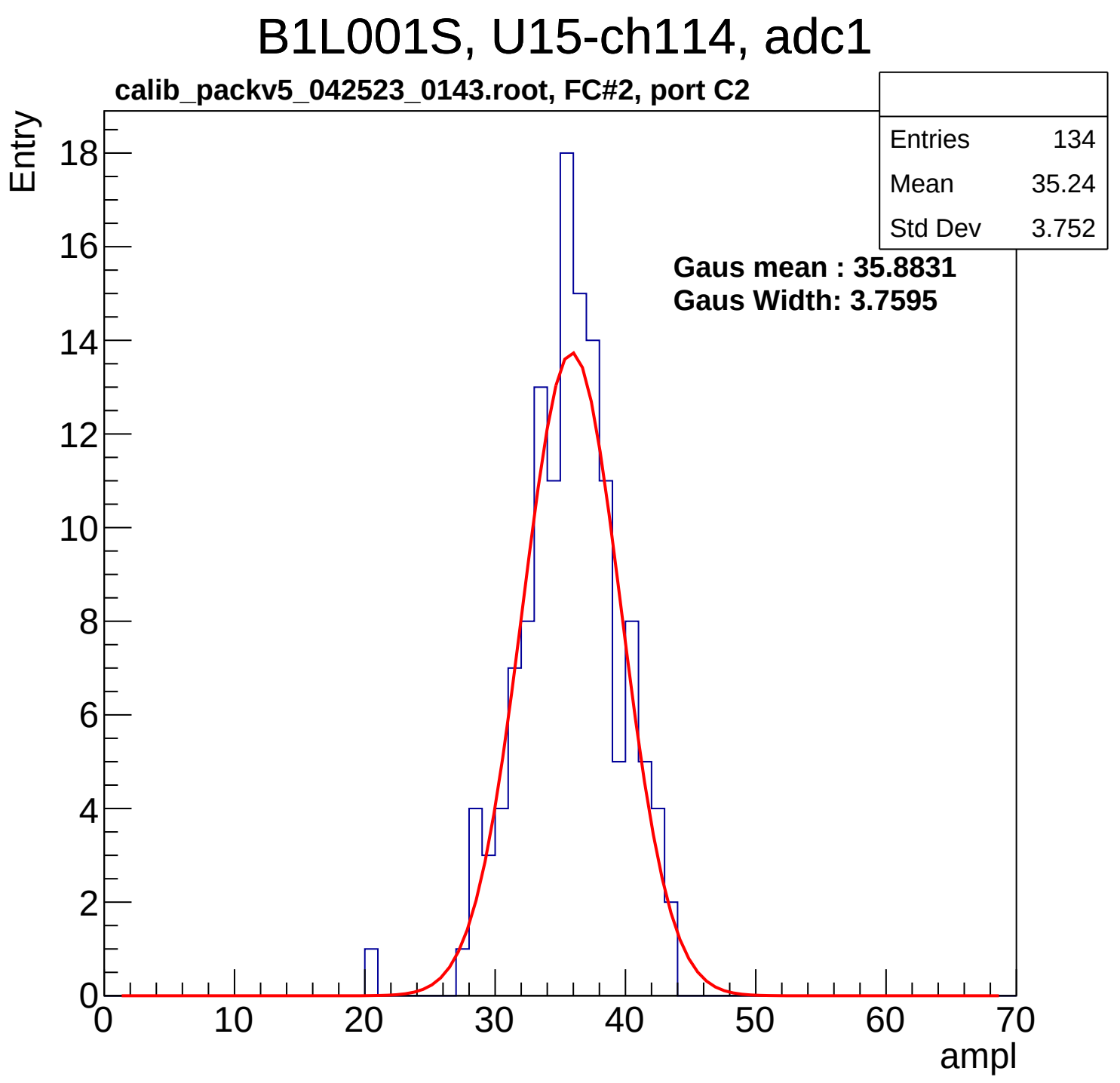
60
70


B1L001S, U15-ch114, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 35.24
Std Dev 3.752
Gaus mean : 35.8831
Gaus Width: 3.7595
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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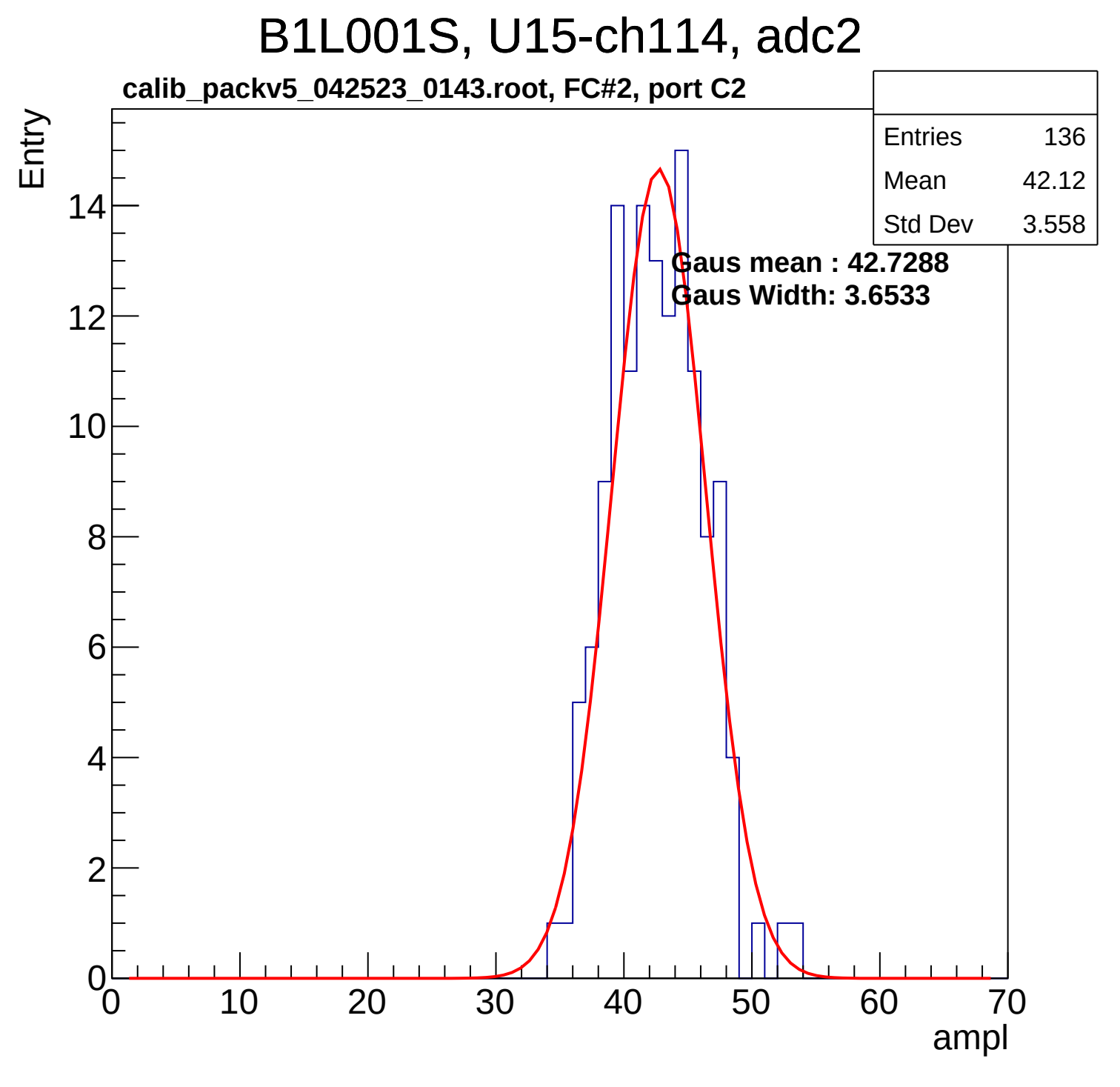
B1L001S, U15-ch114, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 42.12
Std Dev 3.558
Gaus mean : 42.7288
Gaus Width: 3.6533
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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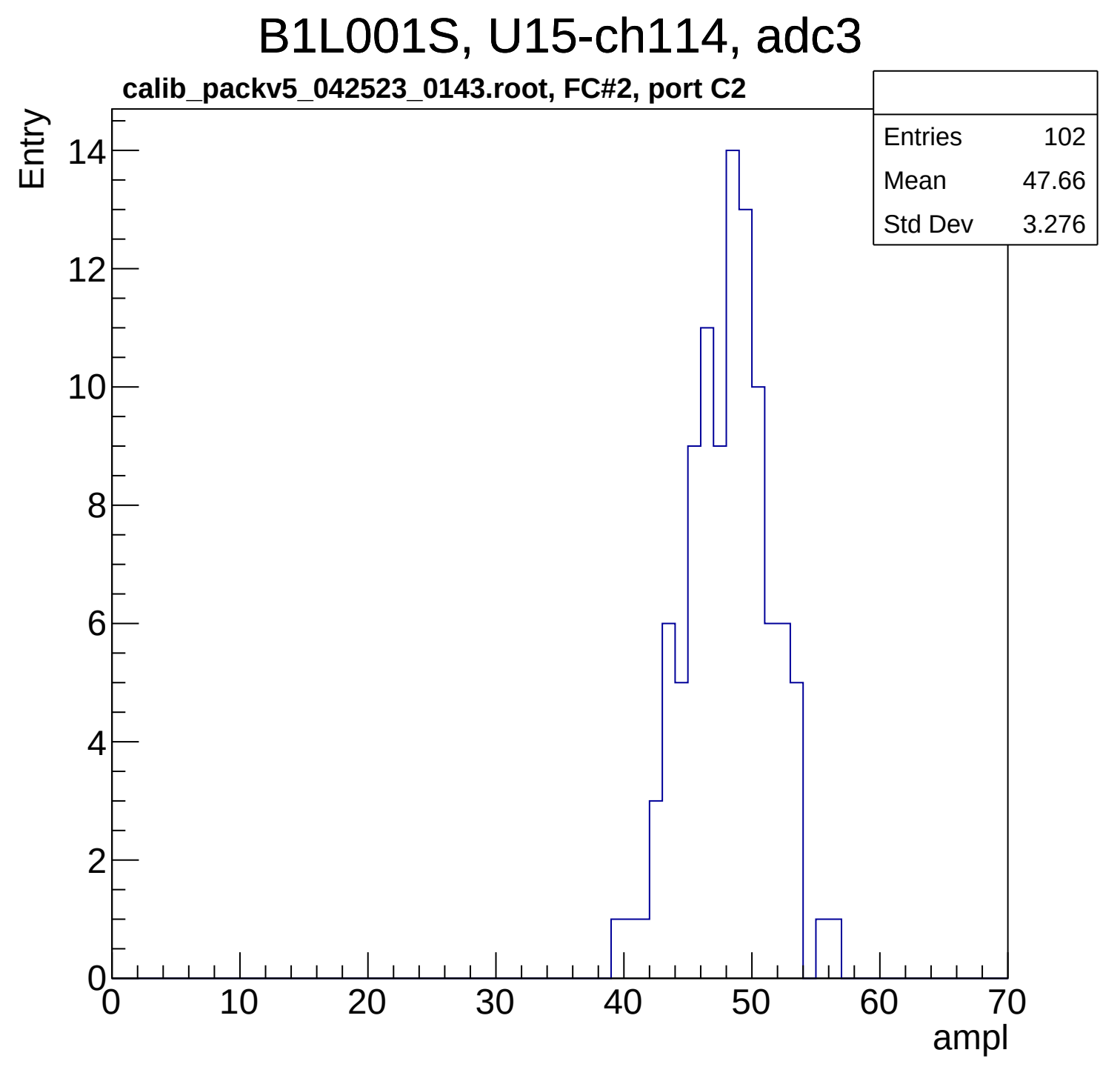
B1L001S, U15-ch114, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 47.66
Std Dev 3.276
Entry
ampl
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8
10
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14
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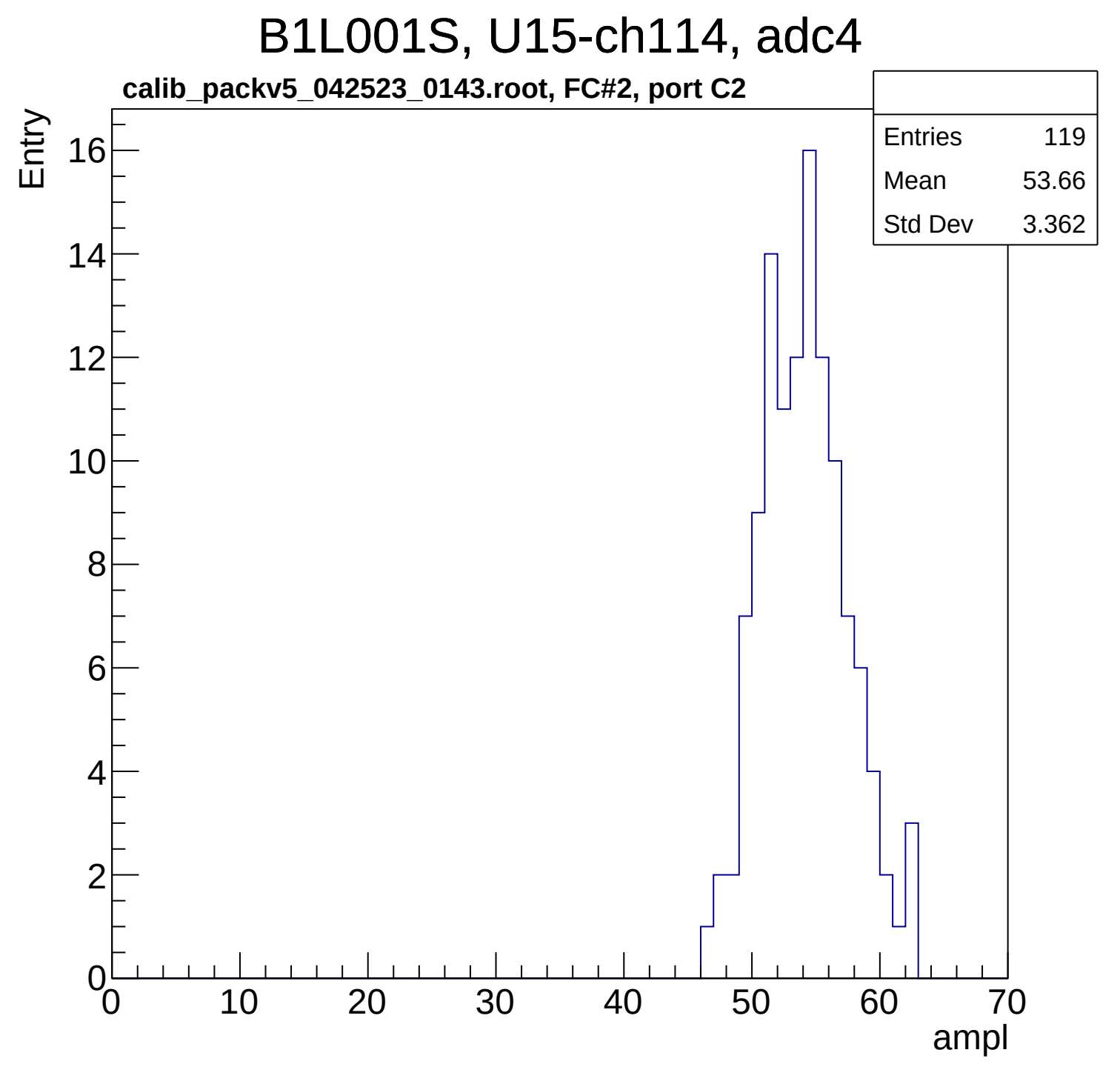
B1L001S, U15-ch114, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 53.66
Std Dev 3.362
Entry
ampl
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10
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0
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60
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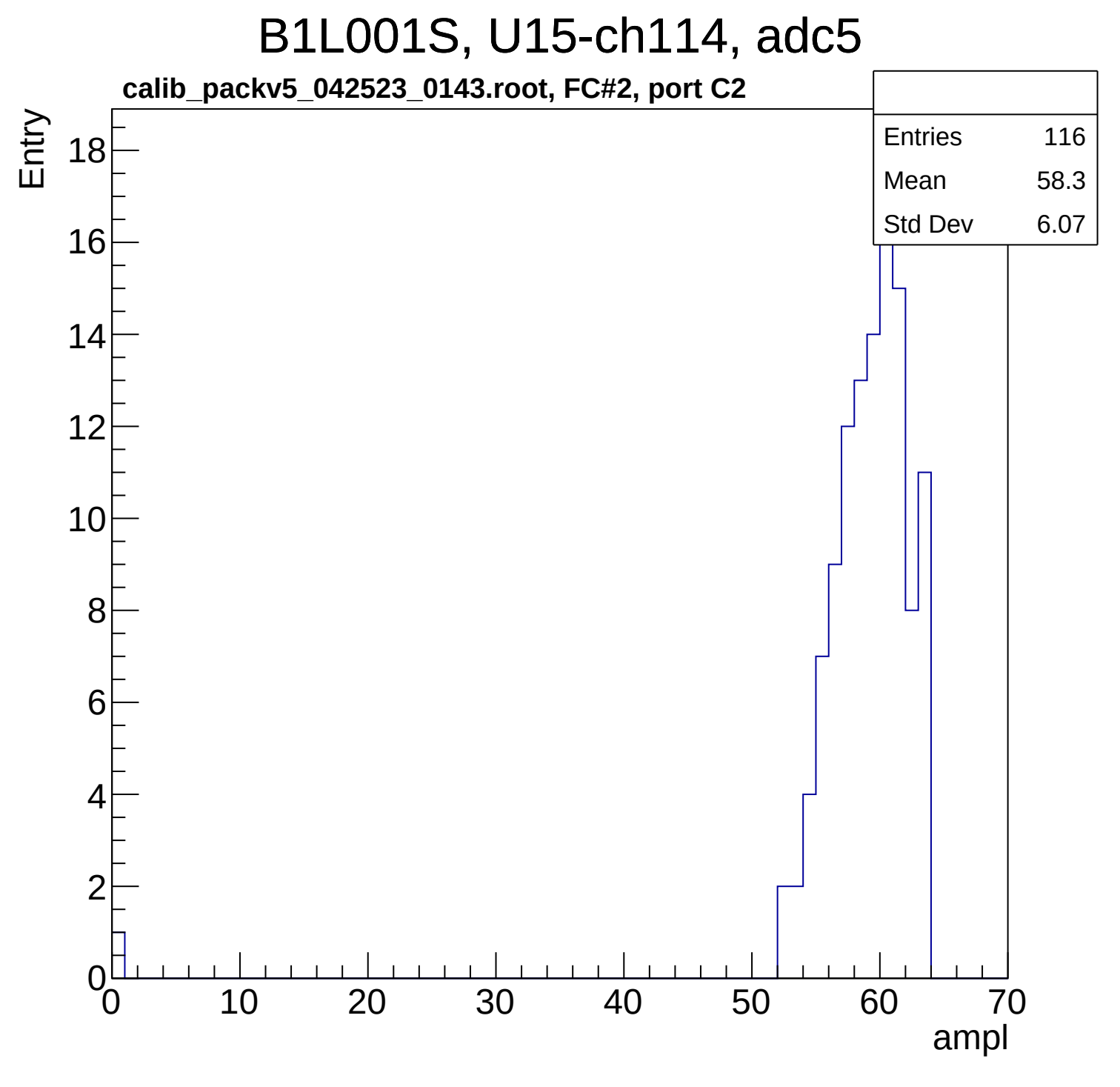
B1L001S, U15-ch114, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 58.3
Std Dev 6.07
Entry
ampl
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6
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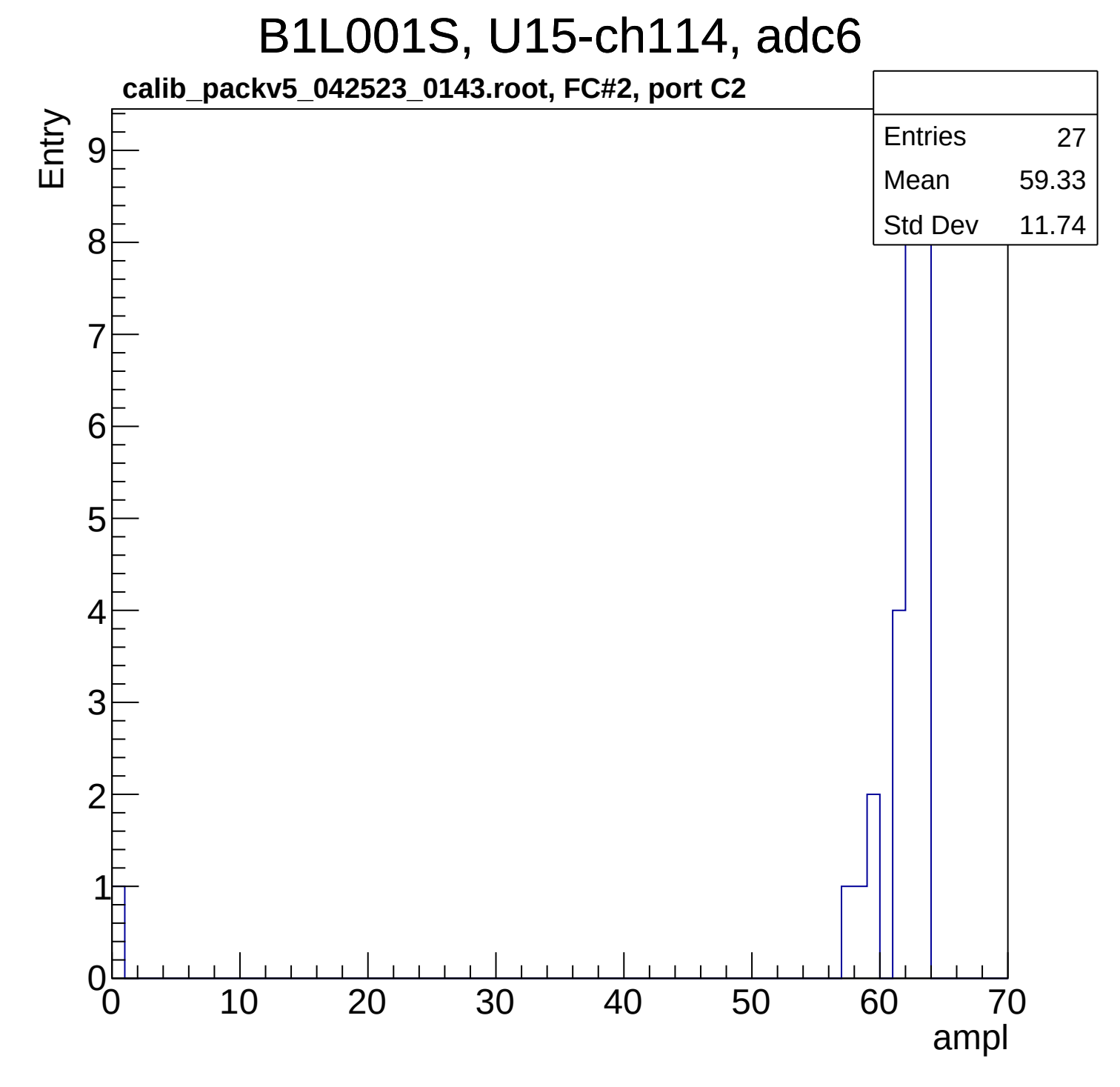
B1L001S, U15-ch114, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 27
Mean 59.33
Std Dev 11.74
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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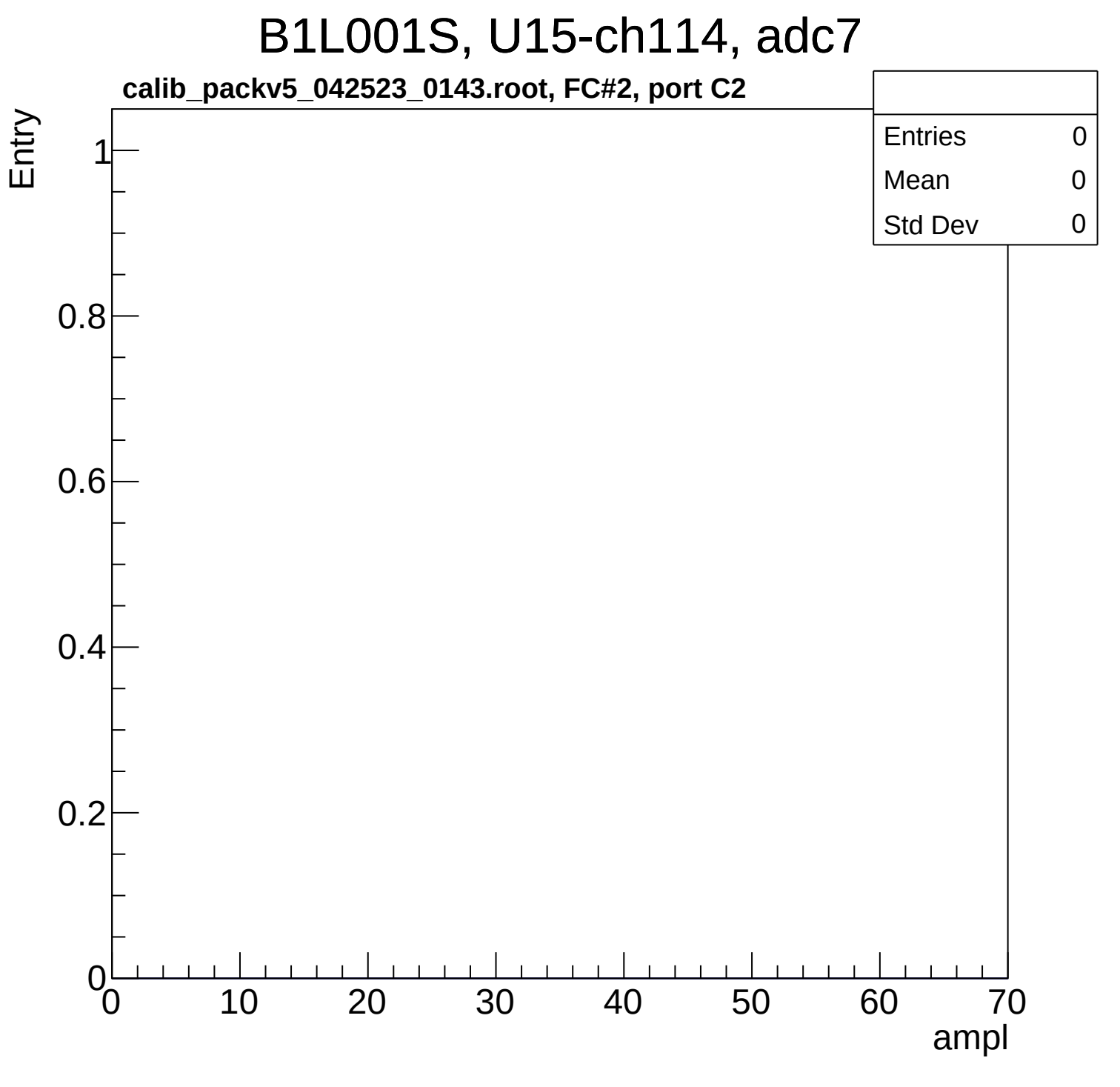
B1L001S, U15-ch114, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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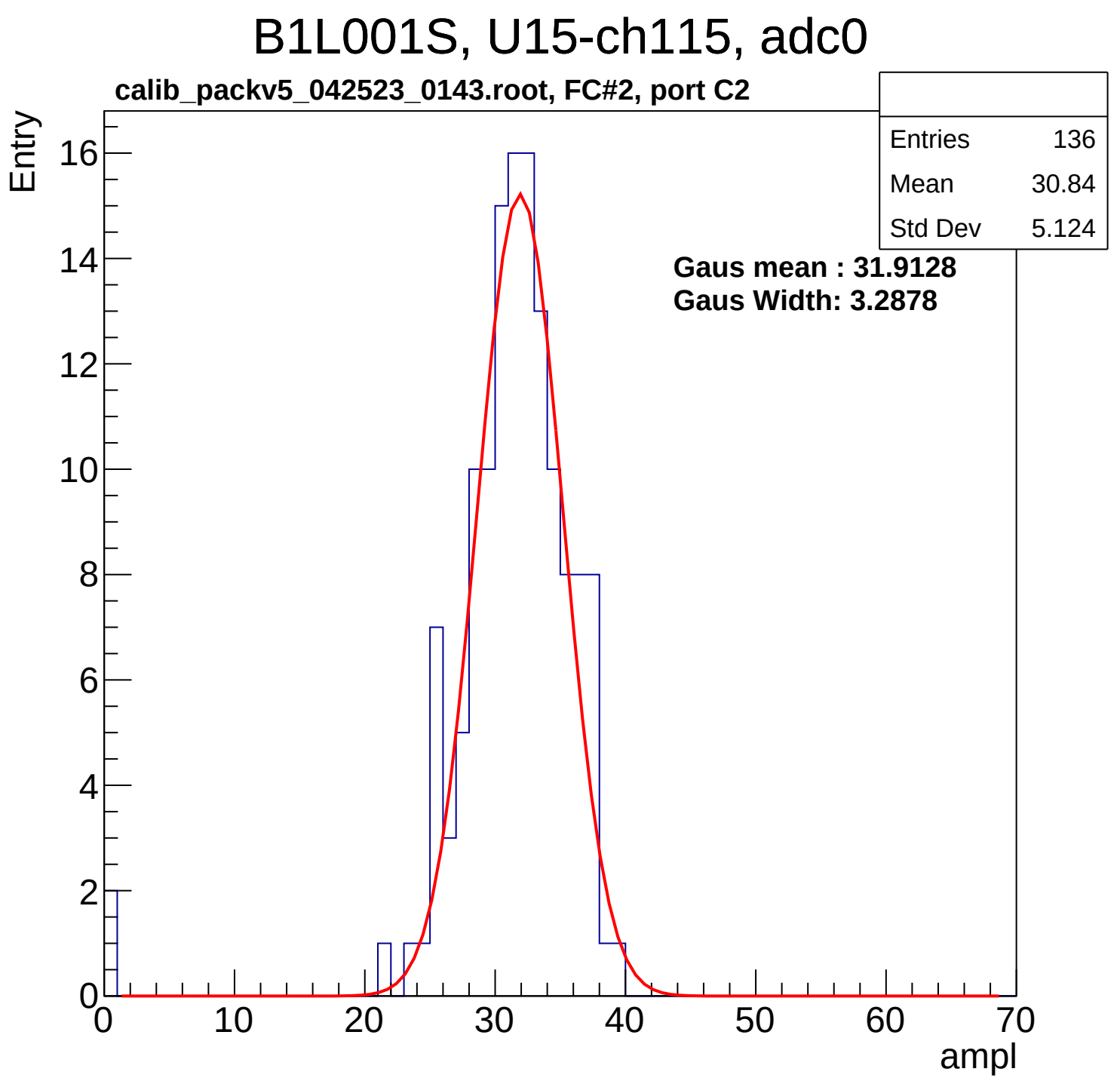
70
ampl


B1L001S, U15-ch115, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 30.84
Std Dev 5.124
Gaus mean : 31.9128
Gaus Width: 3.2878
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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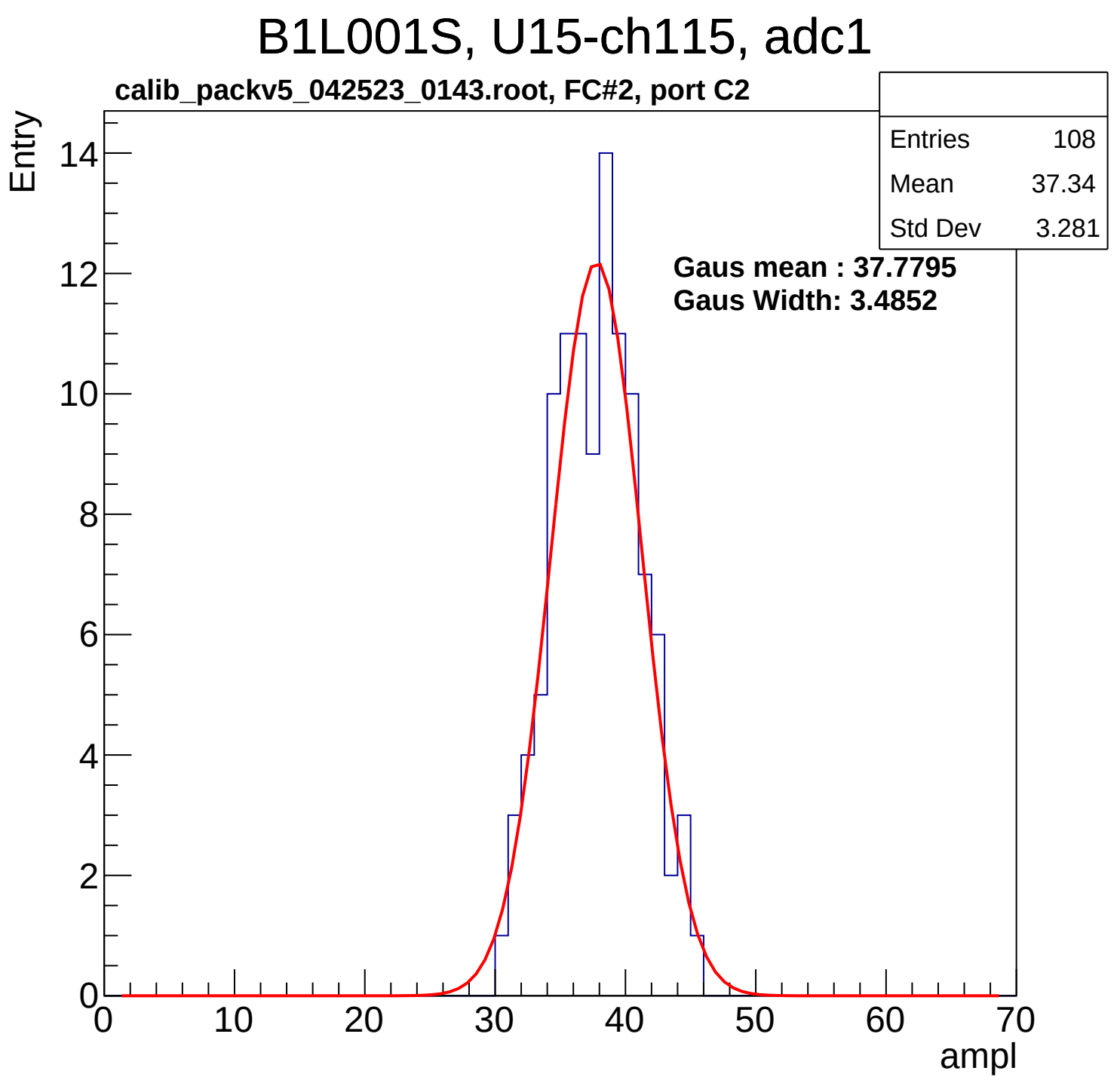
B1L001S, U15-ch115, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 37.34
Std Dev 3.281
Gaus mean : 37.7795
Gaus Width: 3.4852
Entry
ampl
0
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4
6
8
10
12
14
0
10
20
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40
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60
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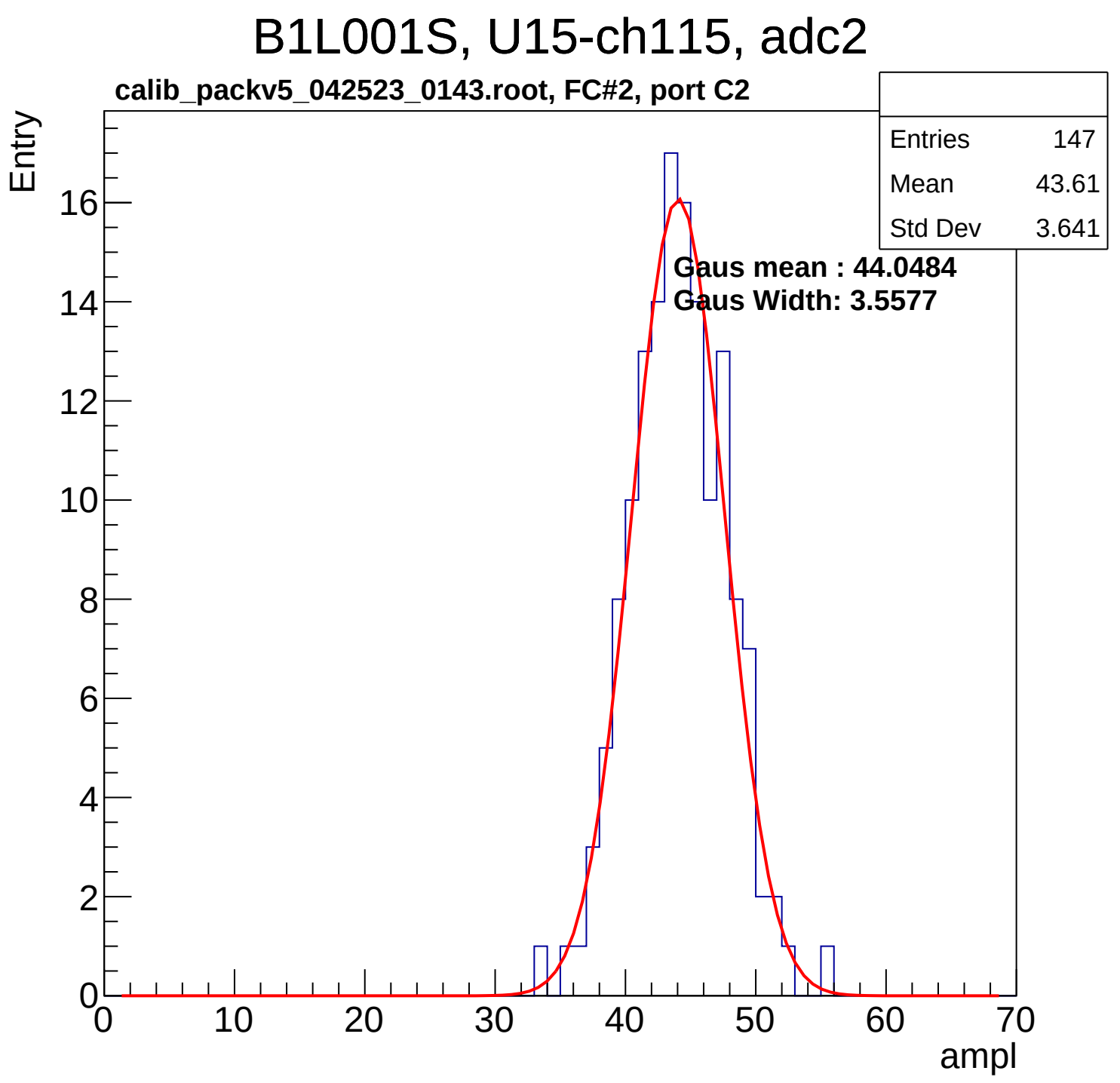
B1L001S, U15-ch115, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 43.61
Std Dev 3.641
Gaus mean : 44.0484
Gaus Width: 3.5577
Entry
ampl
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4
6
8
10
12
14
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0
10
20
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60
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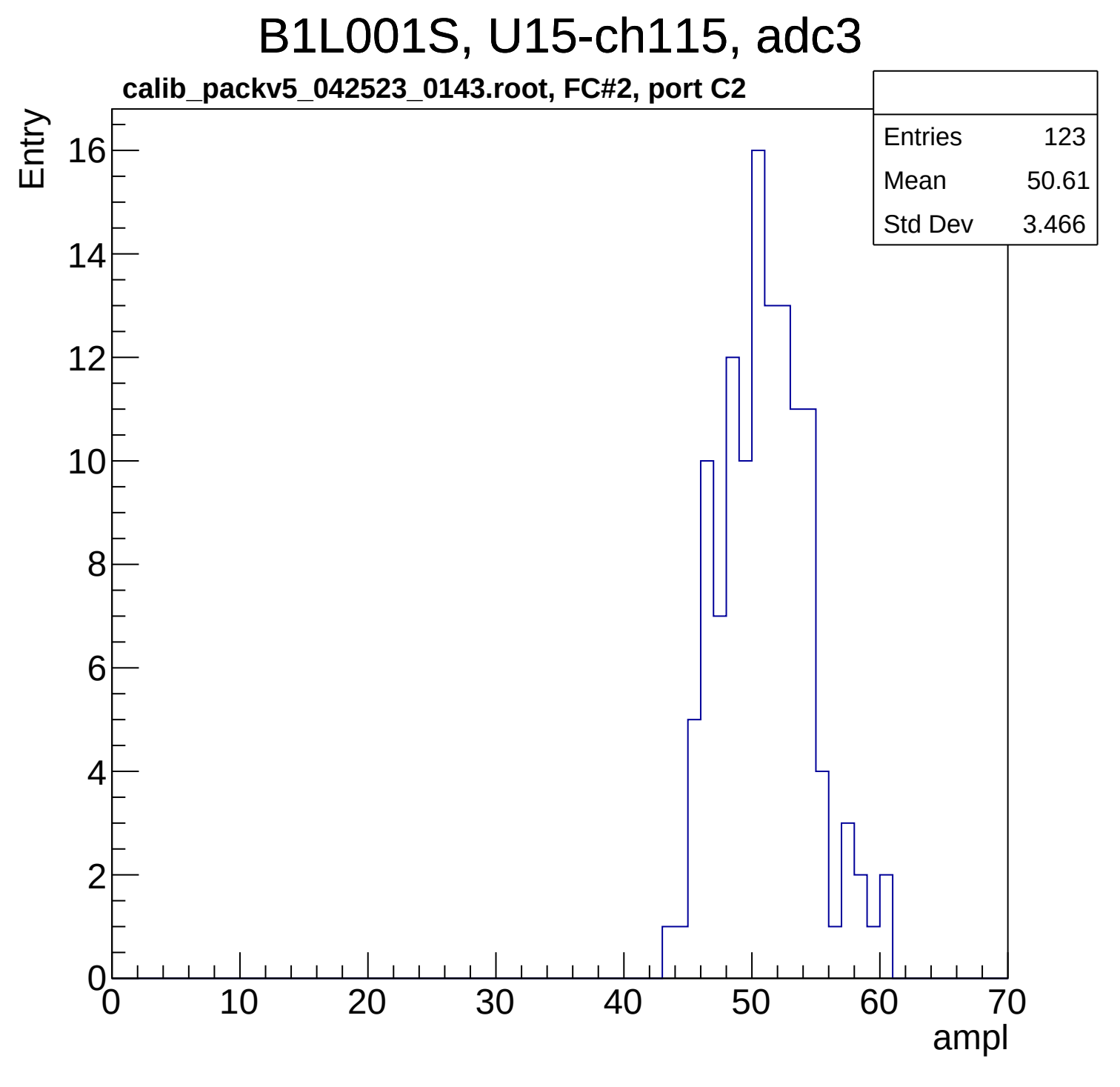
B1L001S, U15-ch115, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 50.61
Std Dev 3.466
Entry
ampl
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2
4
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8
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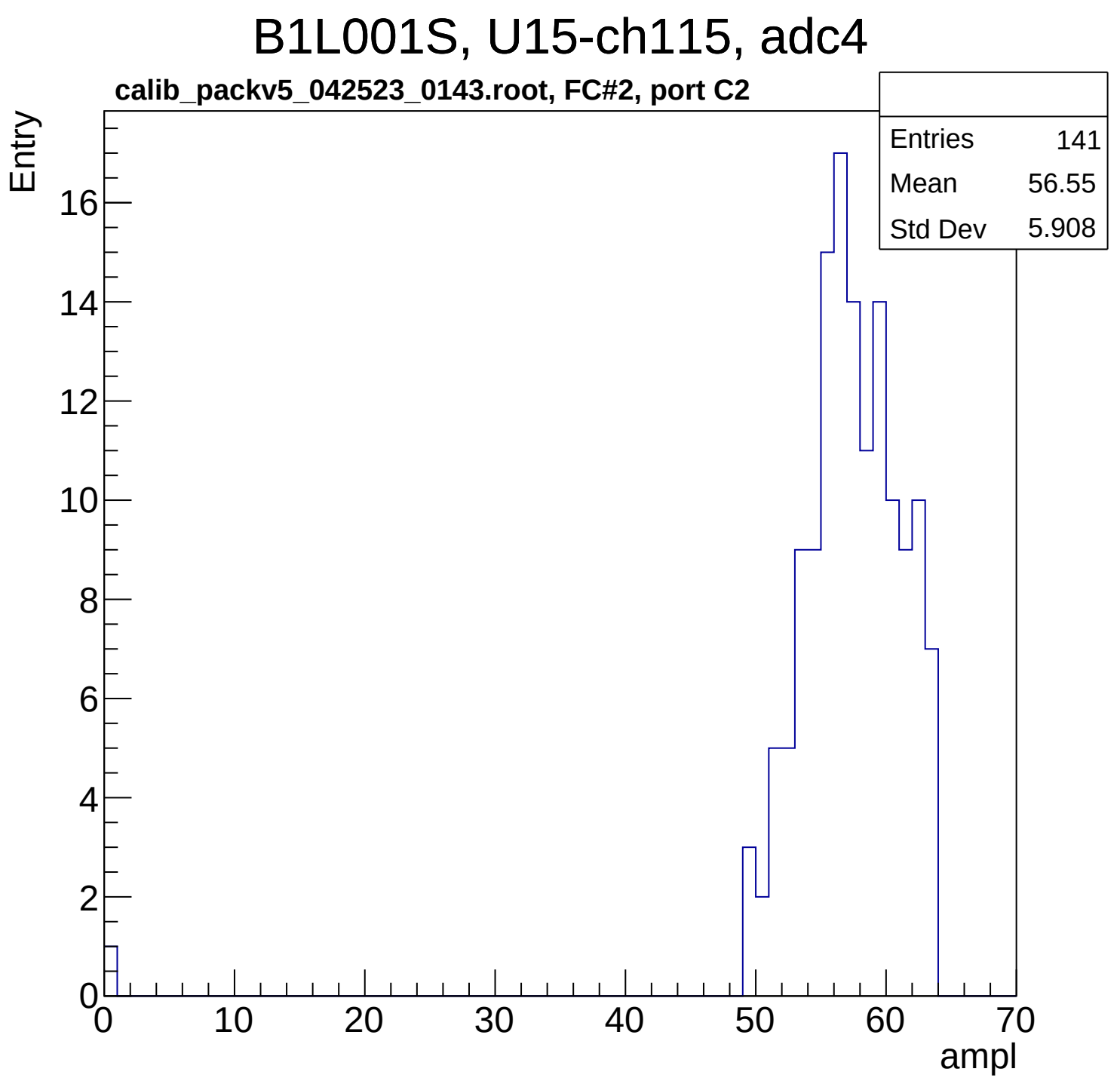
B1L001S, U15-ch115, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 56.55
Std Dev 5.908
Entry
ampl
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6
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10
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10
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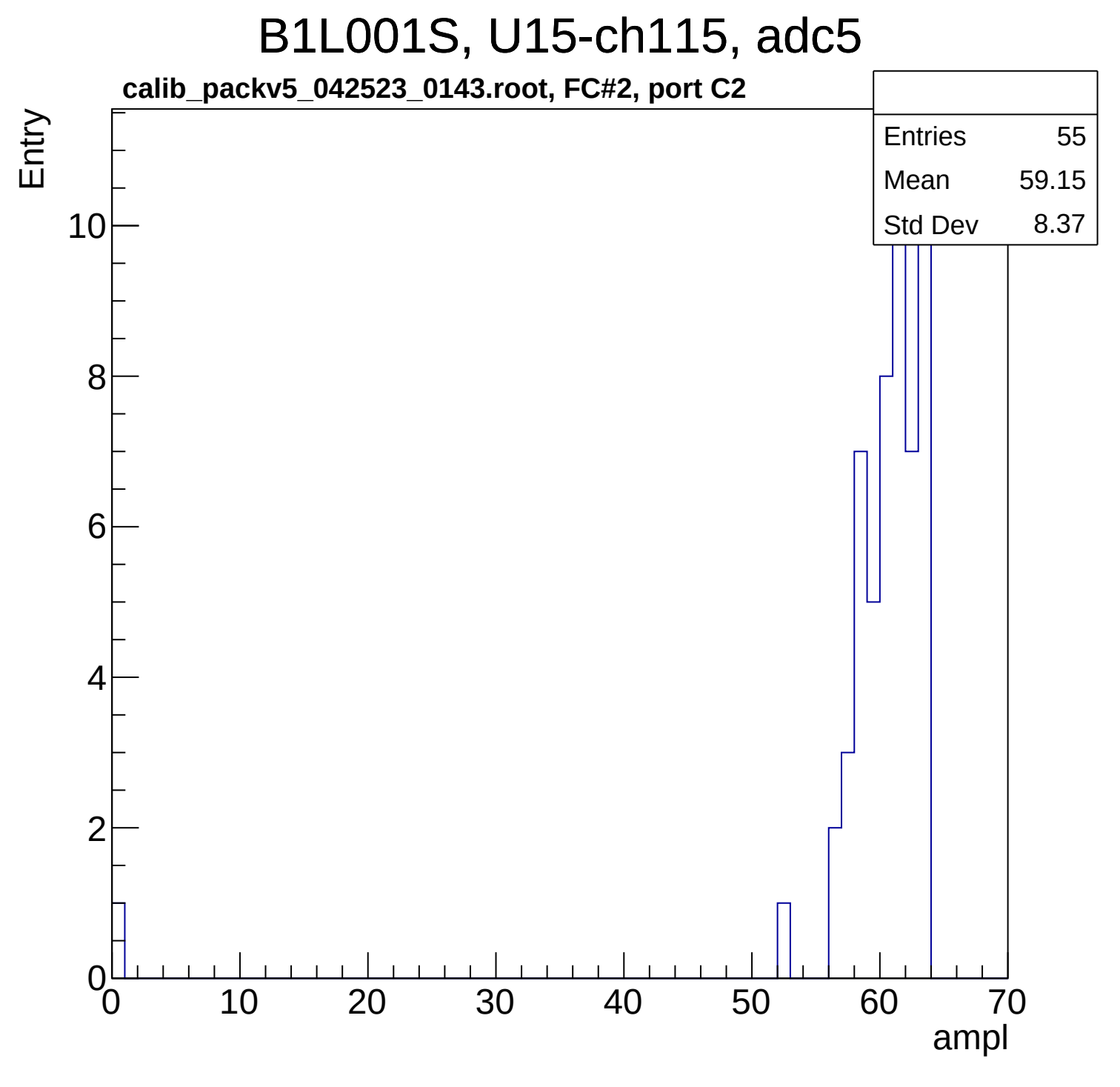
B1L001S, U15-ch115, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 55
Mean 59.15
Std Dev 8.37
Entry
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10
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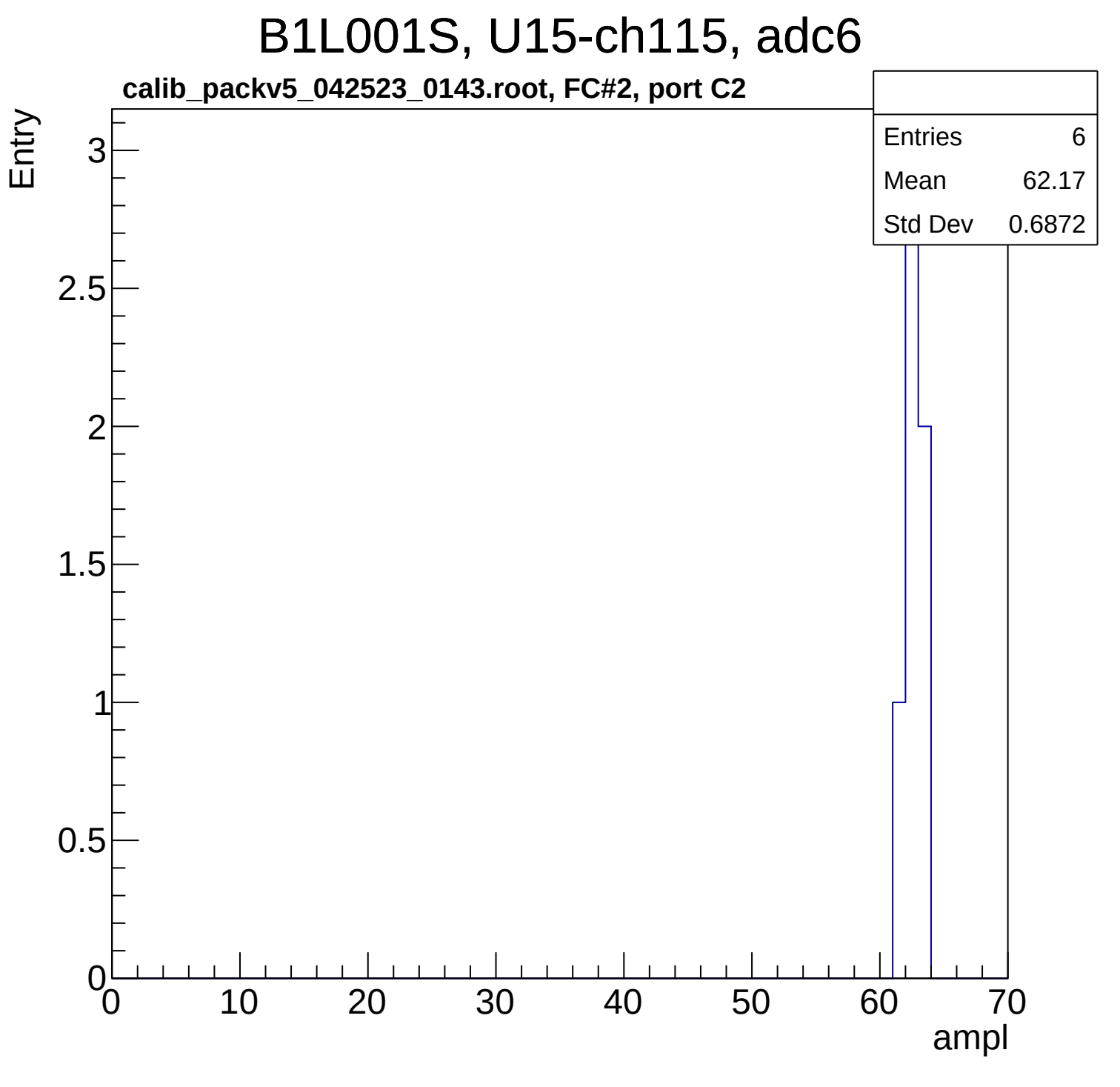
60
70
ampl


B1L001S, U15-ch115, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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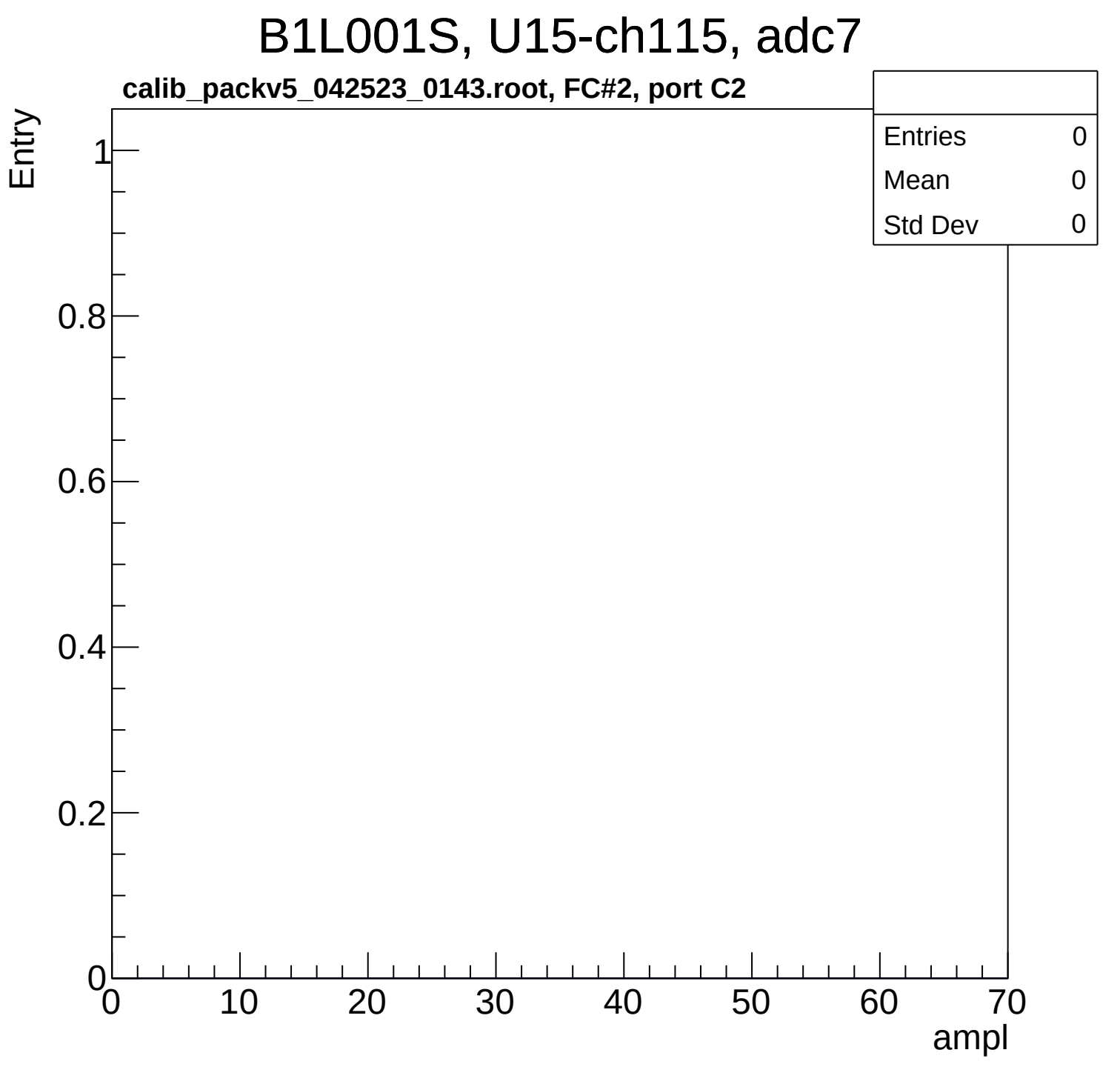
B1L001S, U15-ch115, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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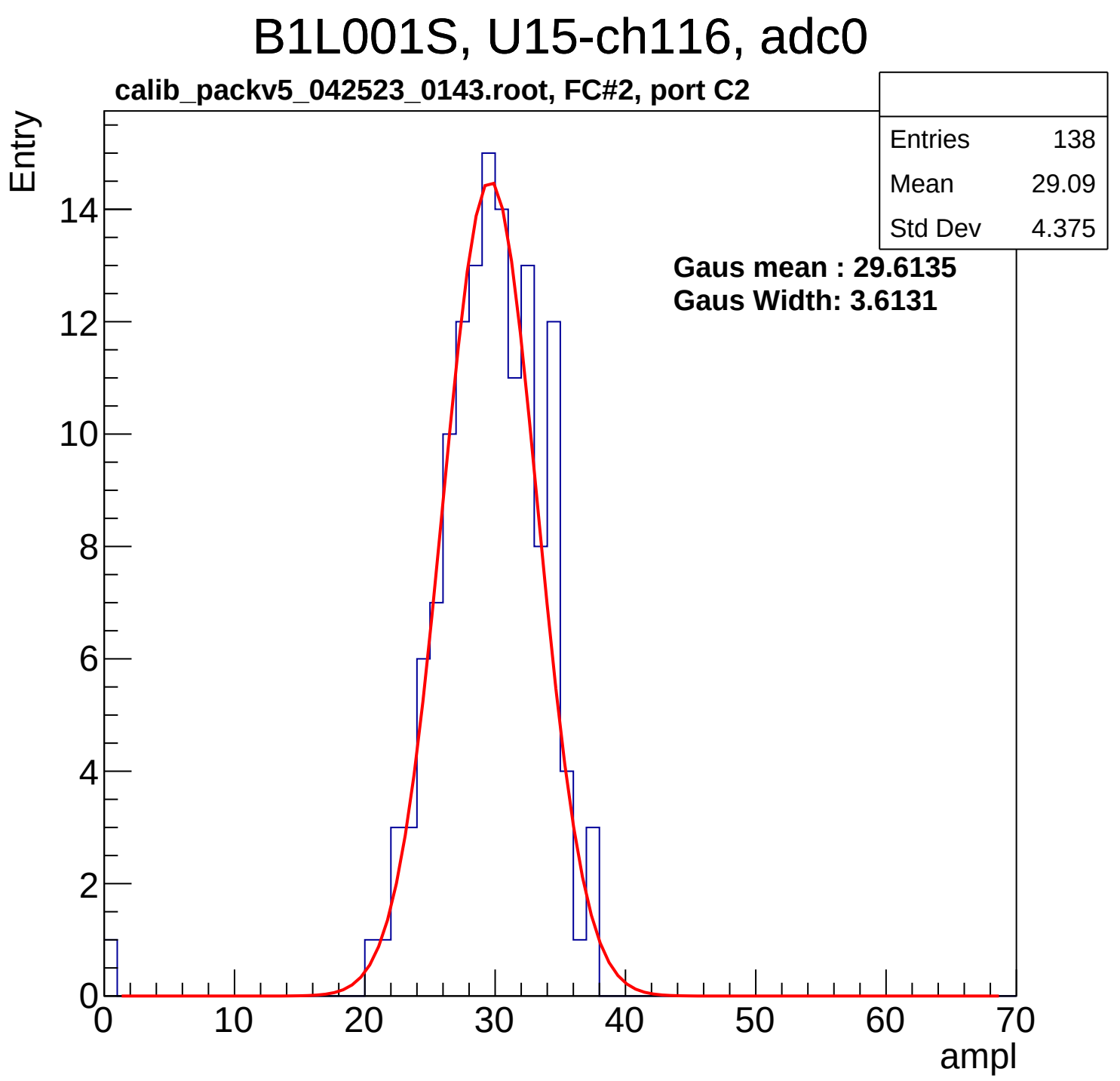
40
50
60
70
ampl


B1L001S, U15-ch116, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 29.09
Std Dev 4.375
Gaus mean : 29.6135
Gaus Width: 3.6131
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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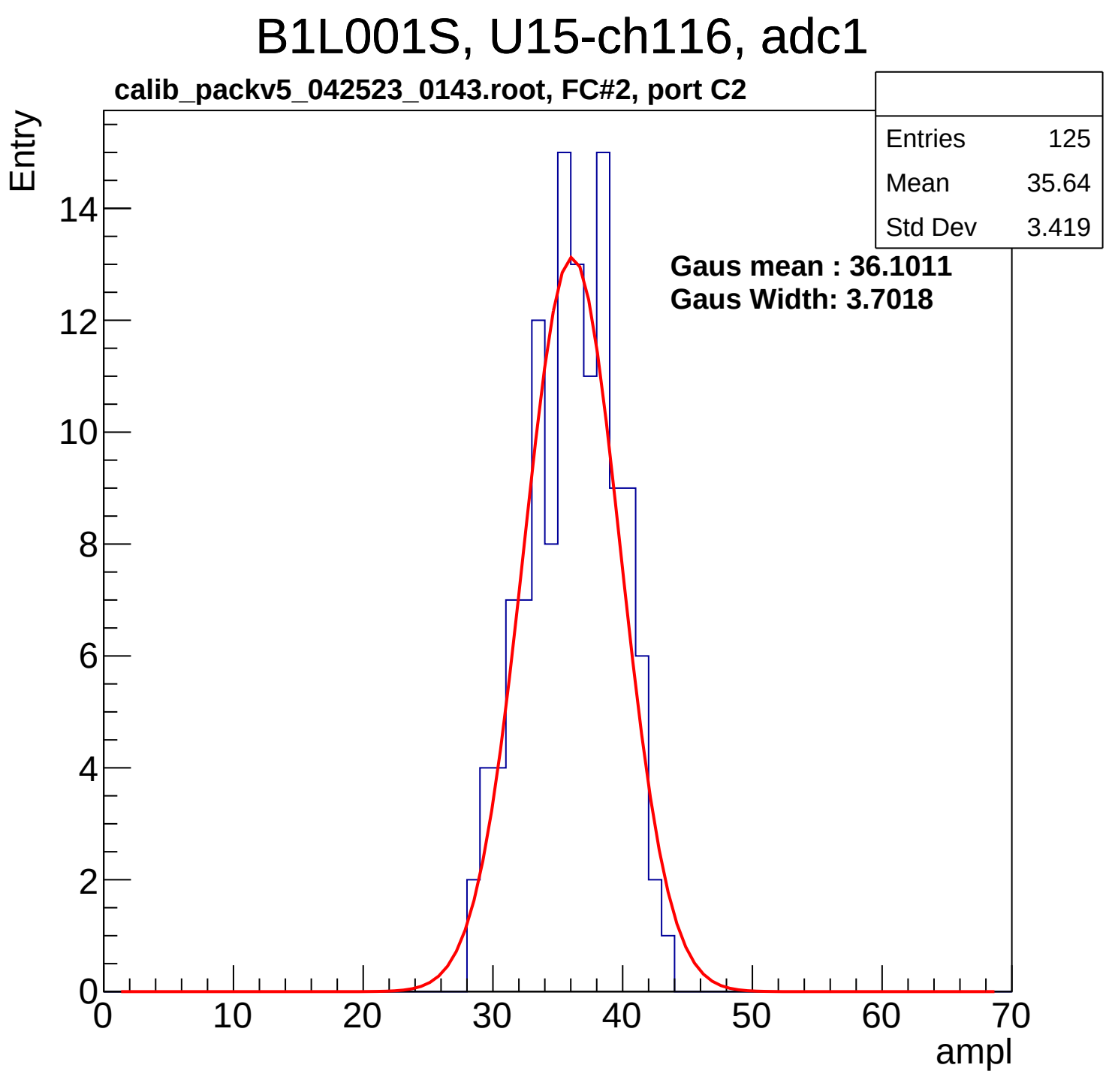
B1L001S, U15-ch116, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 35.64
Std Dev 3.419
Gaus mean : 36.1011
Gaus Width: 3.7018
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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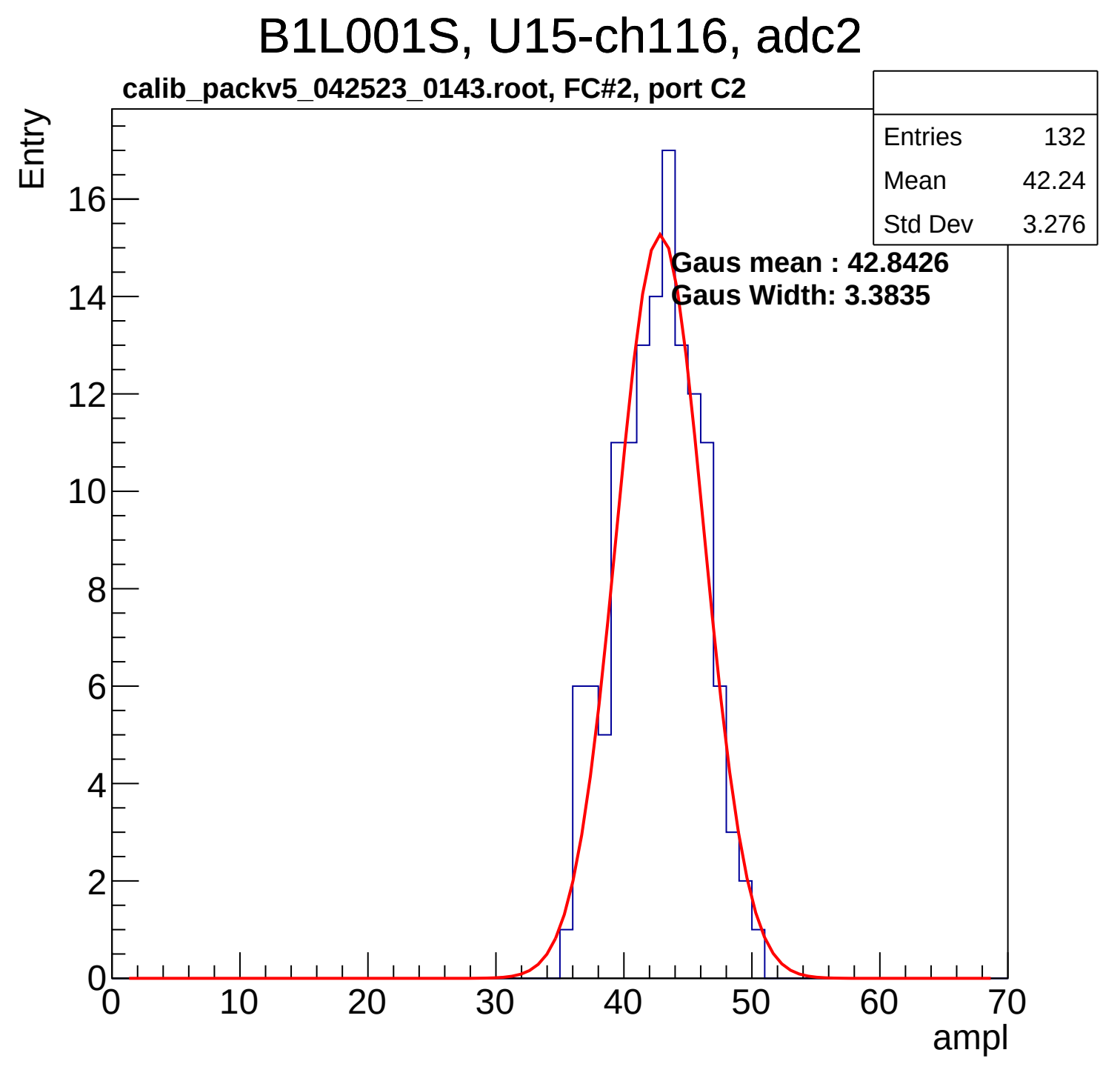
B1L001S, U15-ch116, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 42.24
Std Dev 3.276
Gaus mean : 42.8426
Gaus Width: 3.3835
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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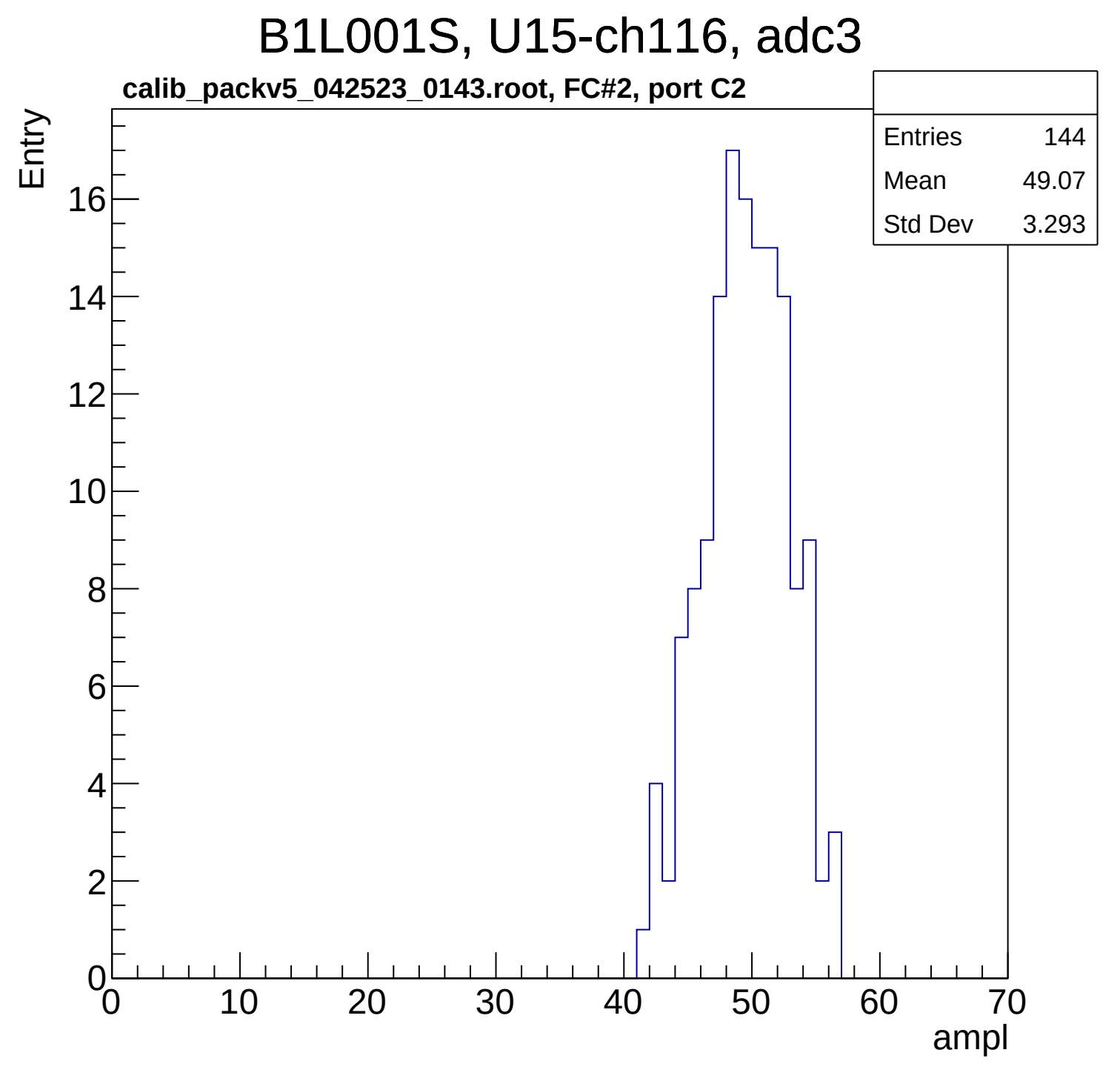
B1L001S, U15-ch116, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 49.07
Std Dev 3.293
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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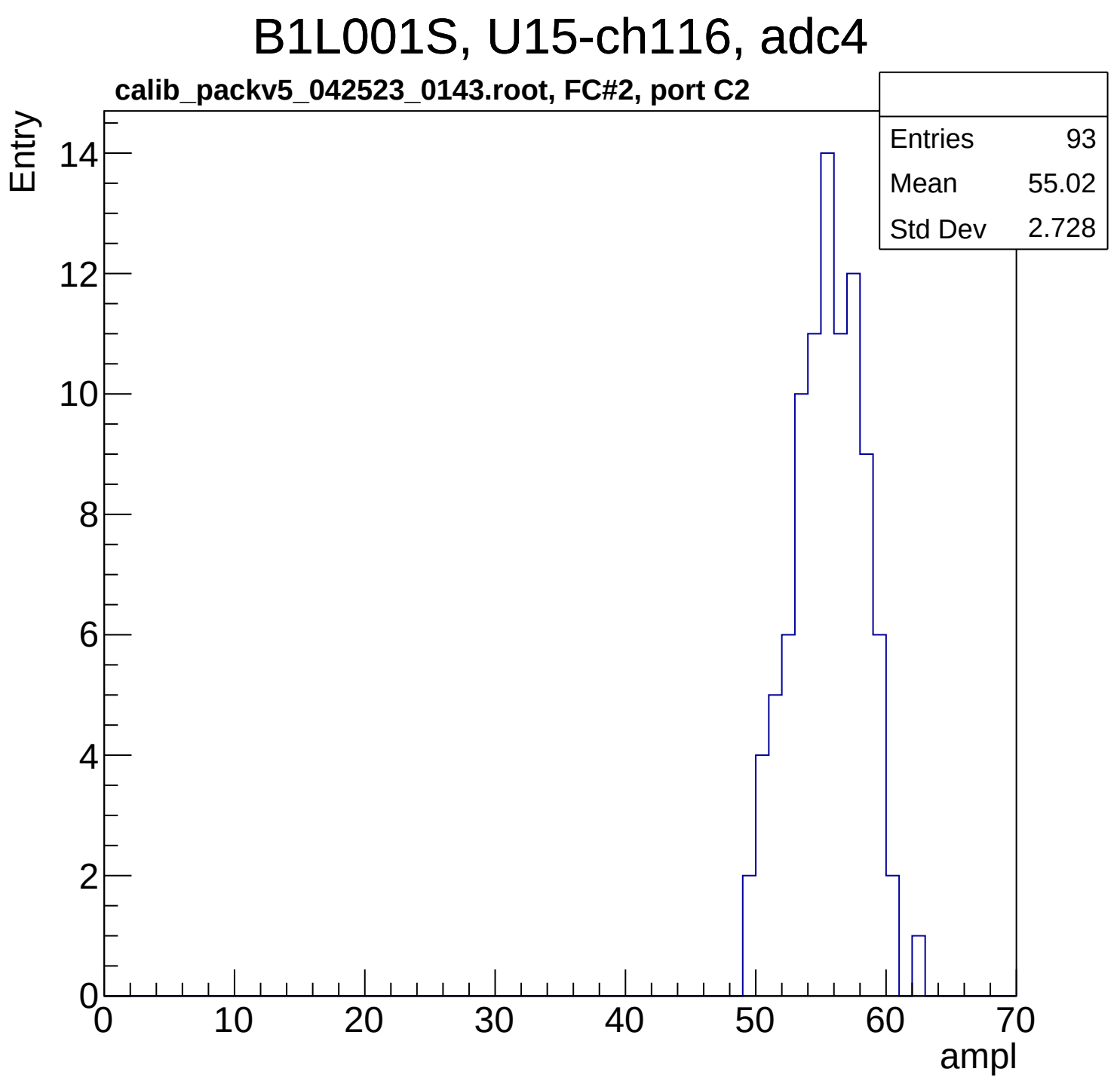
B1L001S, U15-ch116, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 55.02
Std Dev 2.728
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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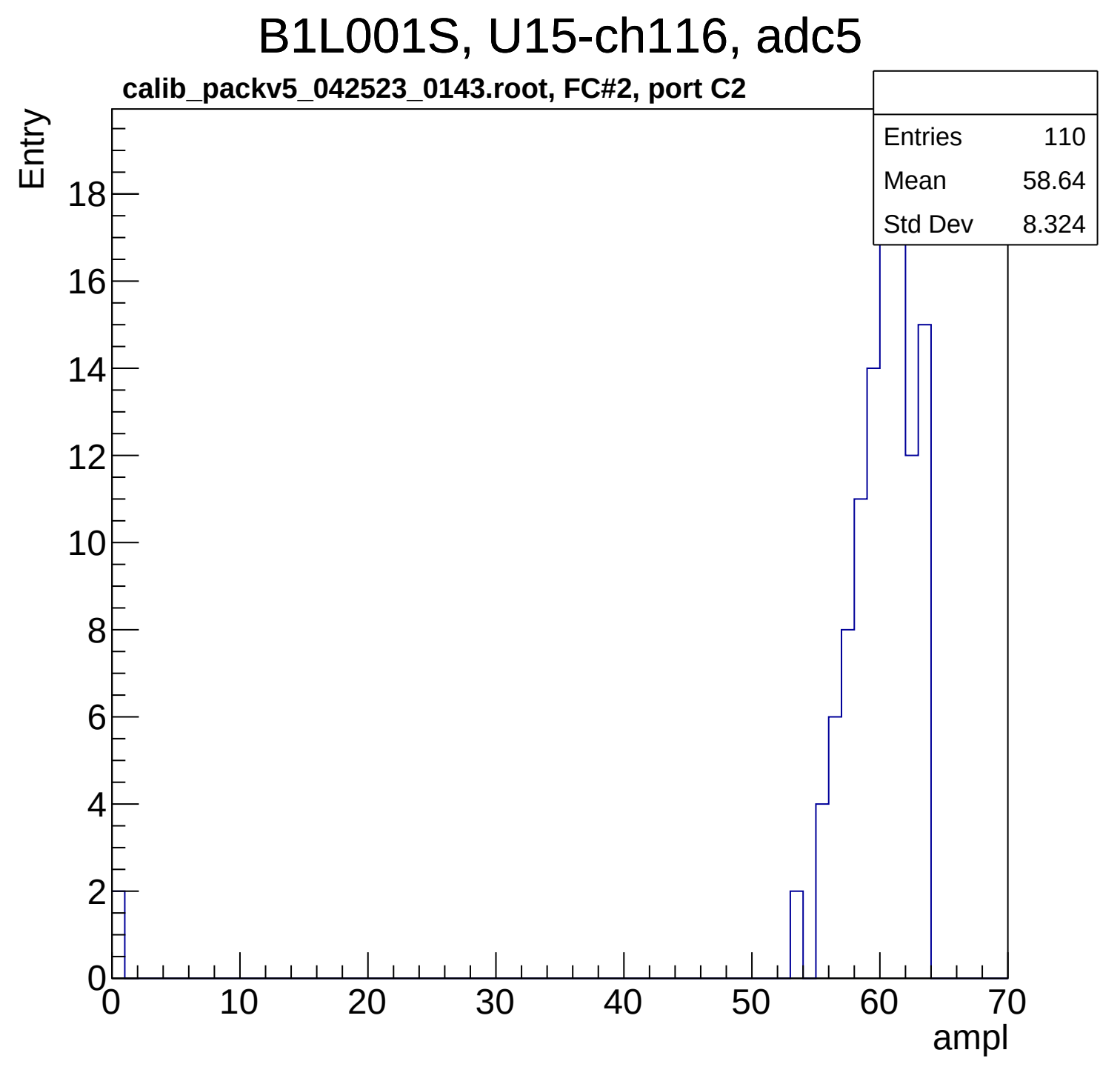
B1L001S, U15-ch116, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 58.64
Std Dev 8.324
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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60
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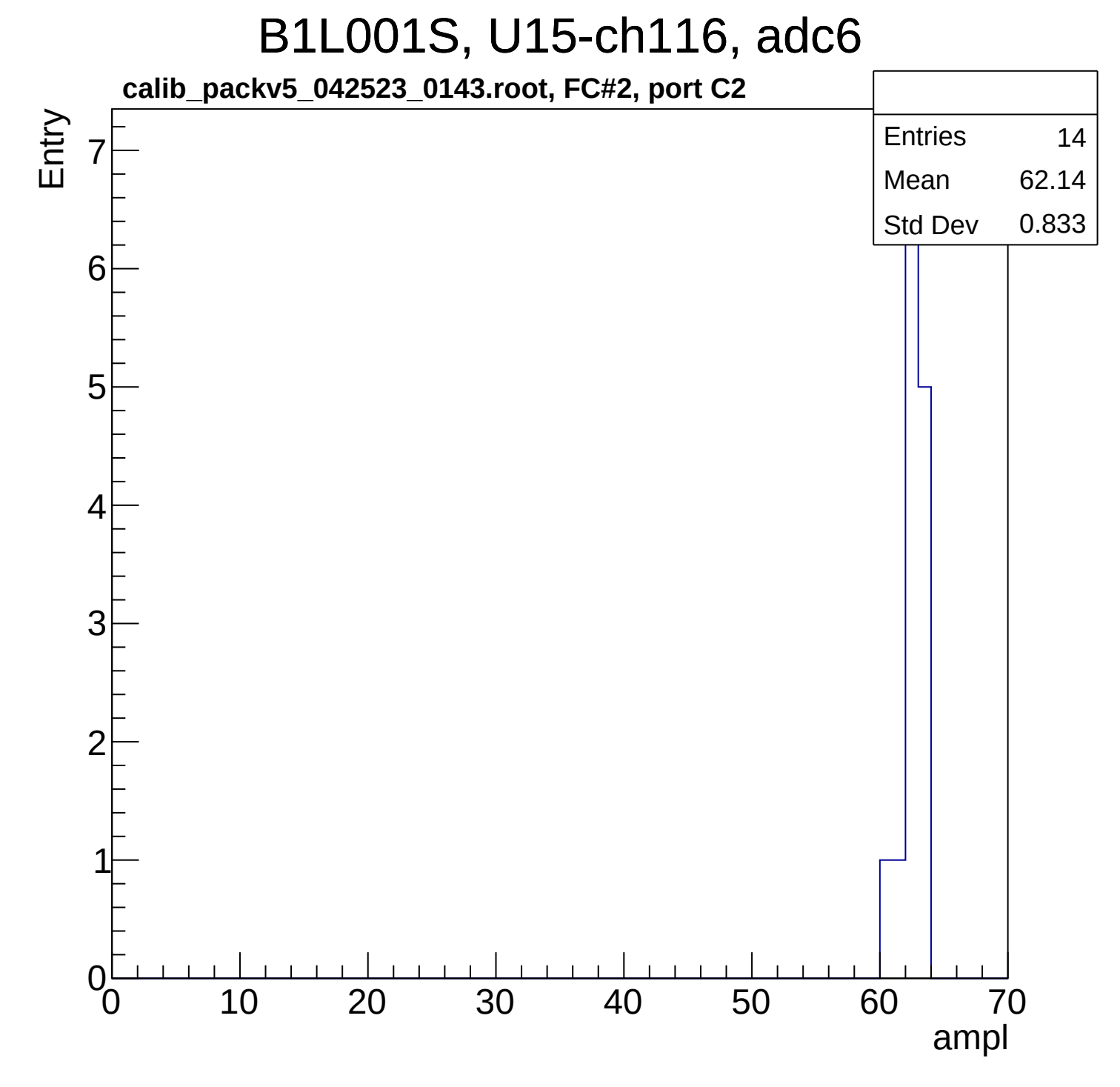
B1L001S, U15-ch116, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 62.14
Std Dev 0.833
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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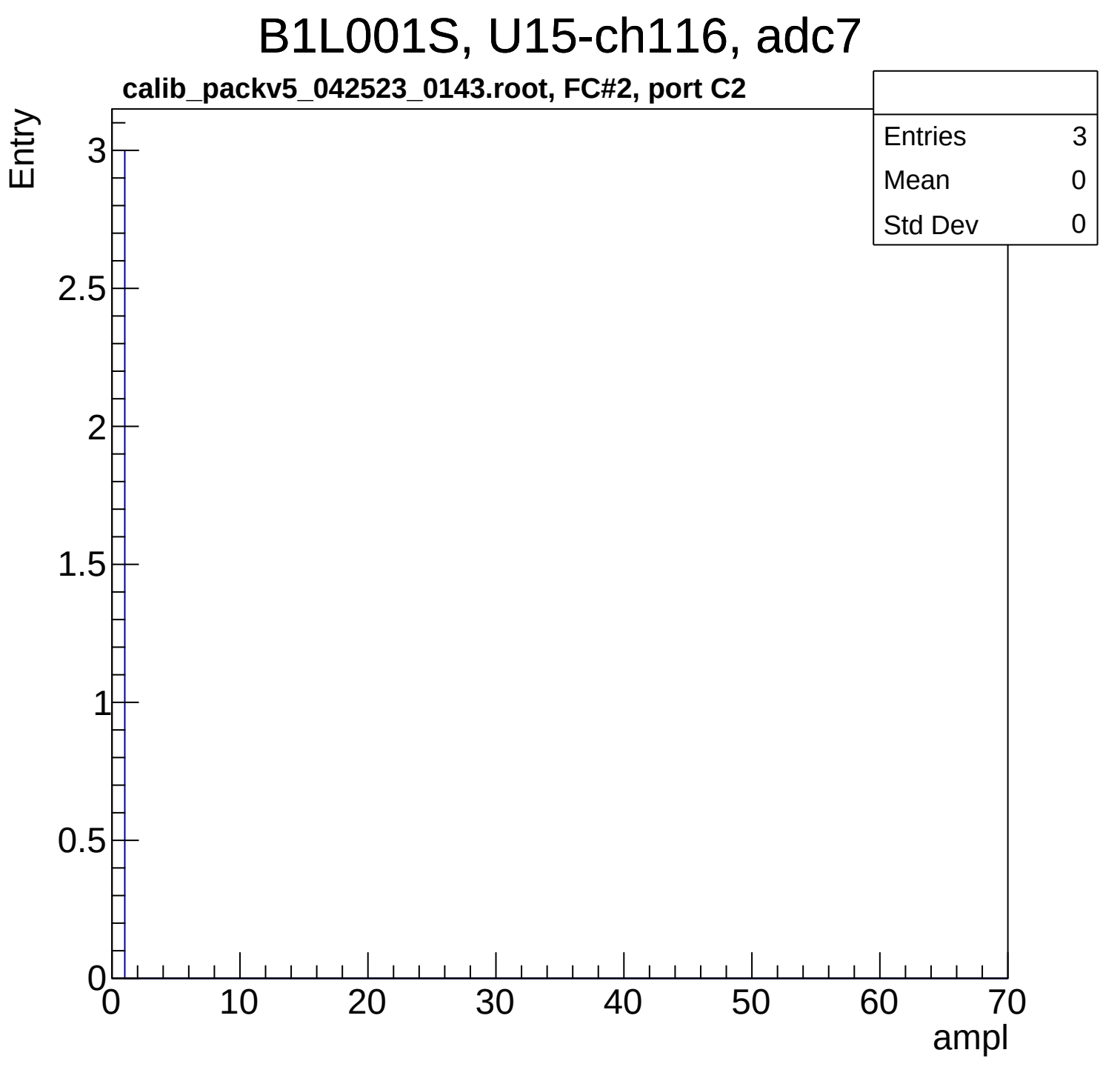
B1L001S, U15-ch116, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 0
Std Dev 0
Entry
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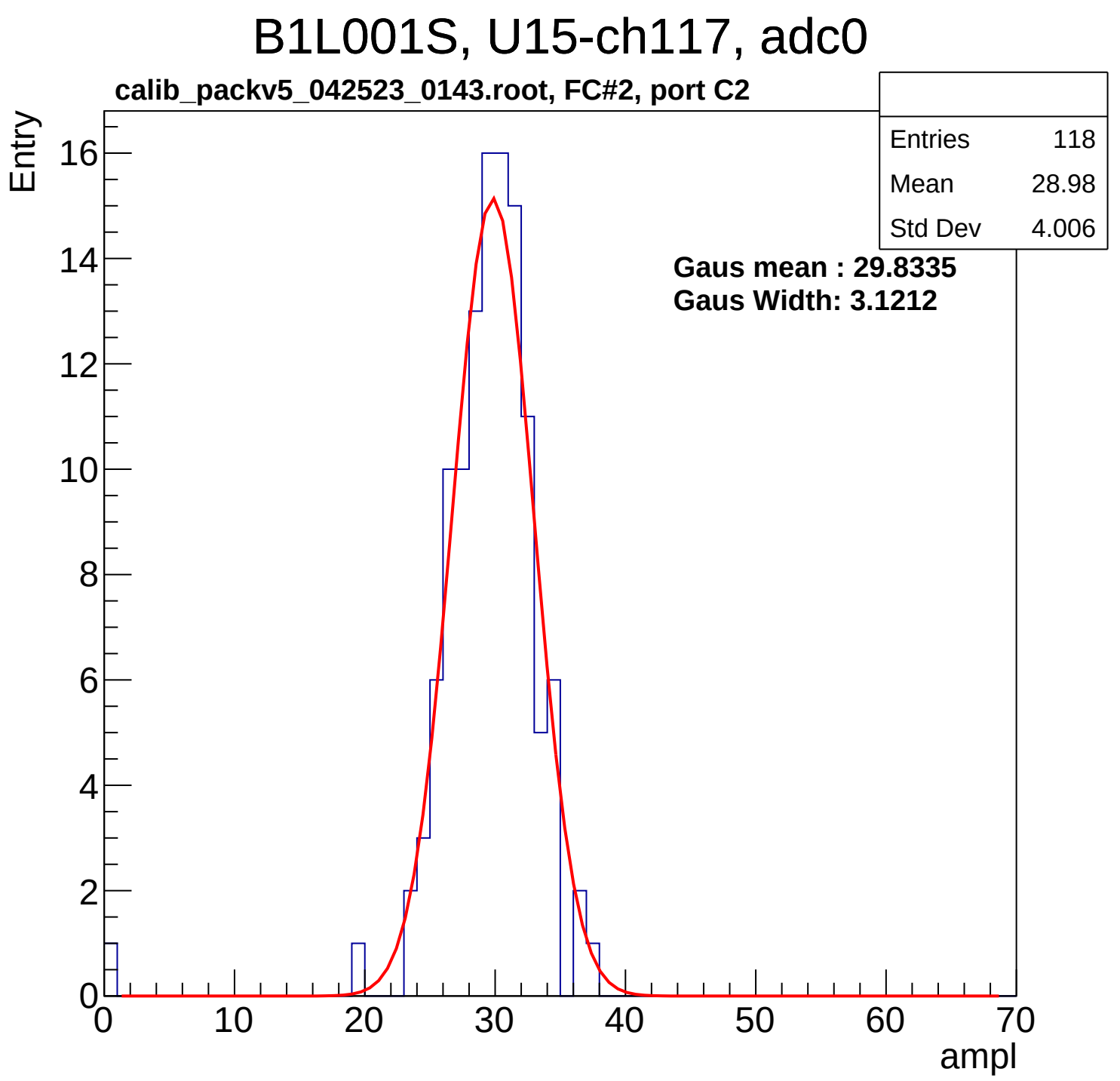
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch117, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 28.98
Std Dev 4.006
Gaus mean : 29.8335
Gaus Width: 3.1212
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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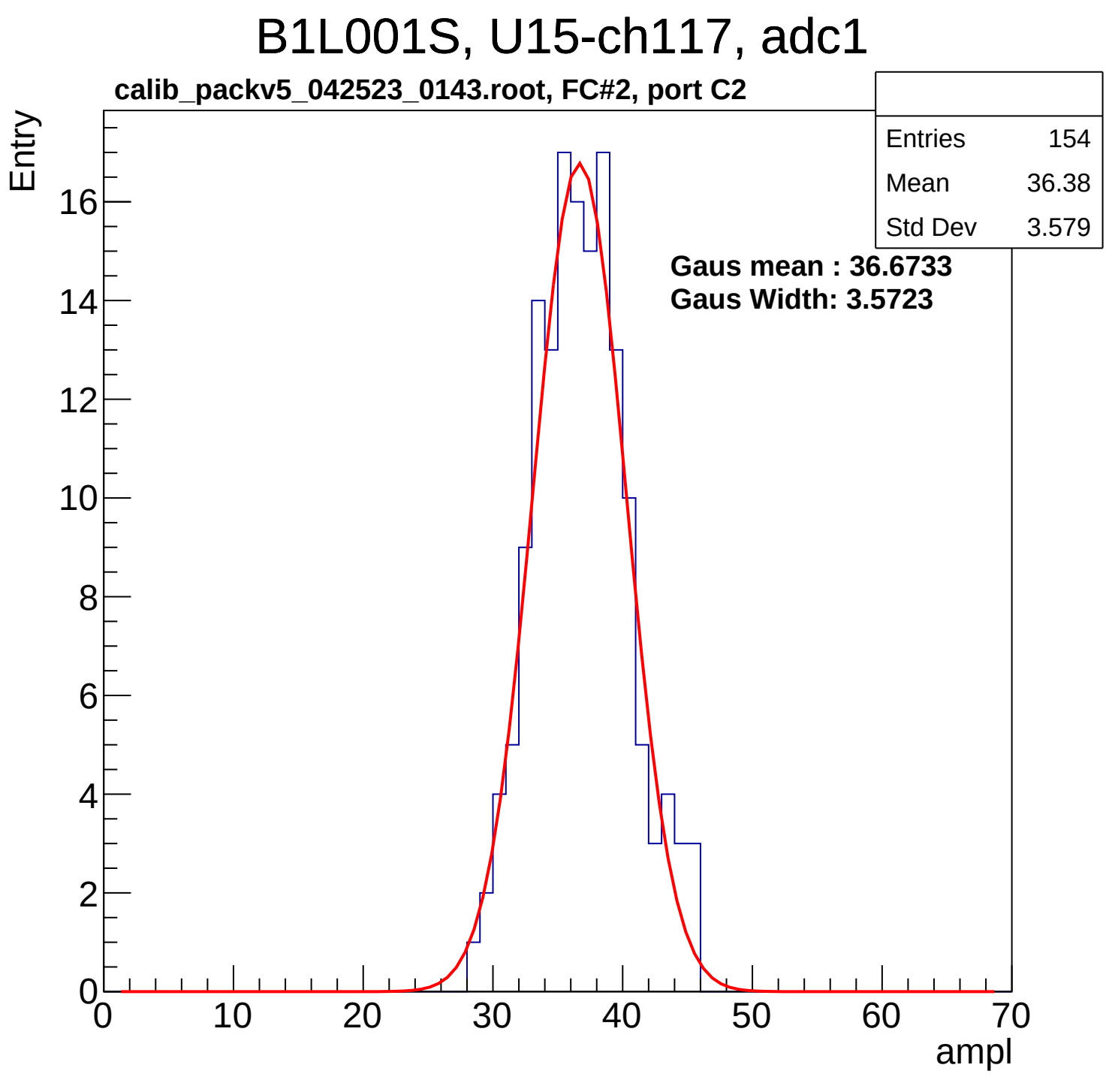
B1L001S, U15-ch117, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 36.38
Std Dev 3.579
Gaus mean : 36.6733
Gaus Width: 3.5723
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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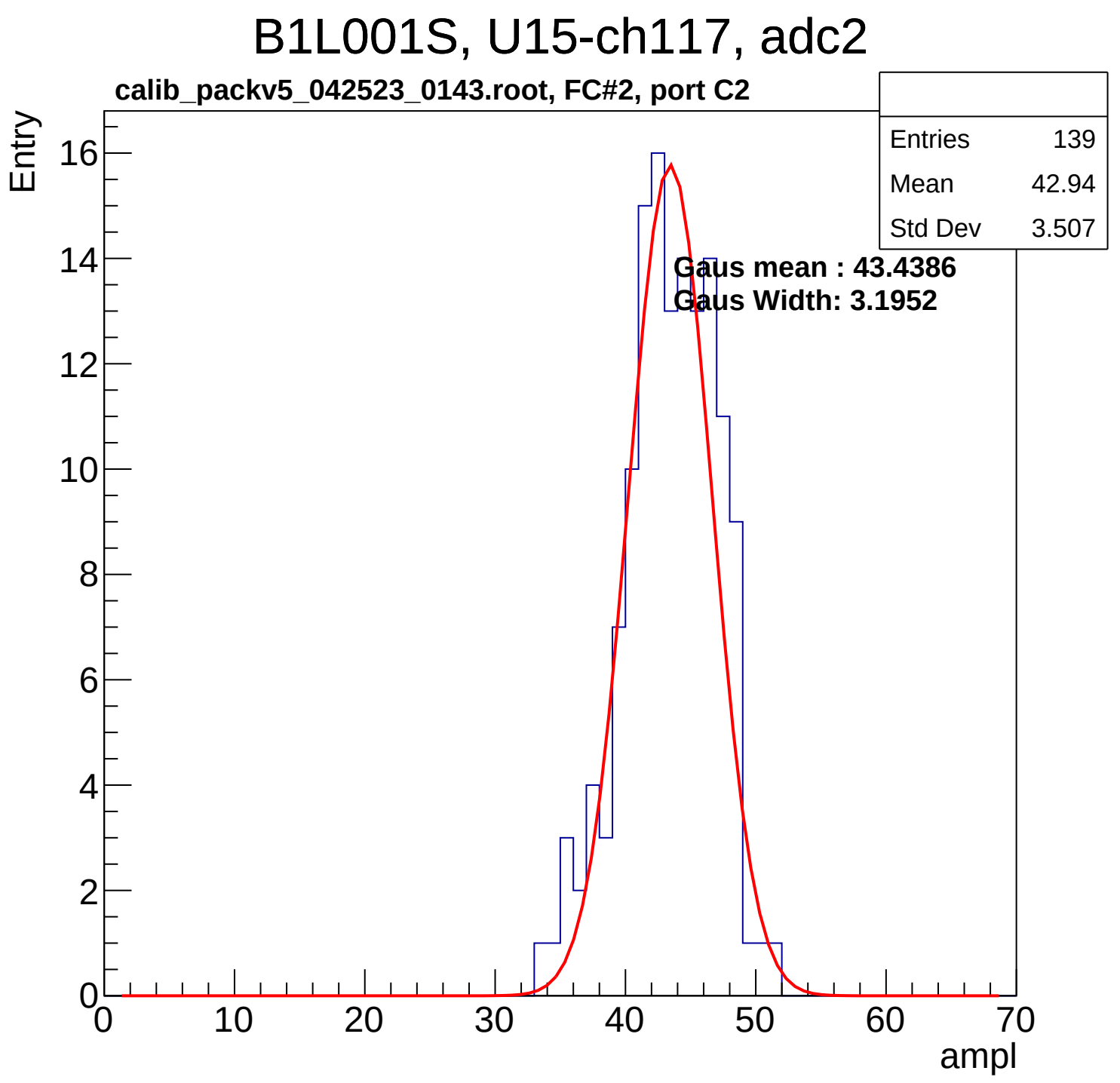
B1L001S, U15-ch117, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 42.94
Std Dev 3.507
Gaus mean : 43.4386
Gaus Width: 3.1952
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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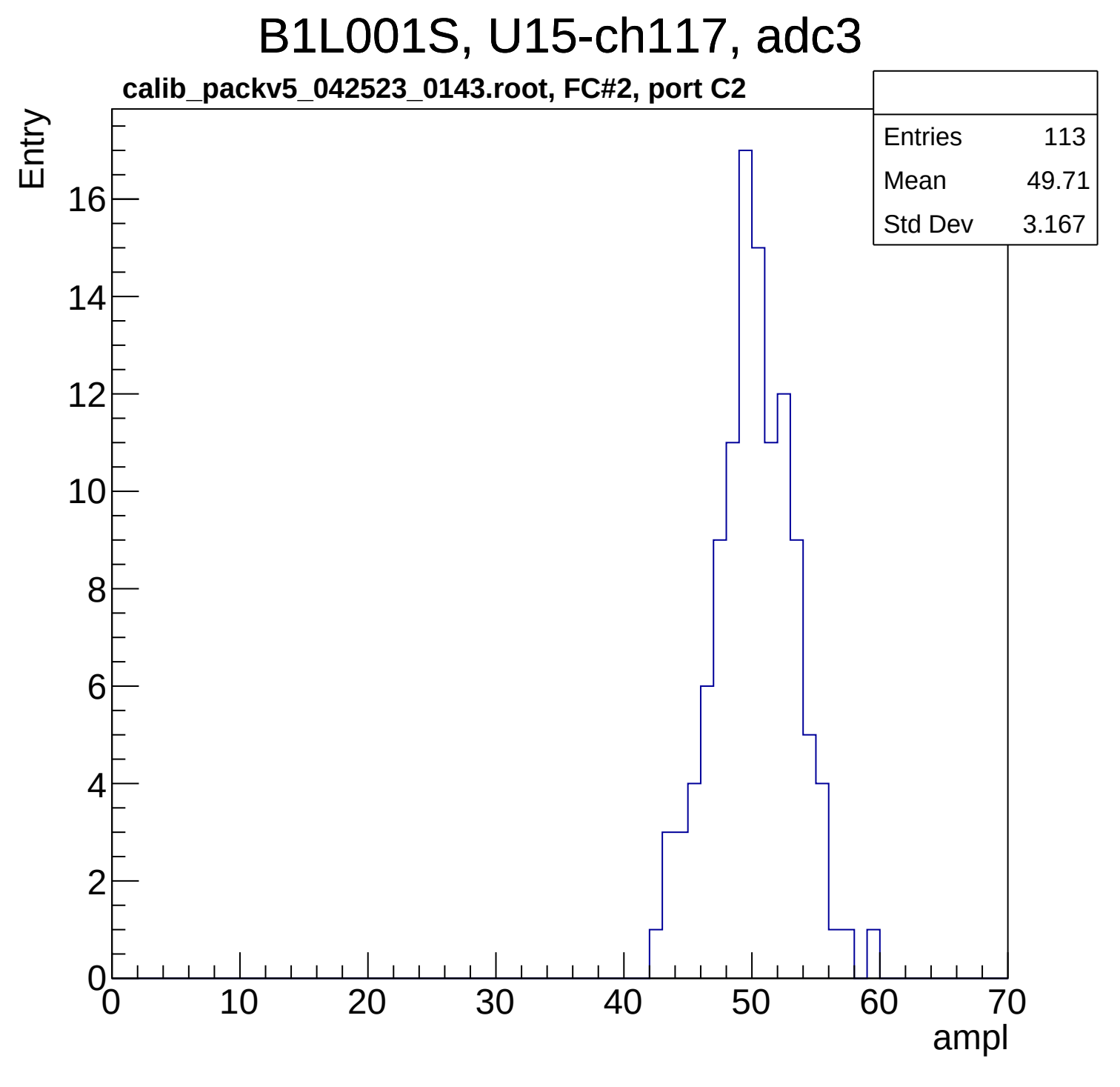
B1L001S, U15-ch117, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.71
Std Dev 3.167
Entry
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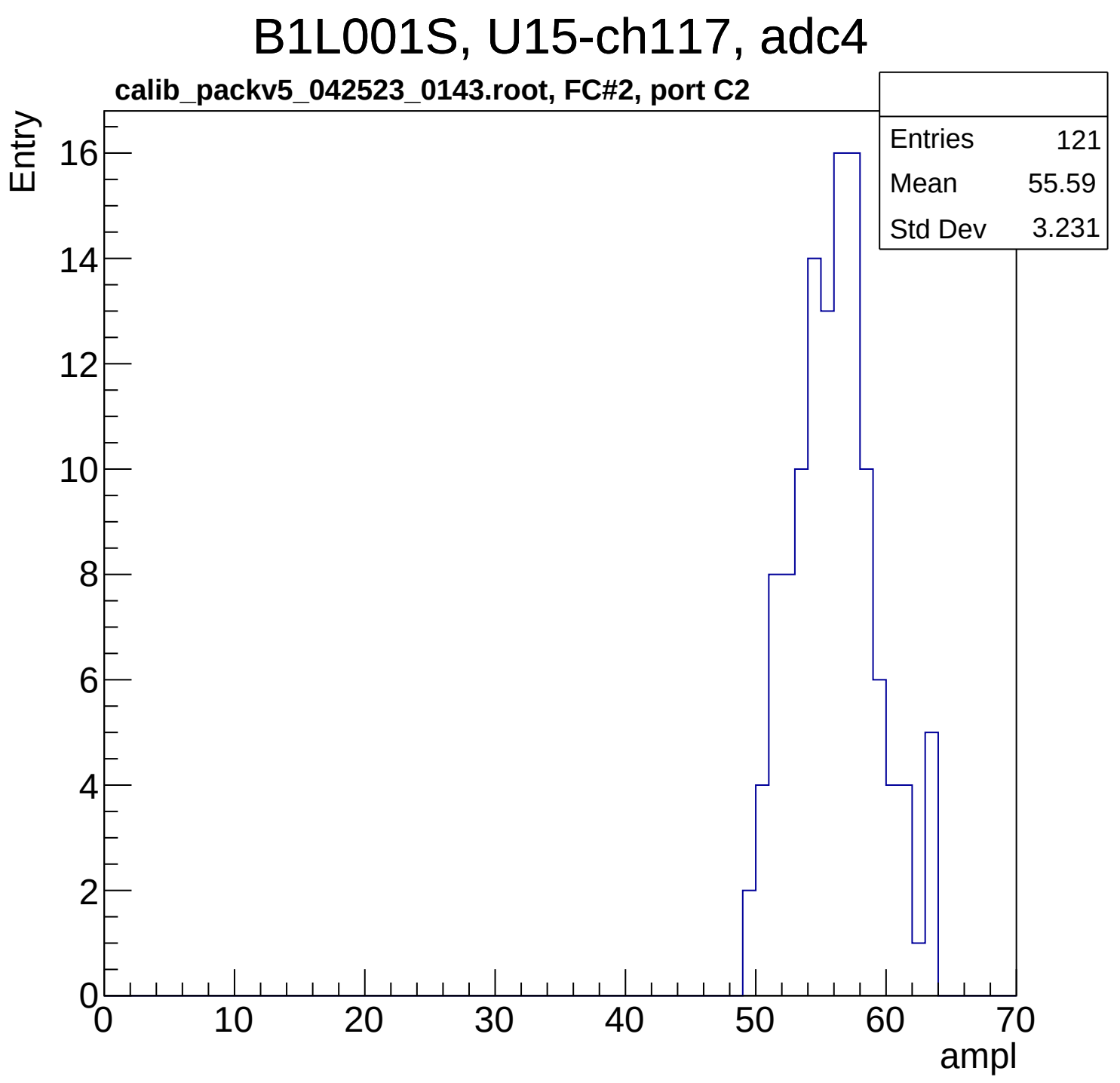
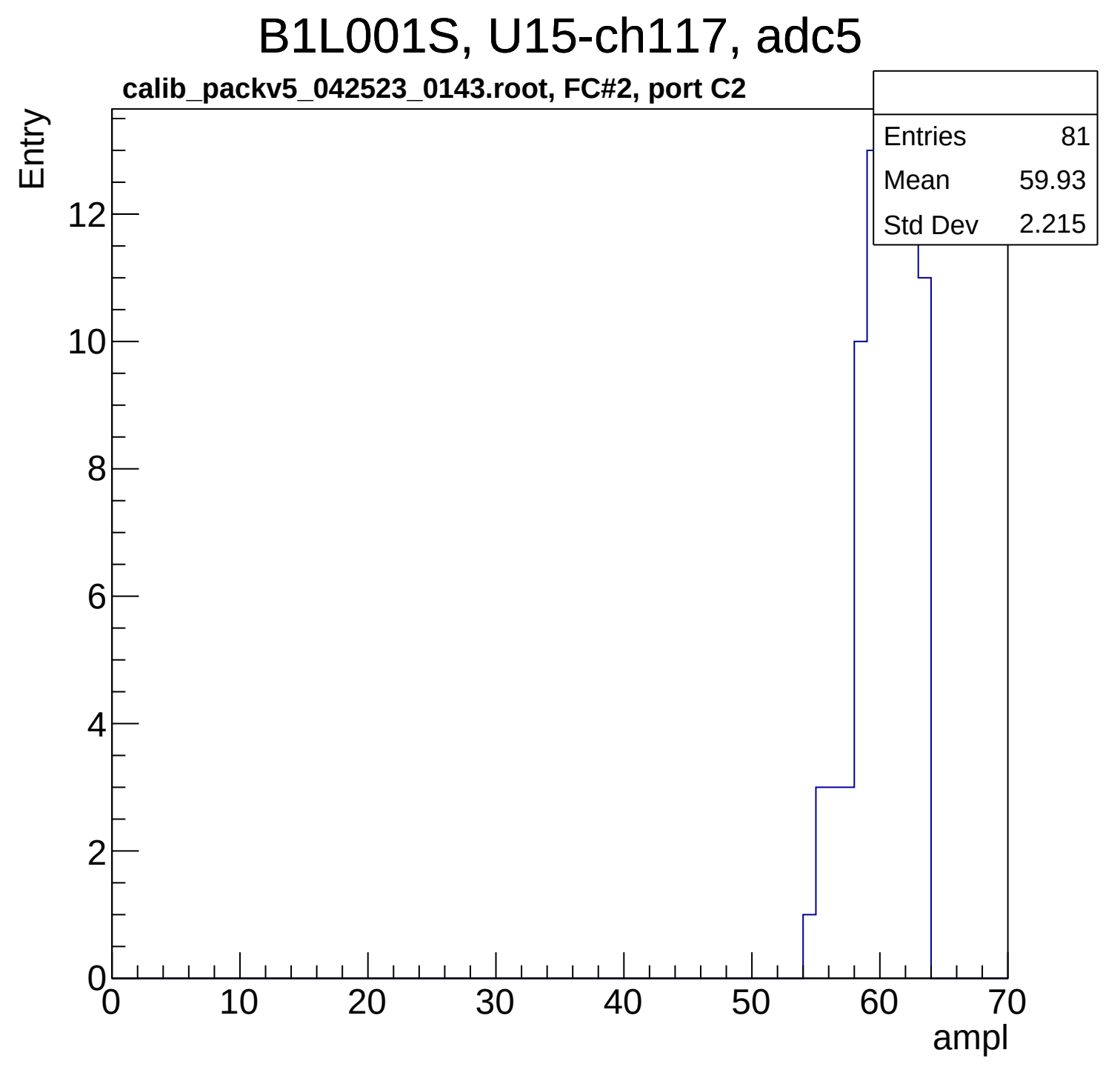
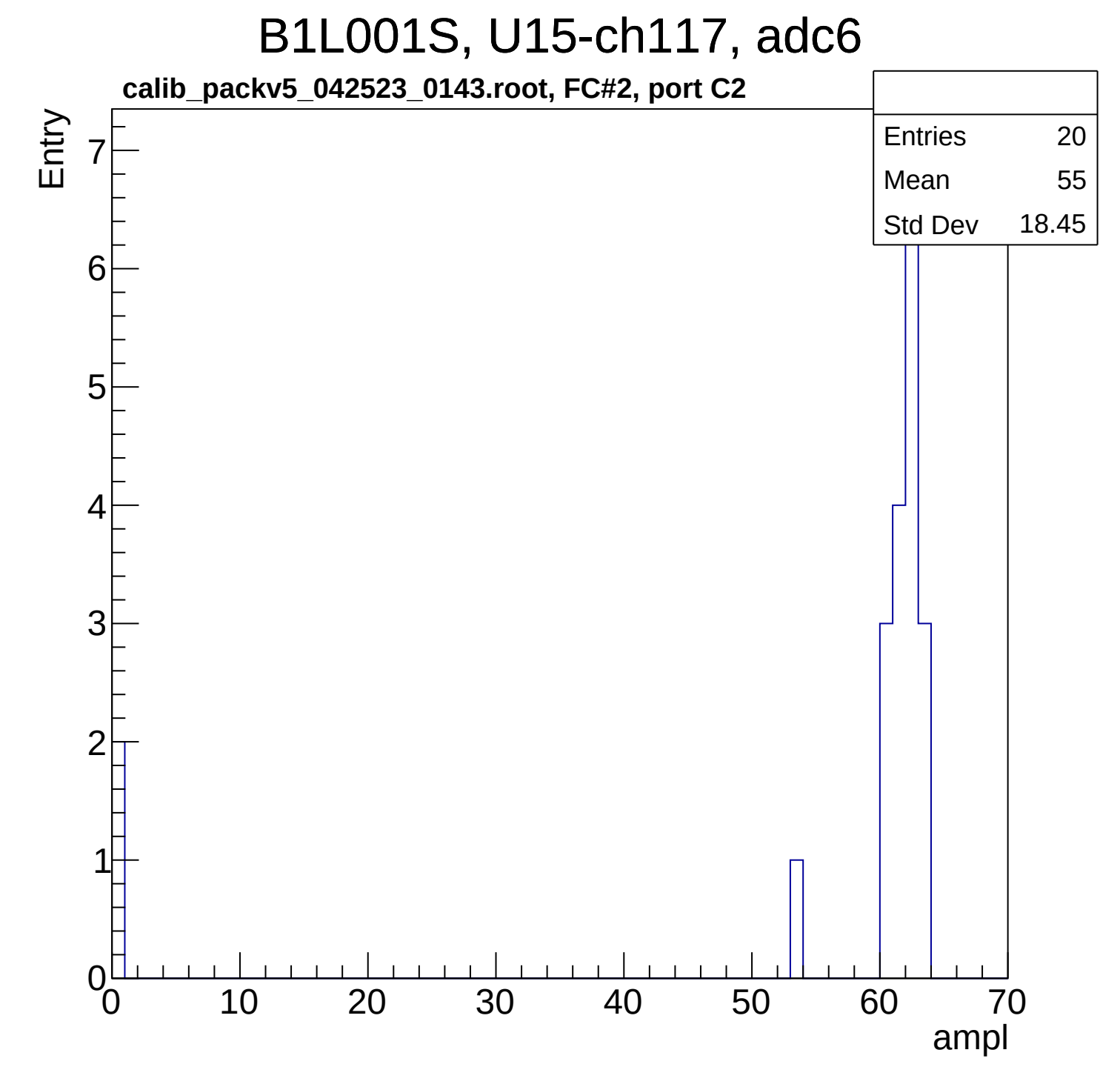
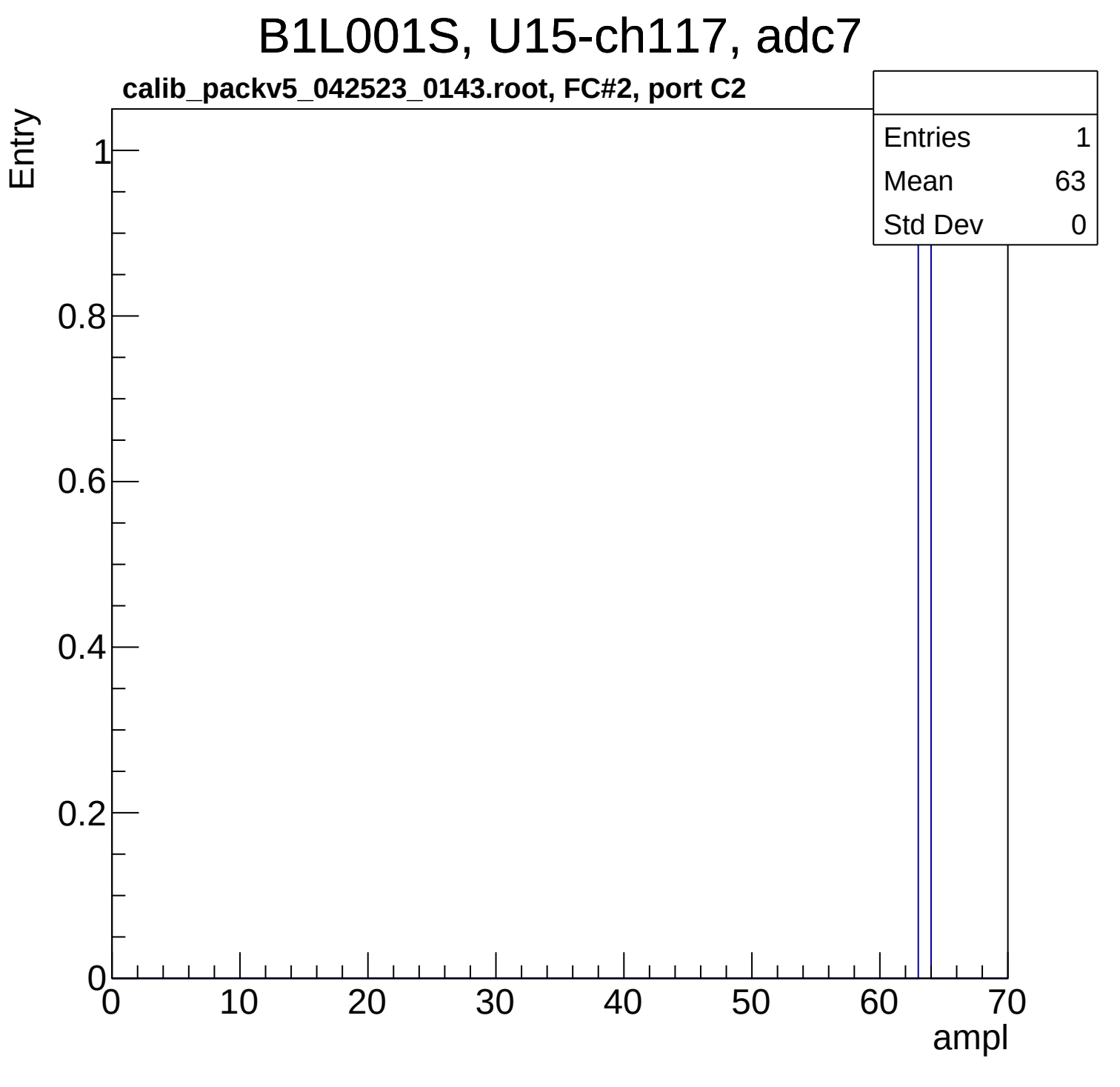
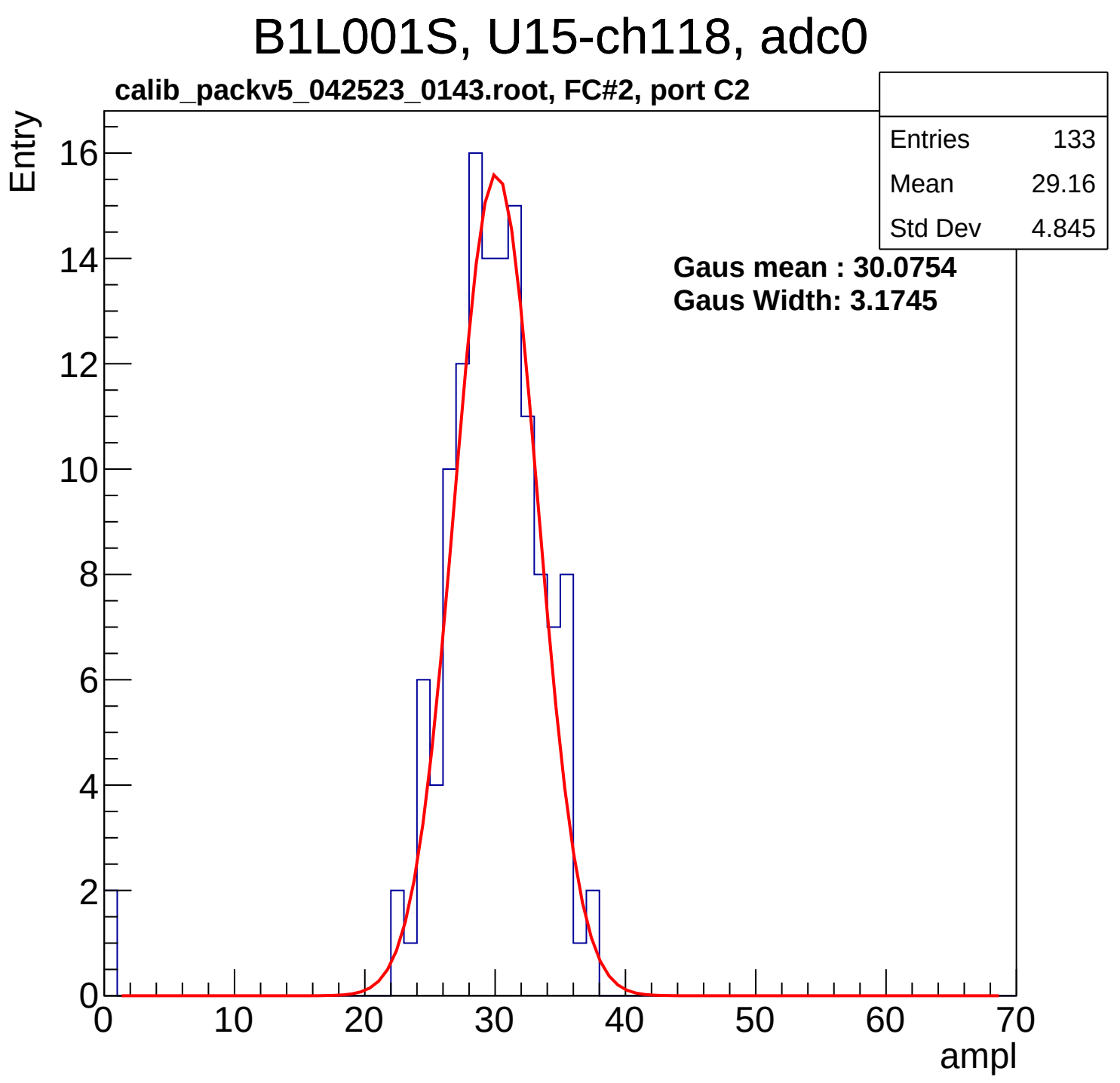
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch117, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 55.59
Std Dev 3.231
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch117, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 59.93
Std Dev 2.215
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U15-ch117, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 20
Mean 55
Std Dev 18.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U15-ch117, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch118, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 29.16
Std Dev 4.845
Gaus mean : 30.0754
Gaus Width: 3.1745
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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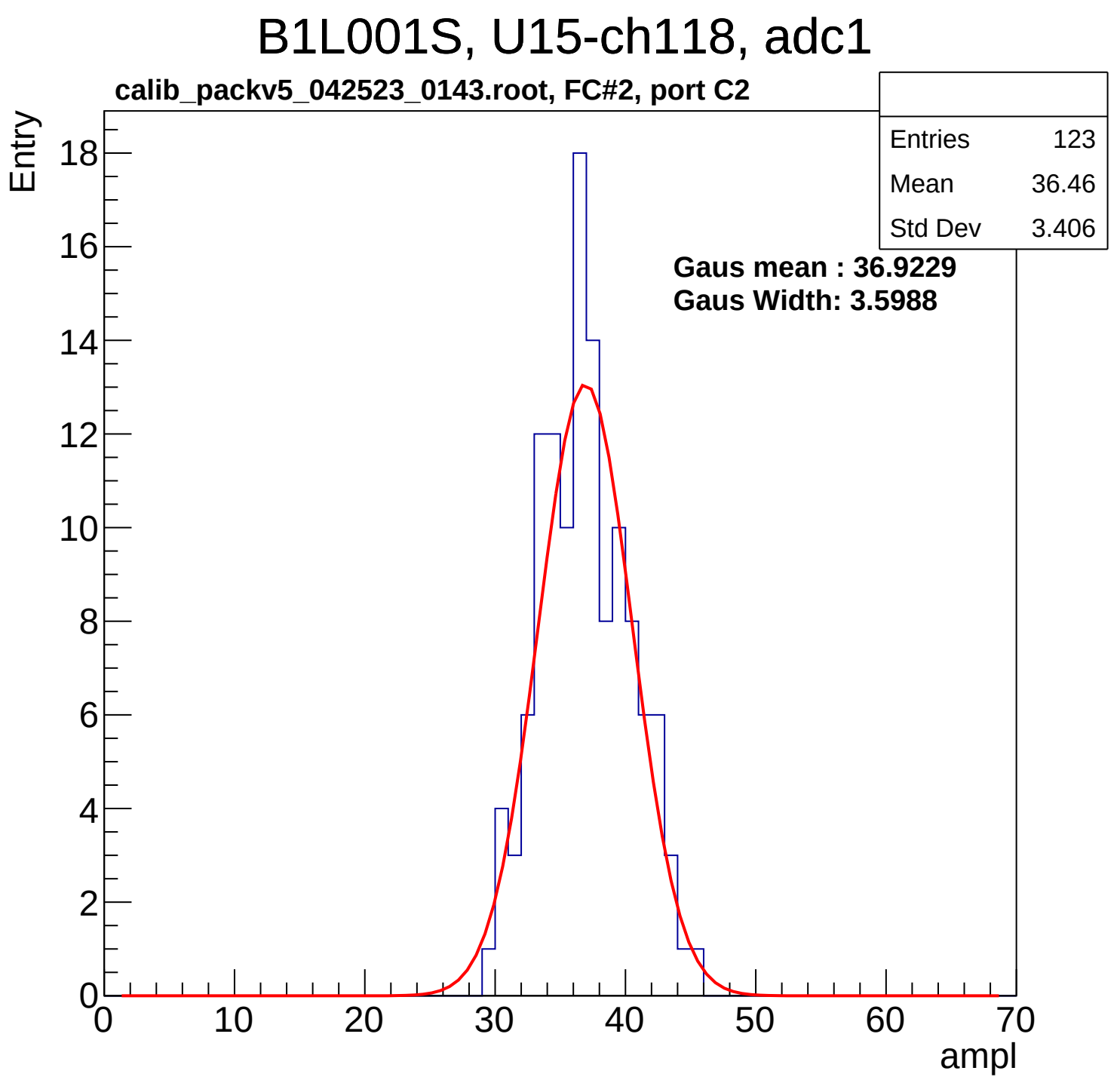
B1L001S, U15-ch118, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 36.46
Std Dev 3.406
Gaus mean : 36.9229
Gaus Width: 3.5988
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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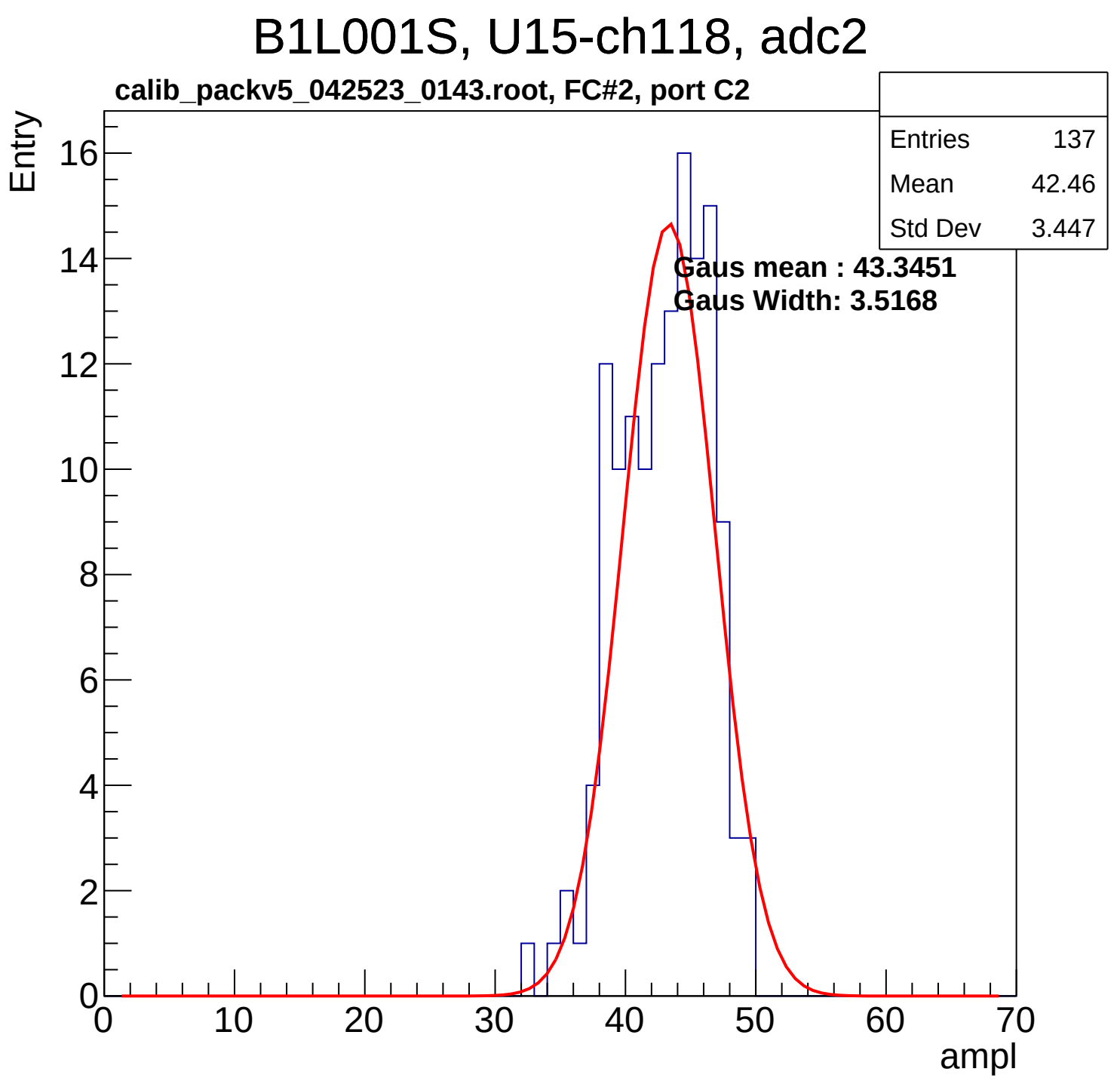
B1L001S, U15-ch118, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 42.46
Std Dev 3.447
Gaus mean : 43.3451
Gaus Width: 3.5168
Entry
ampl
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2
4
6
8
10
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0
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60
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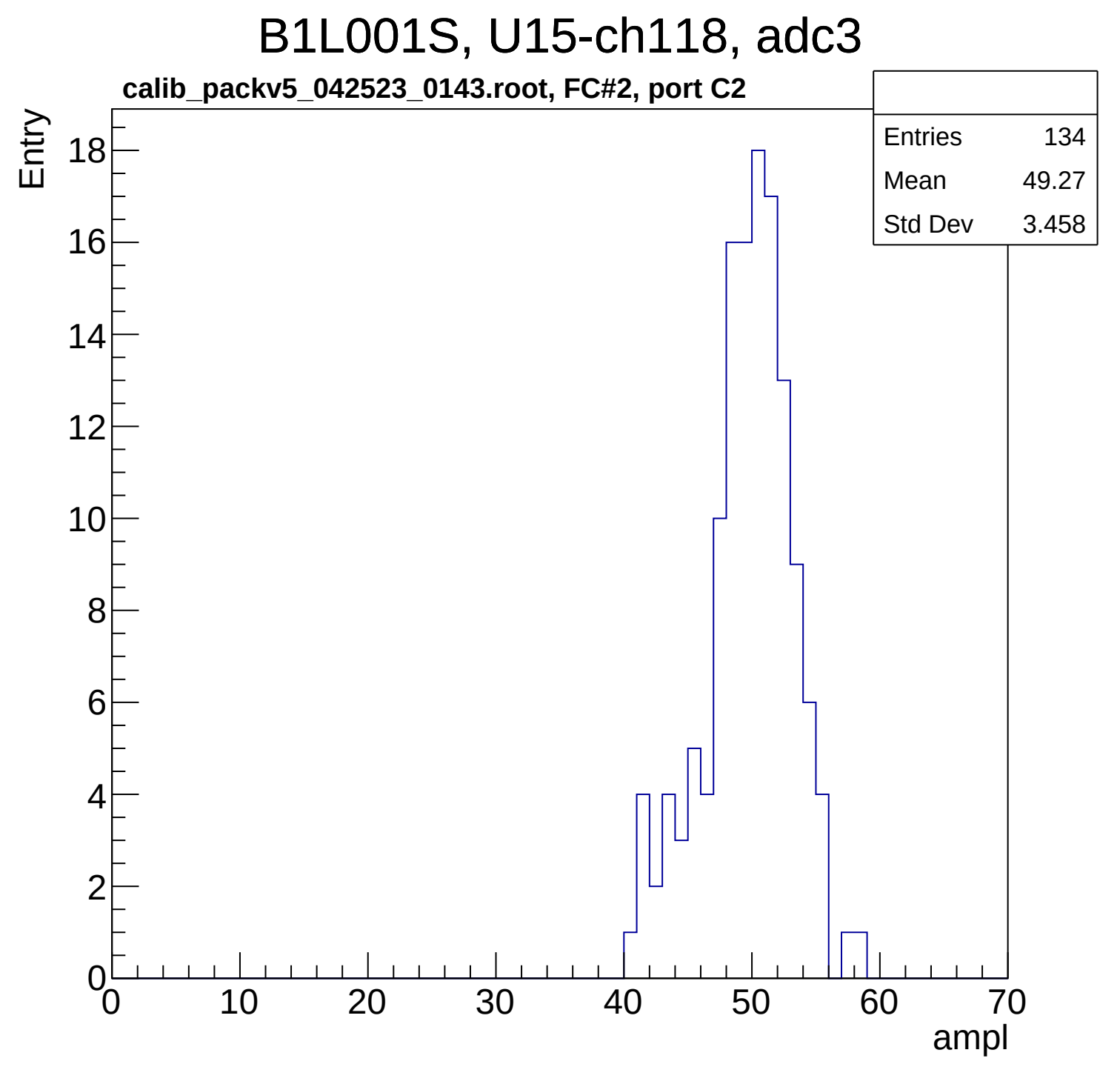
B1L001S, U15-ch118, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 49.27
Std Dev 3.458
Entry
ampl
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10
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16
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0
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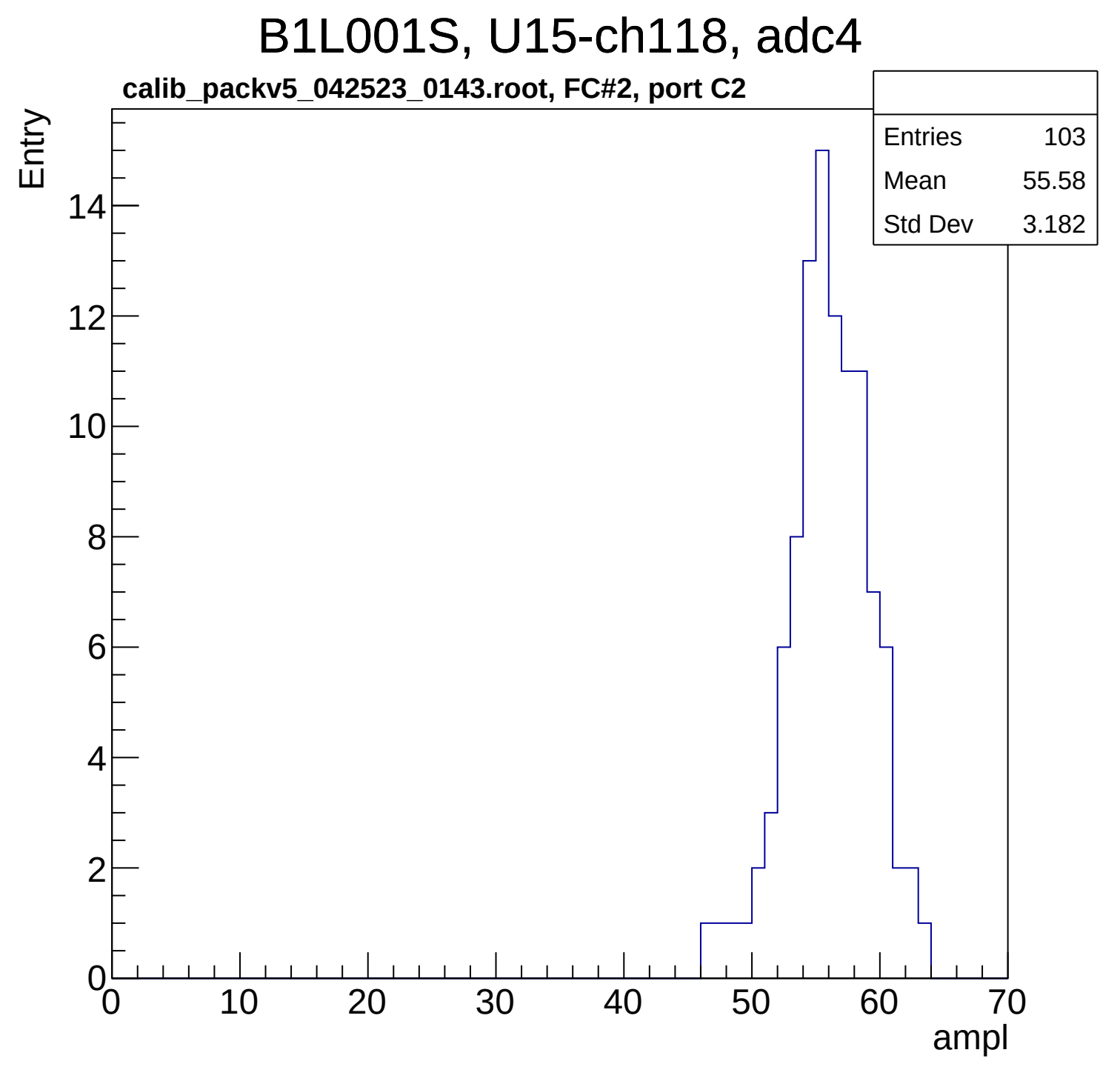
60
70


B1L001S, U15-ch118, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 55.58
Std Dev 3.182
Entry
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4
6
8
10
12
14
0
10
20
30
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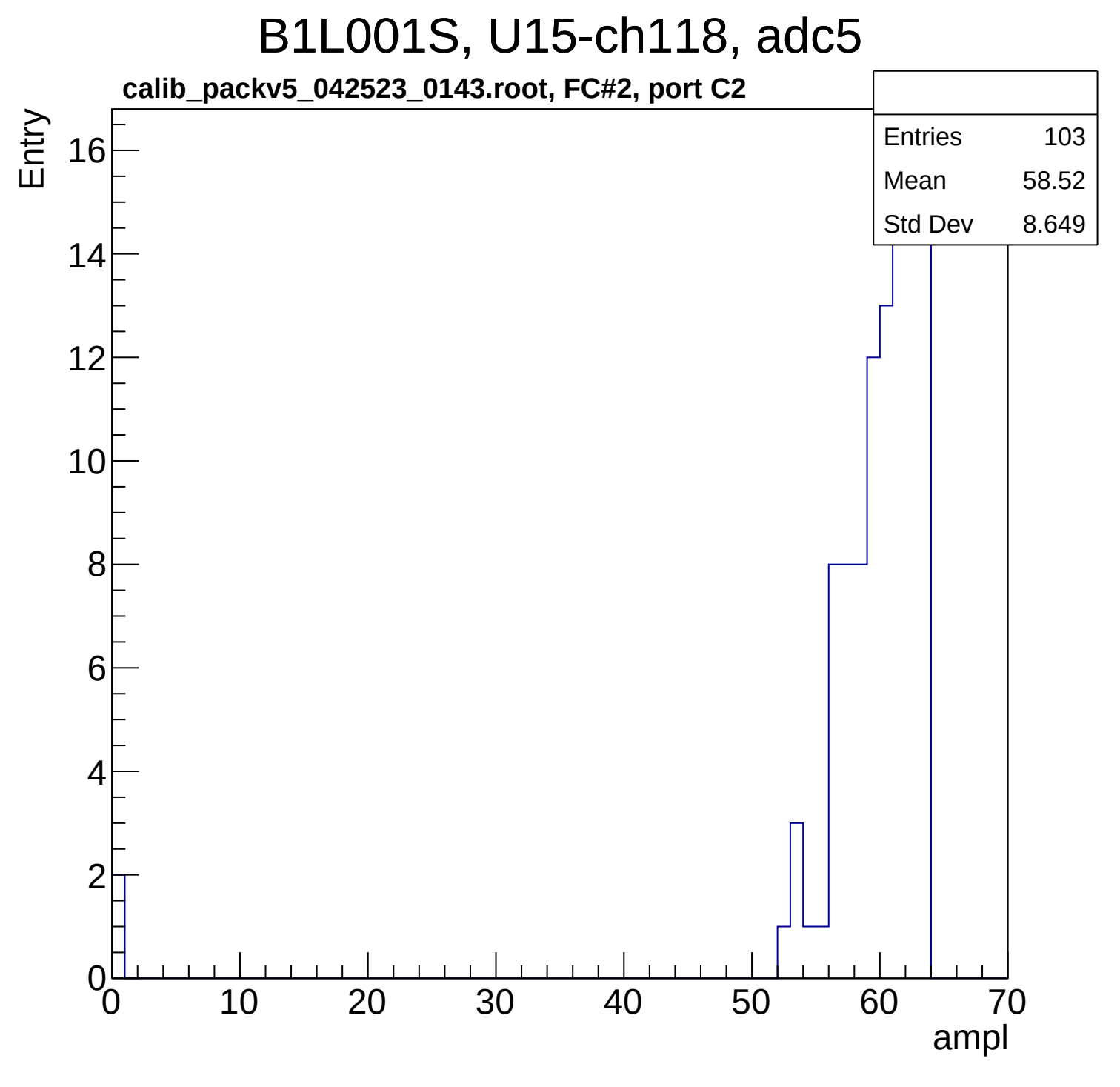
50
60
70
ampl


B1L001S, U15-ch118, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.52
Std Dev 8.649
Entry
ampl
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10
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60
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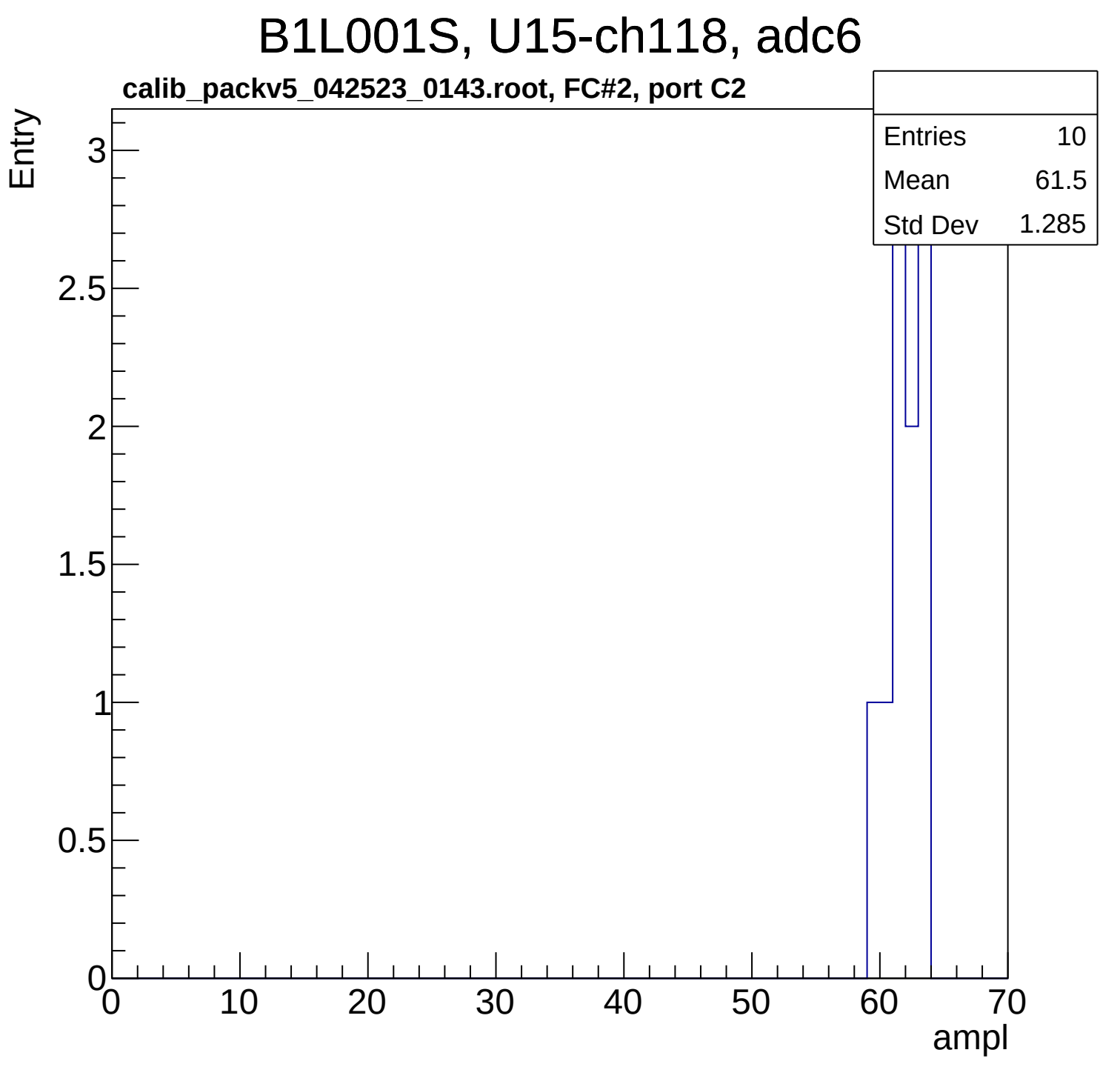
B1L001S, U15-ch118, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 61.5
Std Dev 1.285
Entry
ampl
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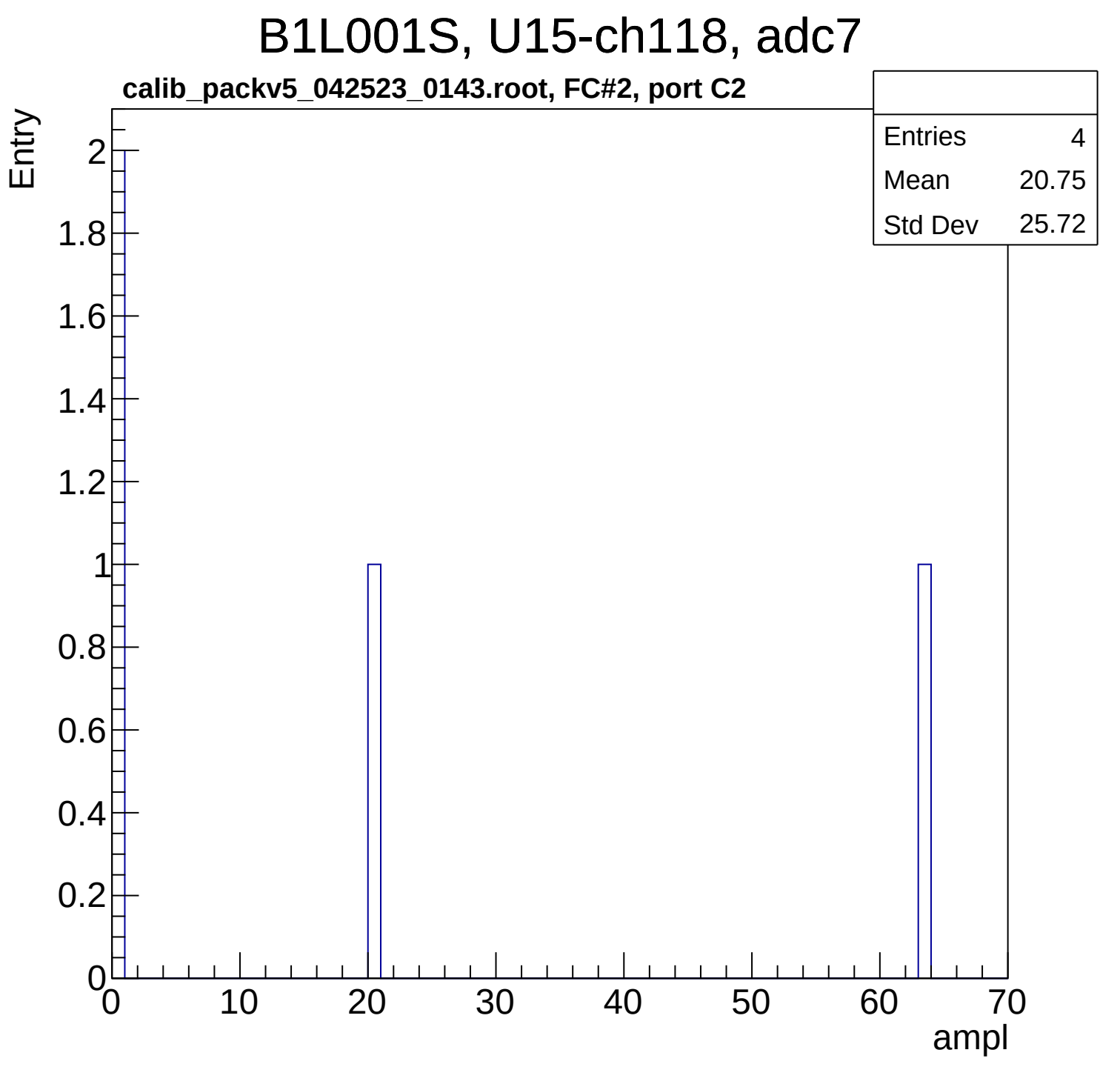
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch118, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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40
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60
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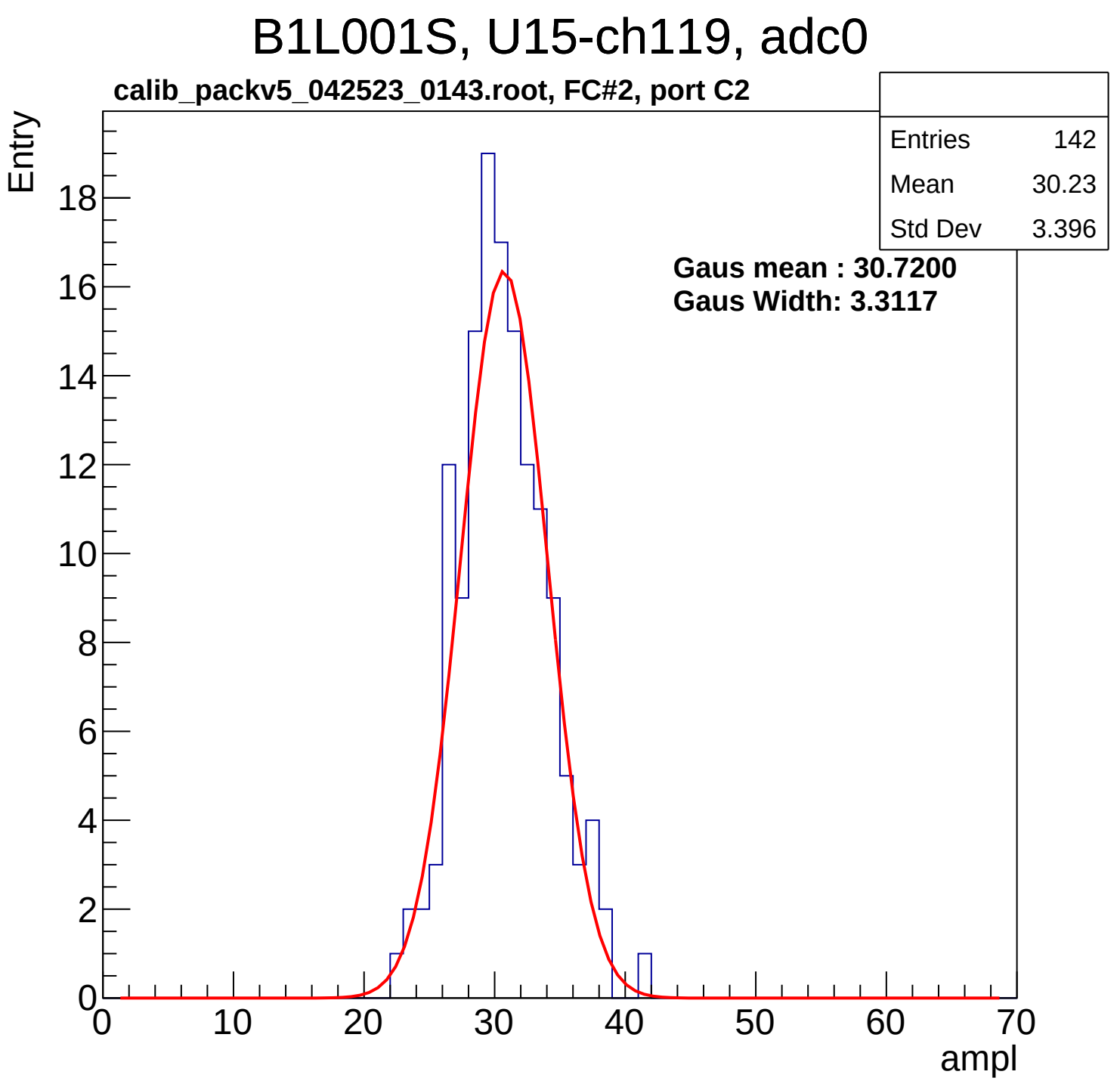
B1L001S, U15-ch119, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 30.23
Std Dev 3.396
Gaus mean : 30.7200
Gaus Width: 3.3117
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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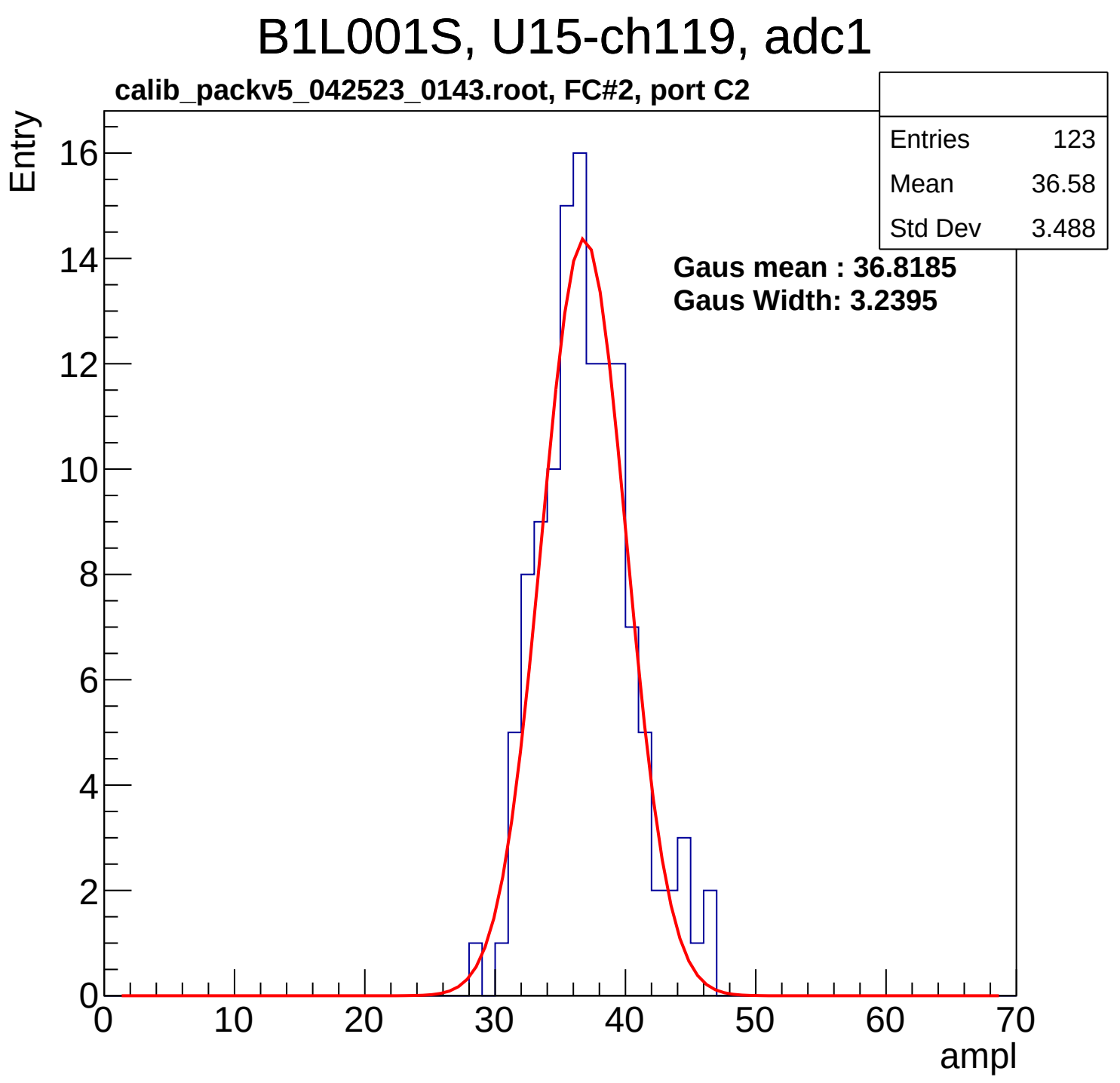
B1L001S, U15-ch119, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 36.58
Std Dev 3.488
Gaus mean : 36.8185
Gaus Width: 3.2395
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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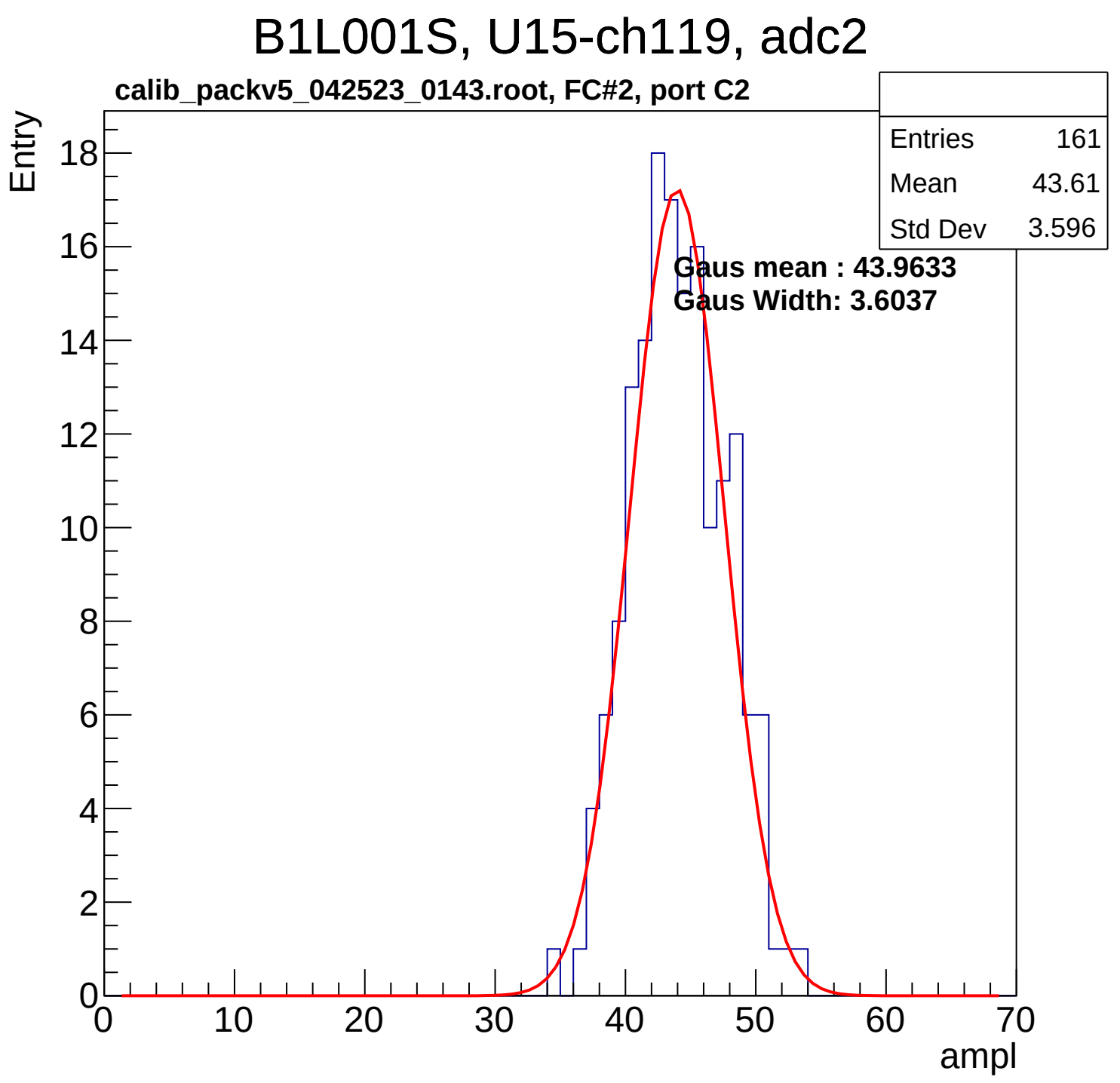
40
50
60
70


B1L001S, U15-ch119, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 43.61
Std Dev 3.596
Gaus mean : 43.9633
Gaus Width: 3.6037
Entry
ampl
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4
6
8
10
12
14
16
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0
10
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40
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60
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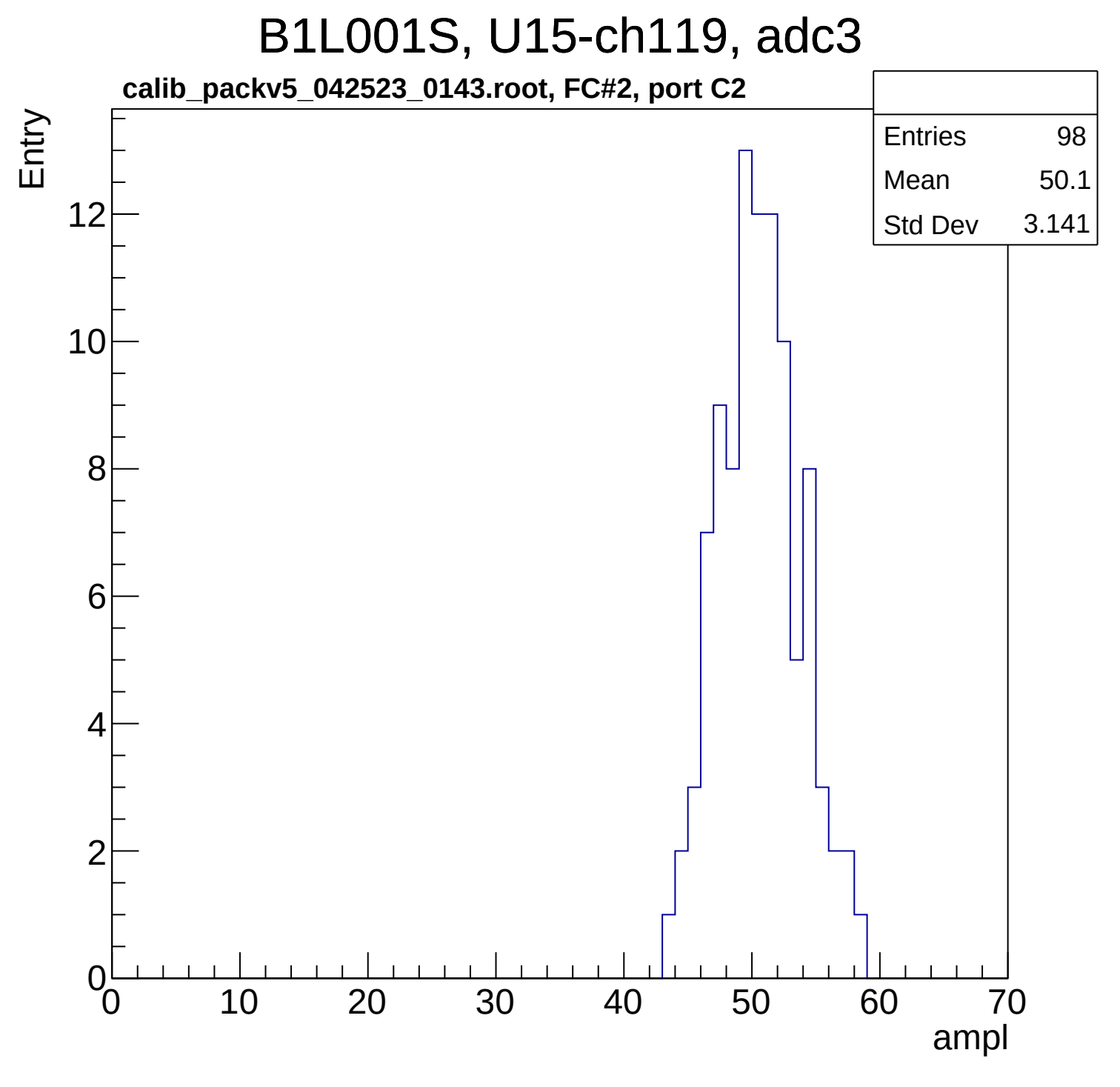
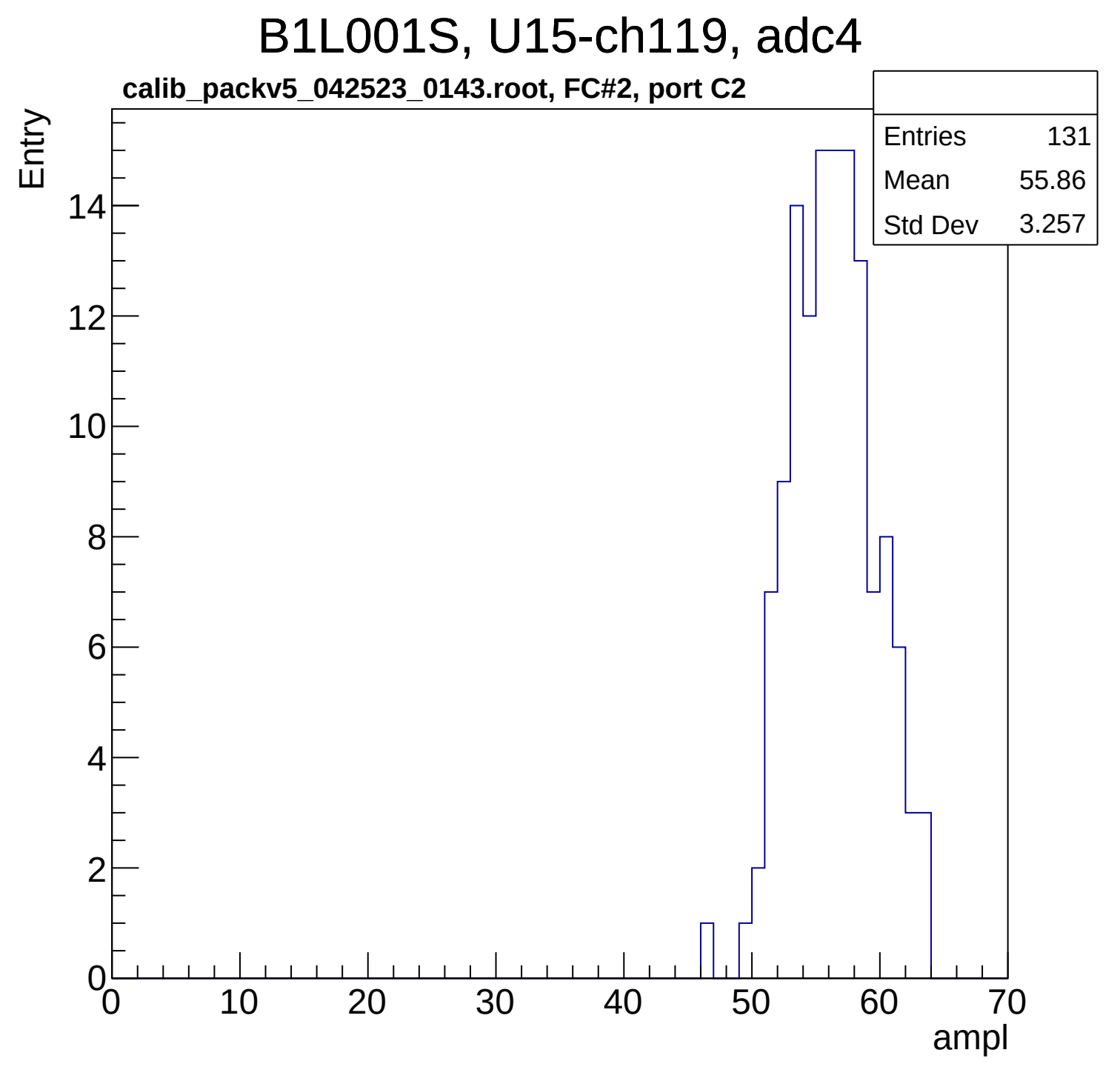

B1L001S, U15-ch119, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 50.1
Std Dev 3.141
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch119, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 55.86
Std Dev 3.257
Entry
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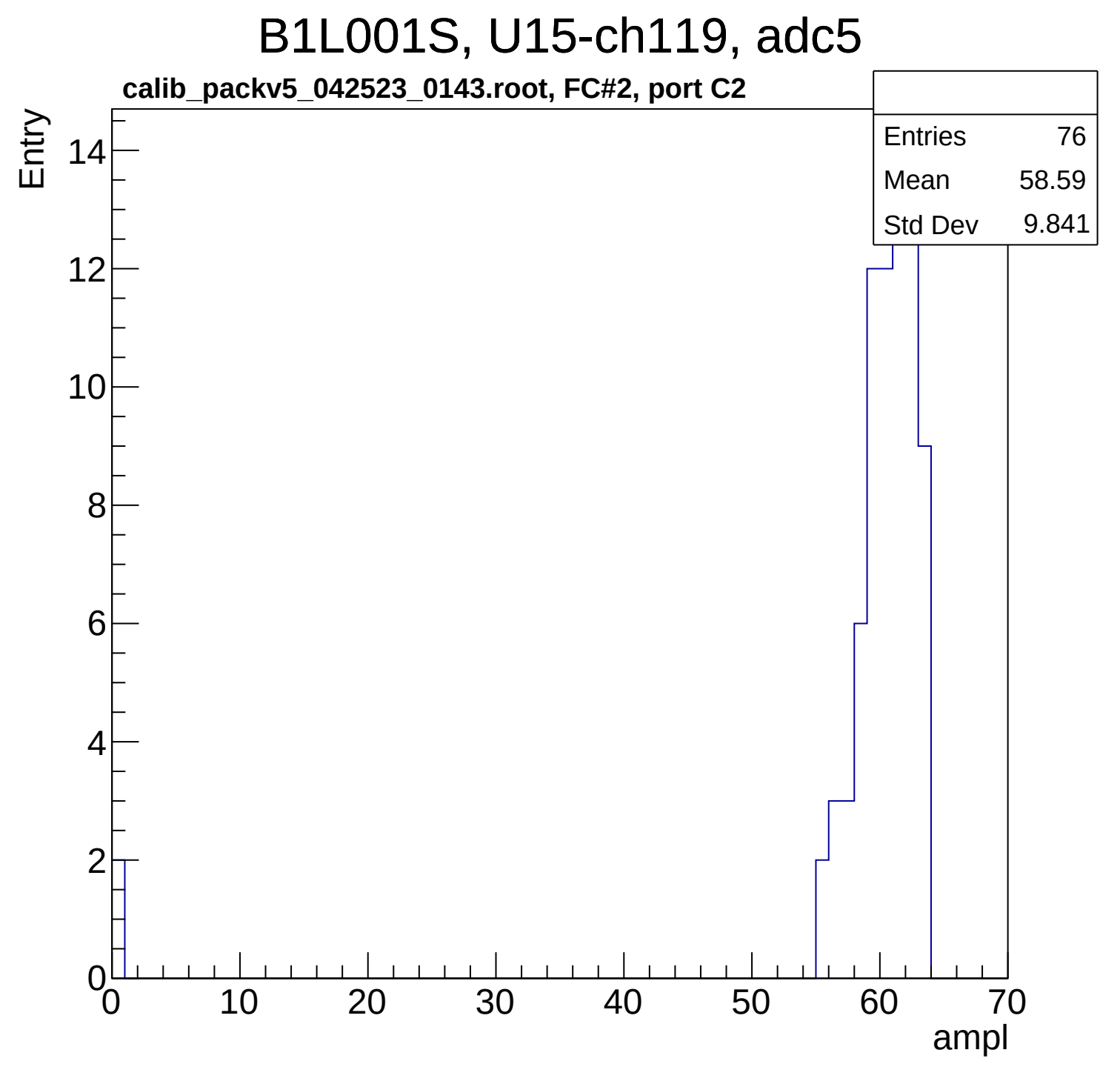
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch119, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 58.59
Std Dev 9.841
Entry
ampl
0
2
4
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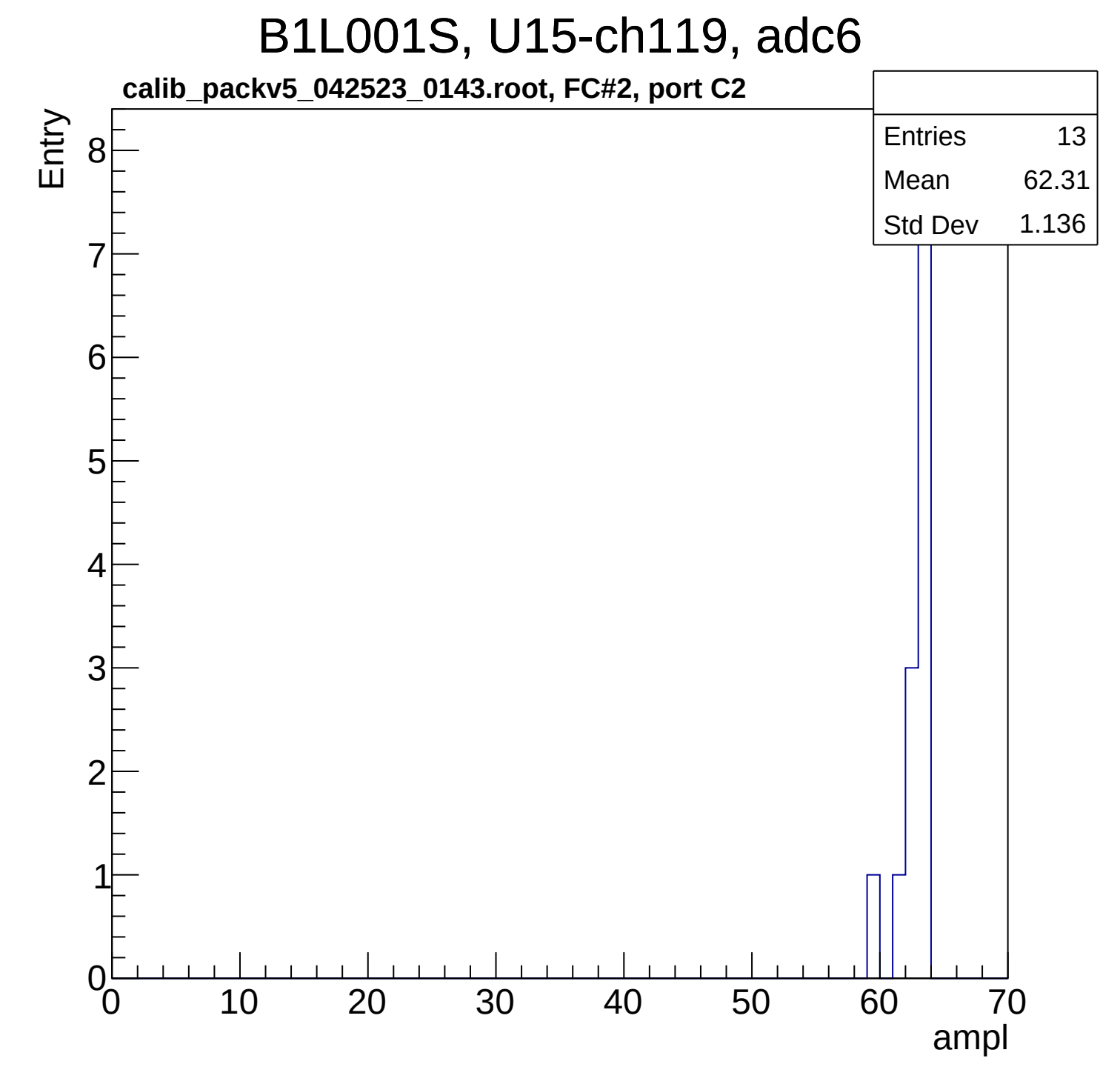
B1L001S, U15-ch119, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62.31
Std Dev 1.136
Entry
ampl
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1
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5
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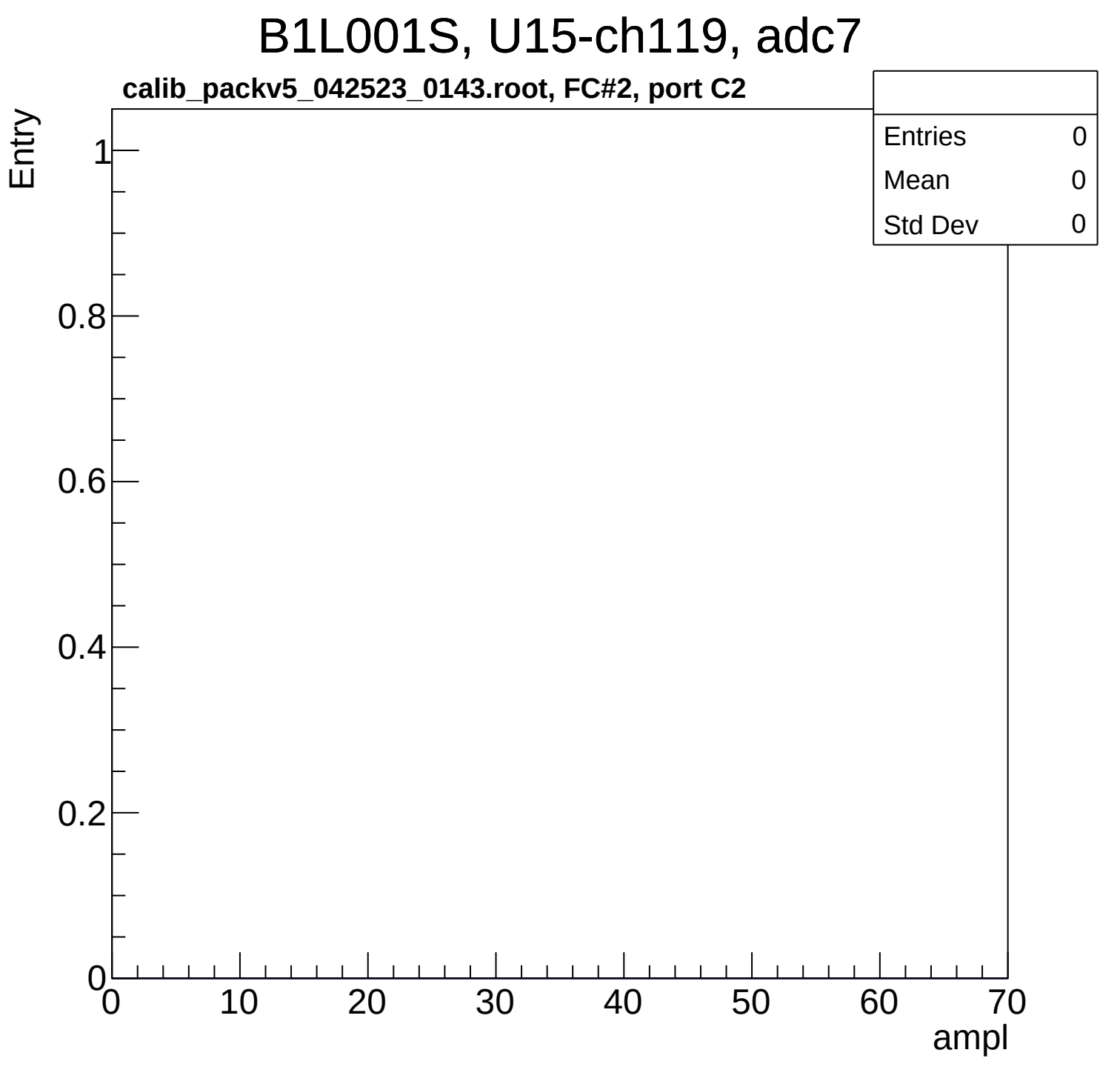
B1L001S, U15-ch119, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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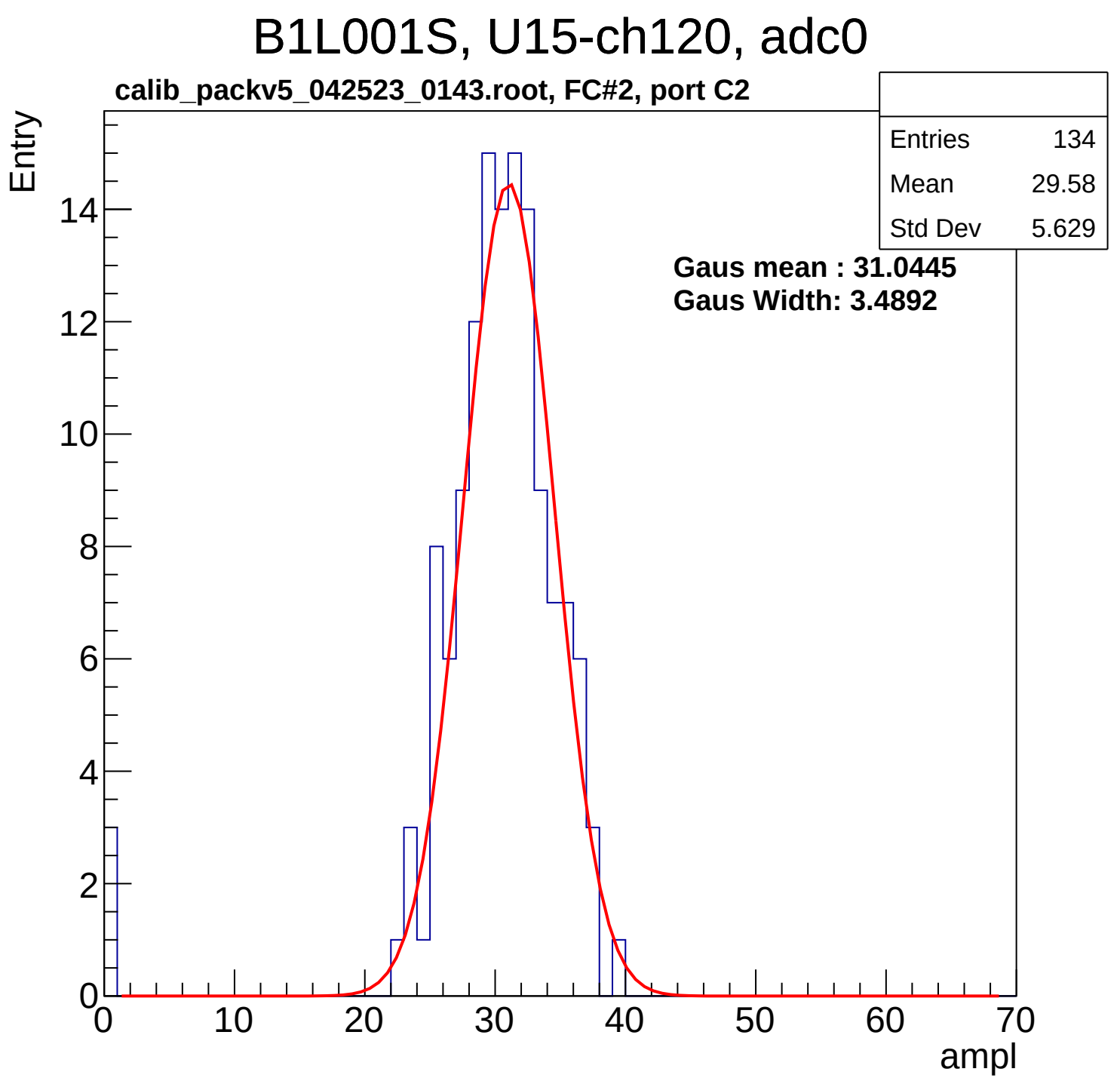
50
60
70
ampl


B1L001S, U15-ch120, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 29.58
Std Dev 5.629
Gaus mean : 31.0445
Gaus Width: 3.4892
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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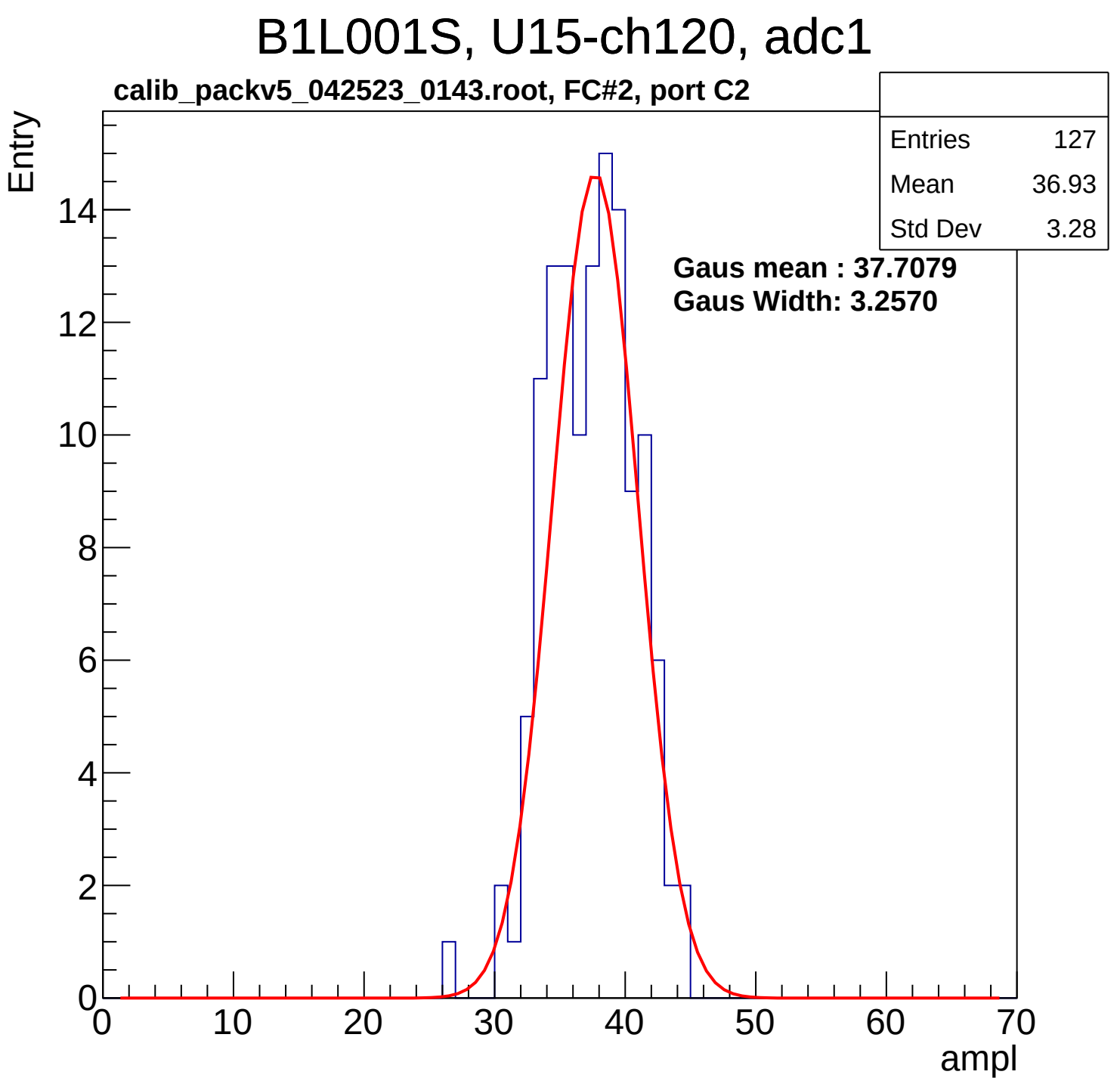
B1L001S, U15-ch120, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 36.93
Std Dev 3.28
Gaus mean : 37.7079
Gaus Width: 3.2570
Entry
ampl
0
2
4
6
8
10
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0
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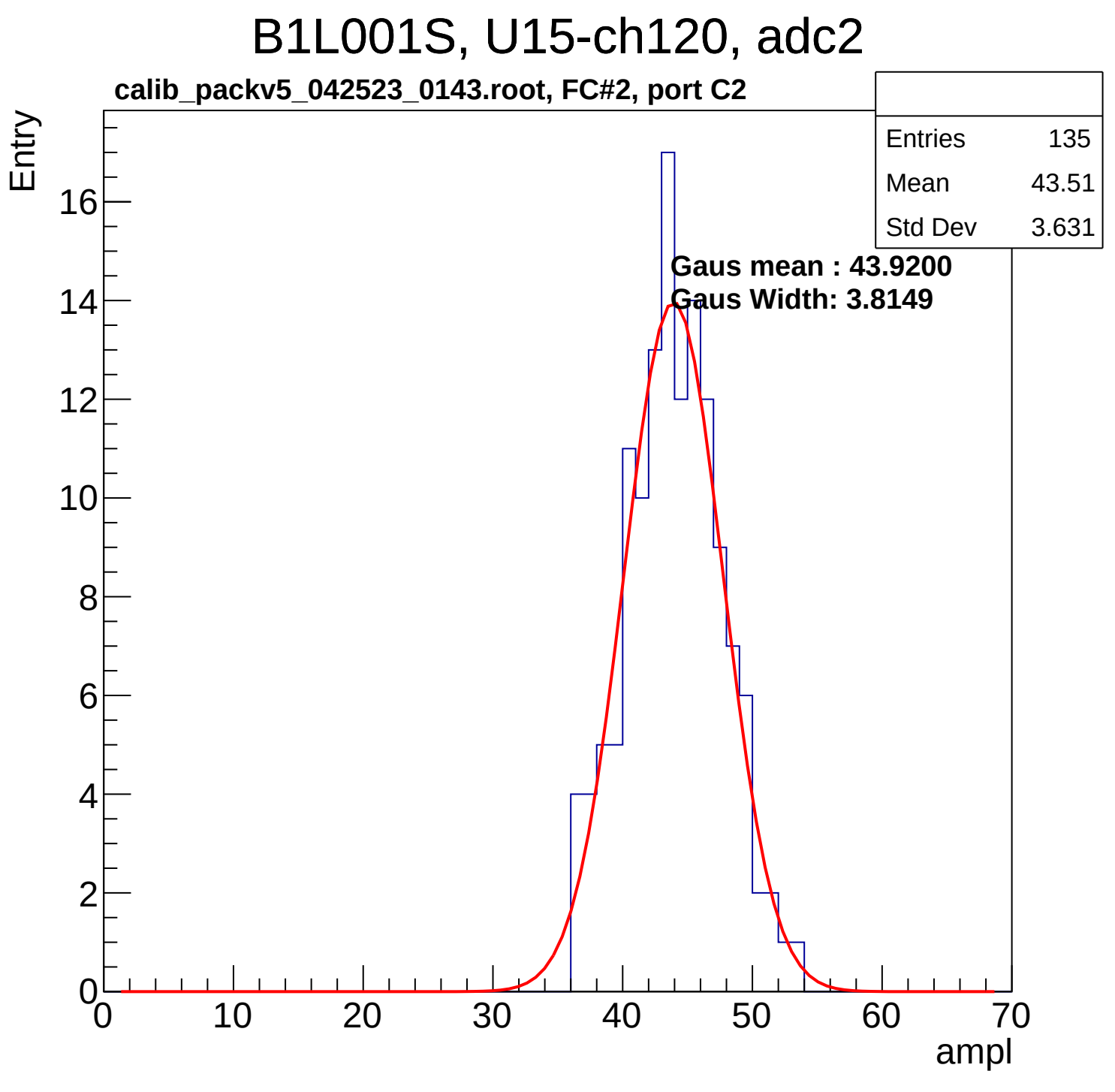
B1L001S, U15-ch120, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 43.51
Std Dev 3.631
Gaus mean : 43.9200
Gaus Width: 3.8149
Entry
ampl
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2
4
6
8
10
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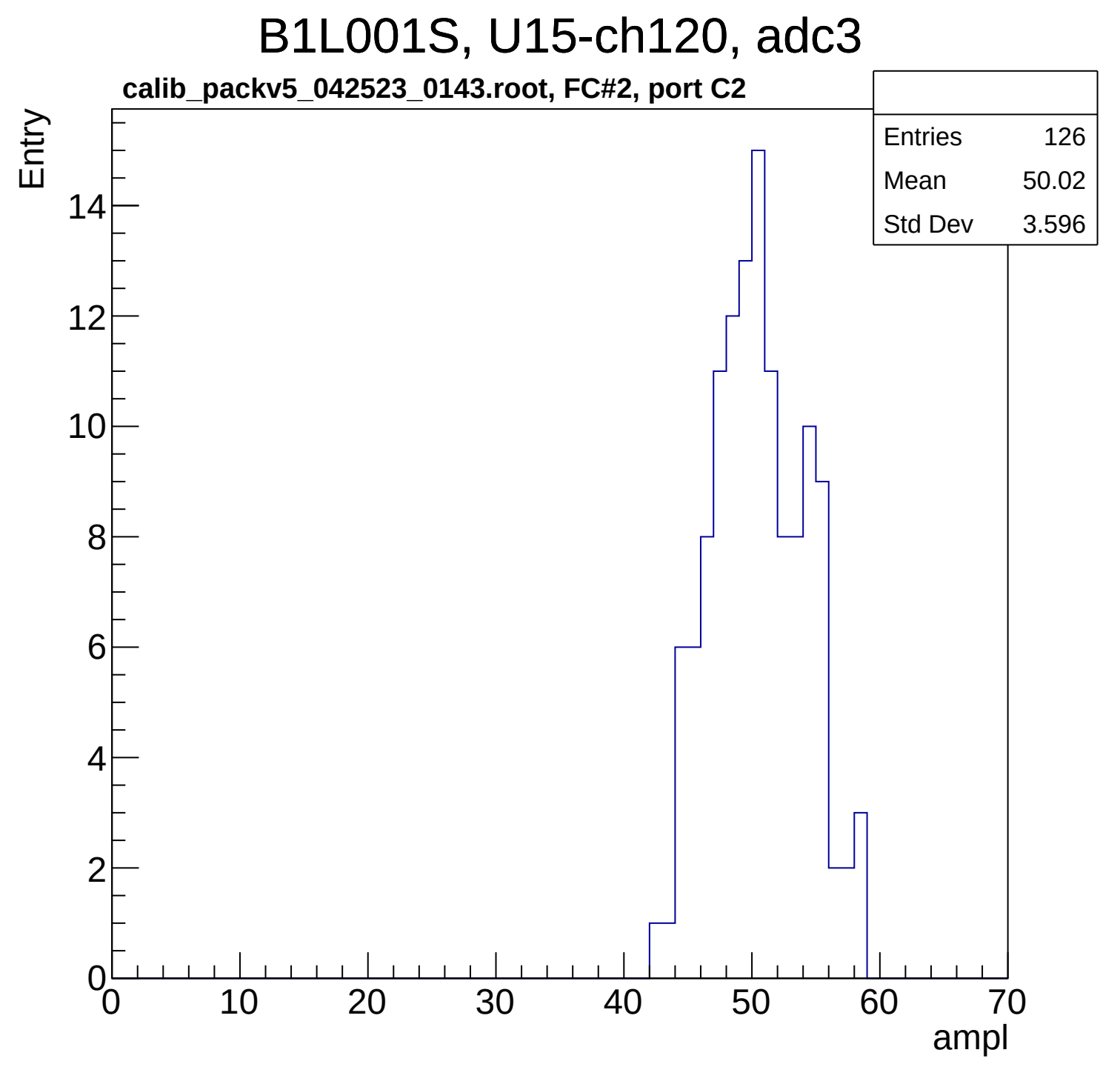
B1L001S, U15-ch120, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 50.02
Std Dev 3.596
Entry
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2
4
6
8
10
12
14
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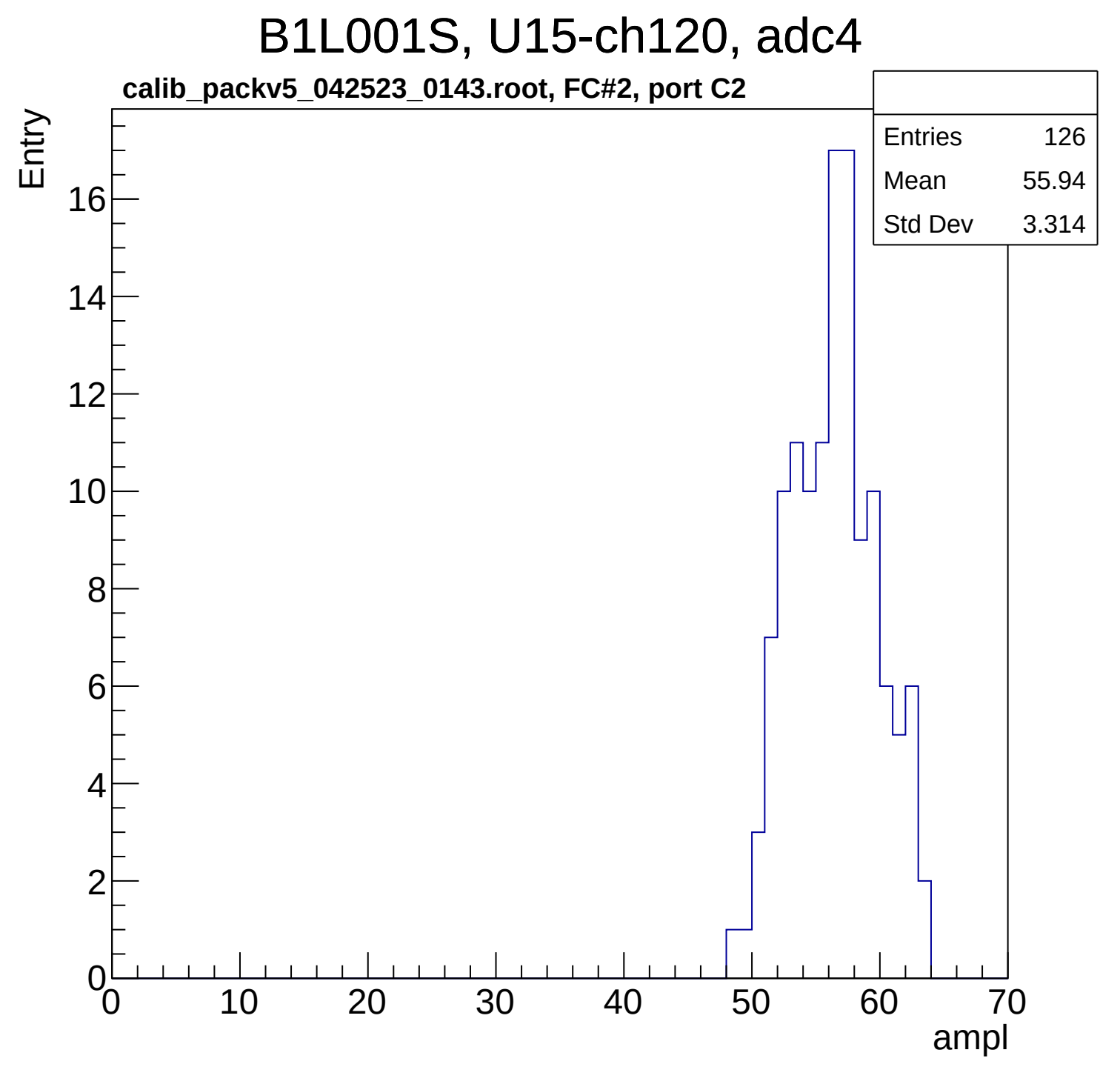
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch120, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 55.94
Std Dev 3.314
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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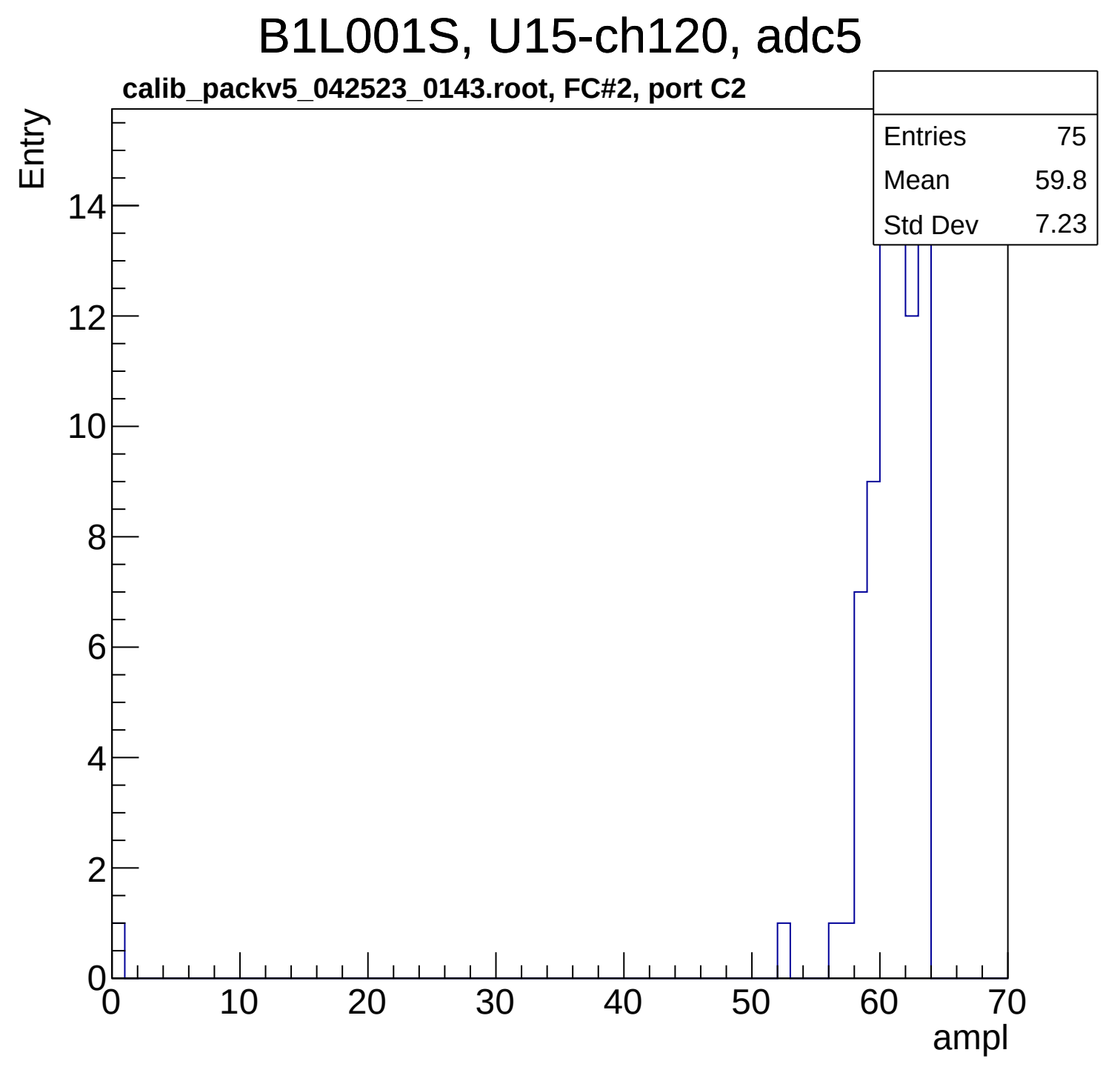
B1L001S, U15-ch120, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 59.8
Std Dev 7.23
Entry
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2
4
6
8
10
12
14
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10
20
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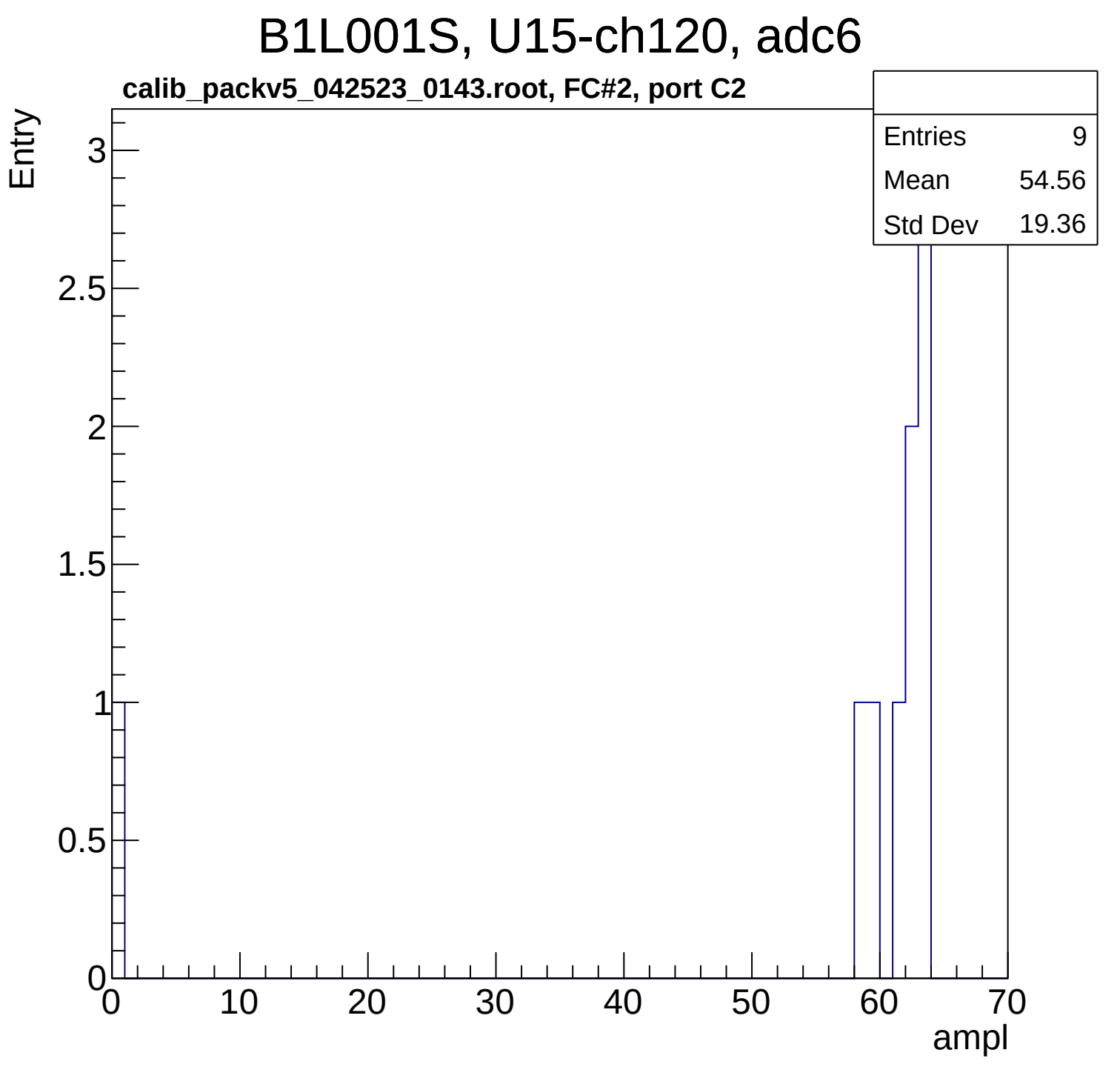
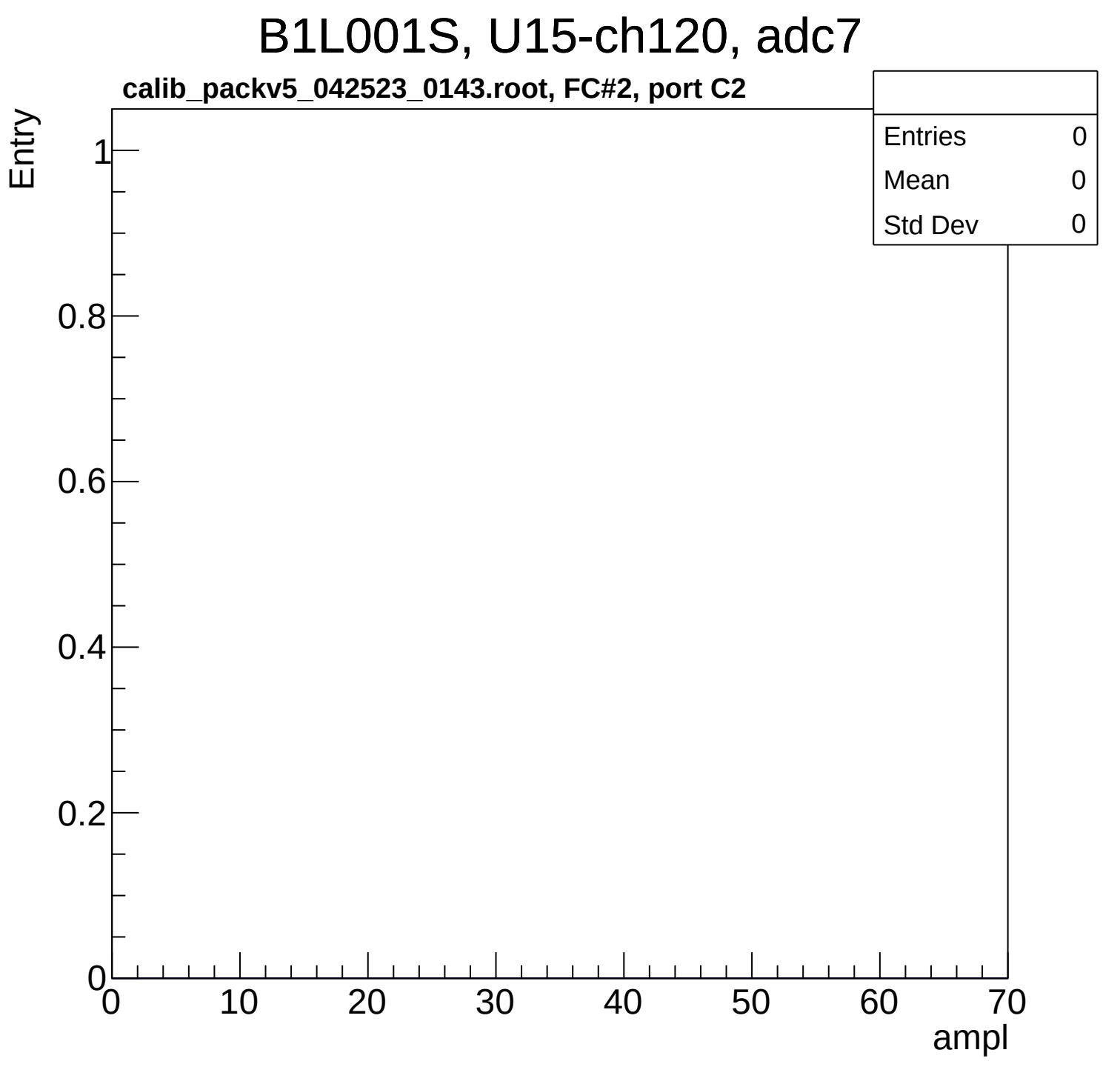
50
60
70
ampl


B1L001S, U15-ch120, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 54.56
Std Dev 19.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U15-ch120, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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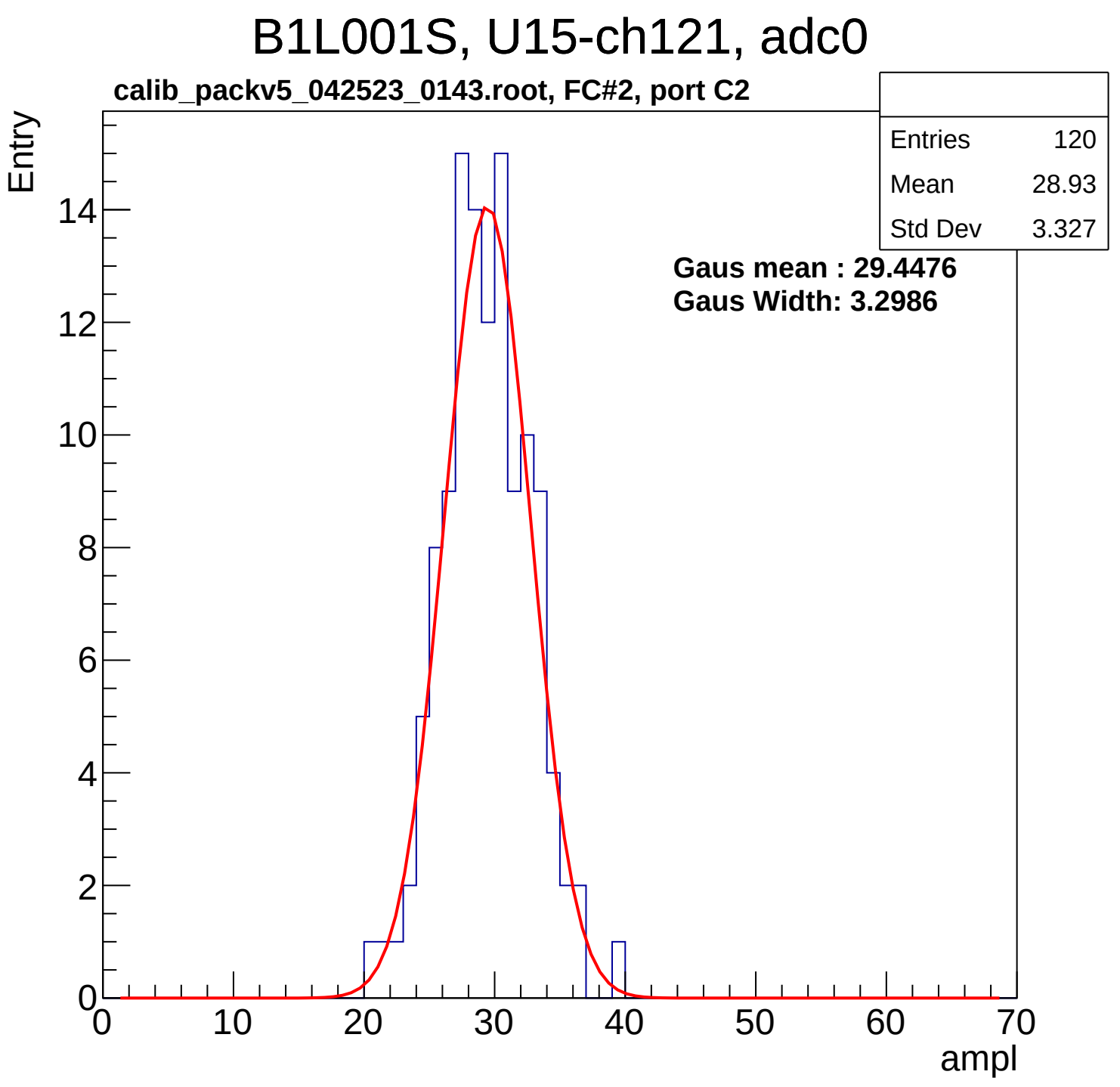
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch121, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 28.93
Std Dev 3.327
Gaus mean : 29.4476
Gaus Width: 3.2986
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
30
40
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60
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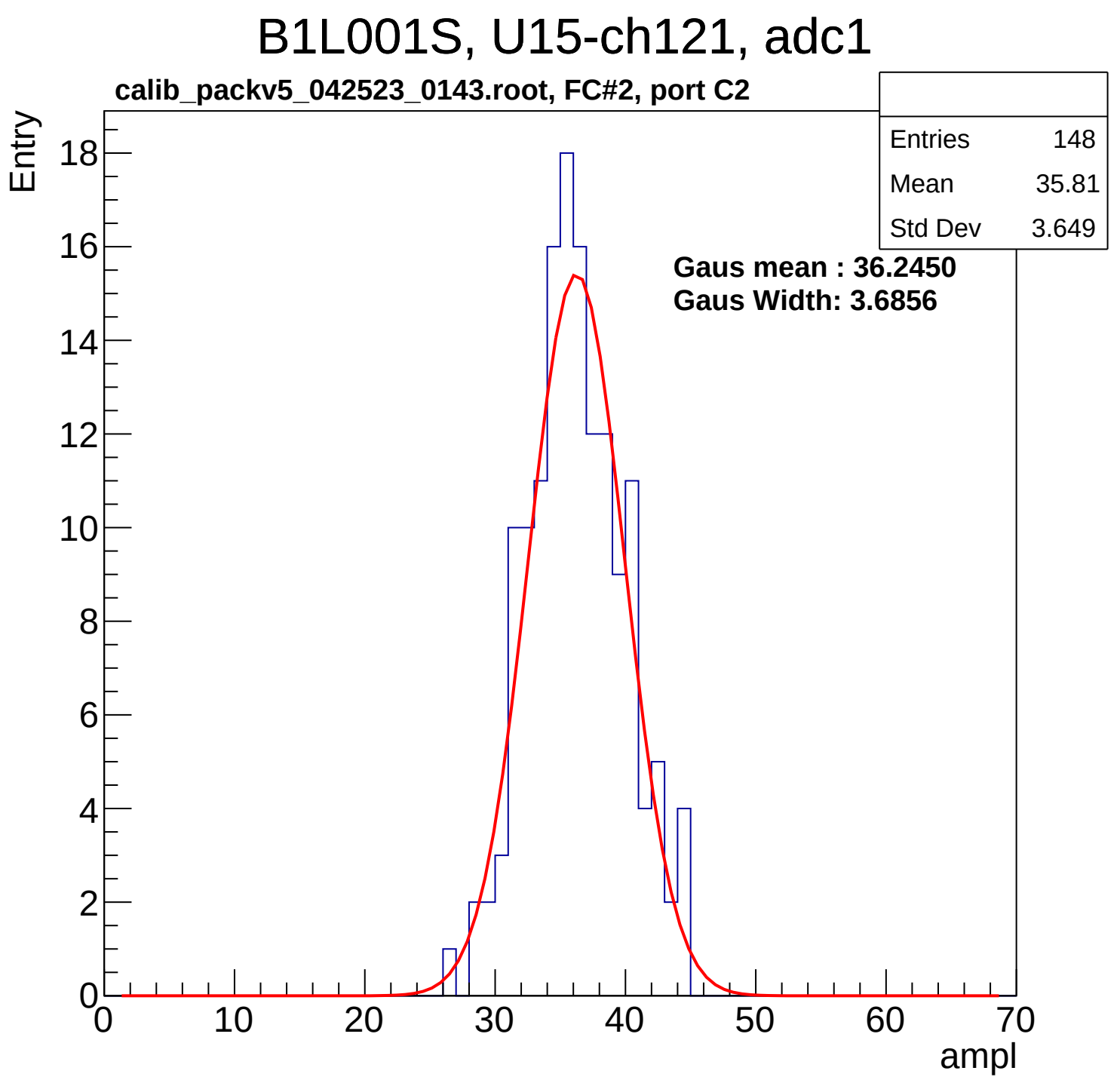
B1L001S, U15-ch121, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 35.81
Std Dev 3.649
Gaus mean : 36.2450
Gaus Width: 3.6856
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
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60
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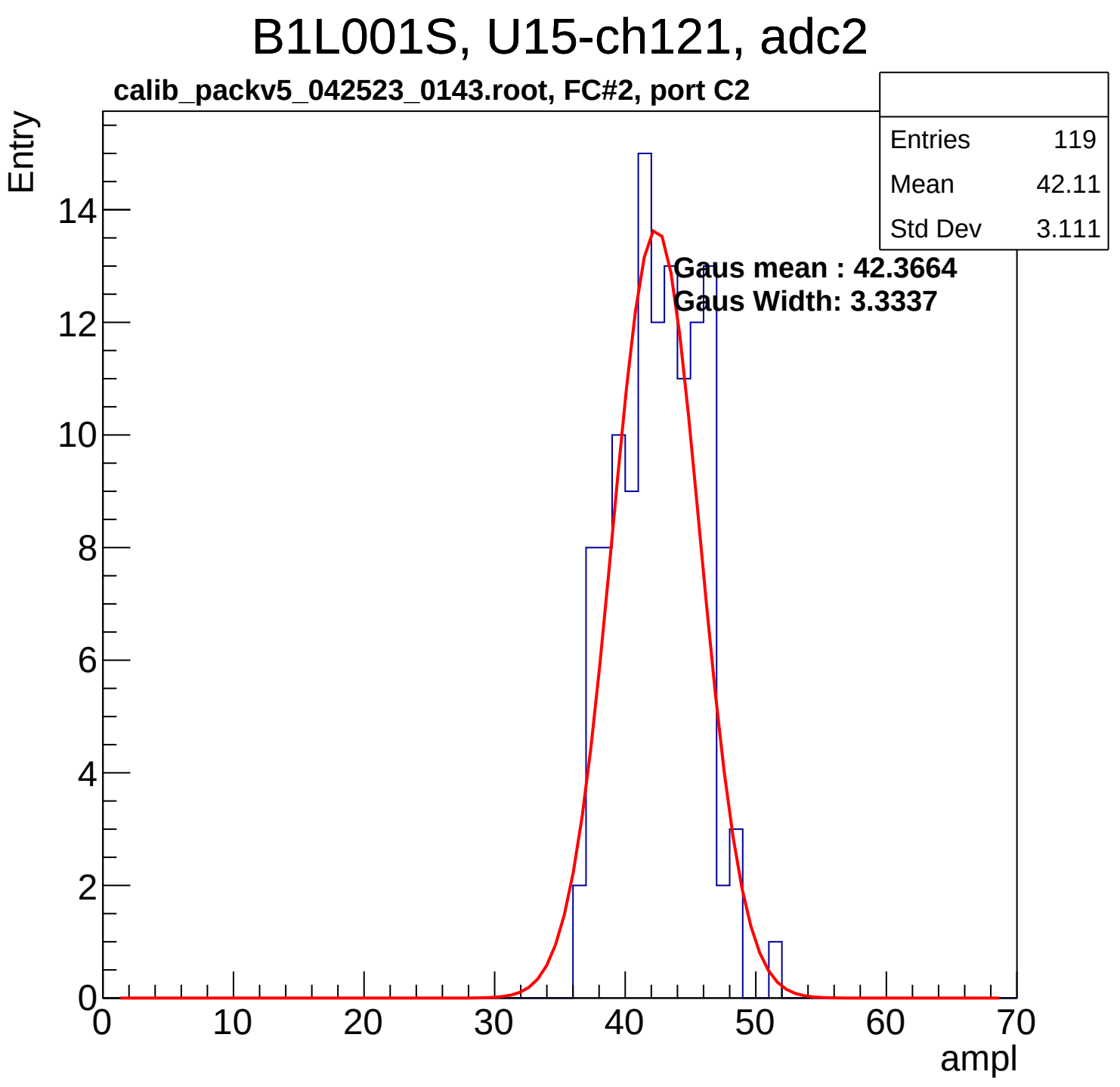
B1L001S, U15-ch121, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 42.11
Std Dev 3.111
Gaus mean : 42.3664
Gaus Width: 3.3337
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
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60
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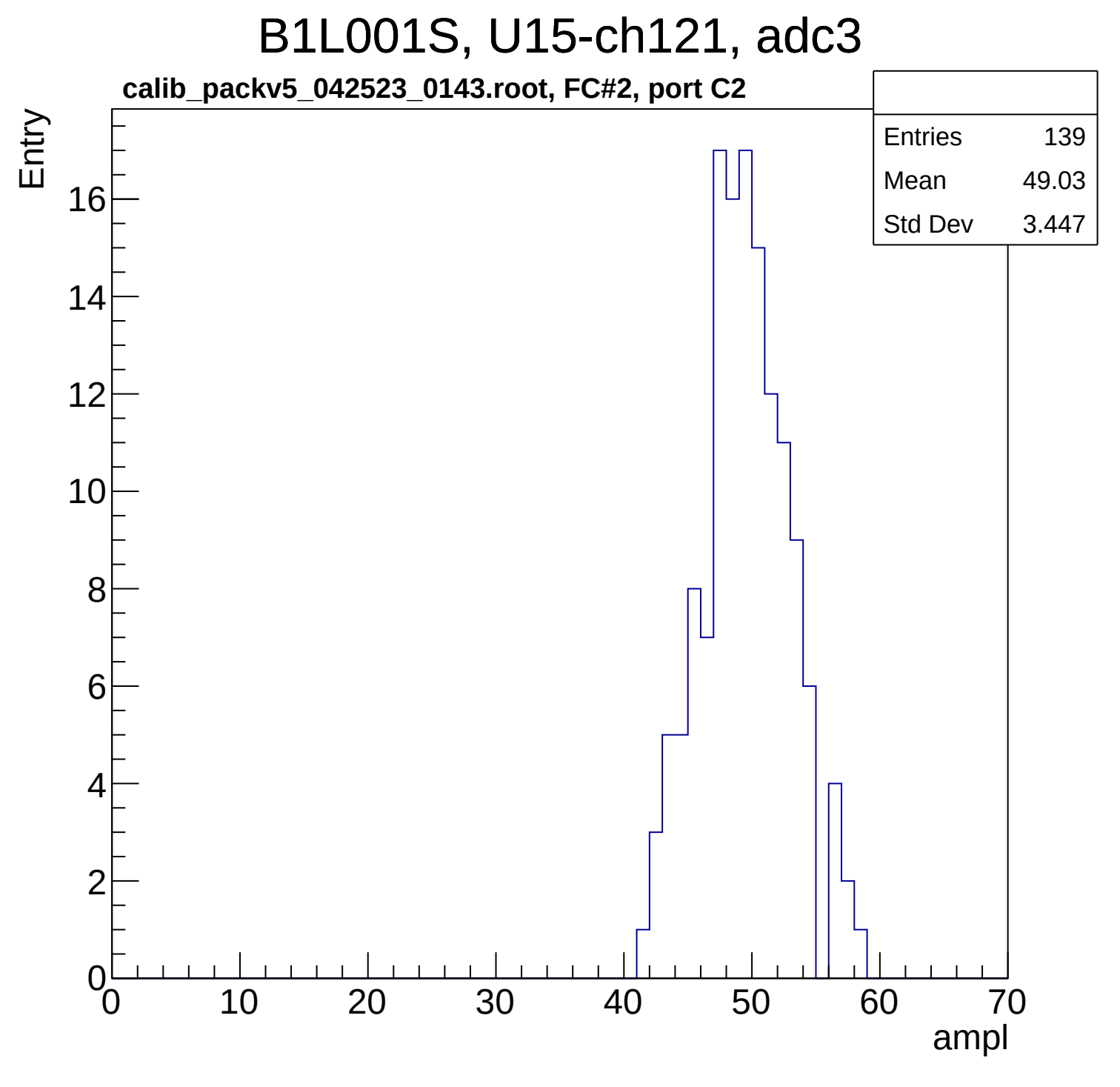
B1L001S, U15-ch121, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.03
Std Dev 3.447
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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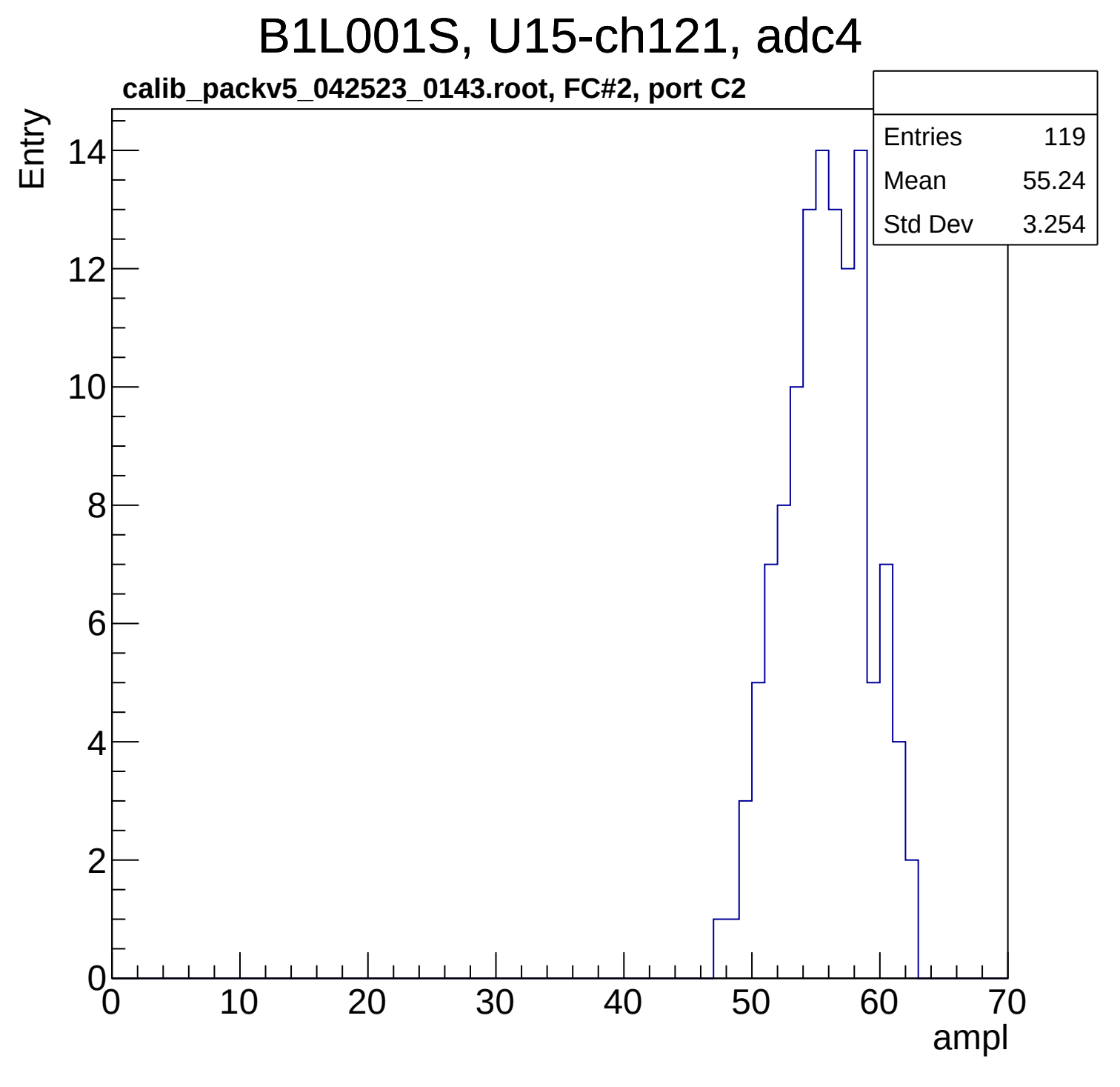
B1L001S, U15-ch121, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 55.24
Std Dev 3.254
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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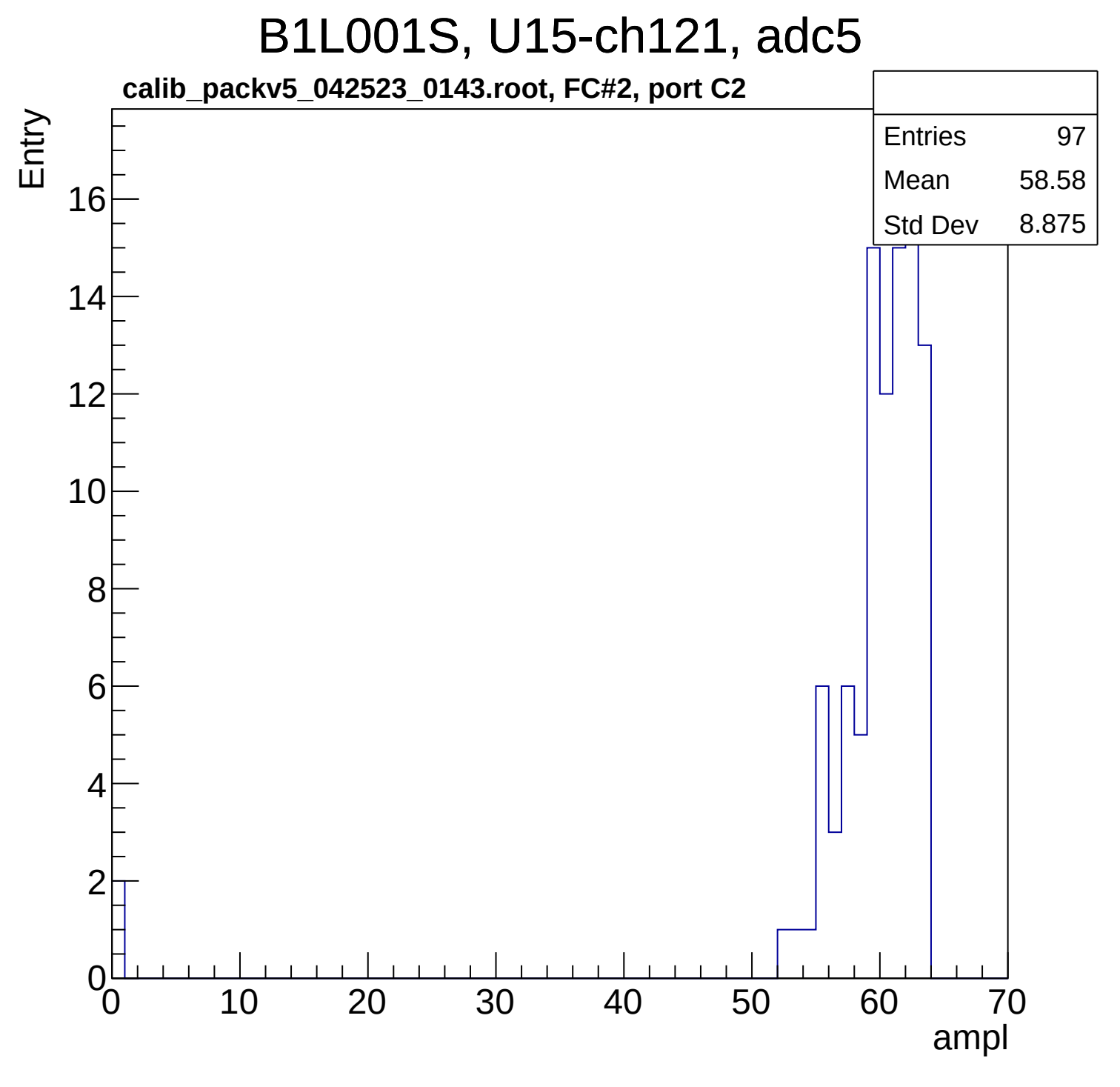
B1L001S, U15-ch121, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 58.58
Std Dev 8.875
Entry
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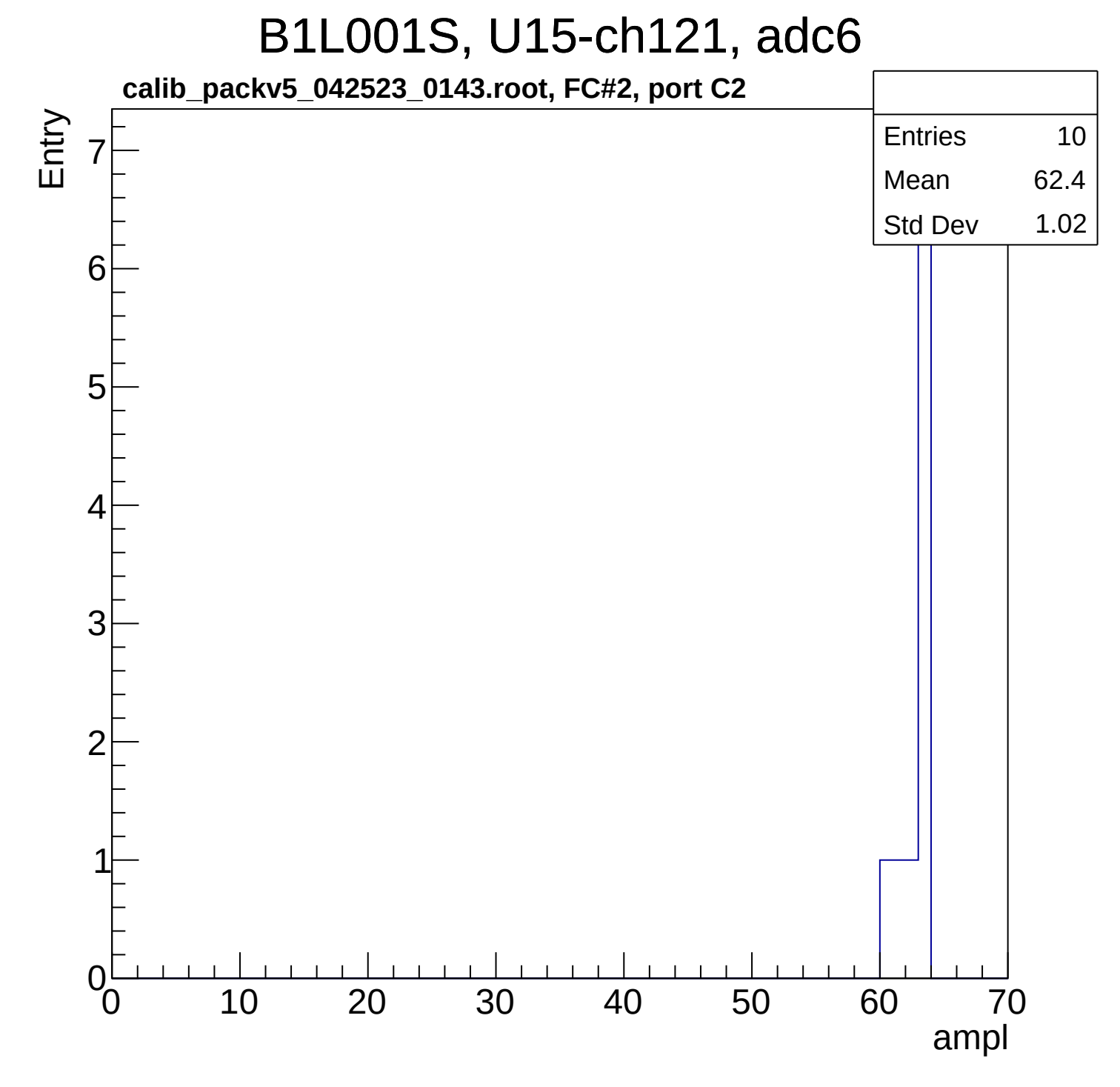
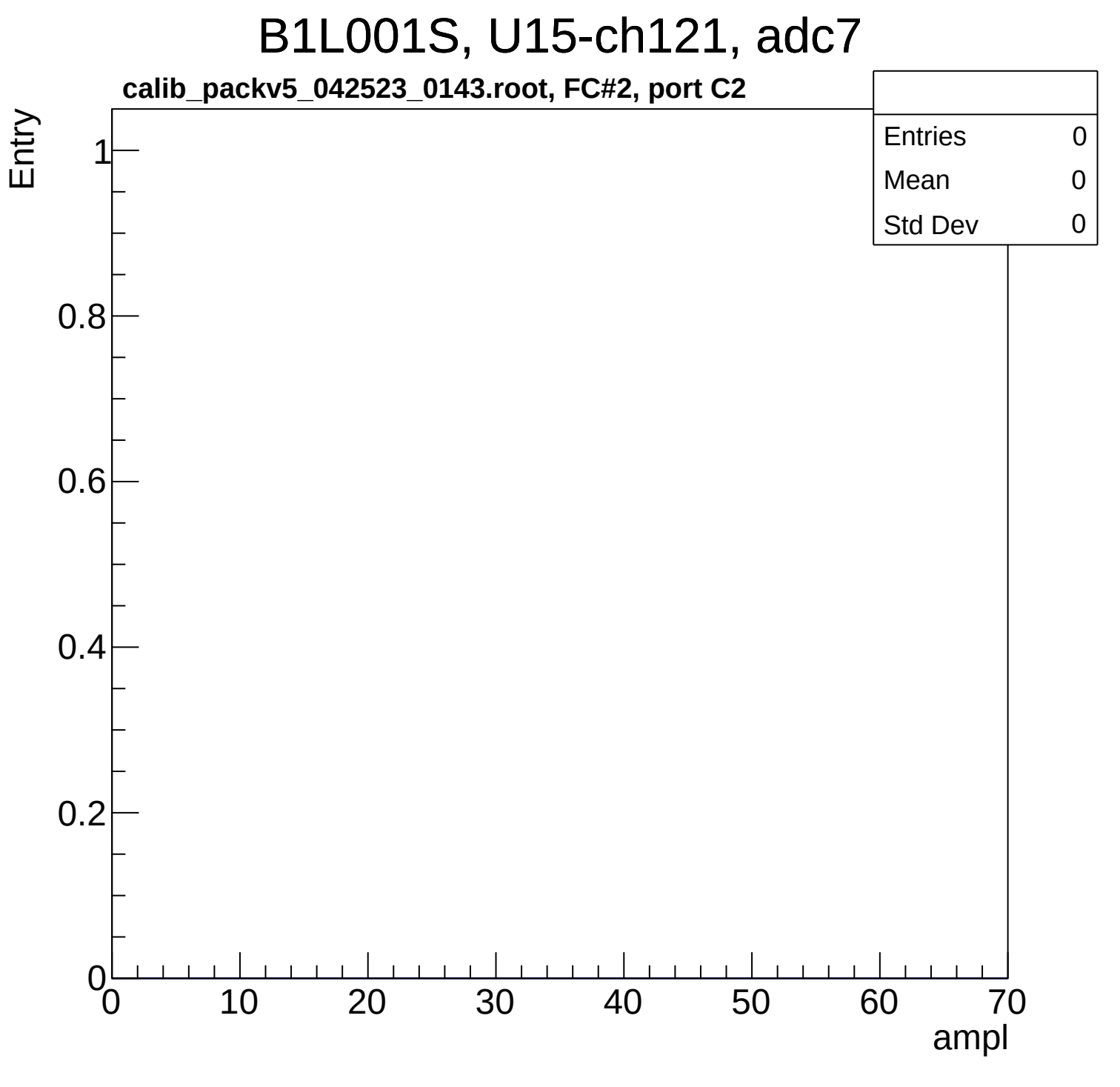
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch121, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 62.4
Std Dev 1.02
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch121, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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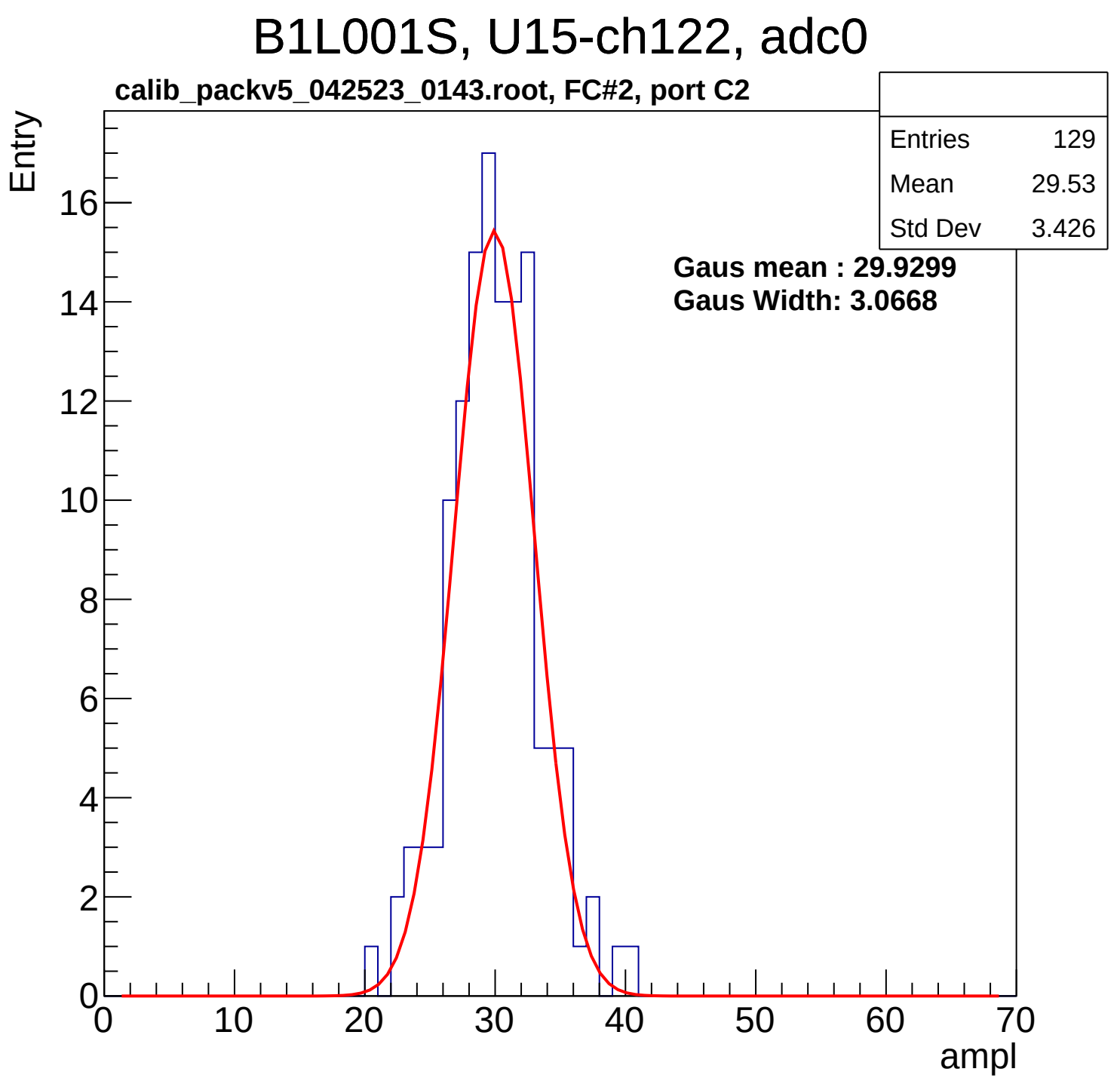
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch122, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 29.53
Std Dev 3.426
Gaus mean : 29.9299
Gaus Width: 3.0668
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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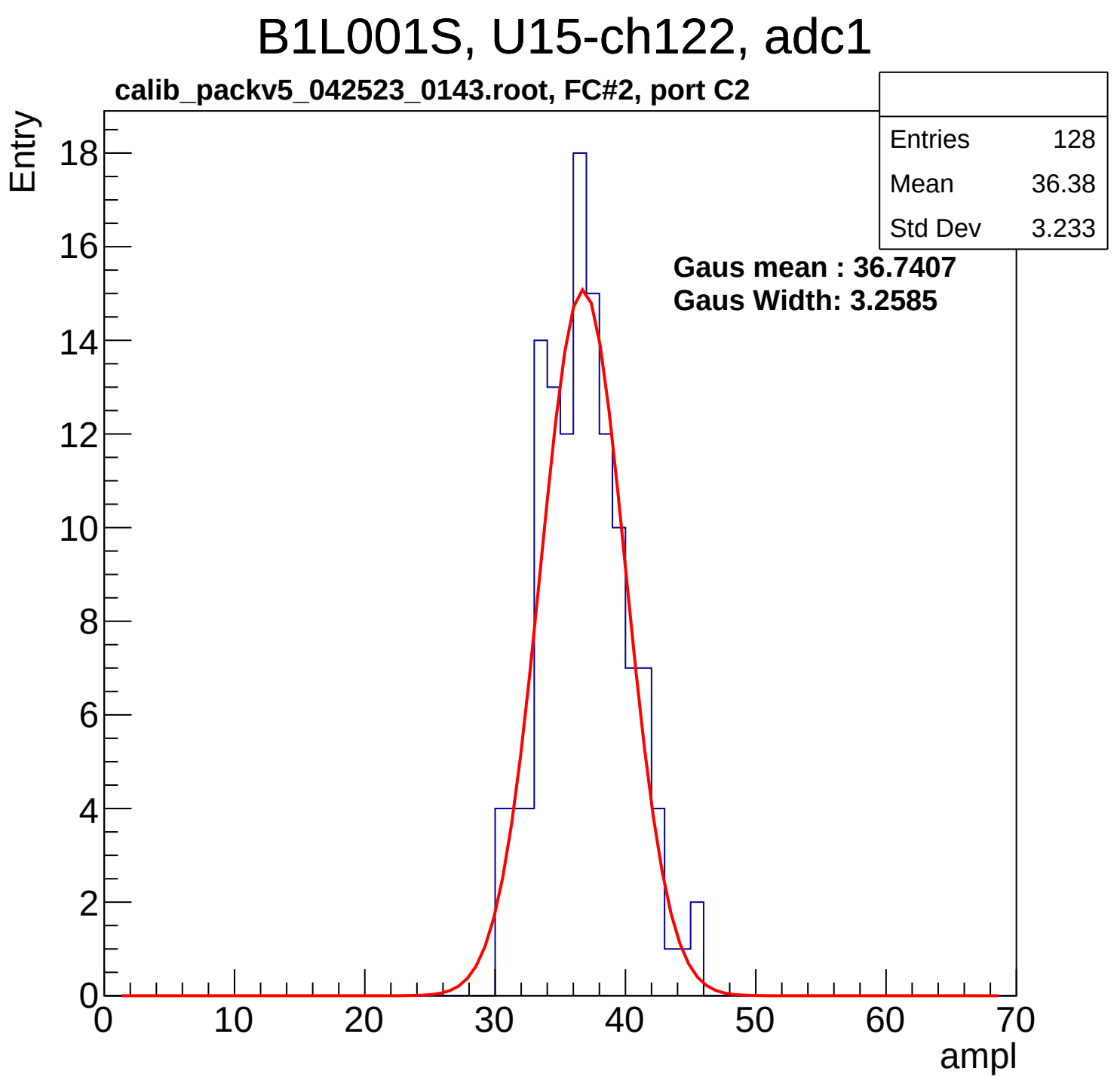
B1L001S, U15-ch122, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 36.38
Std Dev 3.233
Gaus mean : 36.7407
Gaus Width: 3.2585
Entry
ampl
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4
6
8
10
12
14
16
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0
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40
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60
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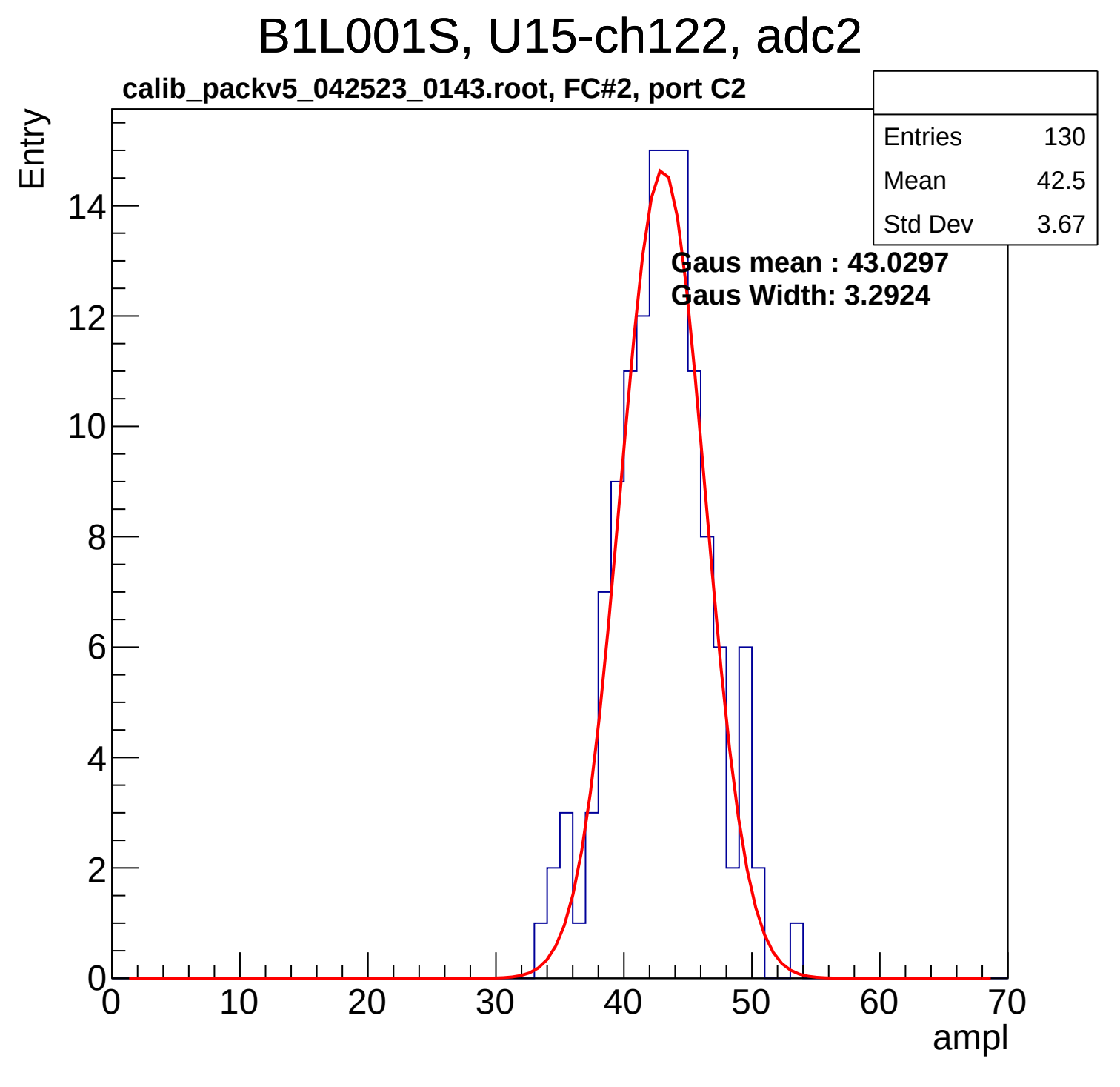
B1L001S, U15-ch122, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.5
Std Dev 3.67
Gaus mean : 43.0297
Gaus Width: 3.2924
Entry
ampl
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6
8
10
12
14
0
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20
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60
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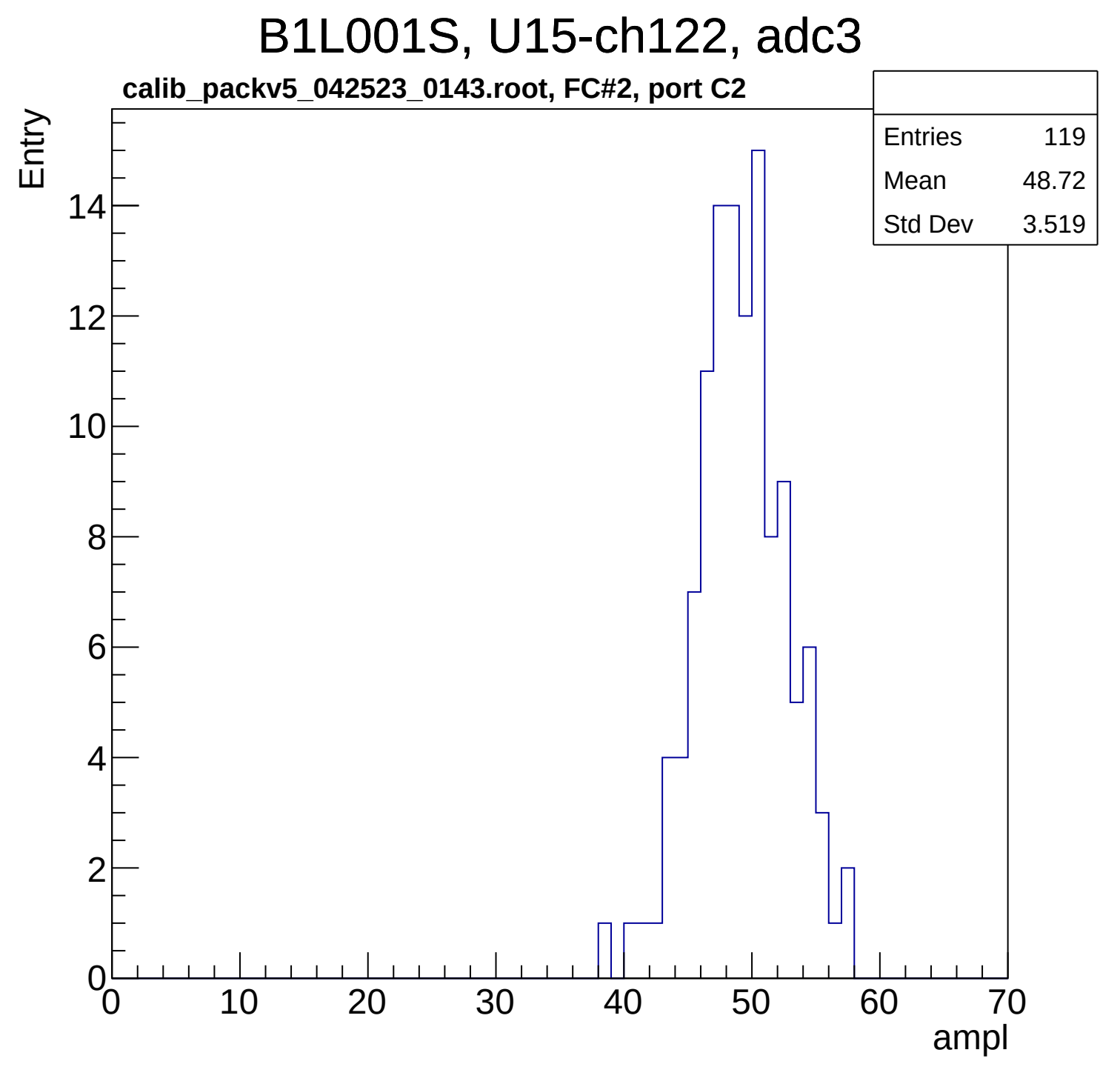
B1L001S, U15-ch122, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 48.72
Std Dev 3.519
Entry
ampl
0
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6
8
10
12
14
0
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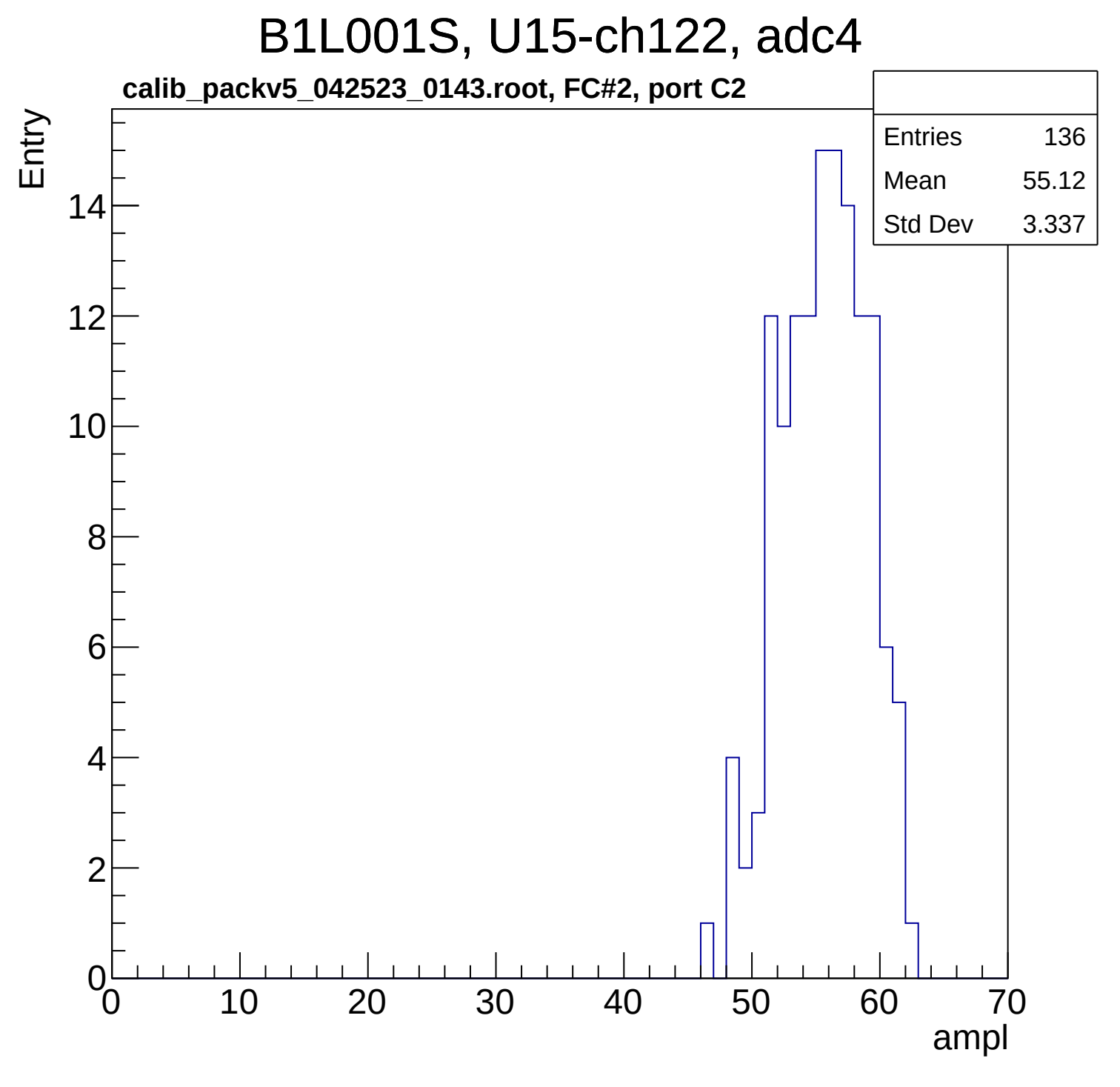
B1L001S, U15-ch122, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 55.12
Std Dev 3.337
Entry
ampl
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4
6
8
10
12
14
0
10
20
30
40
50
60
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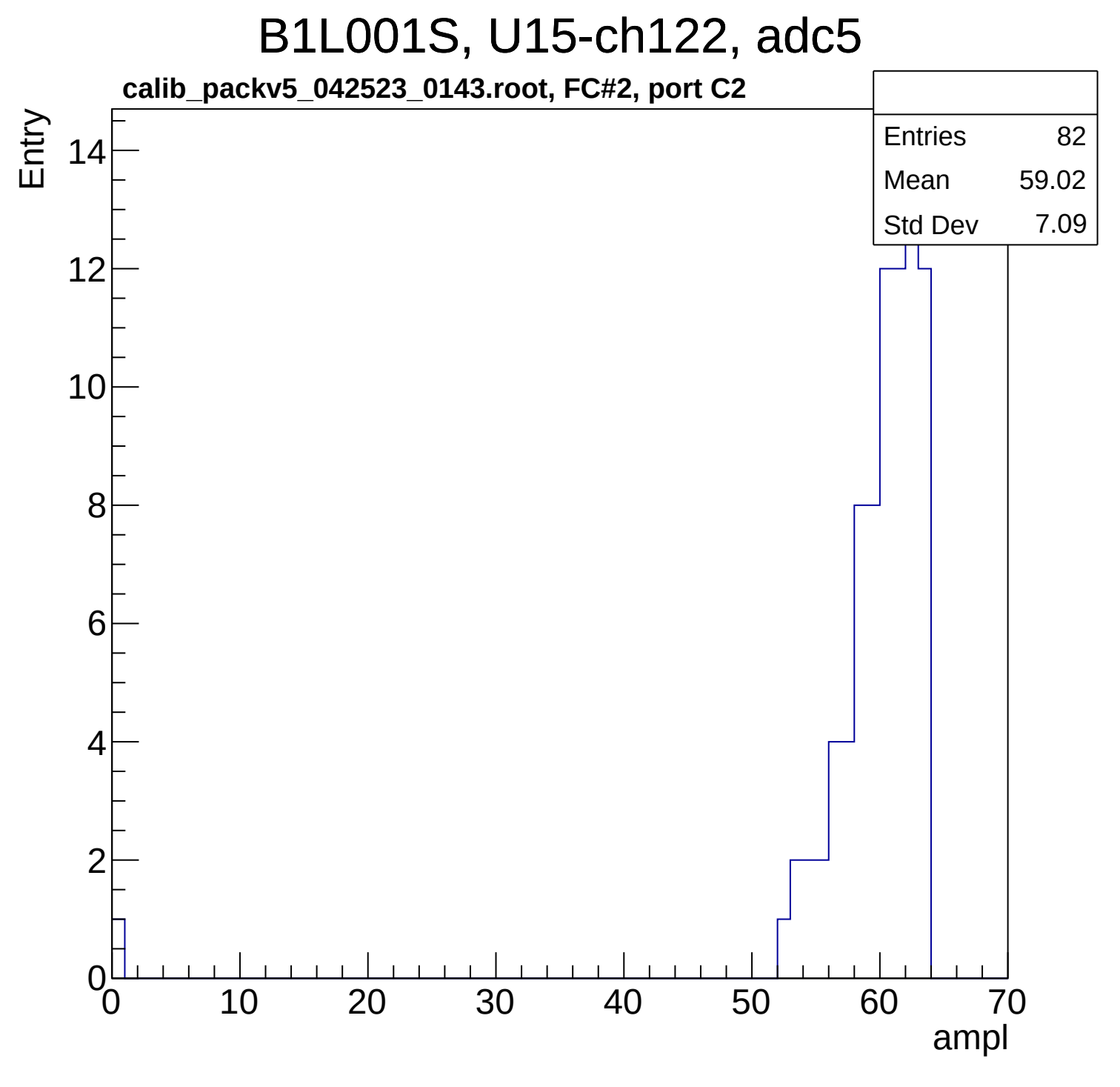
B1L001S, U15-ch122, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 59.02
Std Dev 7.09
Entry
ampl
0
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4
6
8
10
12
14
0
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20
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60
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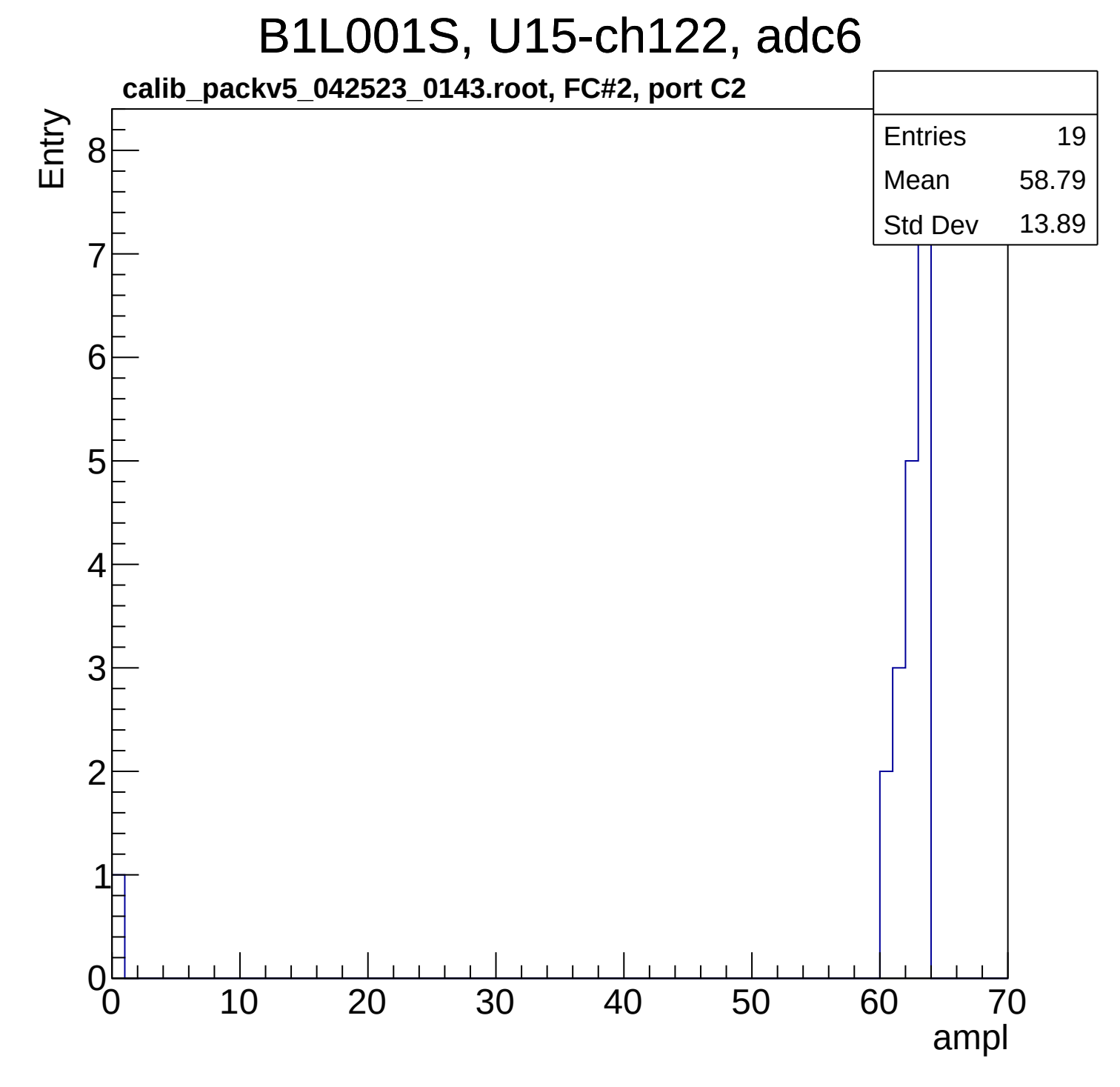
B1L001S, U15-ch122, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 19
Mean 58.79
Std Dev 13.89
Entry
ampl
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3
4
5
6
7
8
0
10
20
30
40
50
60
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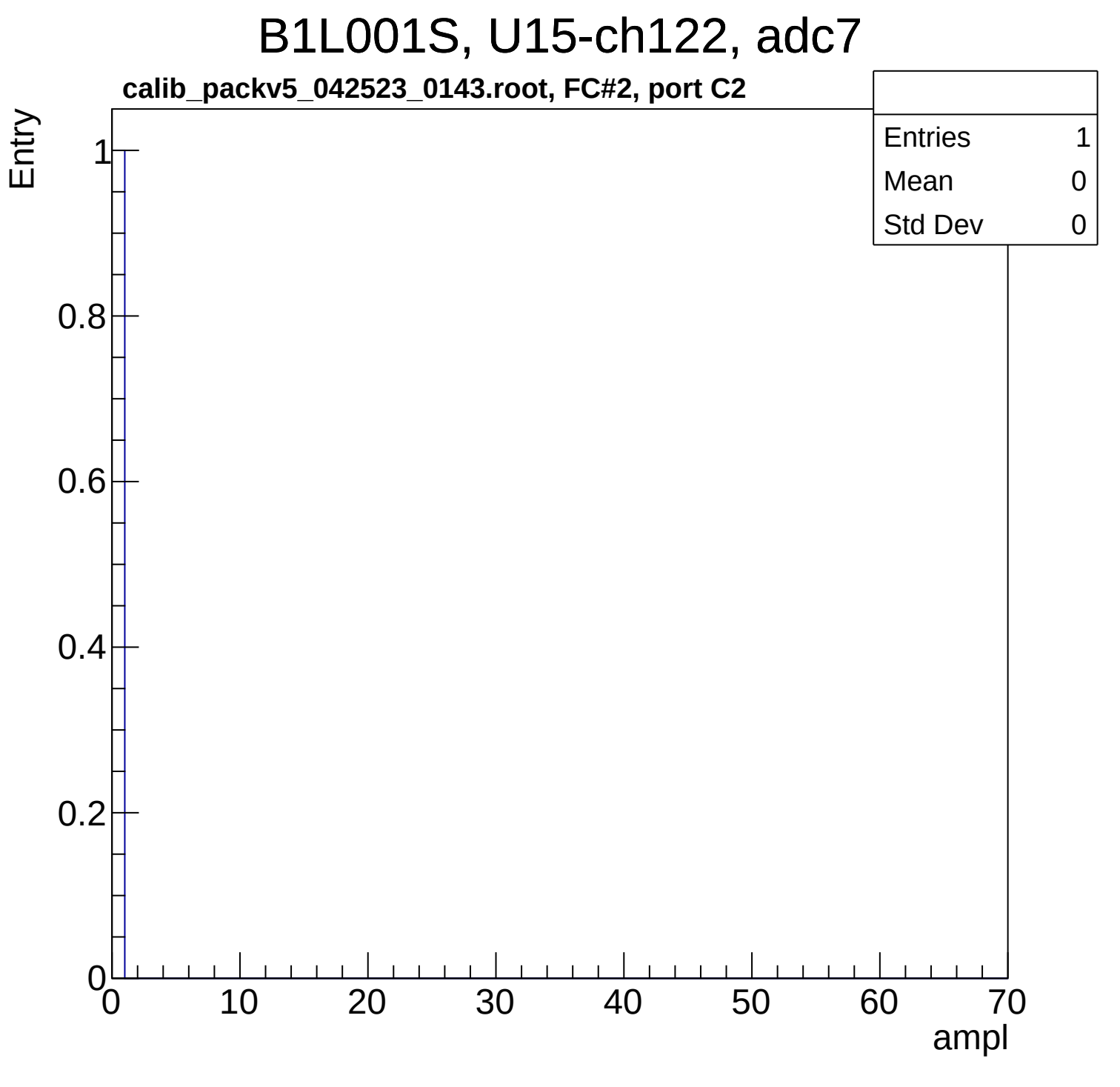
B1L001S, U15-ch122, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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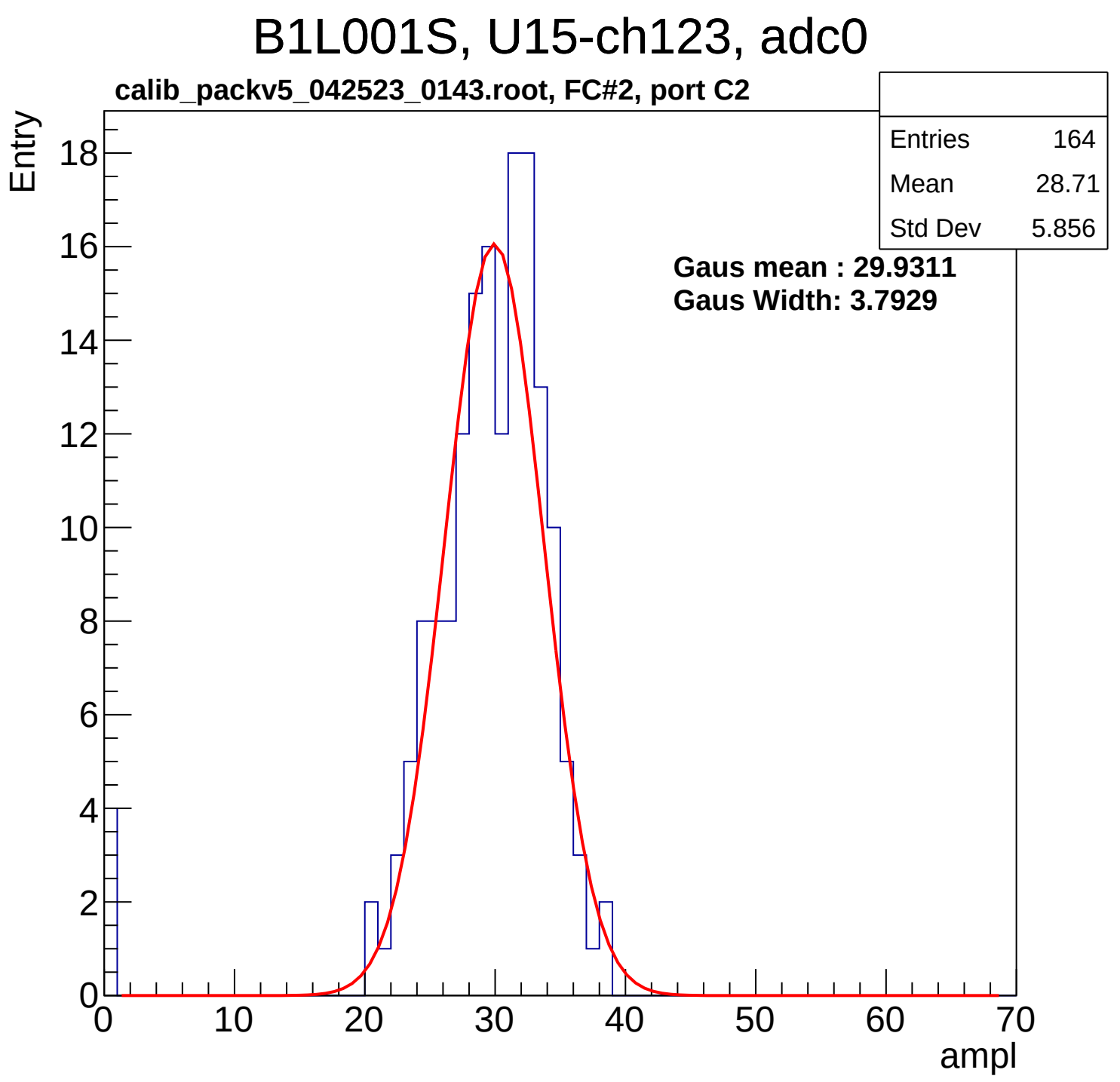
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch123, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 164
Mean 28.71
Std Dev 5.856
Gaus mean : 29.9311
Gaus Width: 3.7929
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
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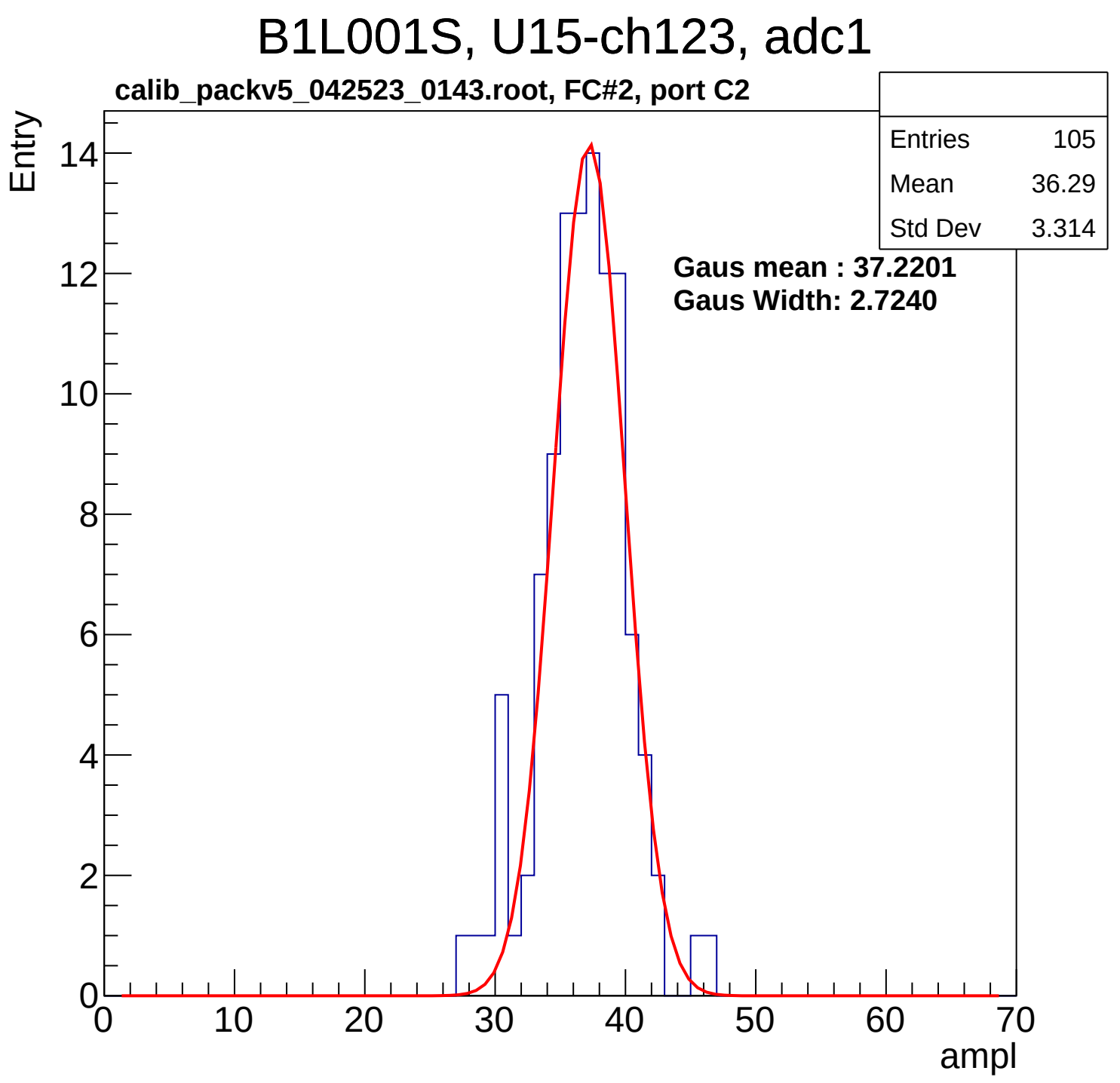
60
70


B1L001S, U15-ch123, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 36.29
Std Dev 3.314
Gaus mean : 37.2201
Gaus Width: 2.7240
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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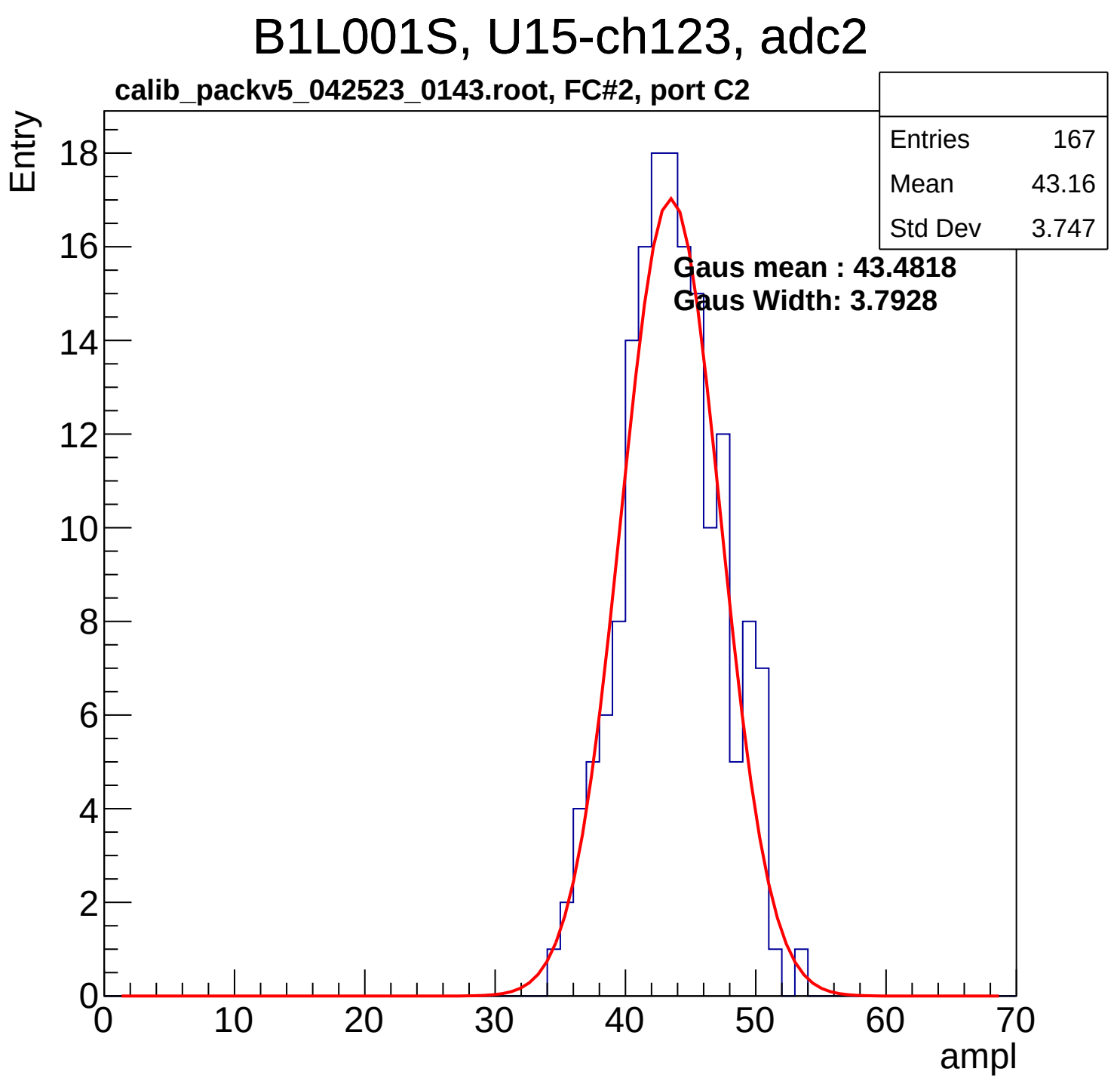
B1L001S, U15-ch123, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 43.16
Std Dev 3.747
Gaus mean : 43.4818
Gaus Width: 3.7928
Entry
ampl
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4
6
8
10
12
14
16
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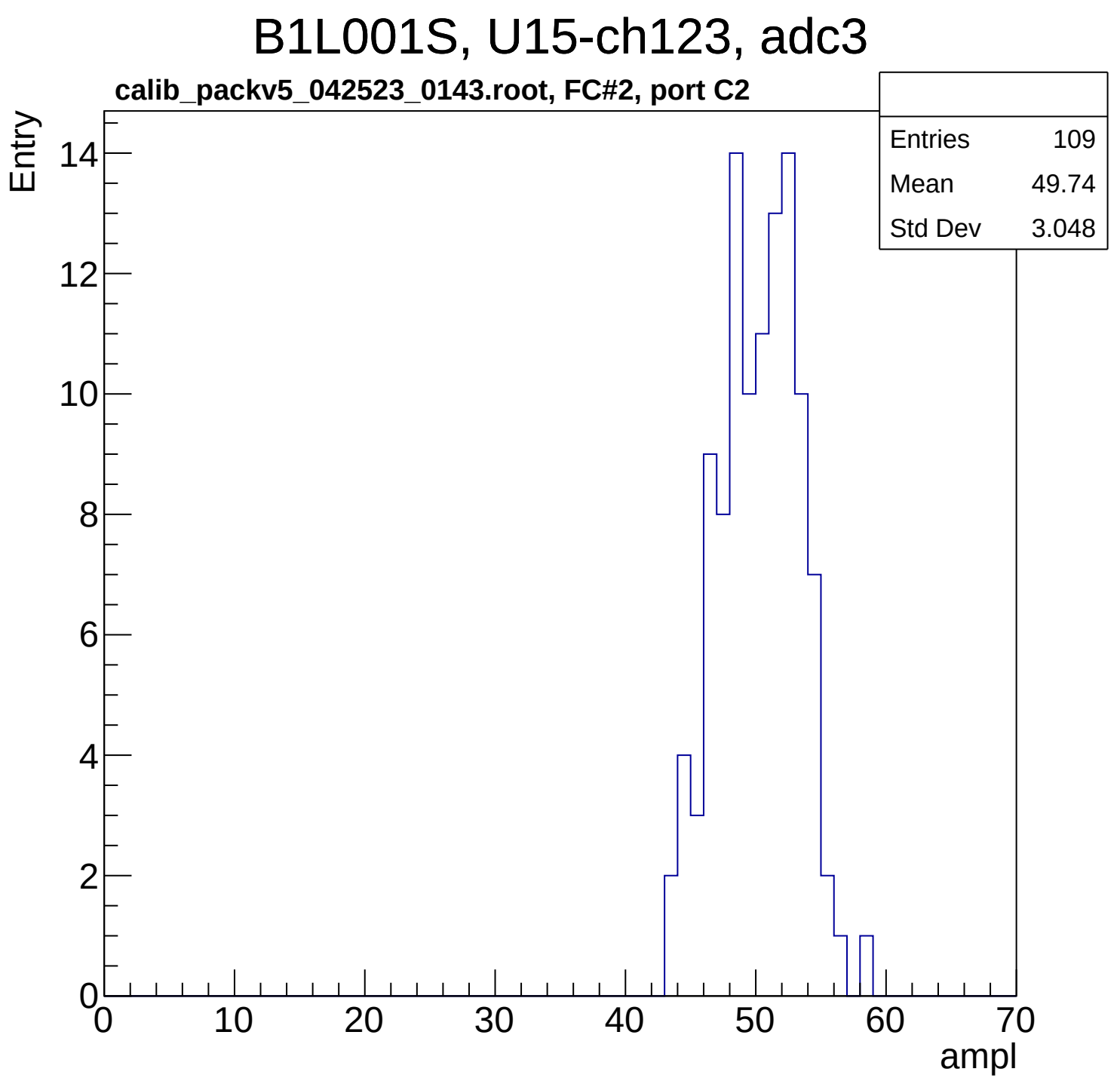
B1L001S, U15-ch123, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 49.74
Std Dev 3.048
Entry
ampl
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6
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10
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0
10
20
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60
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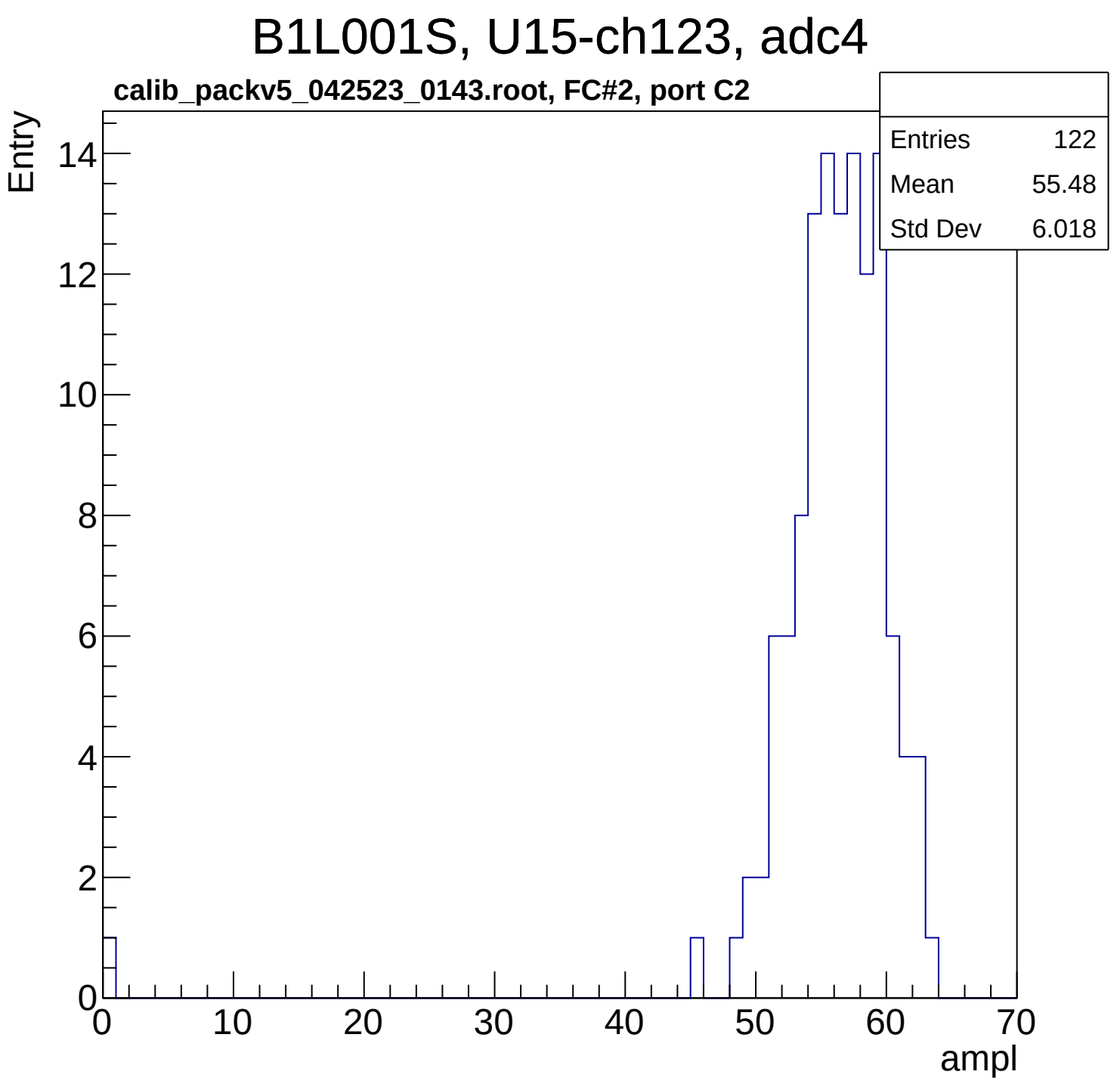
B1L001S, U15-ch123, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 55.48
Std Dev 6.018
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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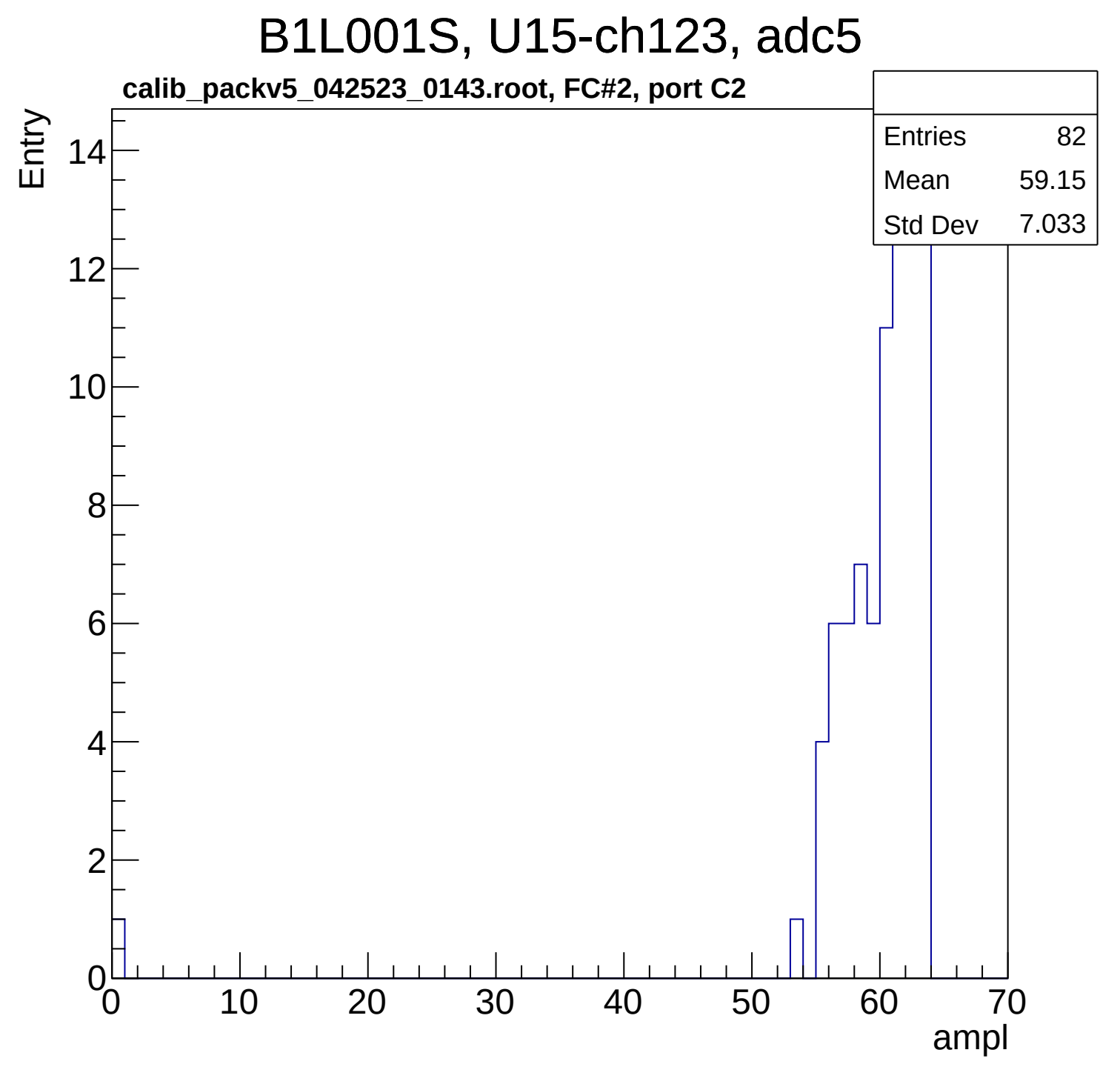
B1L001S, U15-ch123, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 59.15
Std Dev 7.033
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
50
60
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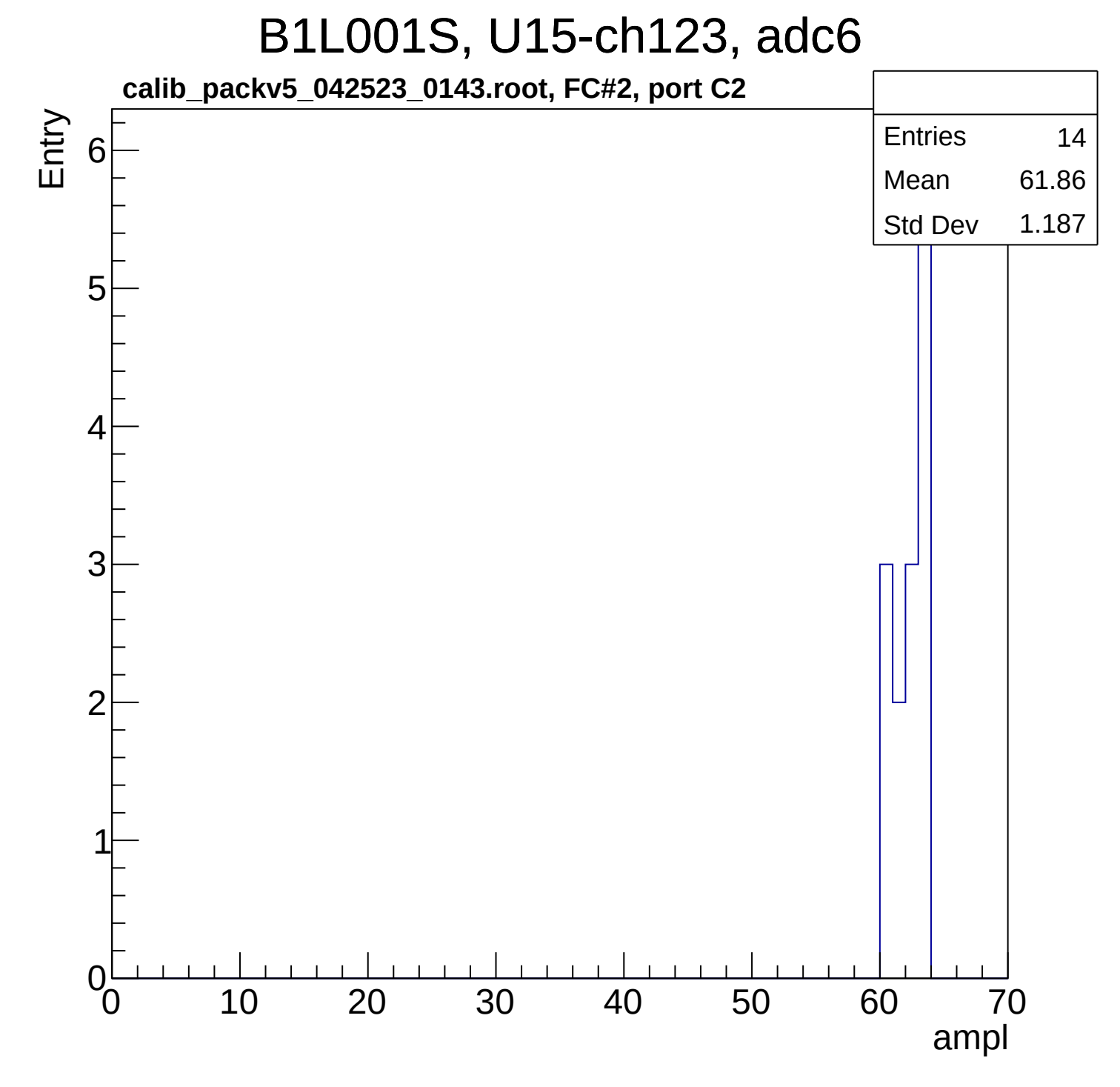
B1L001S, U15-ch123, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 61.86
Std Dev 1.187
Entry
ampl
0
1
2
3
4
5
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0
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20
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40
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60
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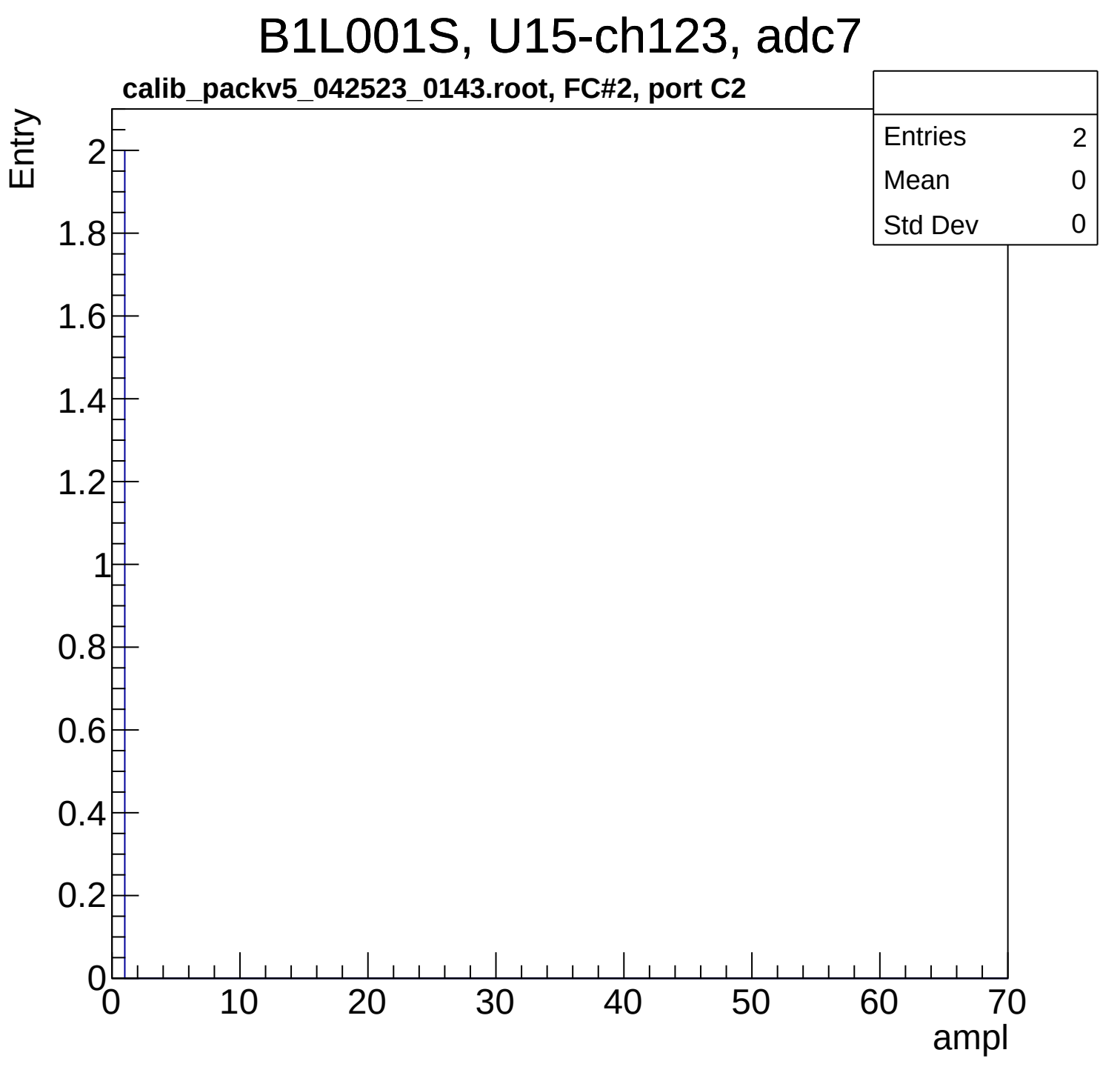
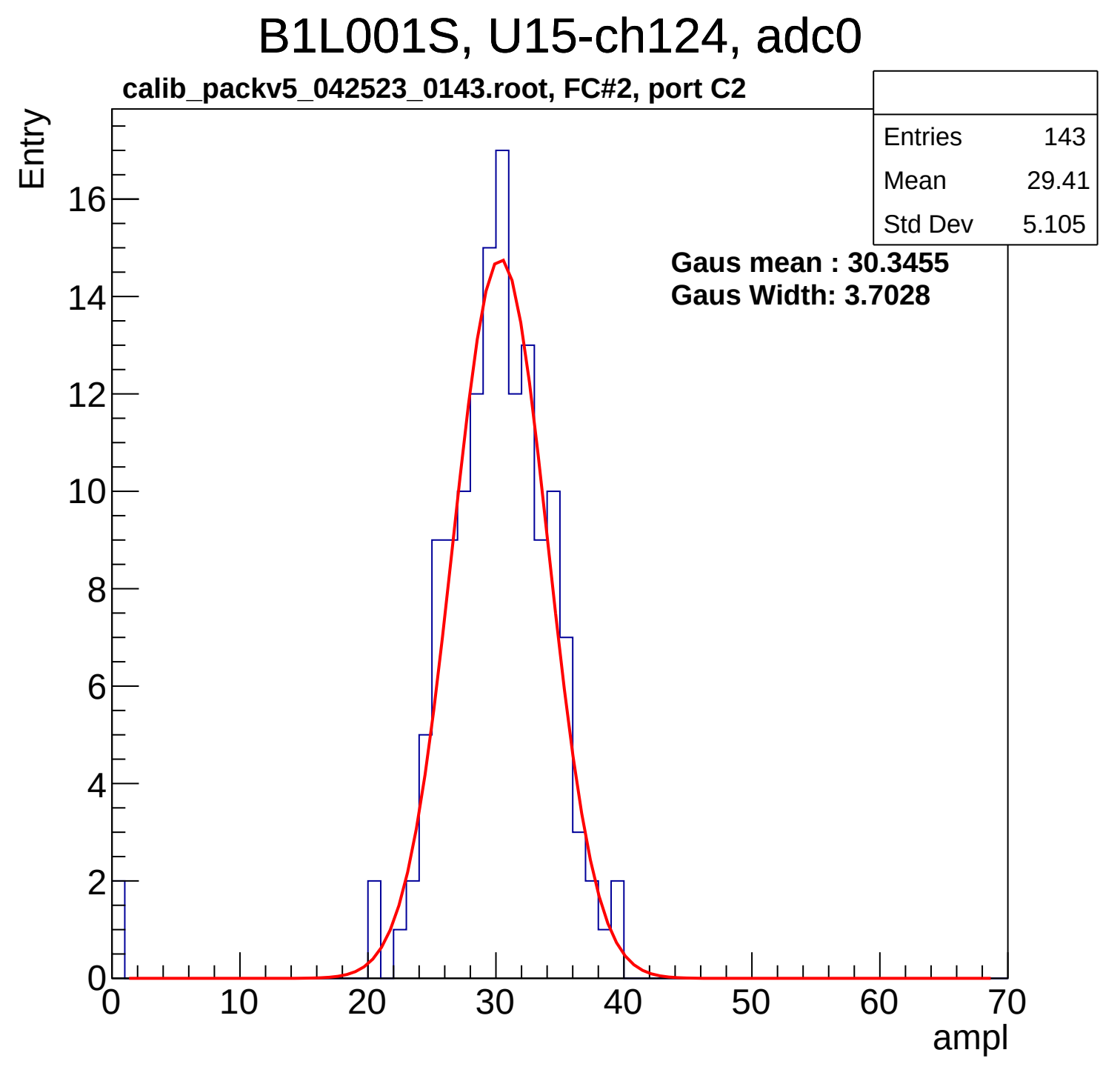

B1L001S, U15-ch123, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch124, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 29.41
Std Dev 5.105
Gaus mean : 30.3455
Gaus Width: 3.7028
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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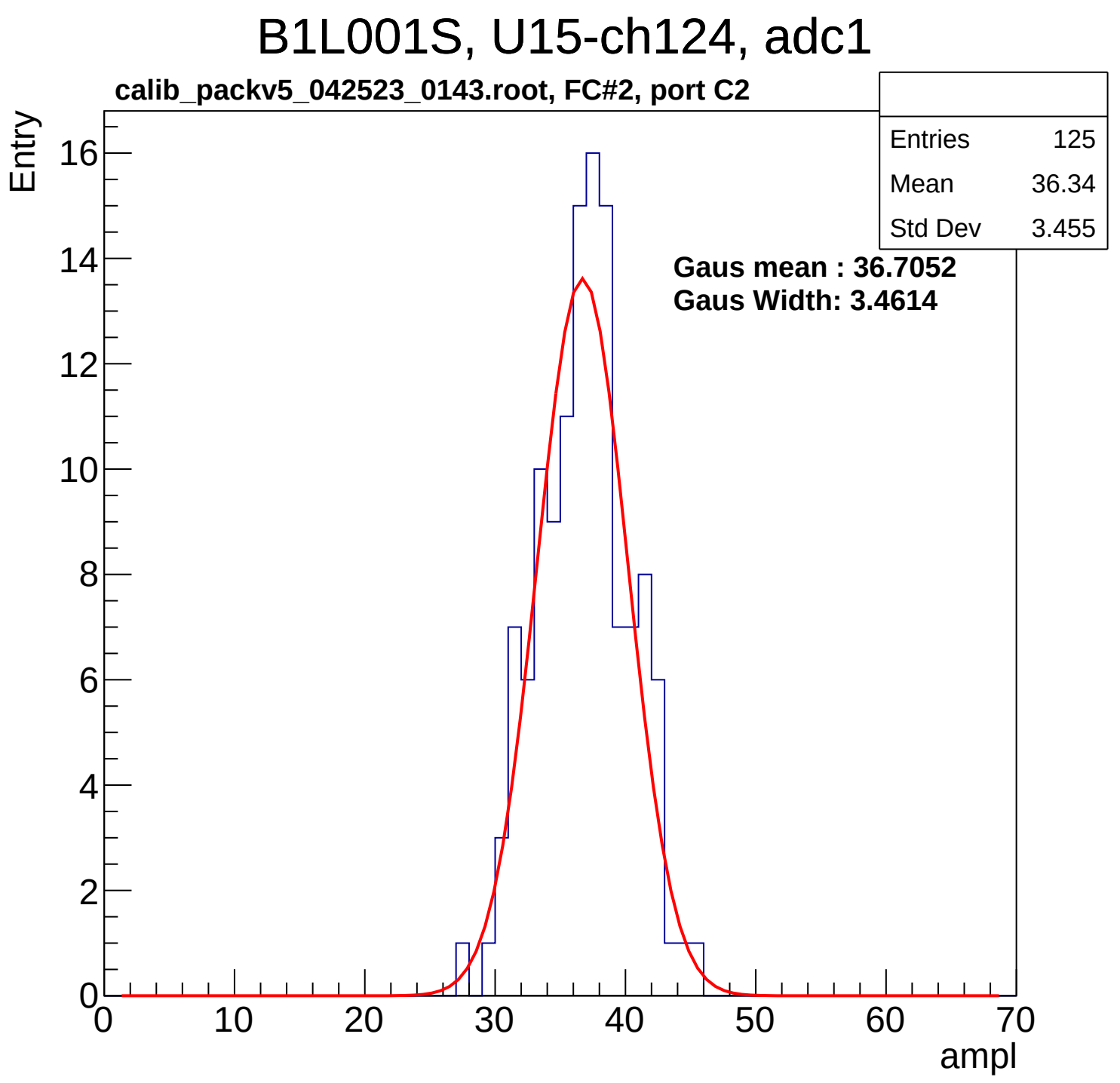
B1L001S, U15-ch124, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 36.34
Std Dev 3.455
Gaus mean : 36.7052
Gaus Width: 3.4614
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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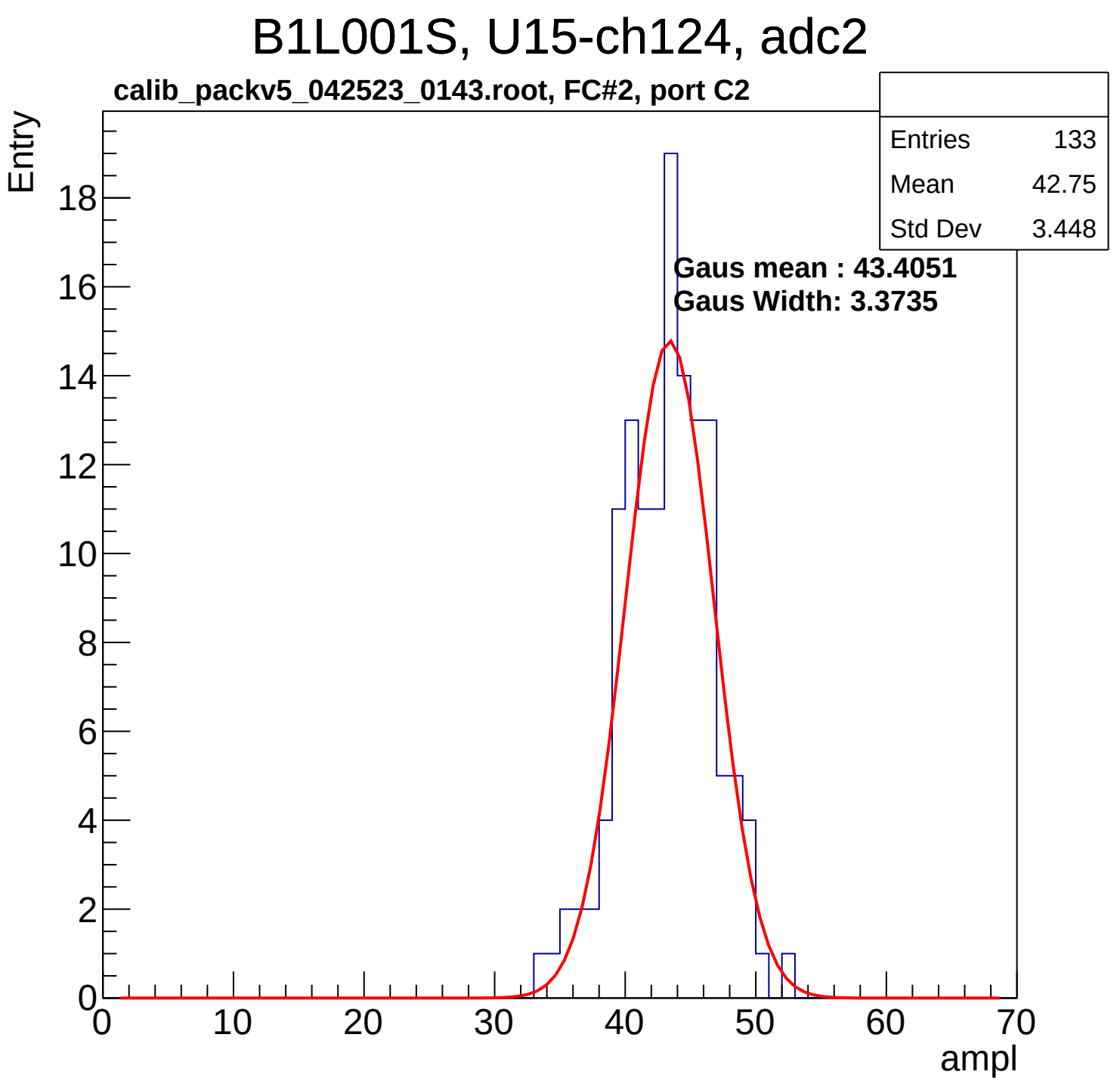
B1L001S, U15-ch124, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 42.75
Std Dev 3.448
Gaus mean : 43.4051
Gaus Width: 3.3735
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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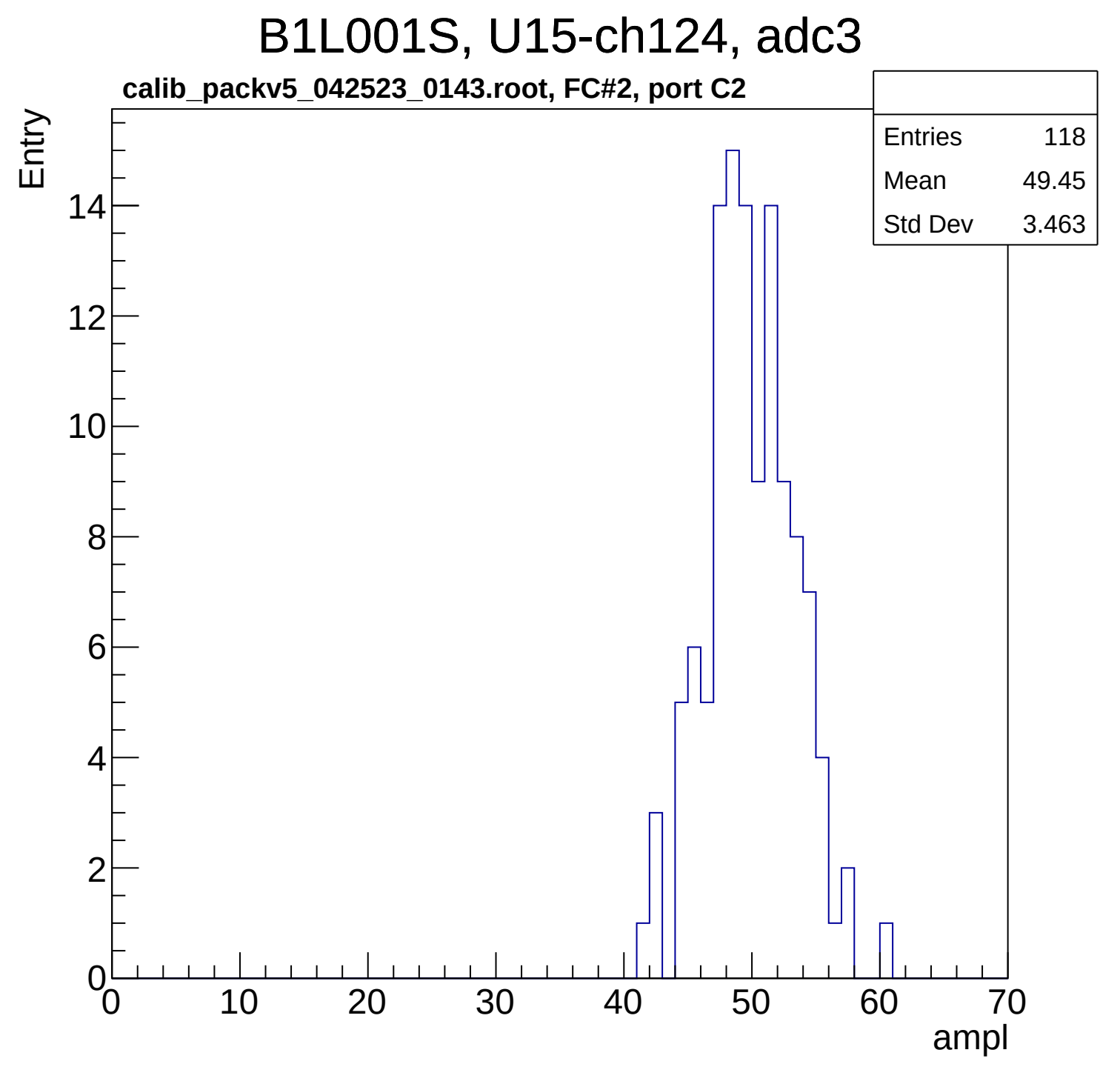
B1L001S, U15-ch124, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 49.45
Std Dev 3.463
Entry
ampl
0
2
4
6
8
10
12
14
0
10
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30
40
50
60
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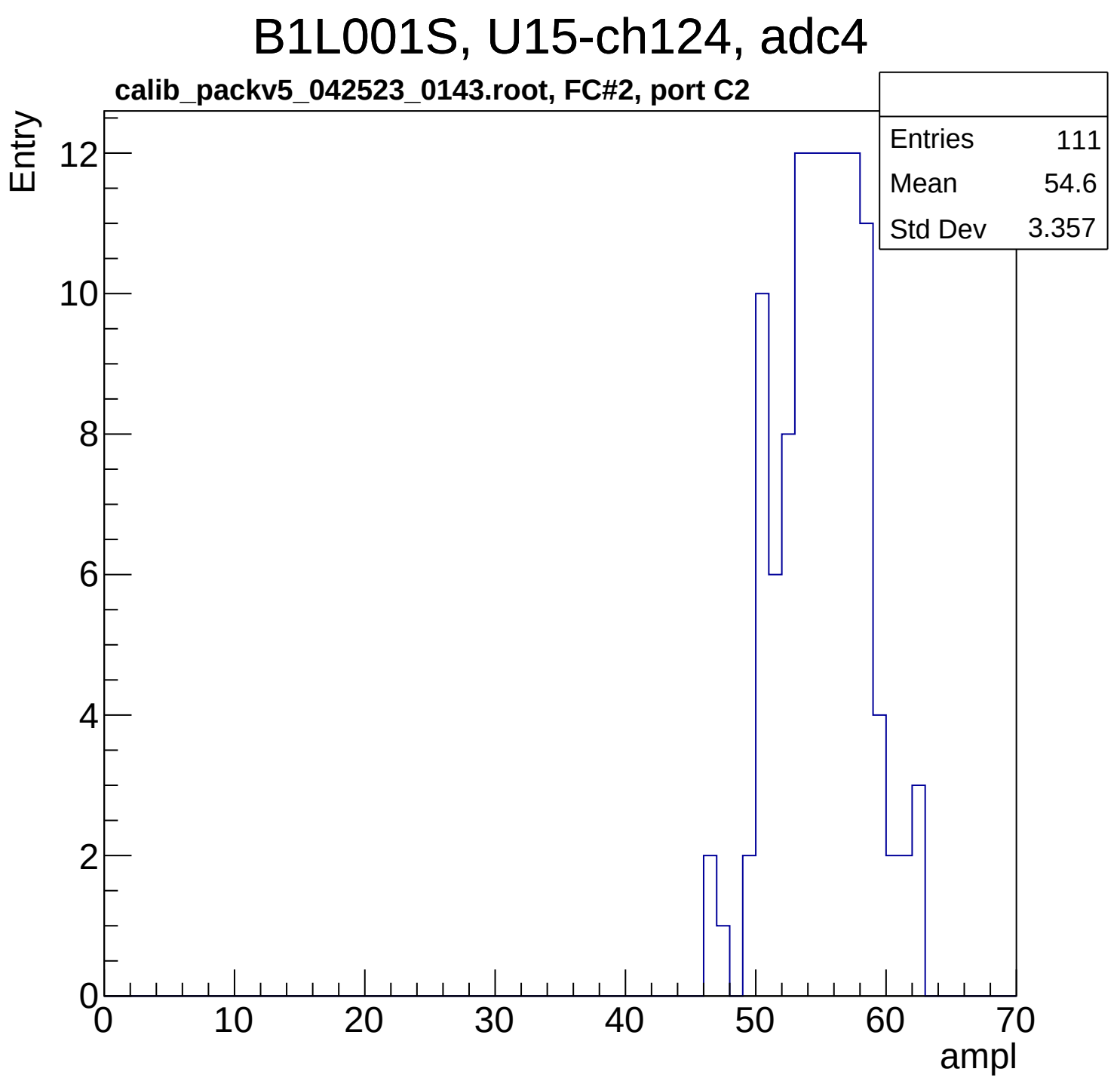
B1L001S, U15-ch124, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 54.6
Std Dev 3.357
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
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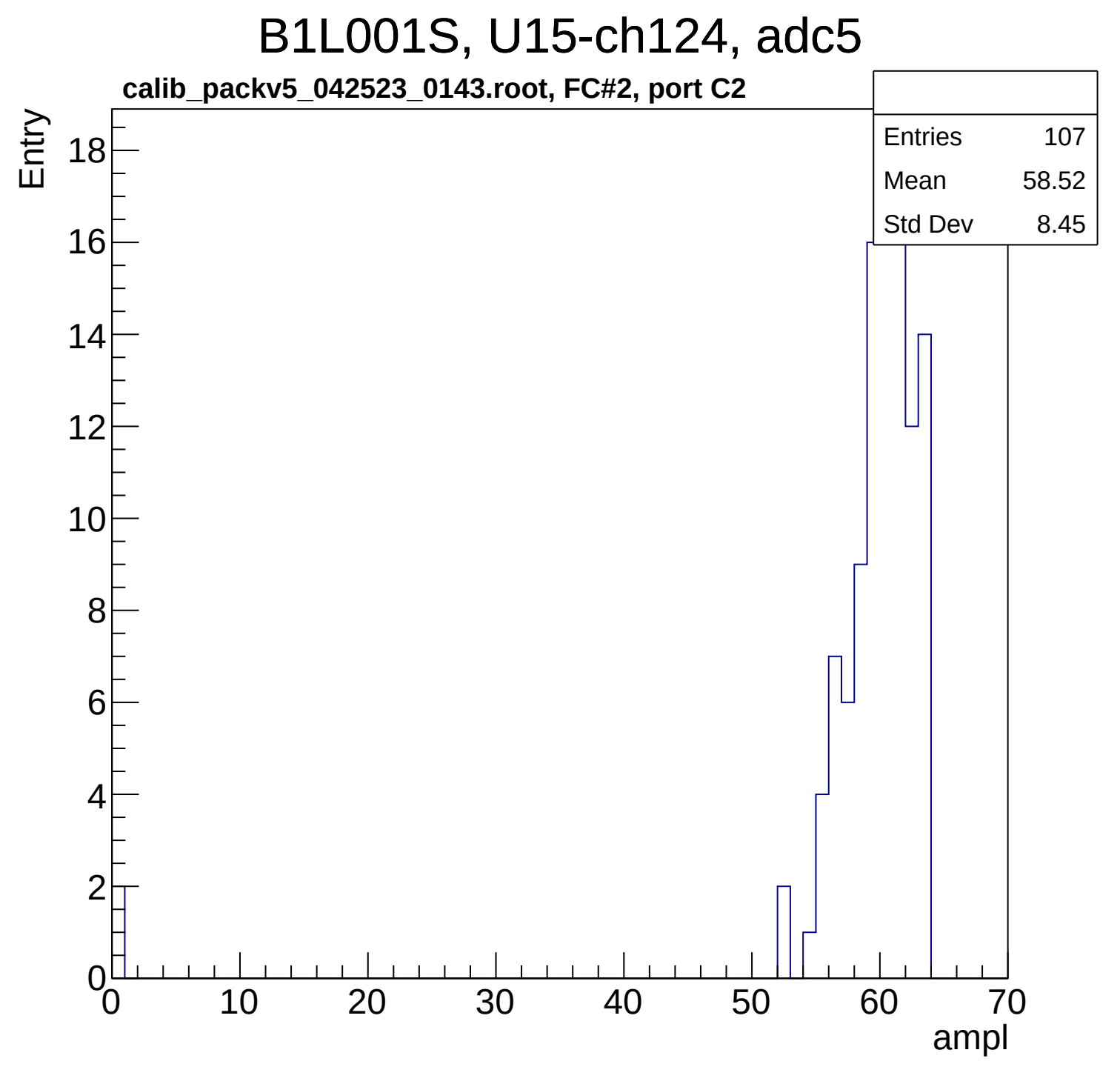
B1L001S, U15-ch124, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 58.52
Std Dev 8.45
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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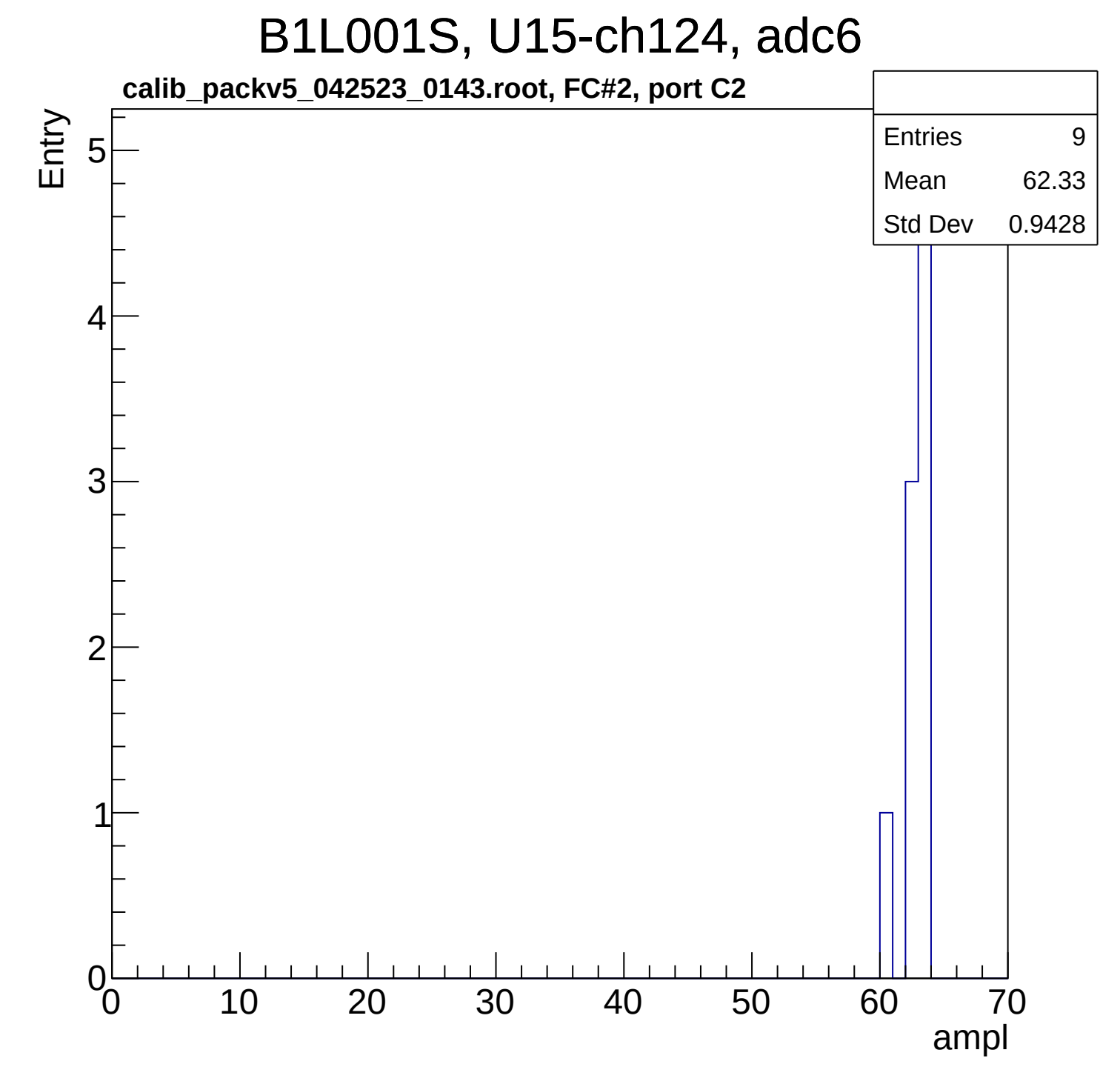
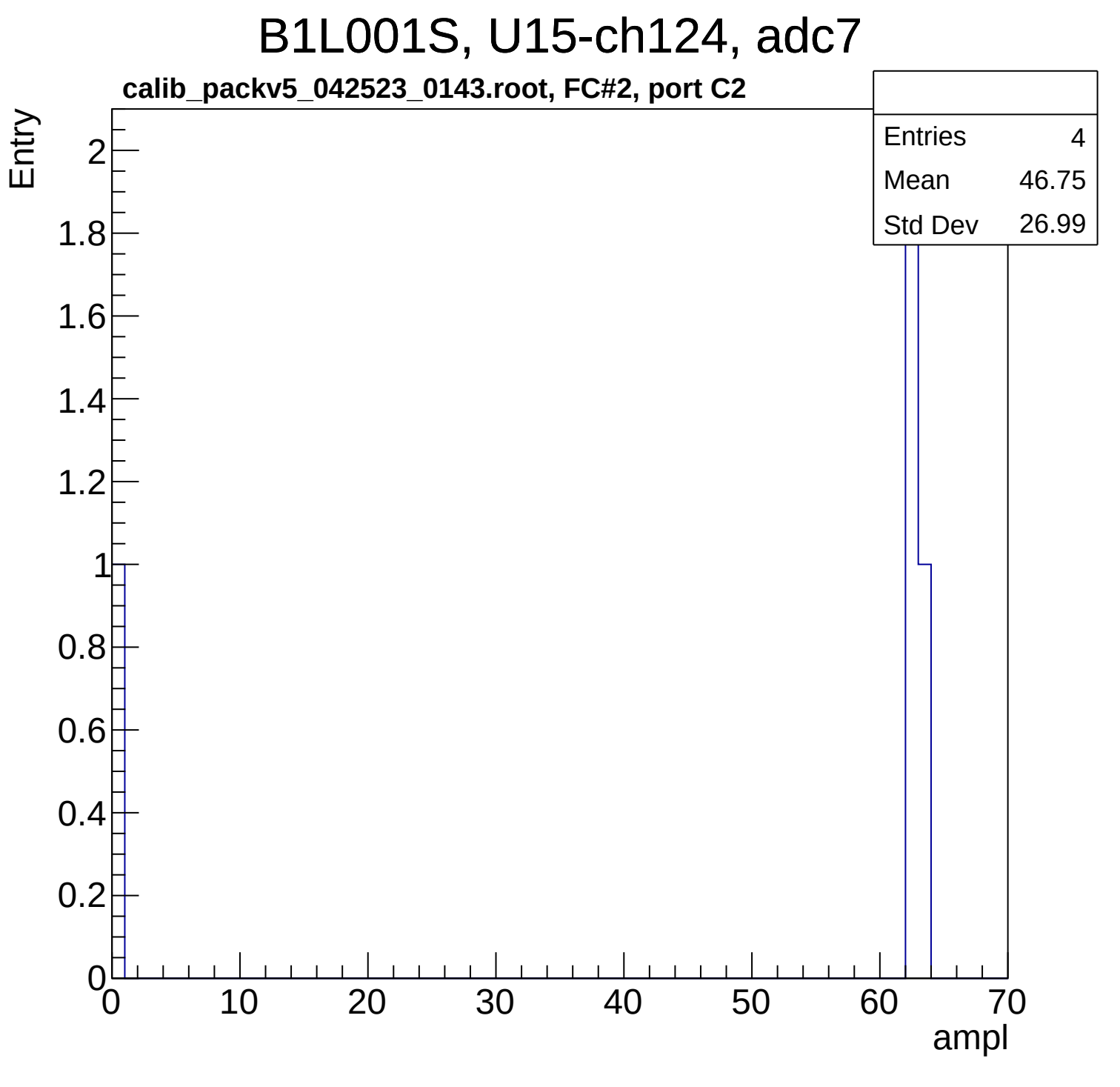

B1L001S, U15-ch124, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L001S, U15-ch124, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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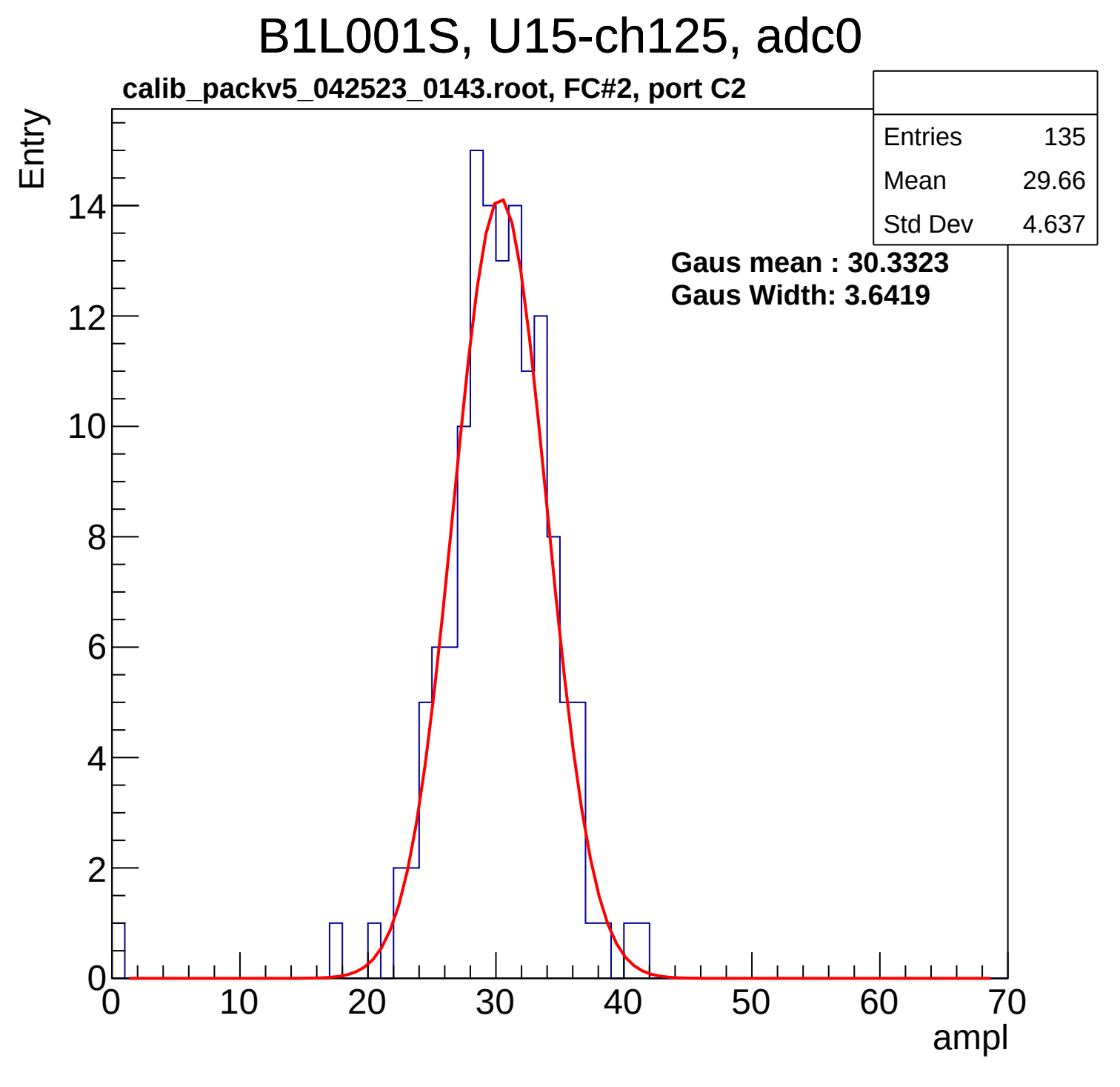
B1L001S, U15-ch125, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 29.66
Std Dev 4.637
Gaus mean : 30.3323
Gaus Width: 3.6419
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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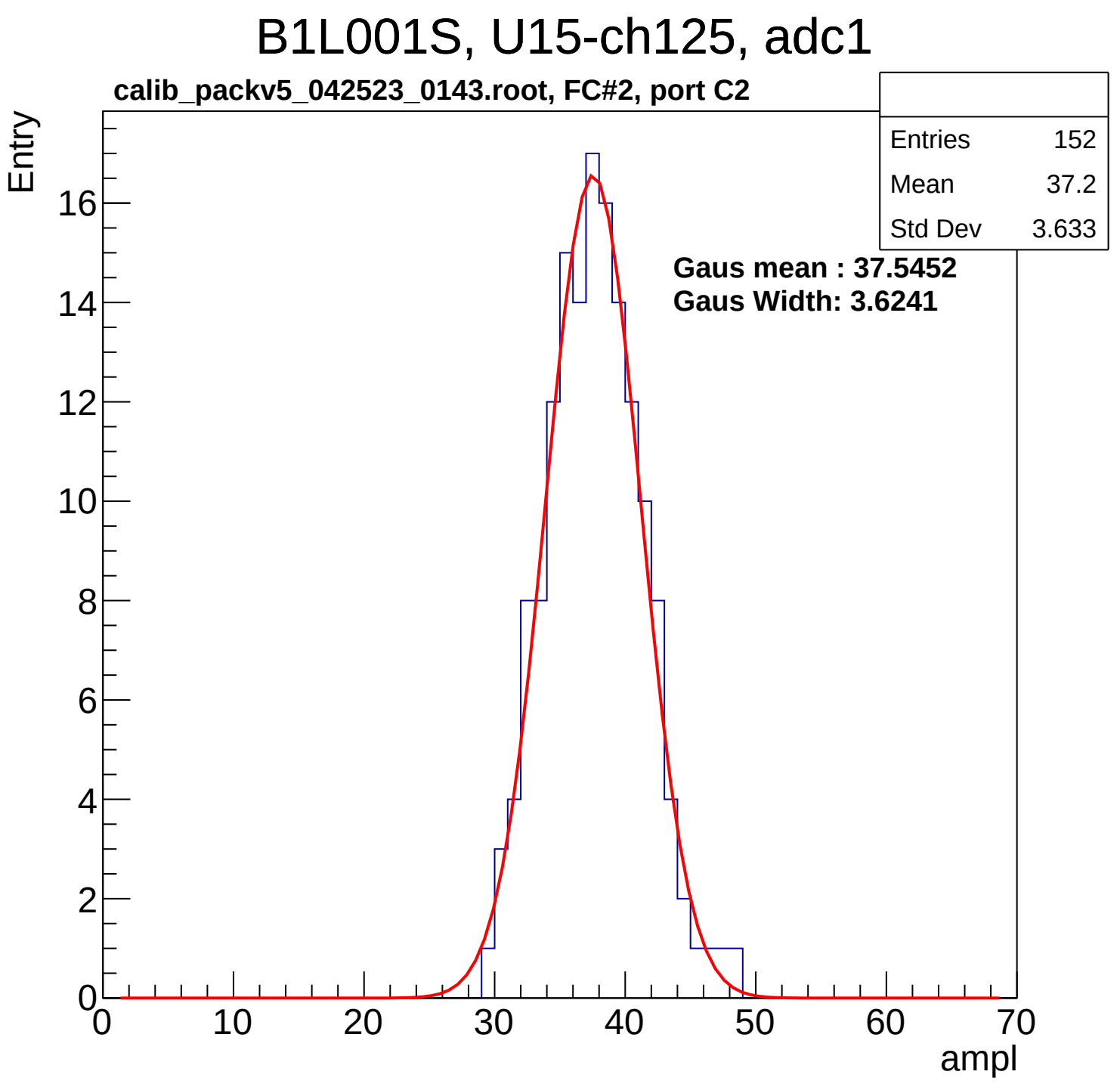
B1L001S, U15-ch125, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 37.2
Std Dev 3.633
Gaus mean : 37.5452
Gaus Width: 3.6241
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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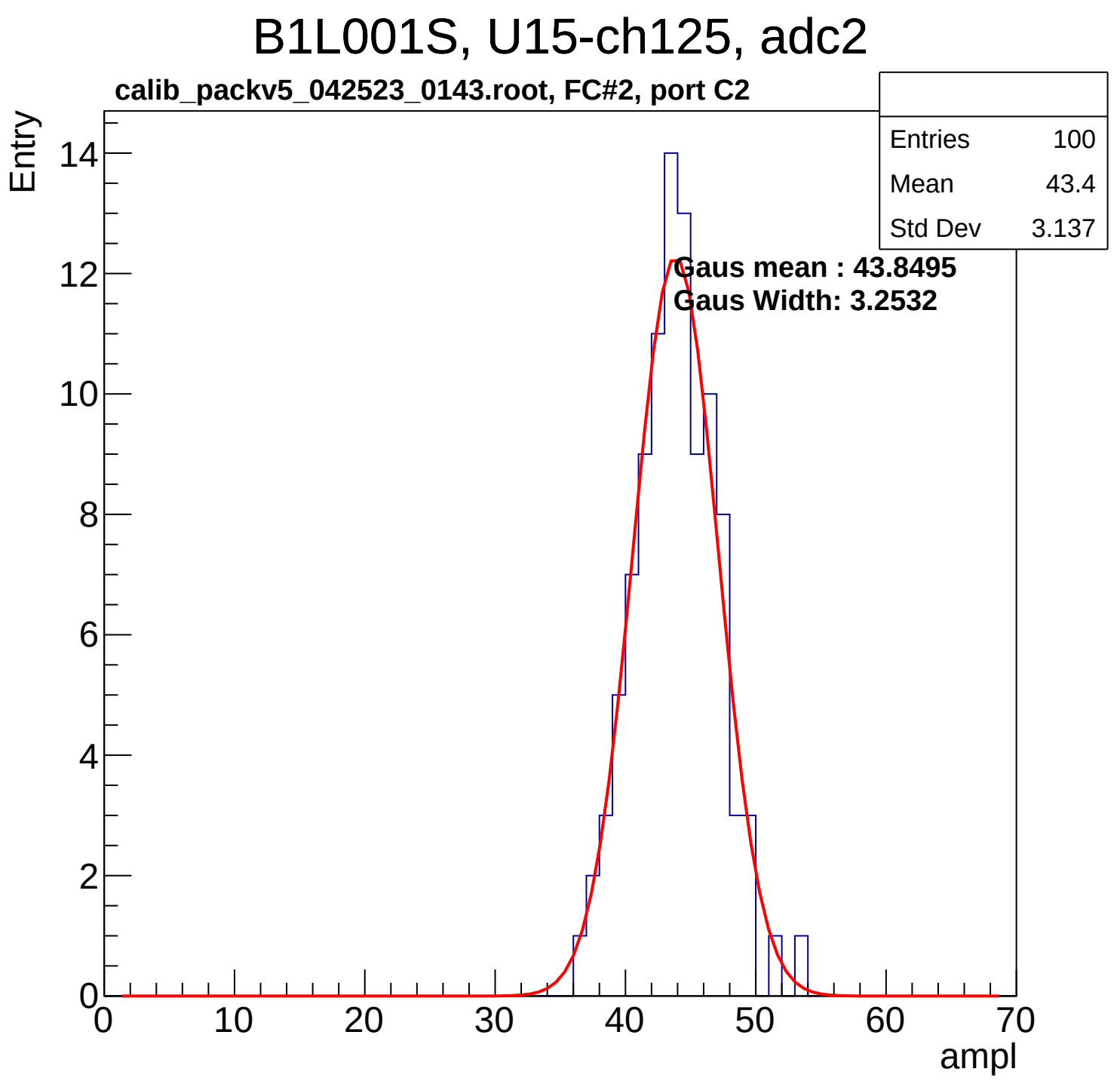
B1L001S, U15-ch125, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 43.4
Std Dev 3.137
Gaus mean : 43.8495
Gaus Width: 3.2532
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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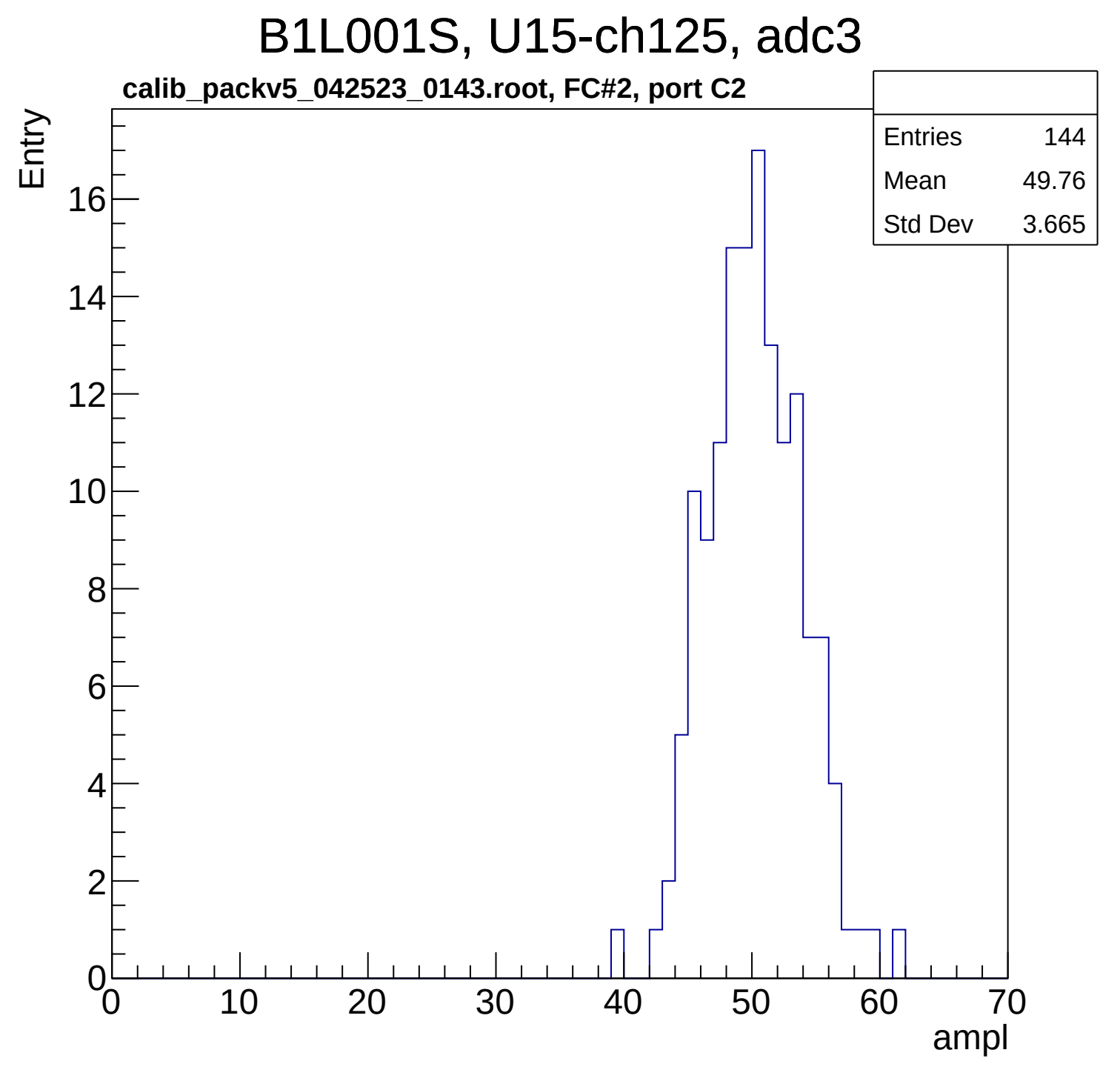
B1L001S, U15-ch125, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 49.76
Std Dev 3.665
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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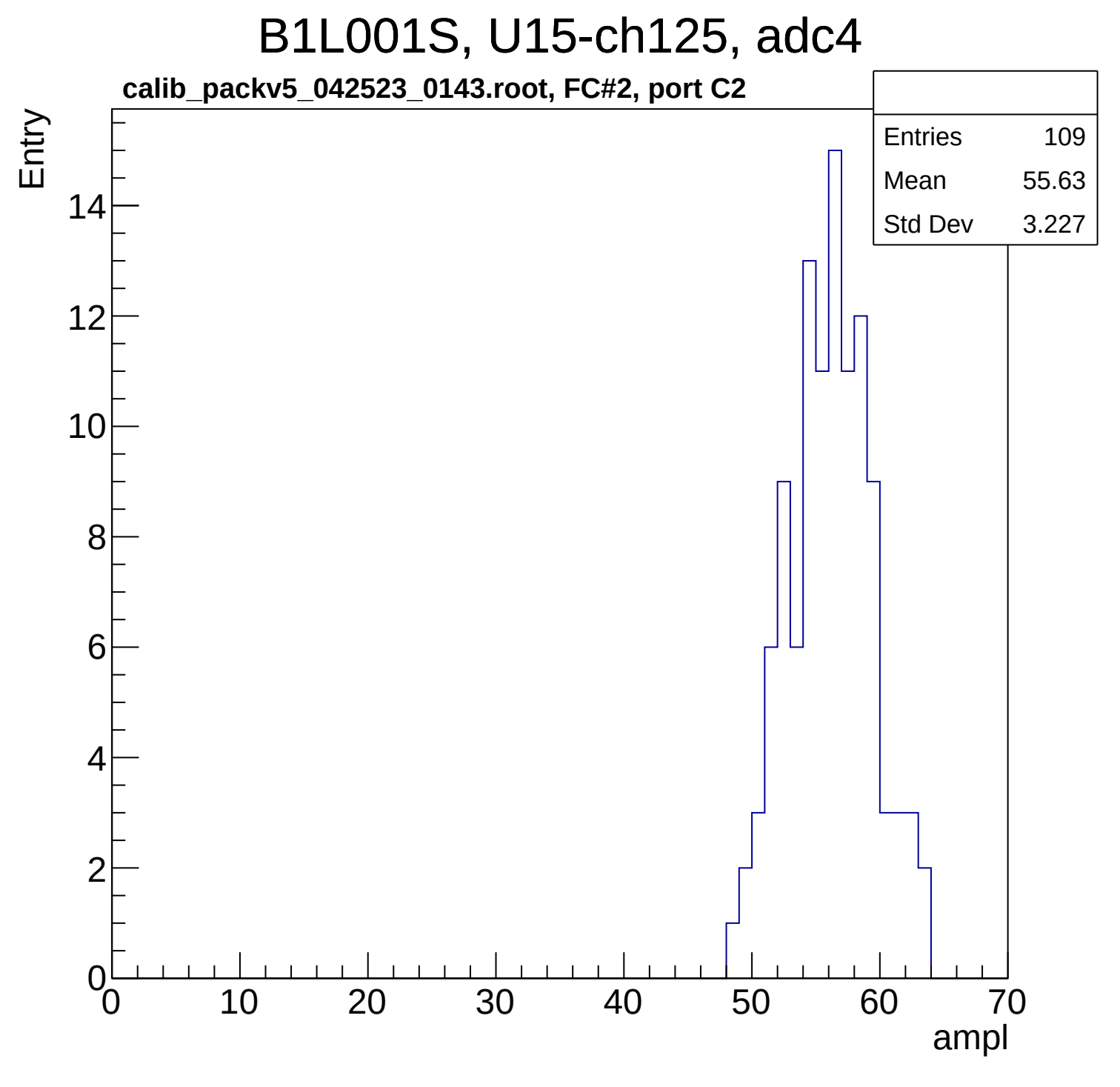
B1L001S, U15-ch125, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 55.63
Std Dev 3.227
Entry
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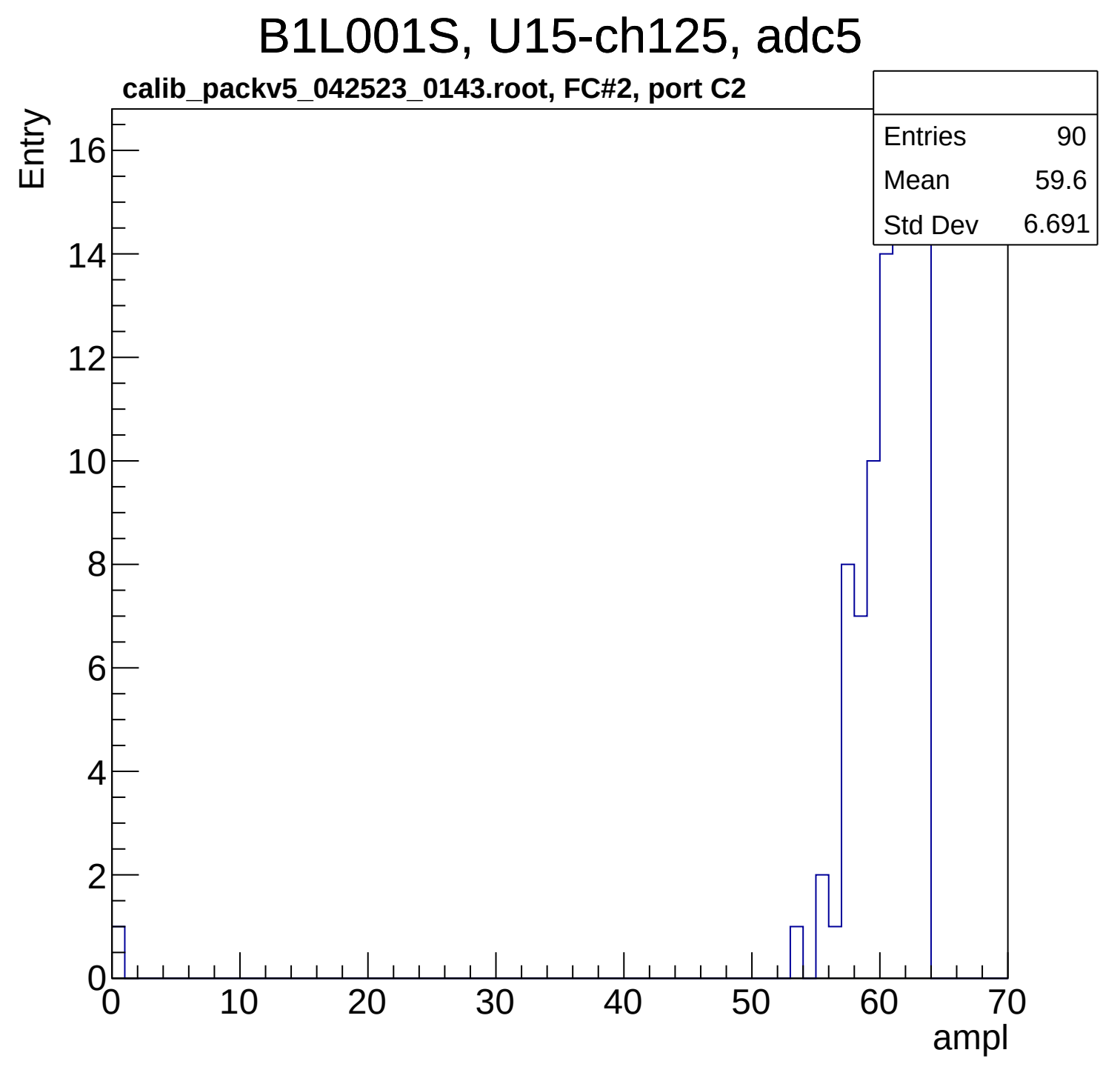
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch125, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 59.6
Std Dev 6.691
Entry
ampl
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4
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8
10
12
14
16
0
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40
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60
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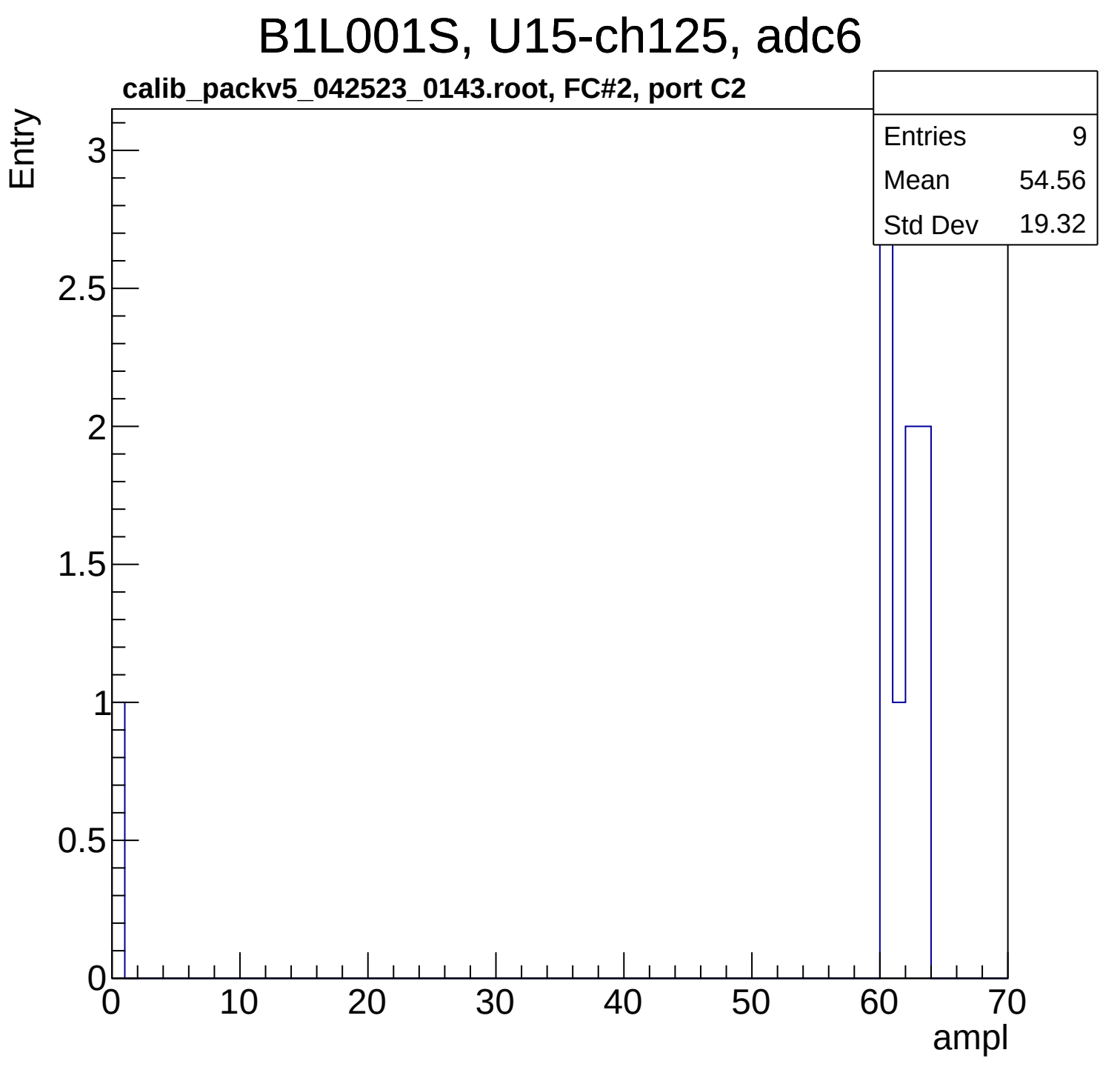
B1L001S, U15-ch125, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 54.56
Std Dev 19.32
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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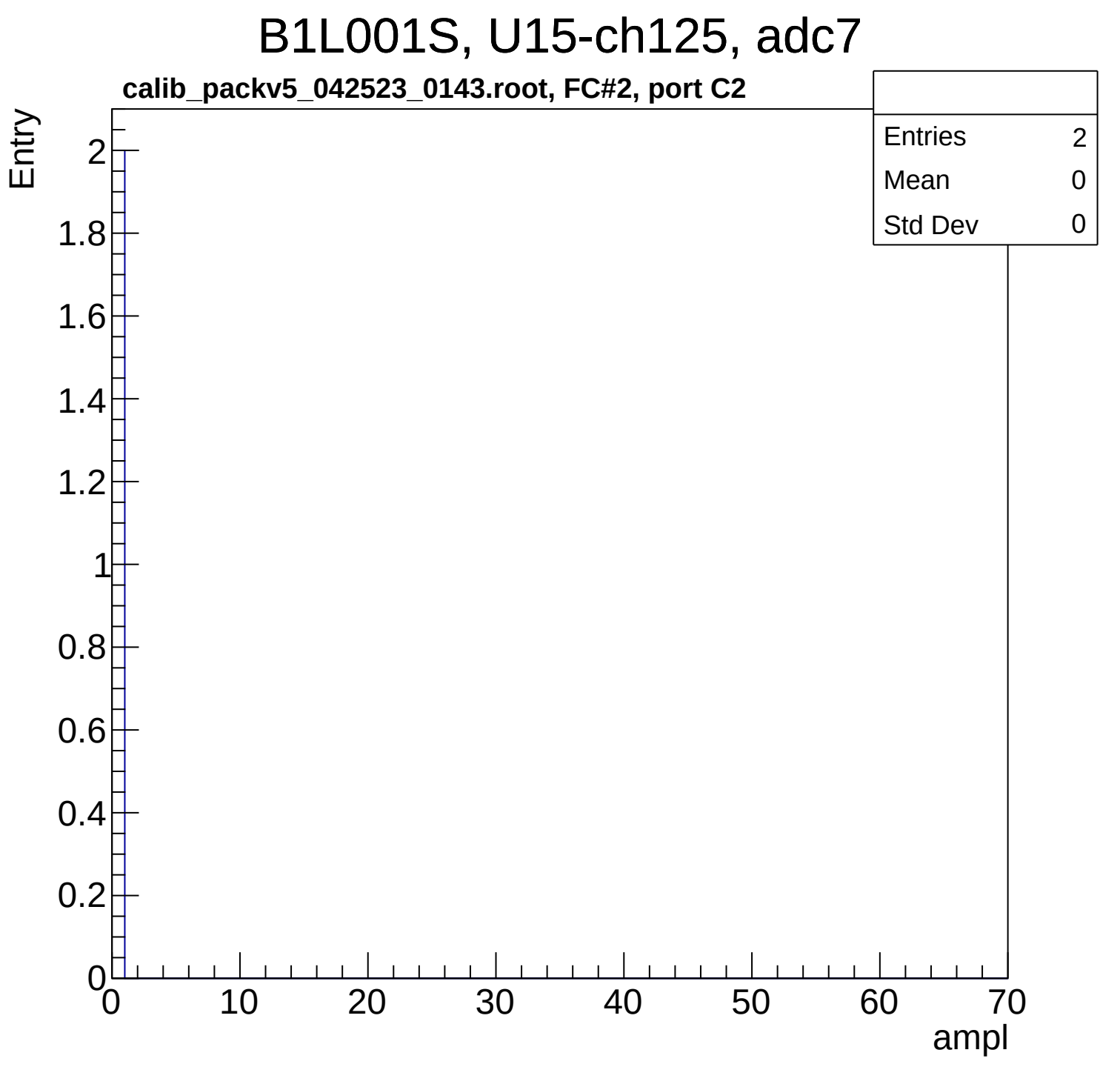
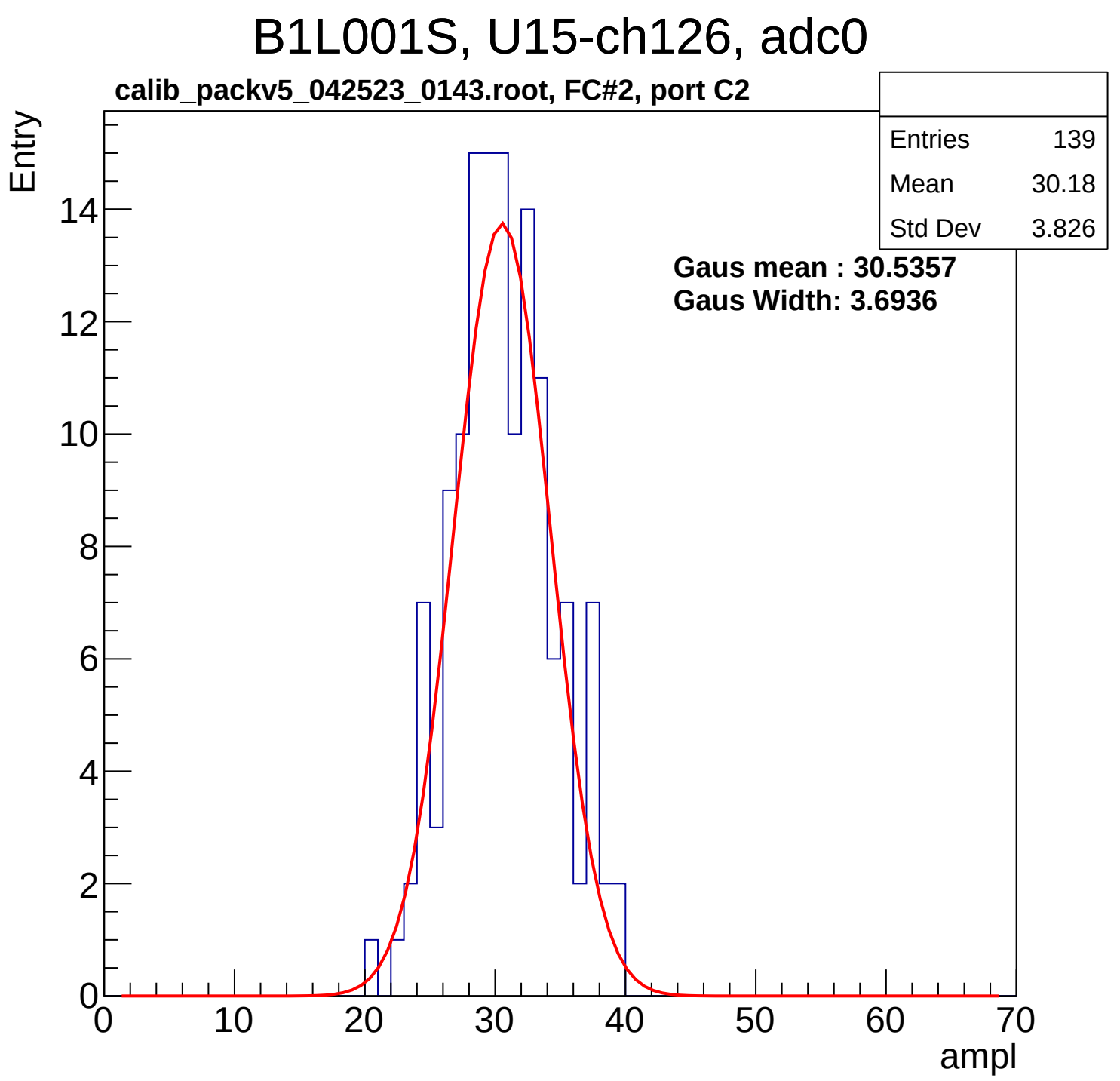

B1L001S, U15-ch125, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch126, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 30.18
Std Dev 3.826
Gaus mean : 30.5357
Gaus Width: 3.6936
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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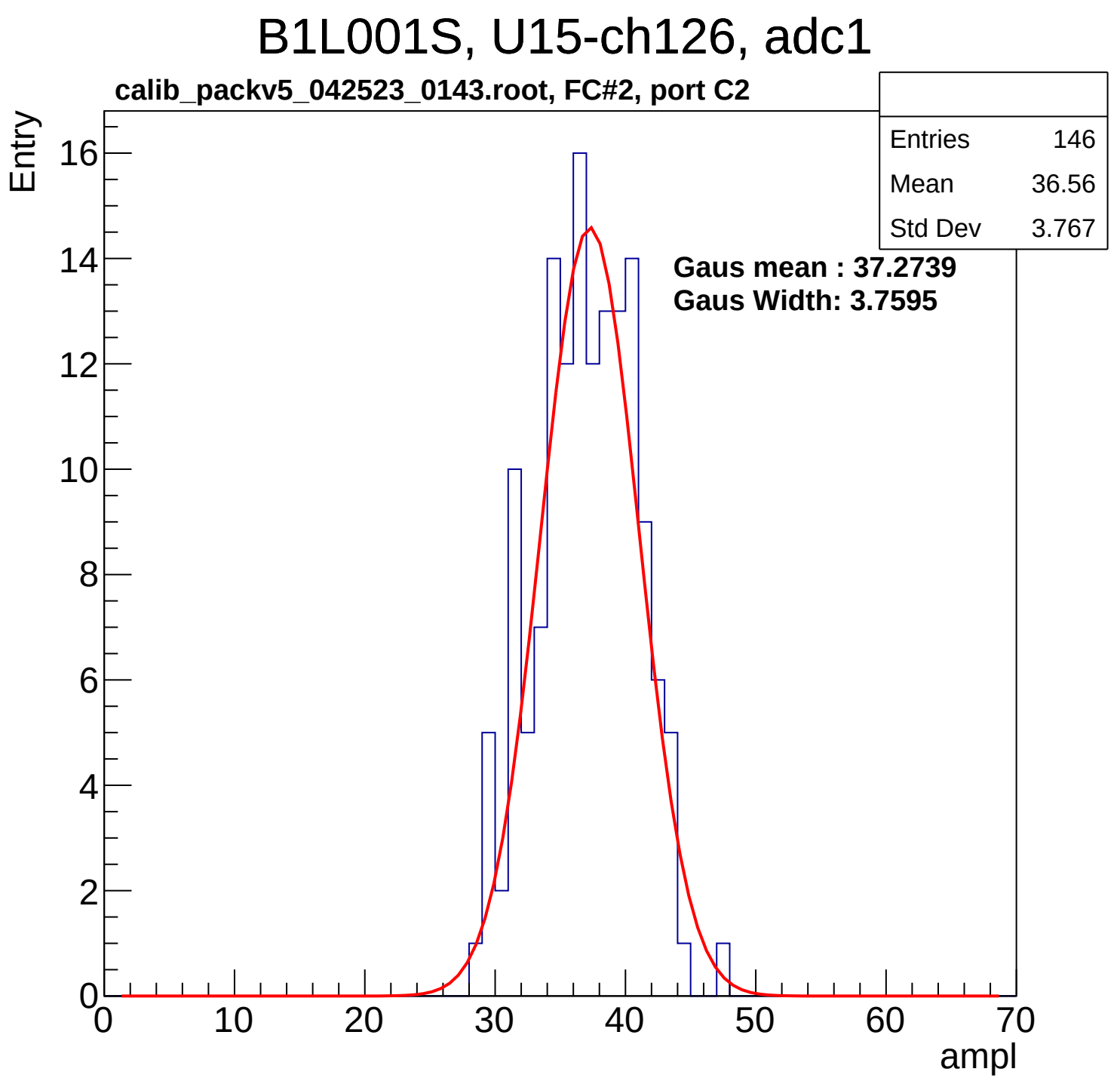
B1L001S, U15-ch126, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 36.56
Std Dev 3.767
Gaus mean : 37.2739
Gaus Width: 3.7595
Entry
ampl
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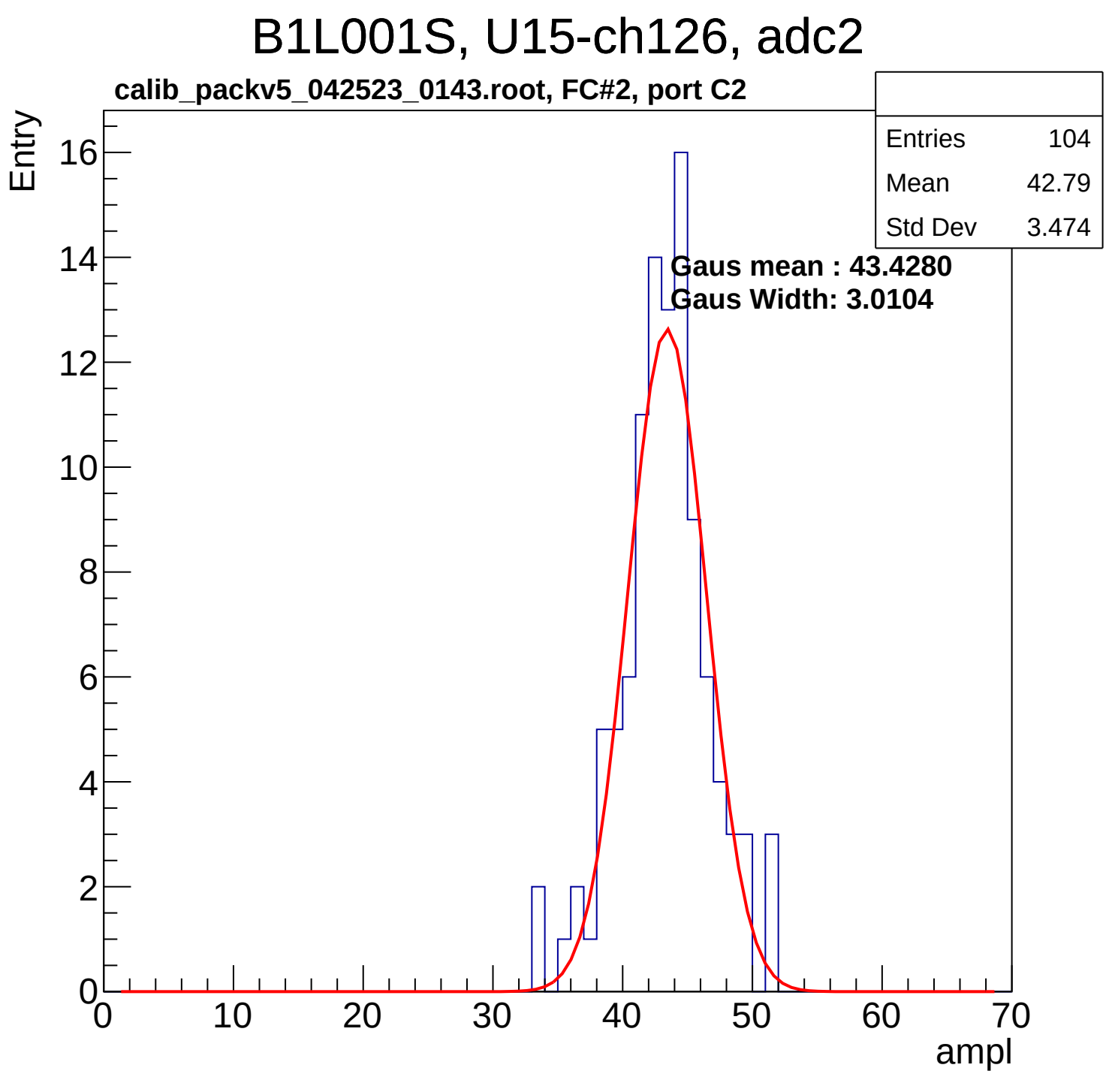
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch126, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 42.79
Std Dev 3.474
Gaus mean : 43.4280
Gaus Width: 3.0104
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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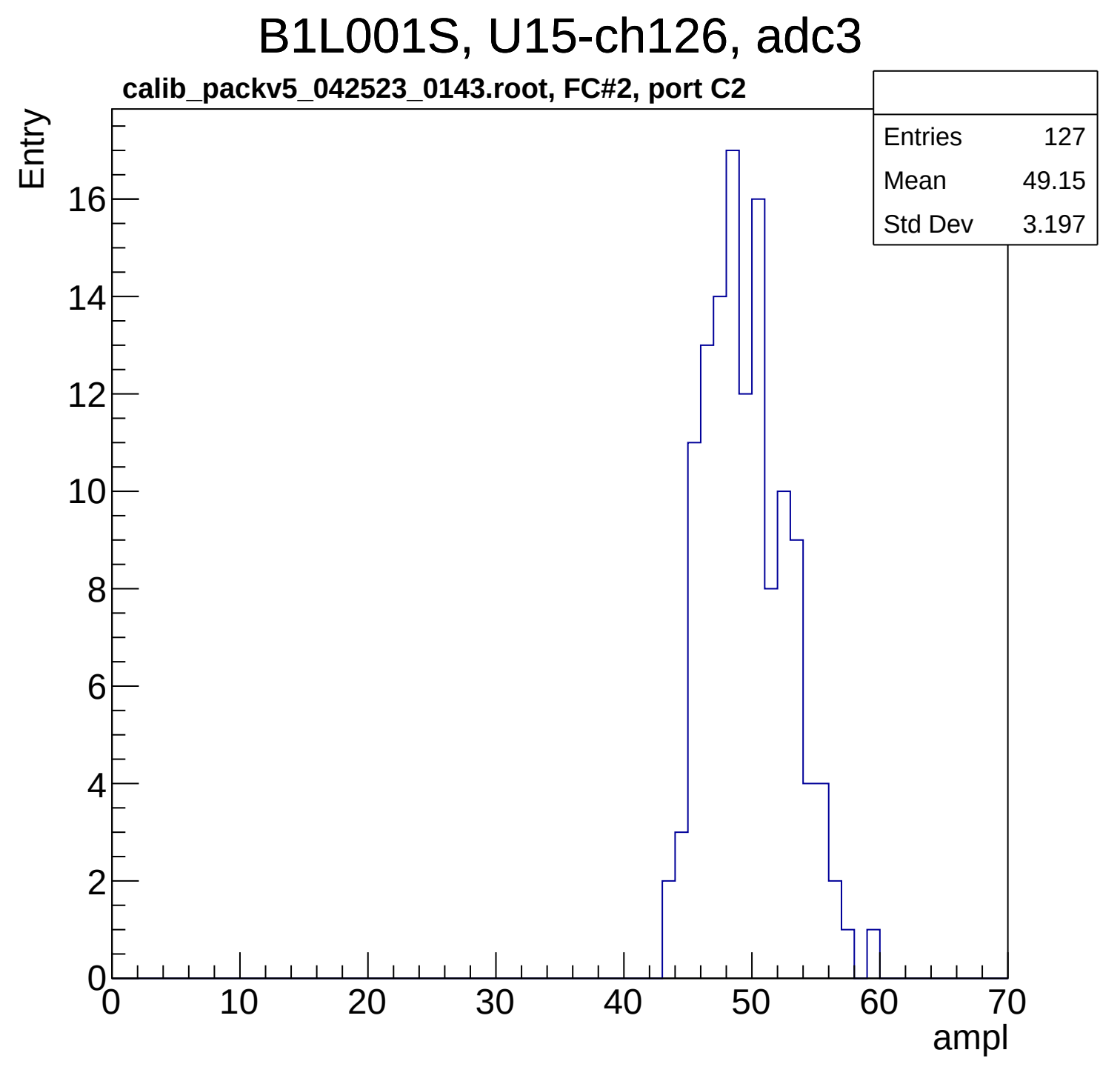
B1L001S, U15-ch126, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 49.15
Std Dev 3.197
Entry
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4
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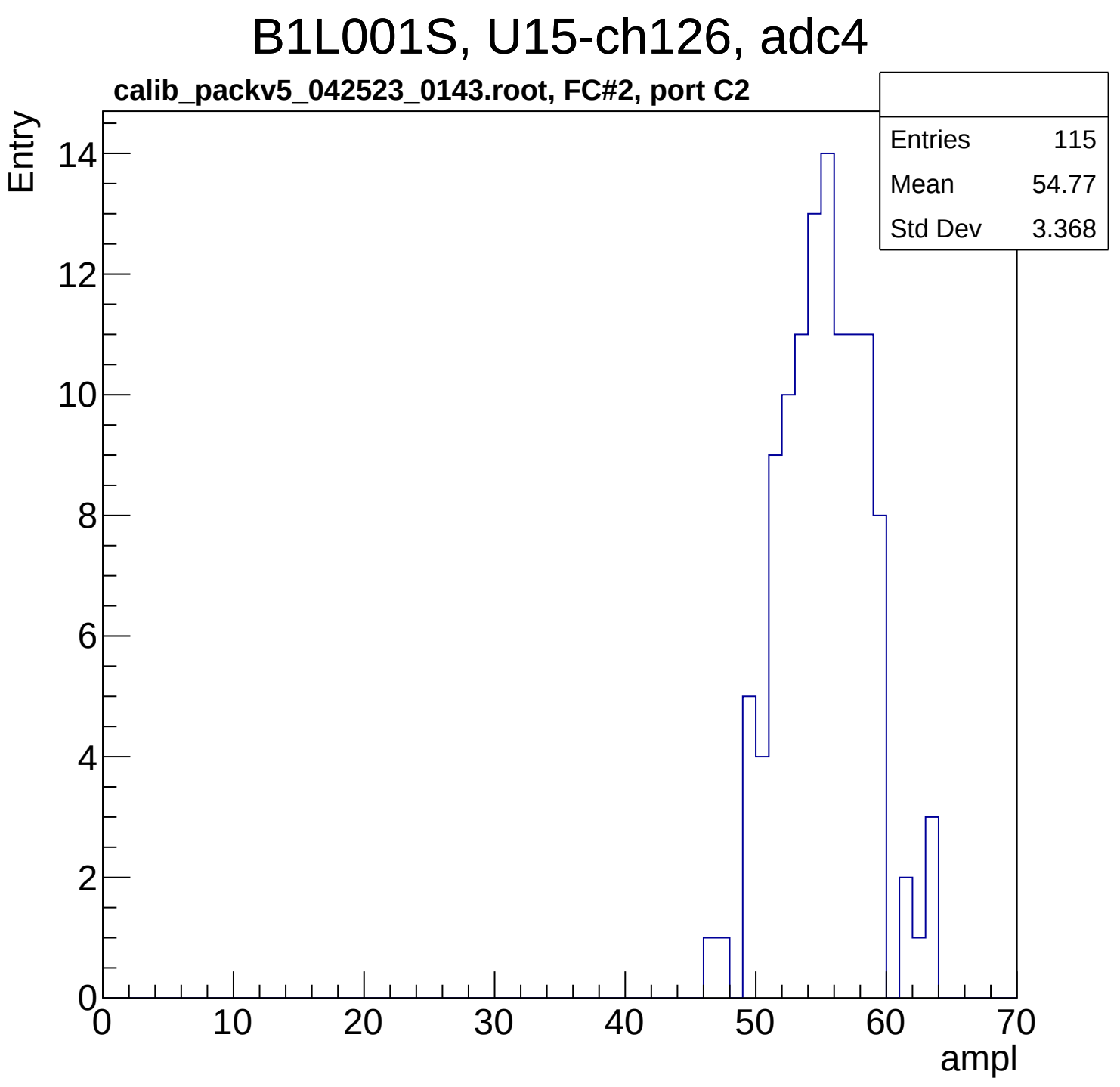
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch126, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 54.77
Std Dev 3.368
Entry
ampl
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0
10
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40
50
60
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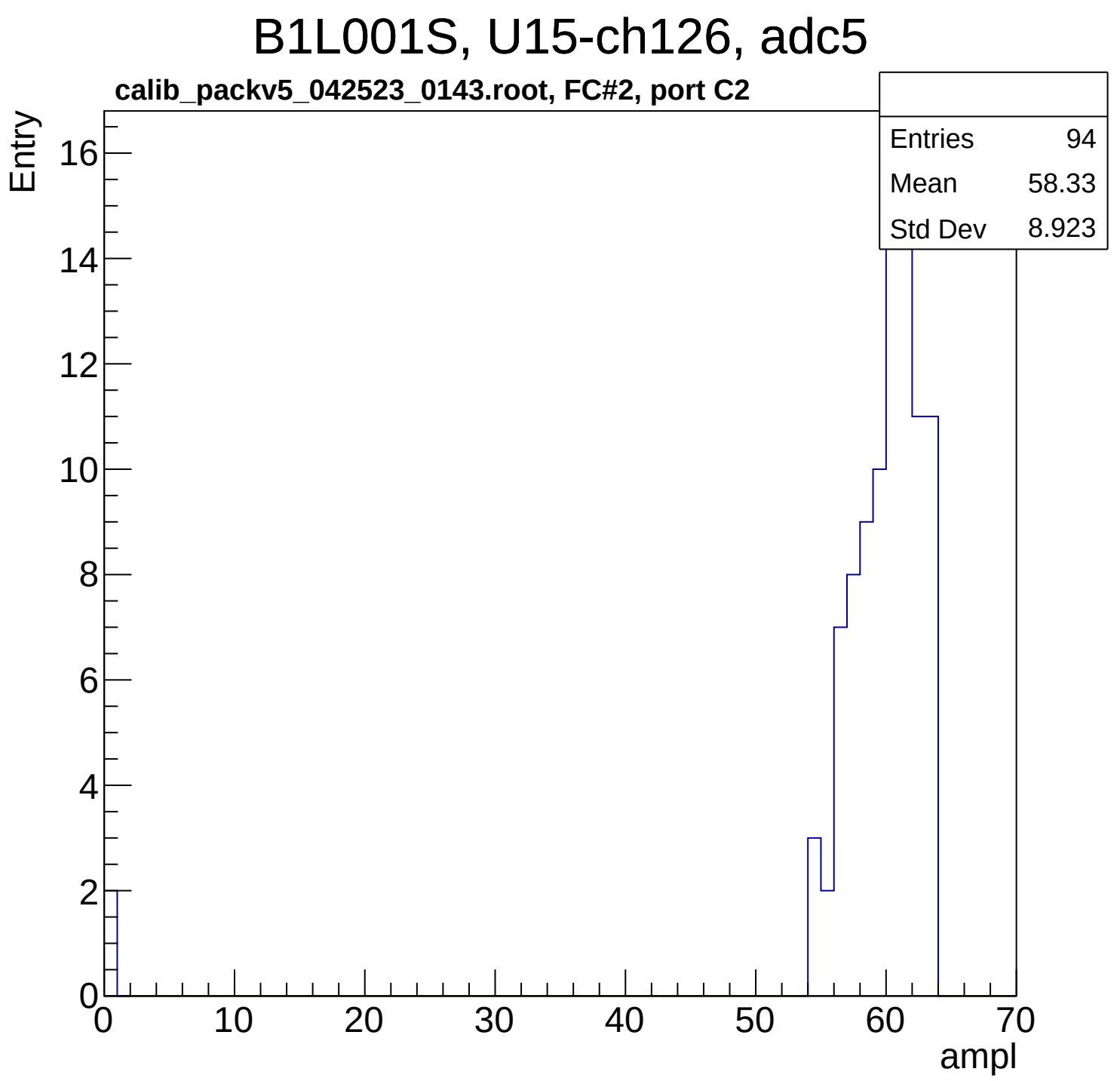
B1L001S, U15-ch126, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 58.33
Std Dev 8.923
Entry
ampl
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2
4
6
8
10
12
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60
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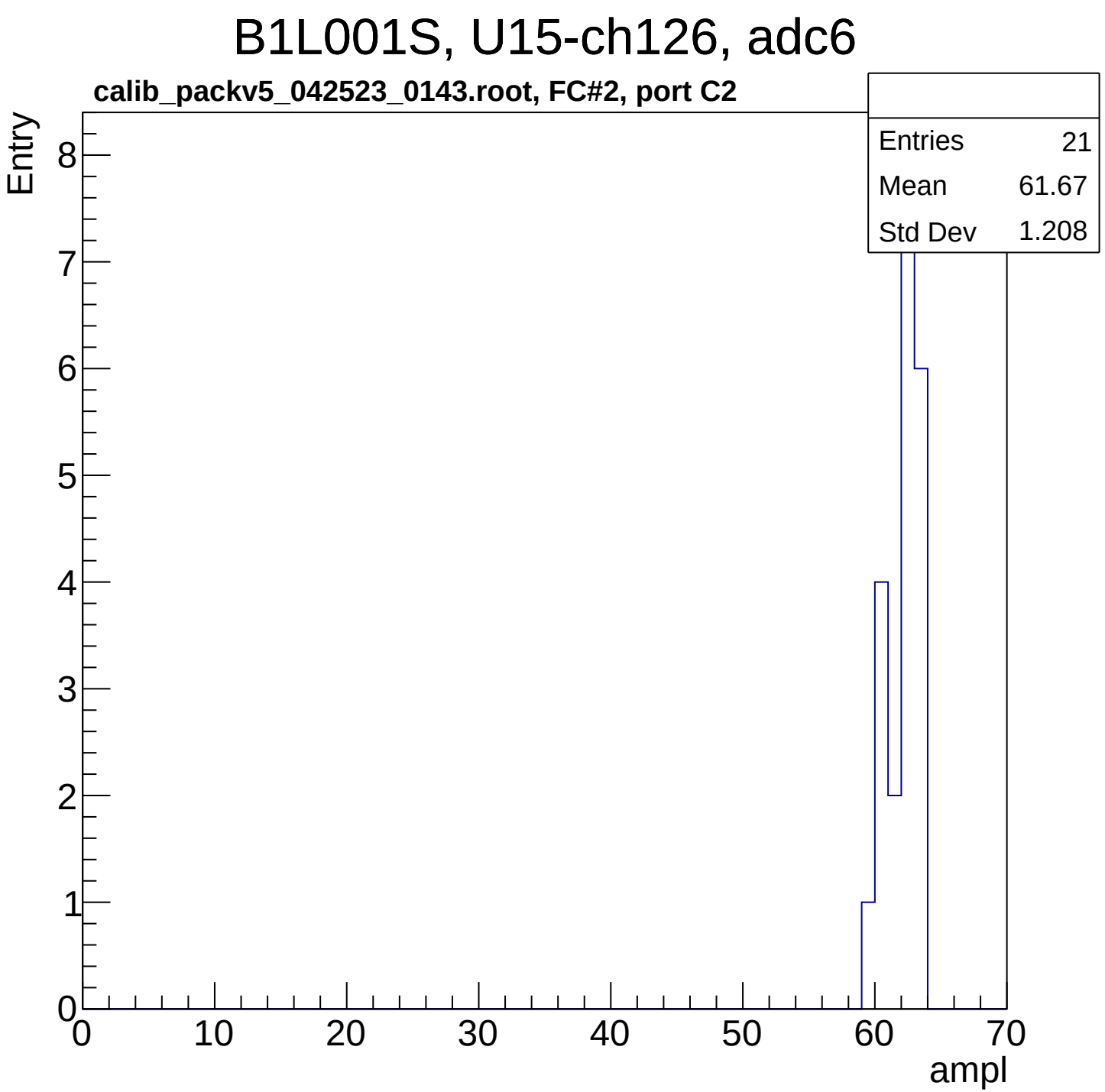
B1L001S, U15-ch126, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 21
Mean 61.67
Std Dev 1.208
Entry
ampl
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1
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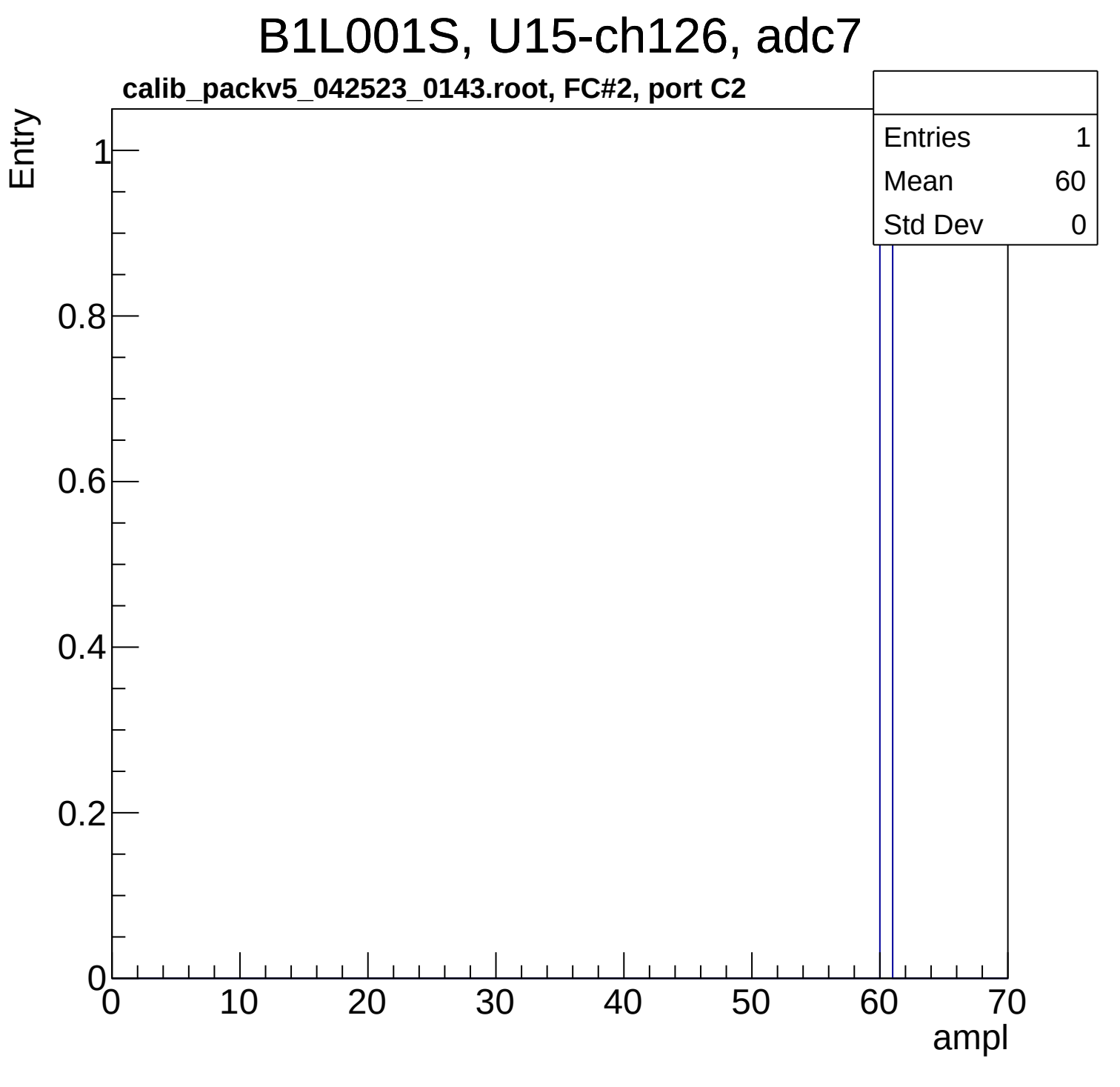
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U15-ch126, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 60
Std Dev 0
Entry
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0.6
0.4
0.2
0
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20
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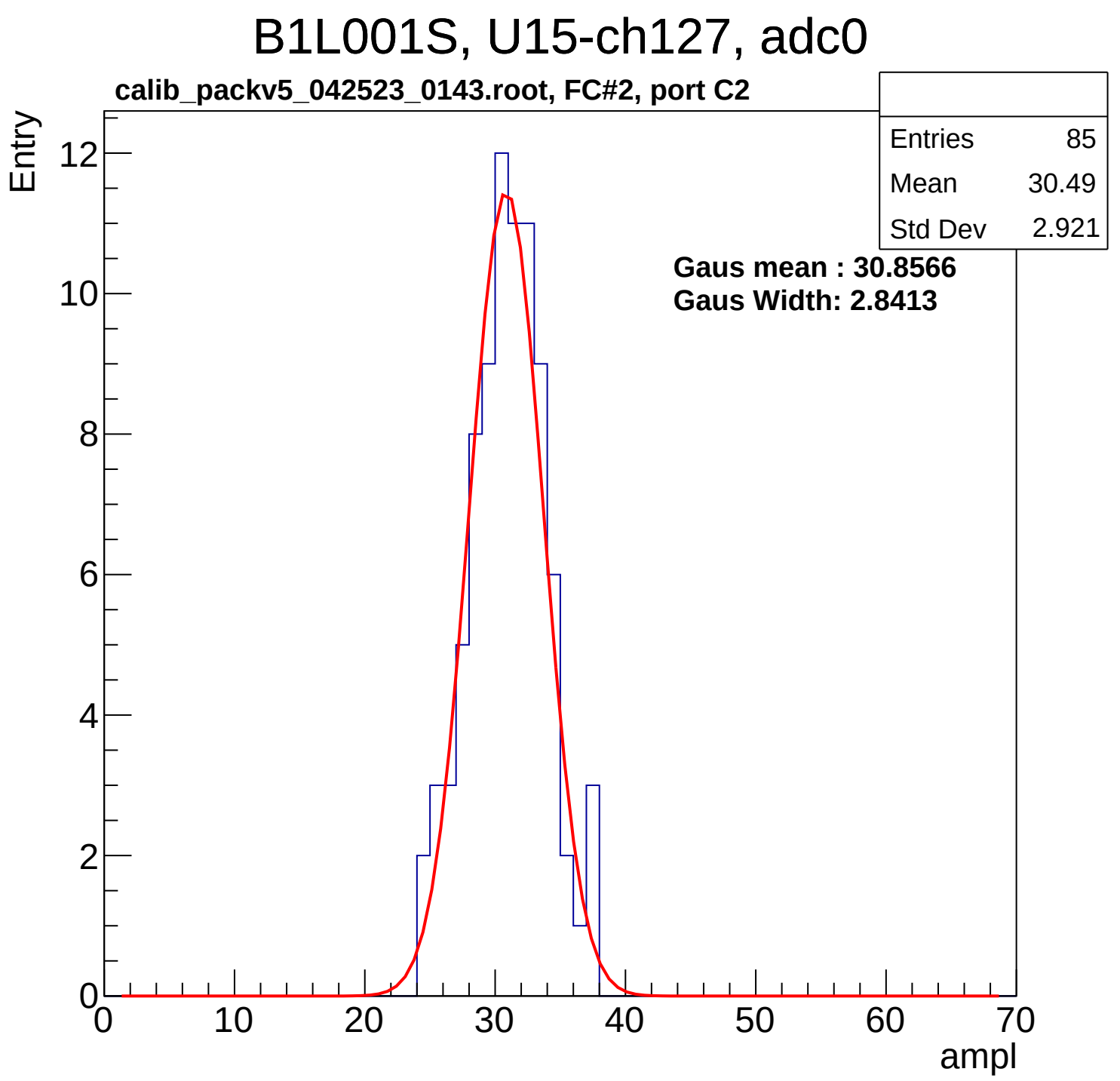
40
50
60
70
ampl


B1L001S, U15-ch127, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 30.49
Std Dev 2.921
Gaus mean : 30.8566
Gaus Width: 2.8413
Entry
ampl
0
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4
6
8
10
12
0
10
20
30
40
50
60
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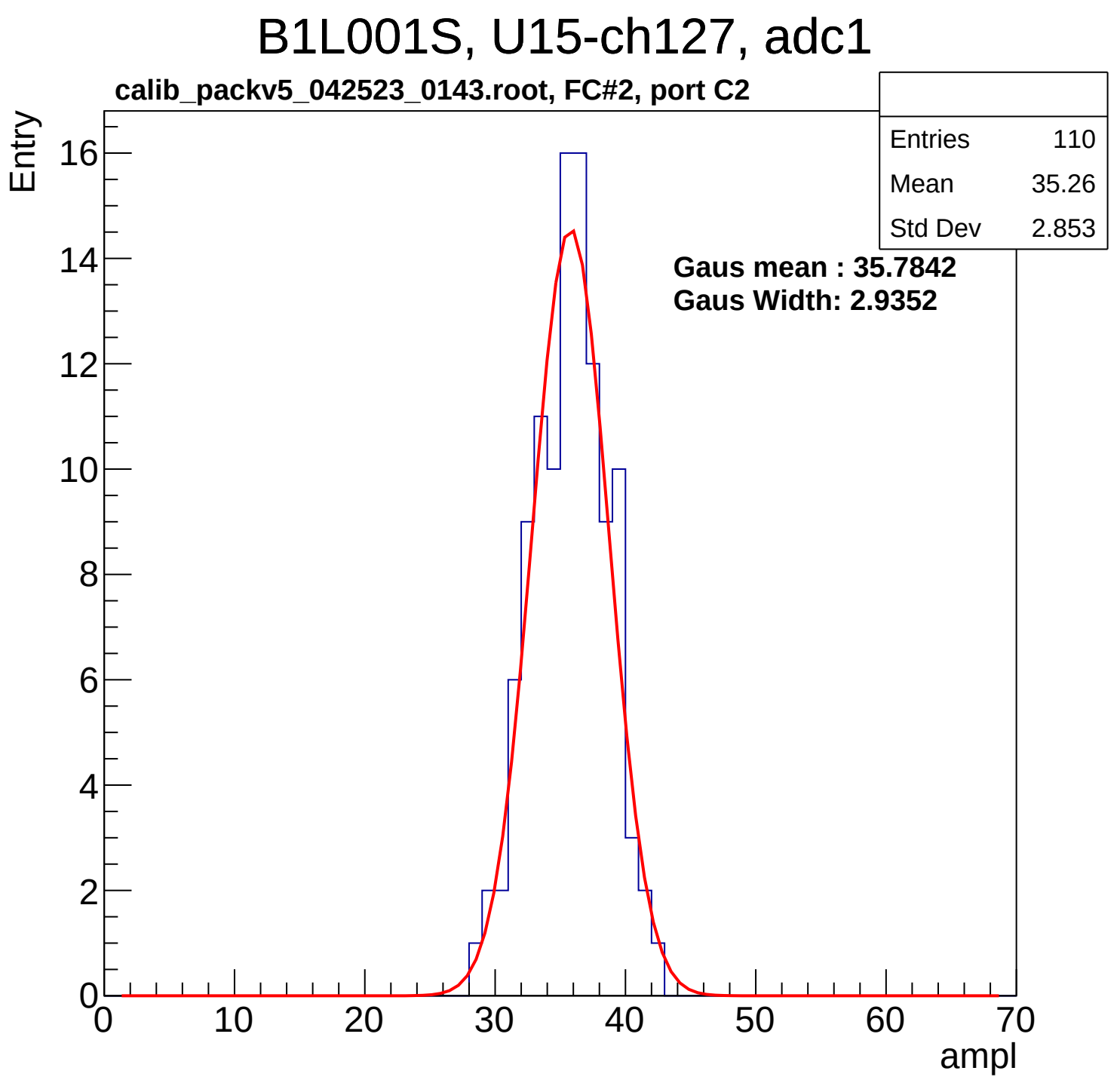
B1L001S, U15-ch127, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 35.26
Std Dev 2.853
Gaus mean : 35.7842
Gaus Width: 2.9352
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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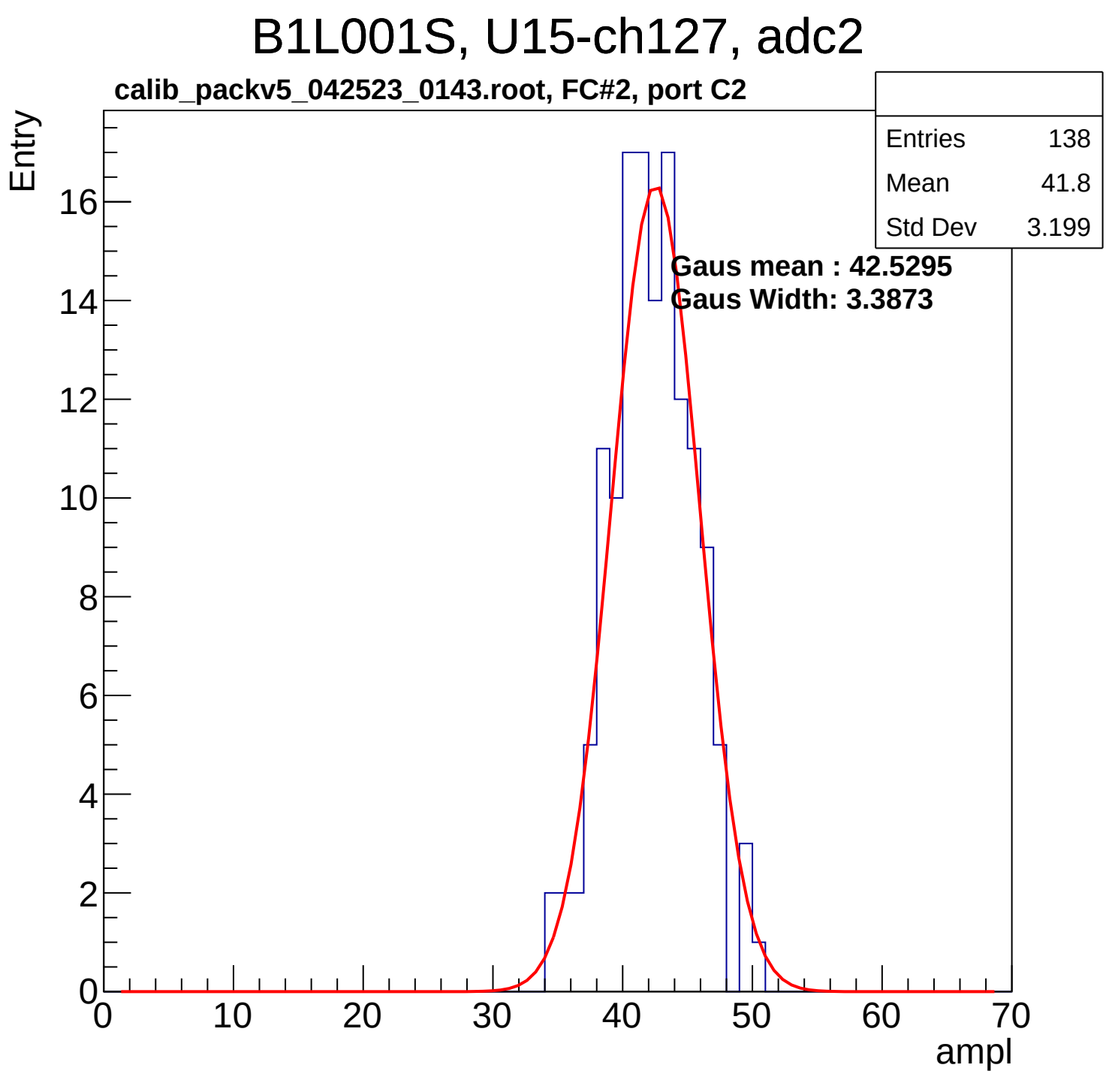
B1L001S, U15-ch127, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 41.8
Std Dev 3.199
Gaus mean : 42.5295
Gaus Width: 3.3873
Entry
ampl
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2
4
6
8
10
12
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16
0
10
20
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40
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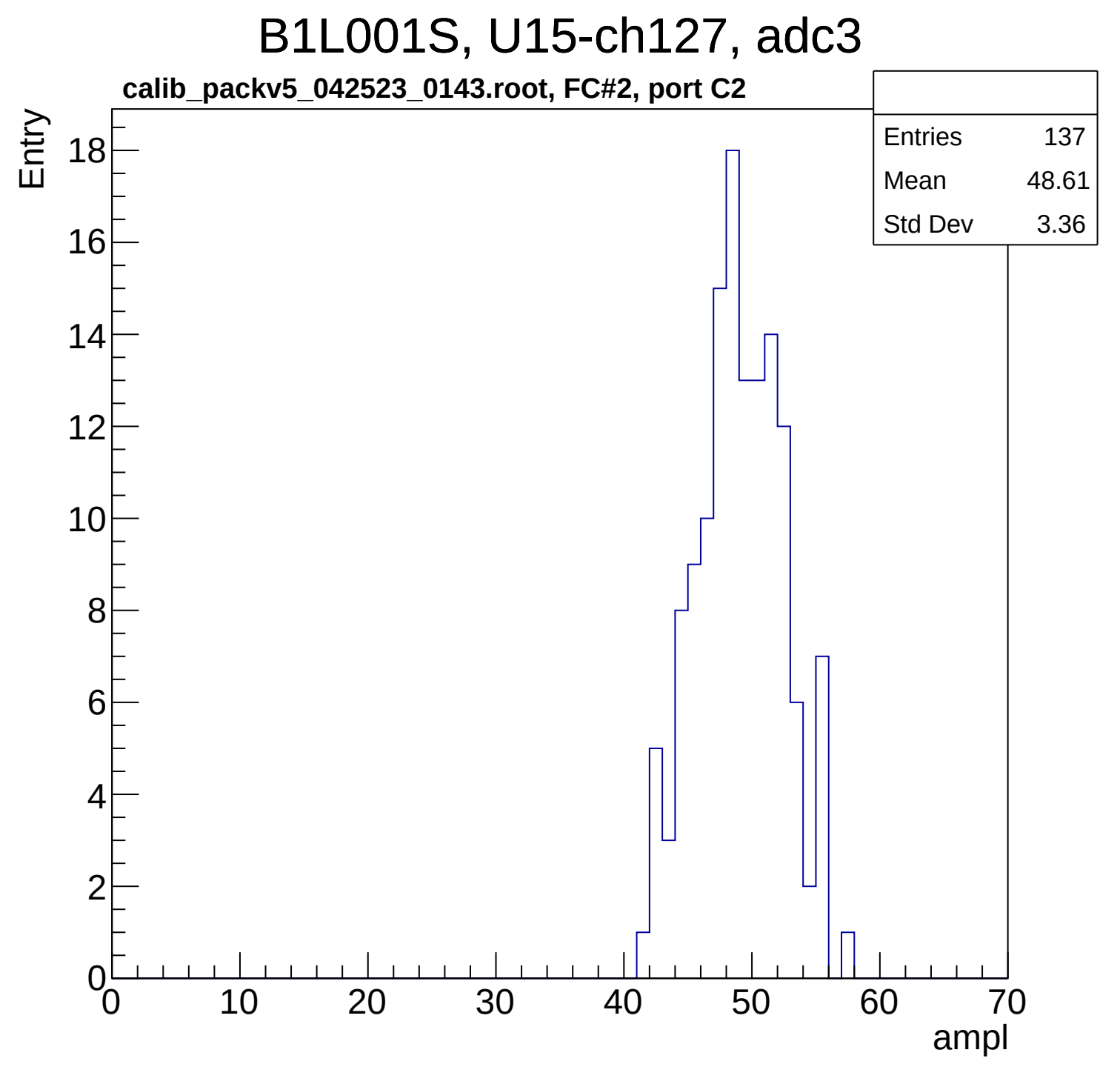
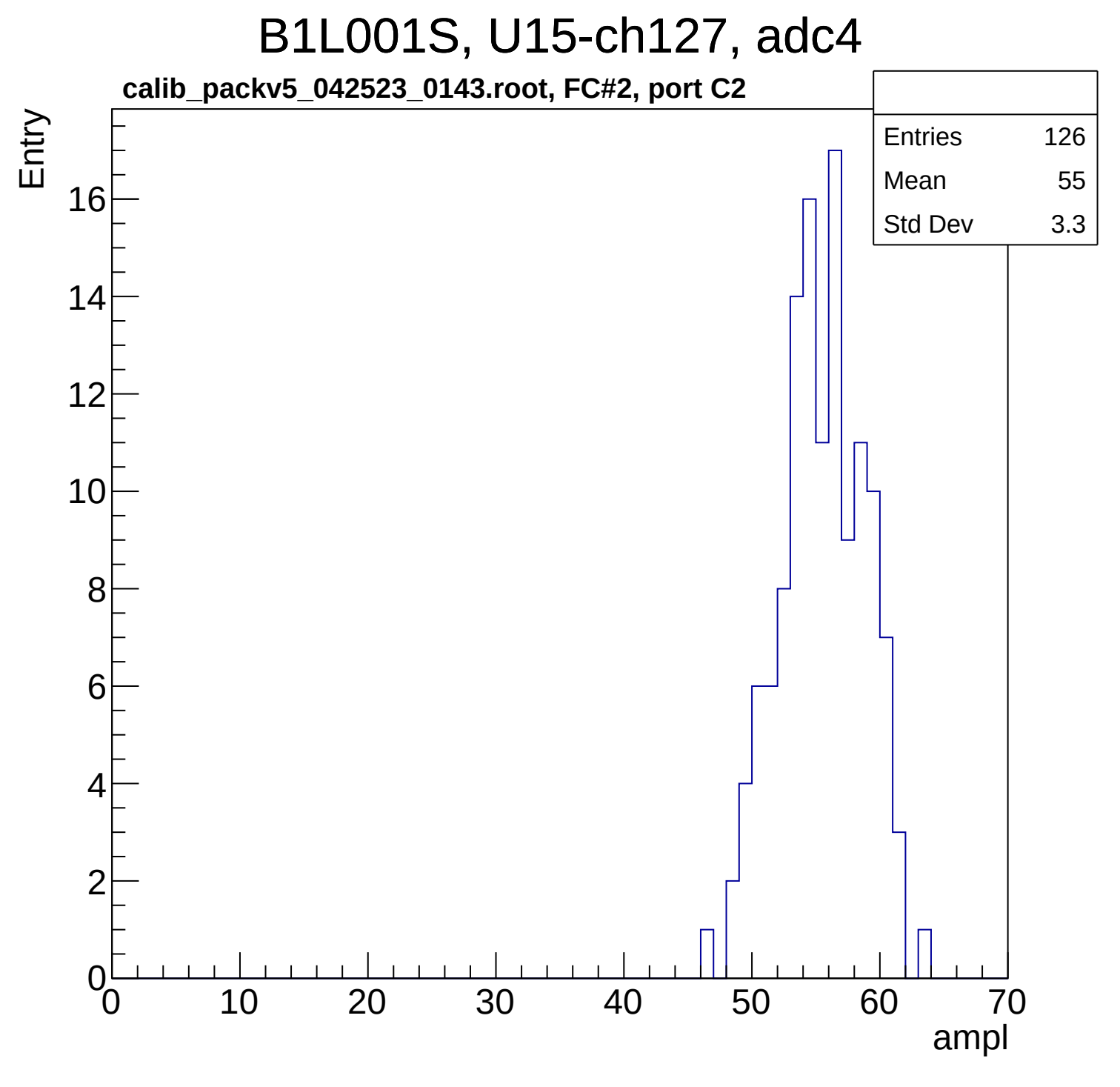
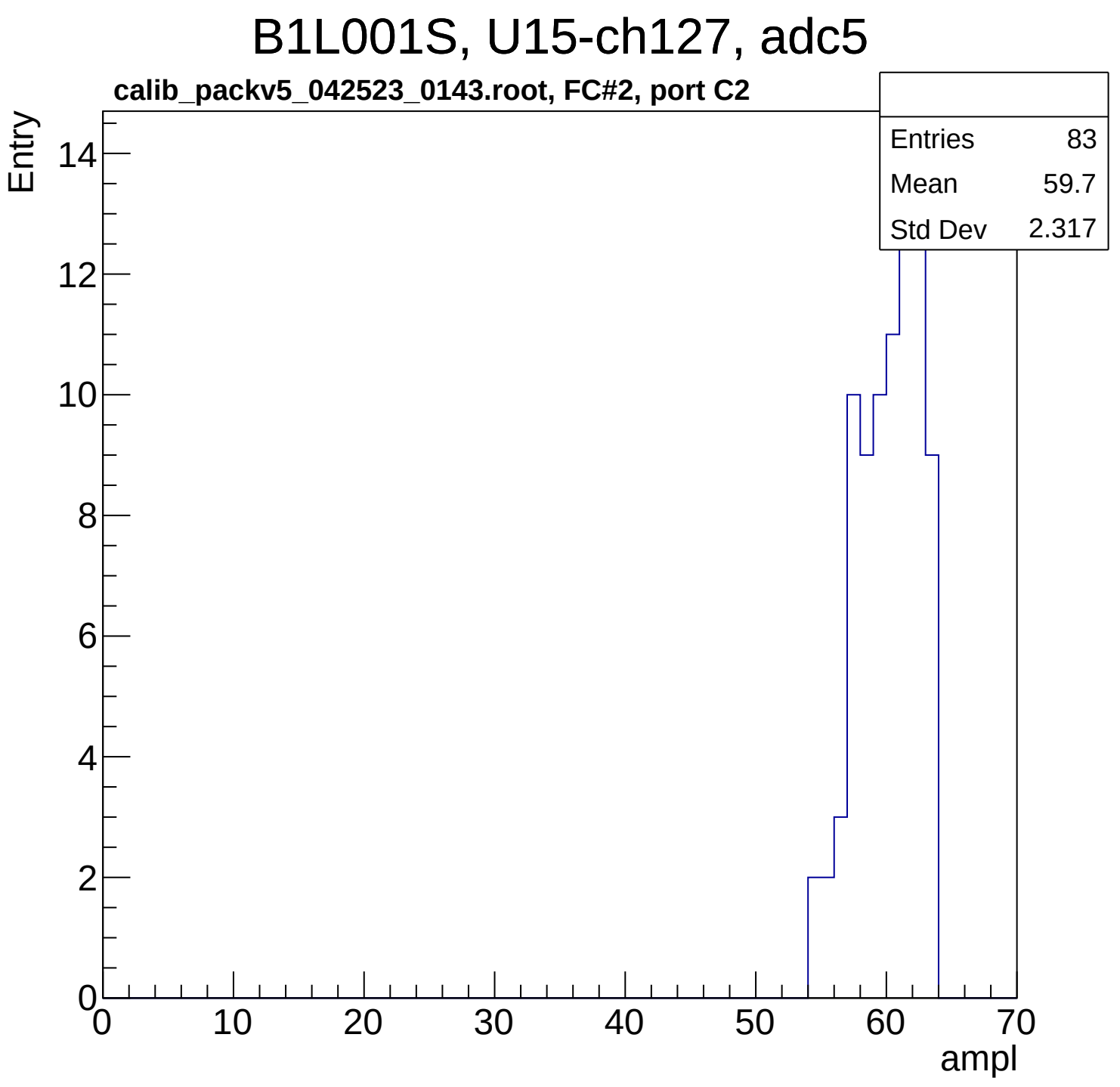
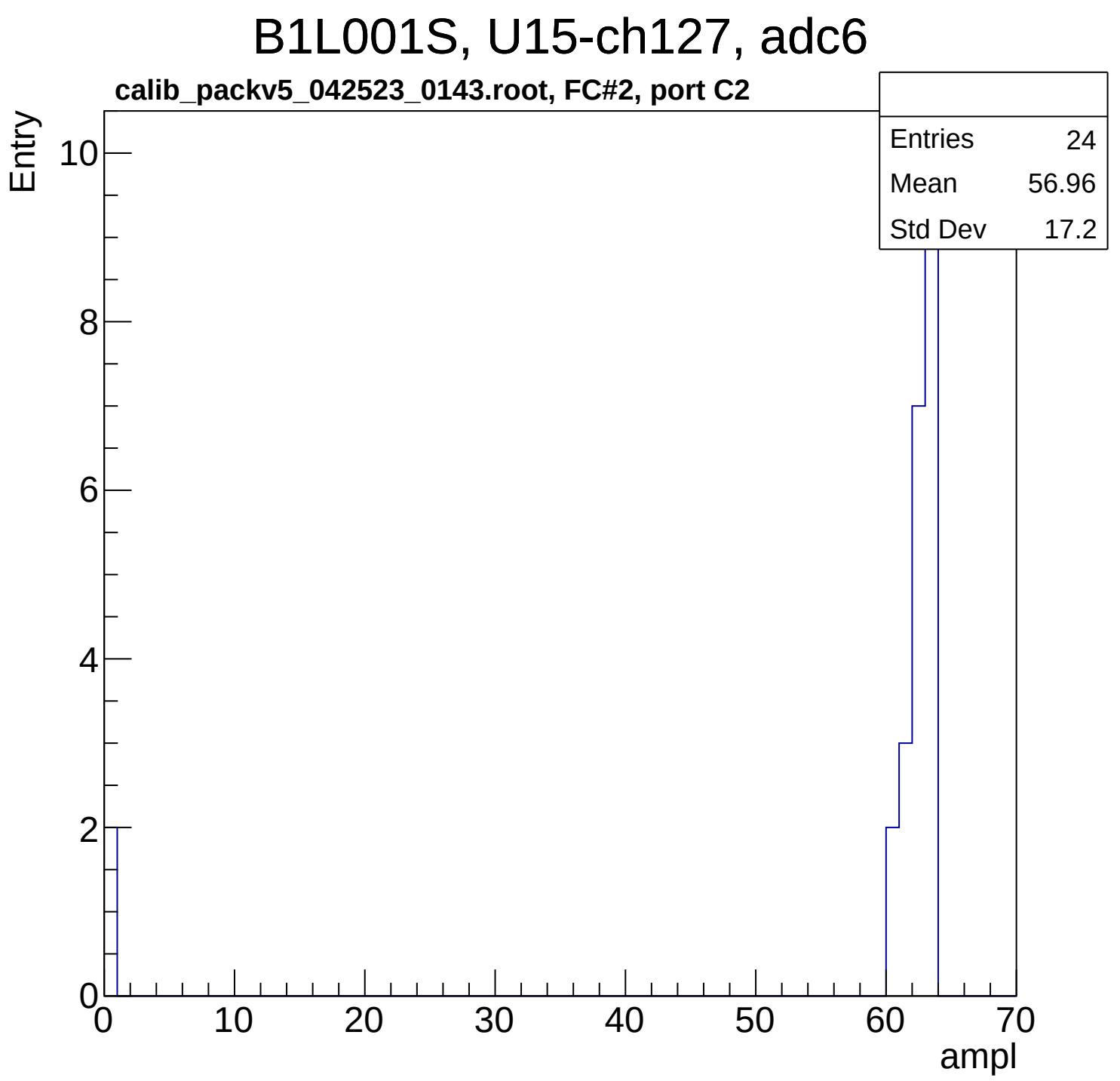
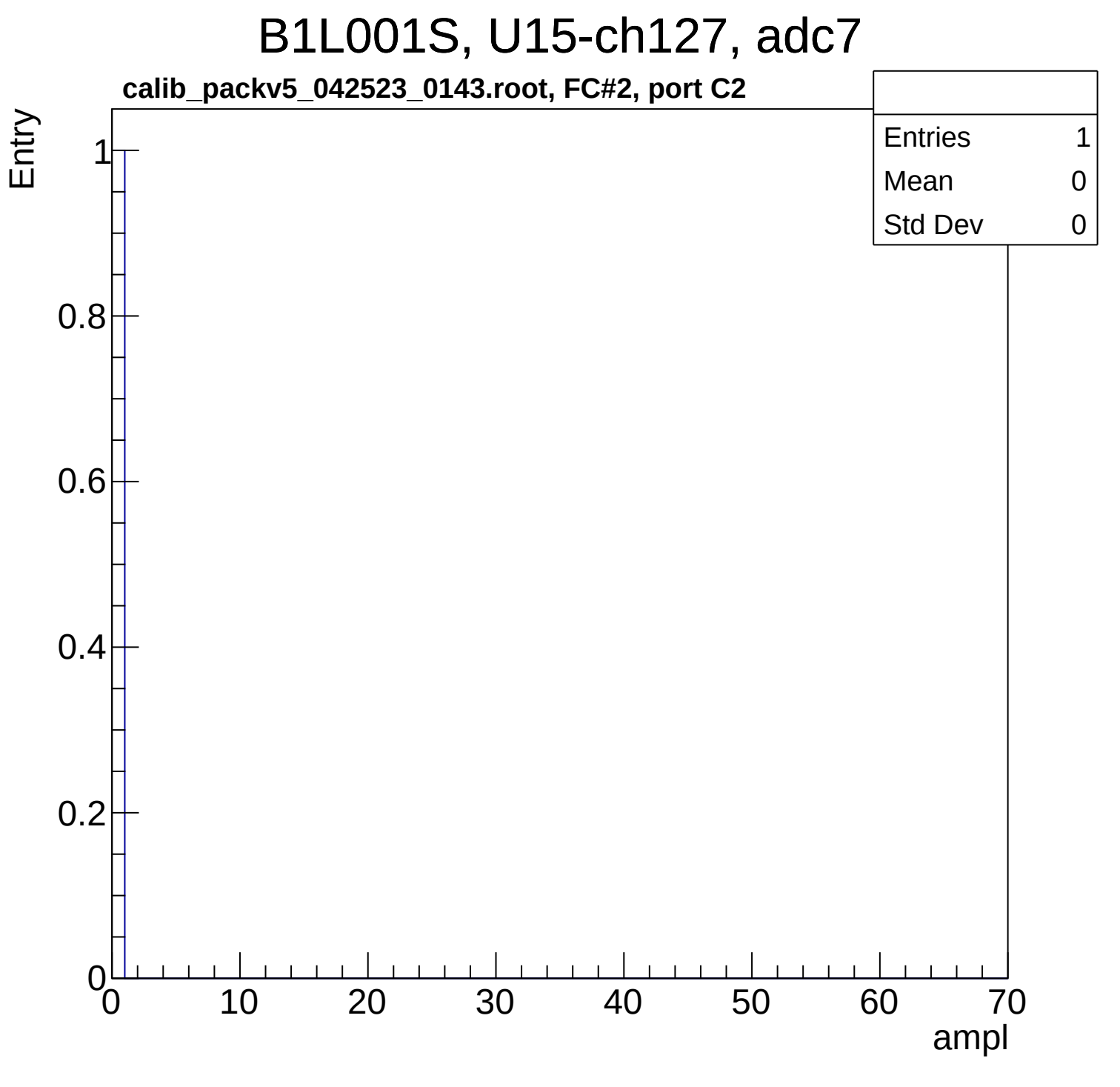
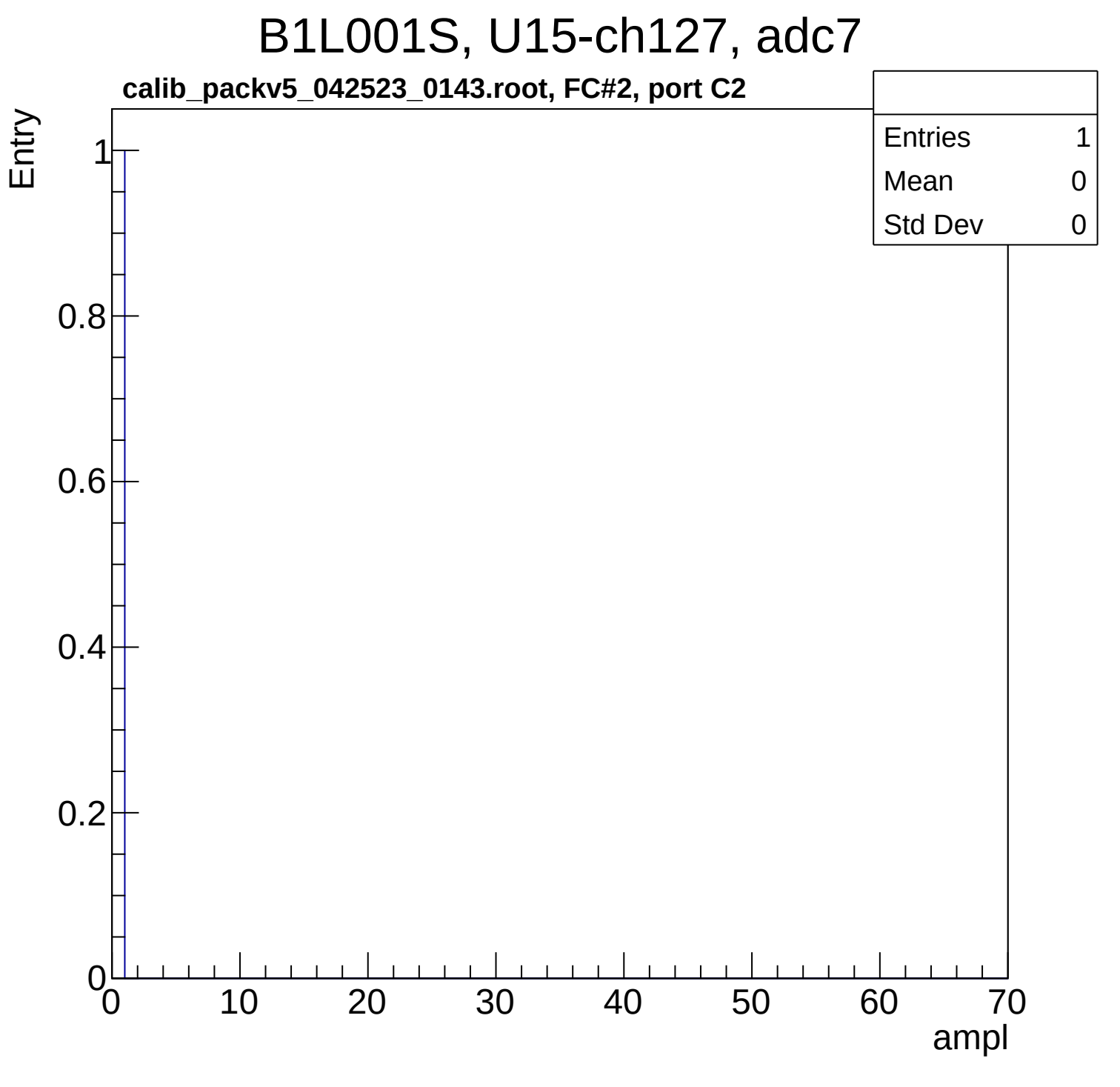

B1L001S, U15-ch127, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 48.61
Std Dev 3.36
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch127, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 55
Std Dev 3.3
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U15-ch127, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 59.7
Std Dev 2.317
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U15-ch127, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 24
Mean 56.96
Std Dev 17.2
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L001S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl