

B1L003S, U11-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.5425

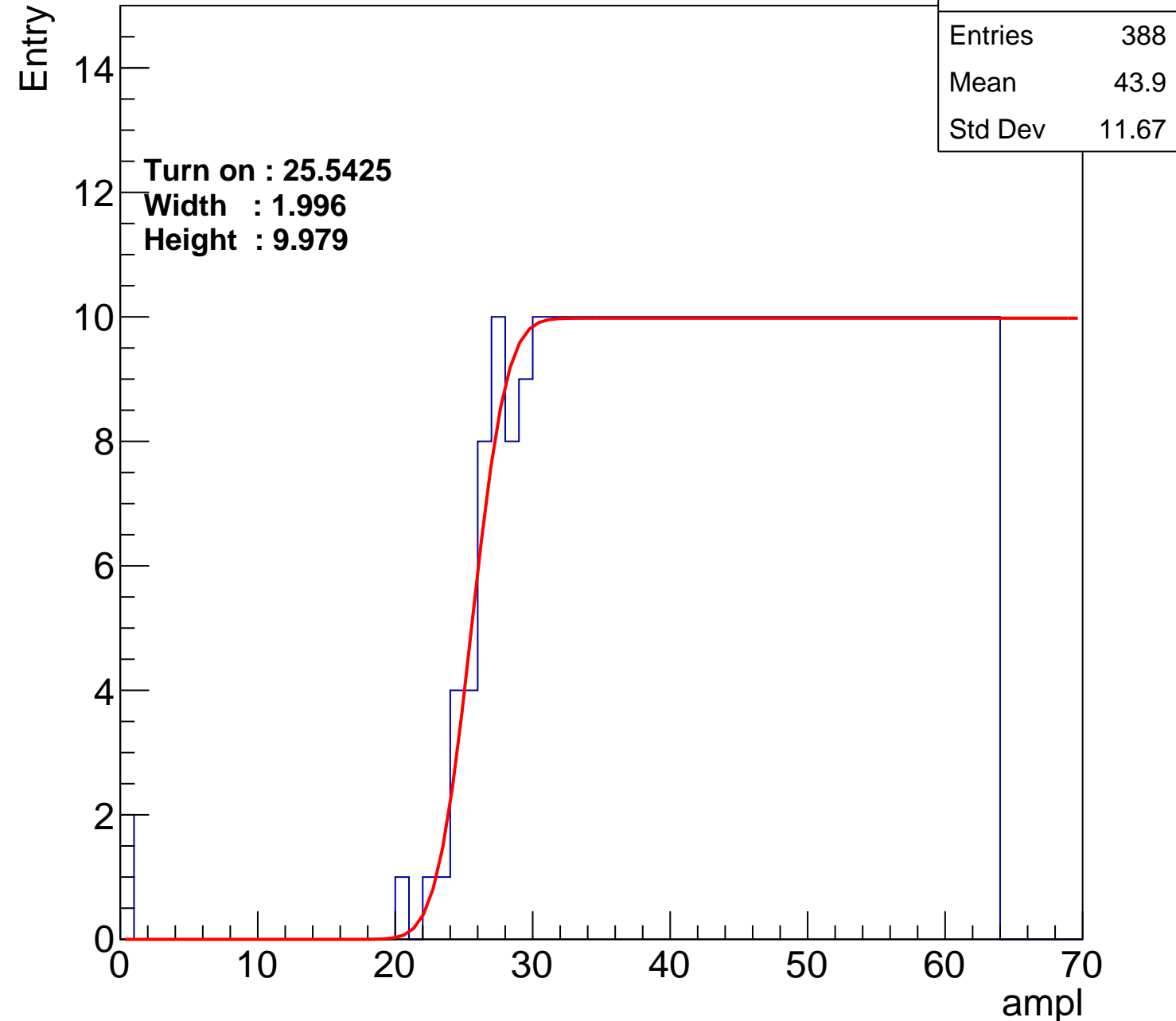
Width : 1.996

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.33
Std Dev	11.65

Turn on : 26.8183

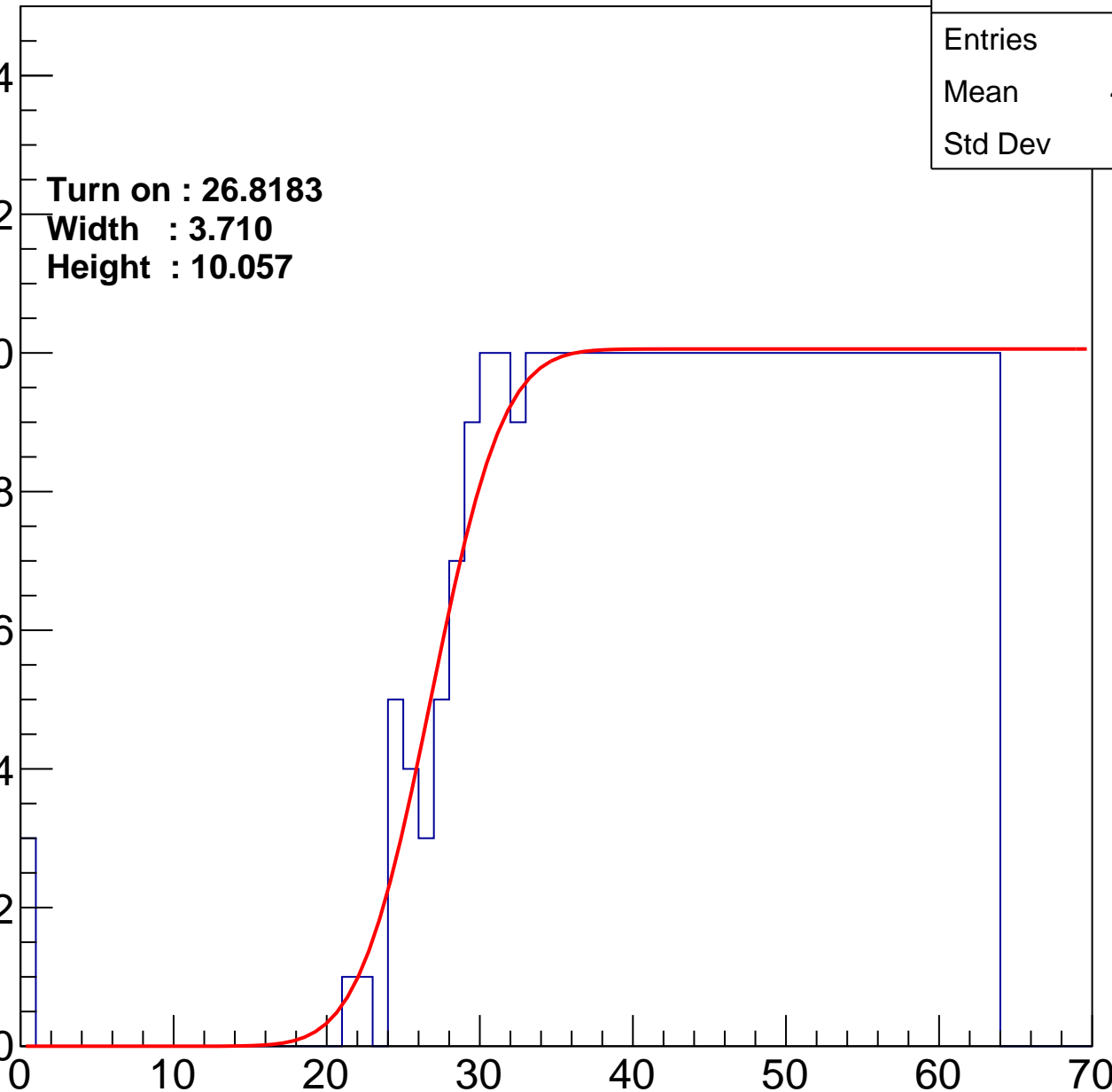
Width : 3.710

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.99
Std Dev	11.3

Turn on : 27.8655

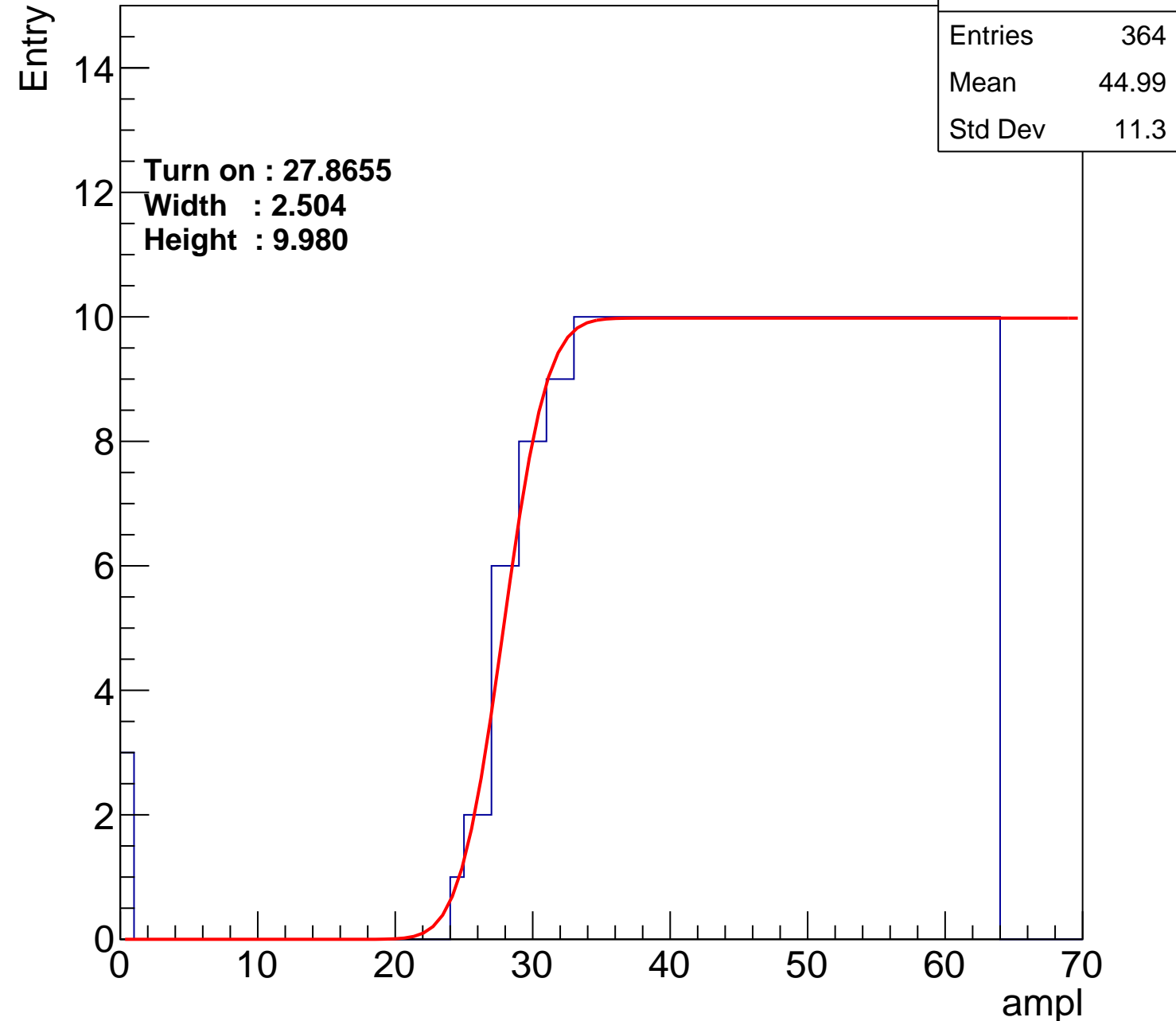
Width : 2.504

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.83
Std Dev	11.1

Turn on : 27.2805

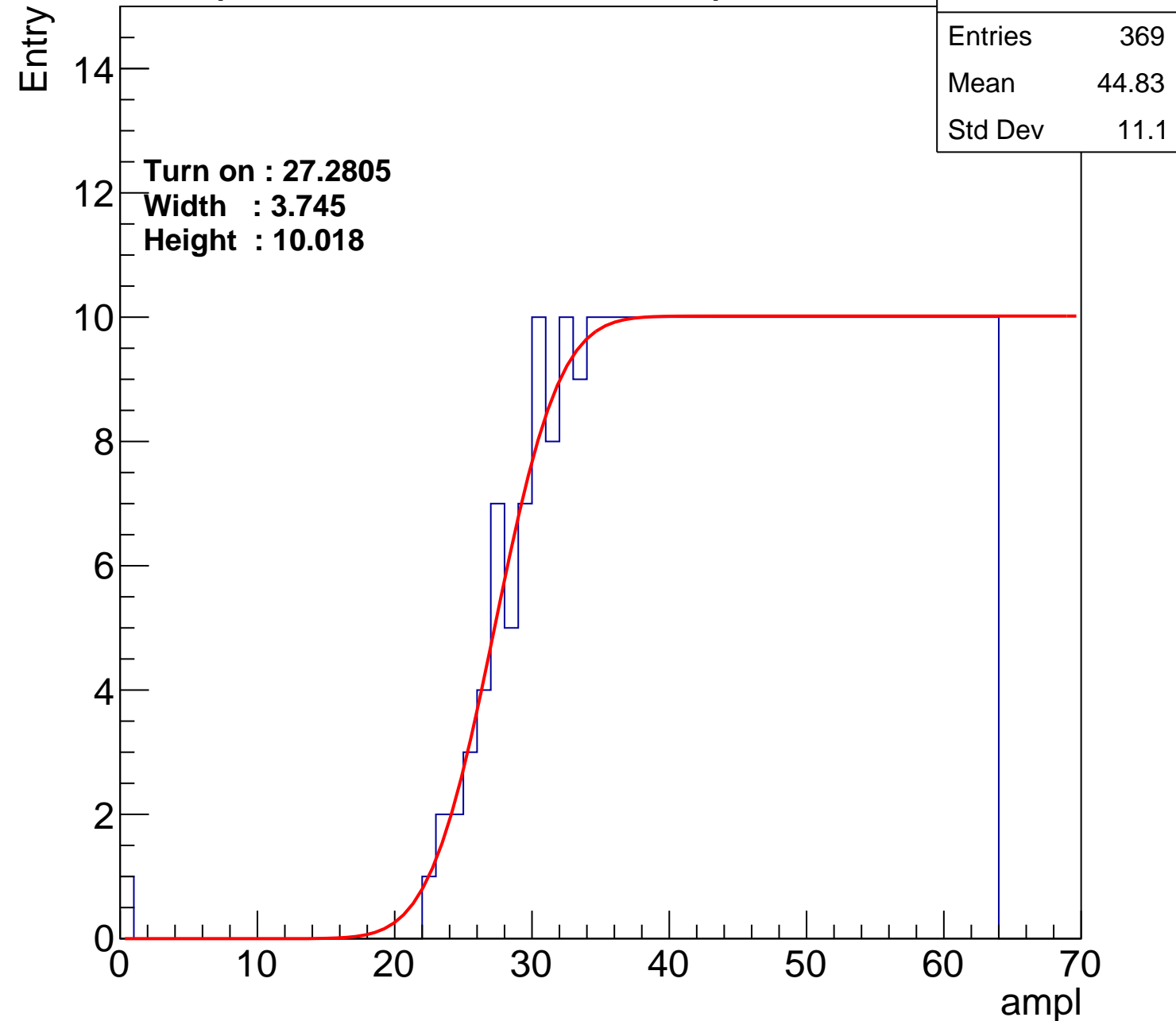
Width : 3.745

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.31
Std Dev	10.97

Turn on : 28.9642

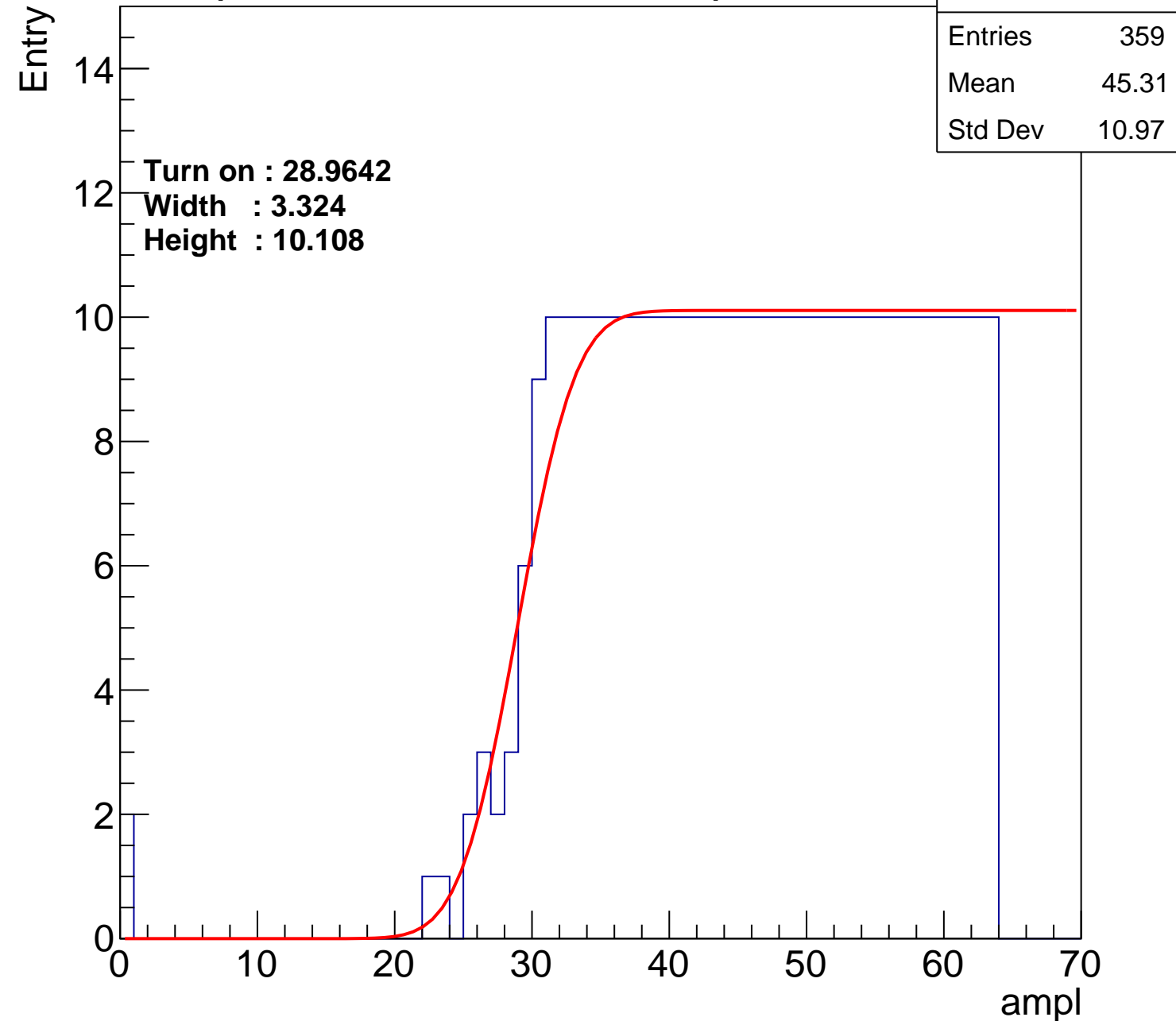
Width : 3.324

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.49
Std Dev	11.04

Turn on : 29.0603

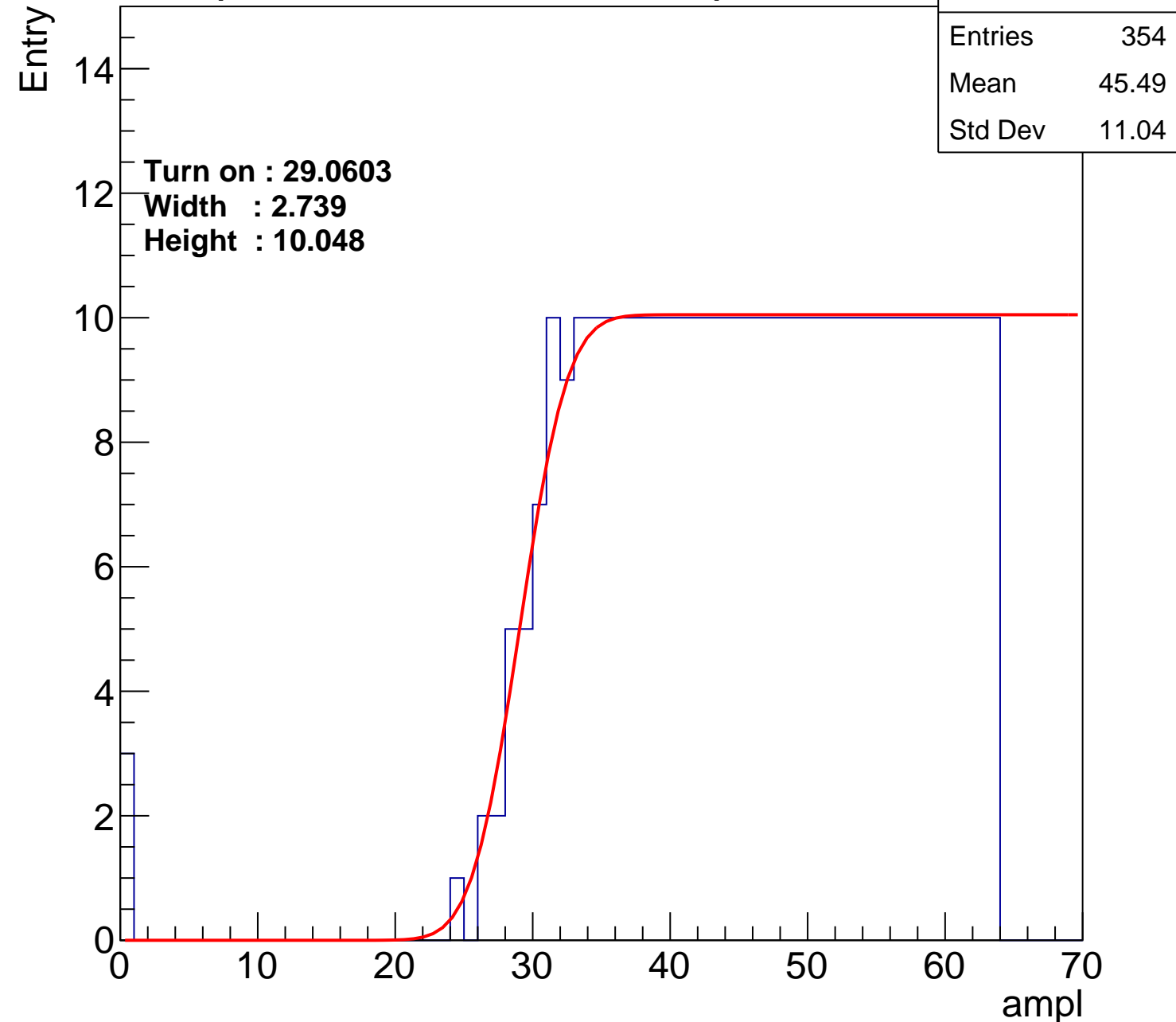
Width : 2.739

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch6

calib_packv5_042523_0143.root, FC#13, port D2

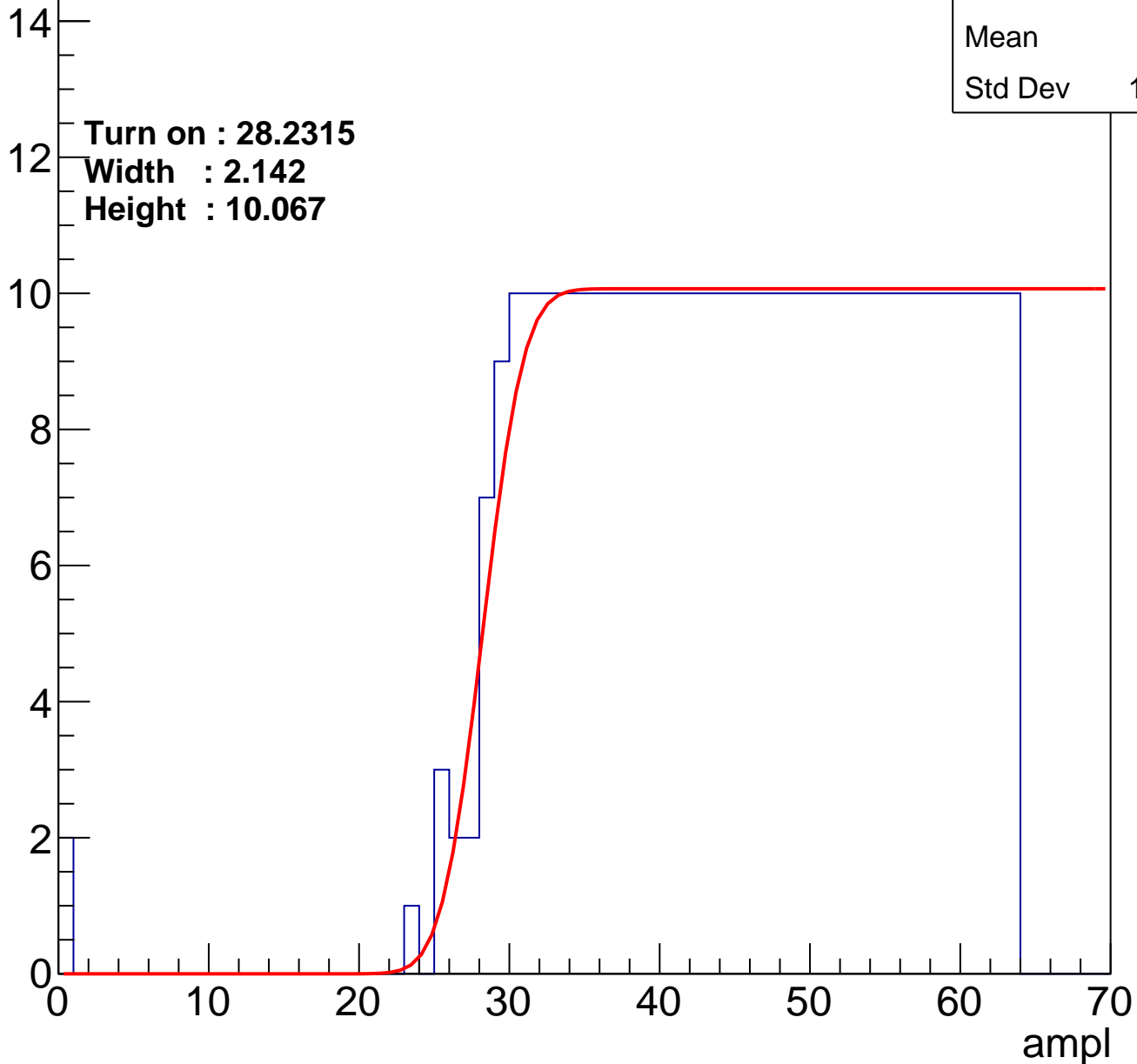
Entry

Entries	366
Mean	45
Std Dev	11.08

Turn on : 28.2315

Width : 2.142

Height : 10.067



B1L003S, U11-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 26.6847

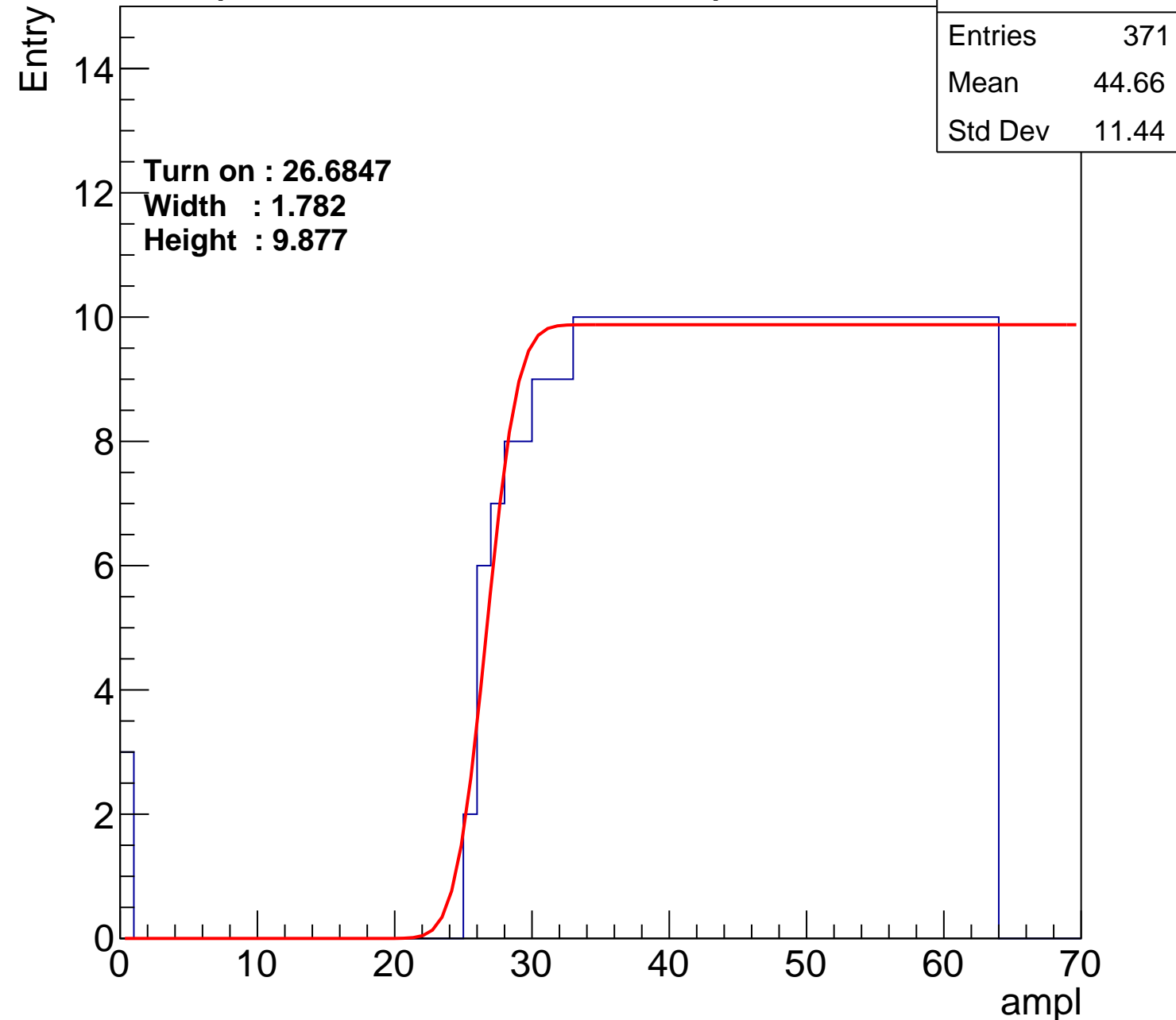
Width : 1.782

Height : 9.877

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.14
Std Dev	11.22

Turn on : 28.1816

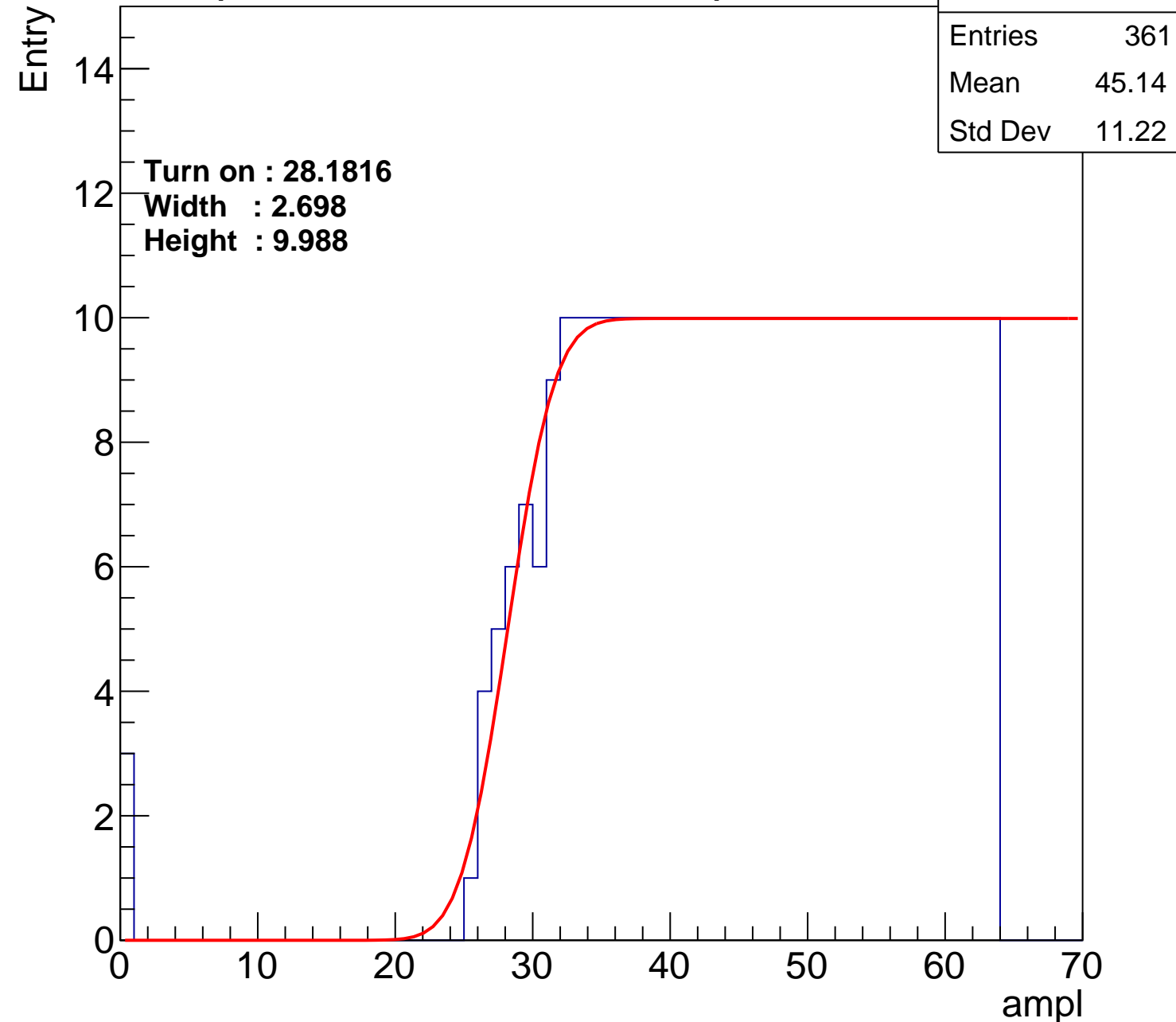
Width : 2.698

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.59
Std Dev	10.62

Turn on : 29.0869

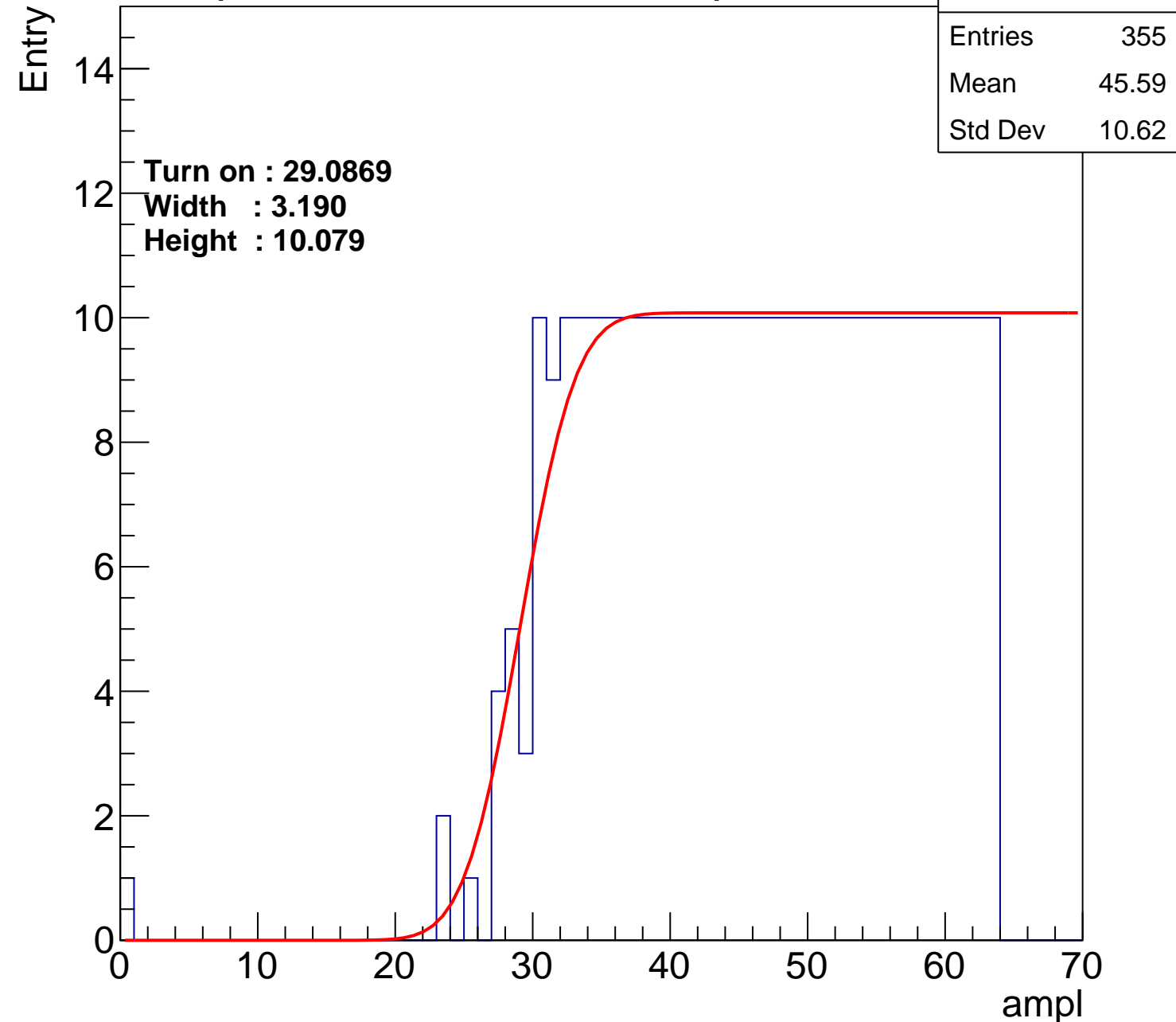
Width : 3.190

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.81
Std Dev	11.55

Turn on : 27.9798

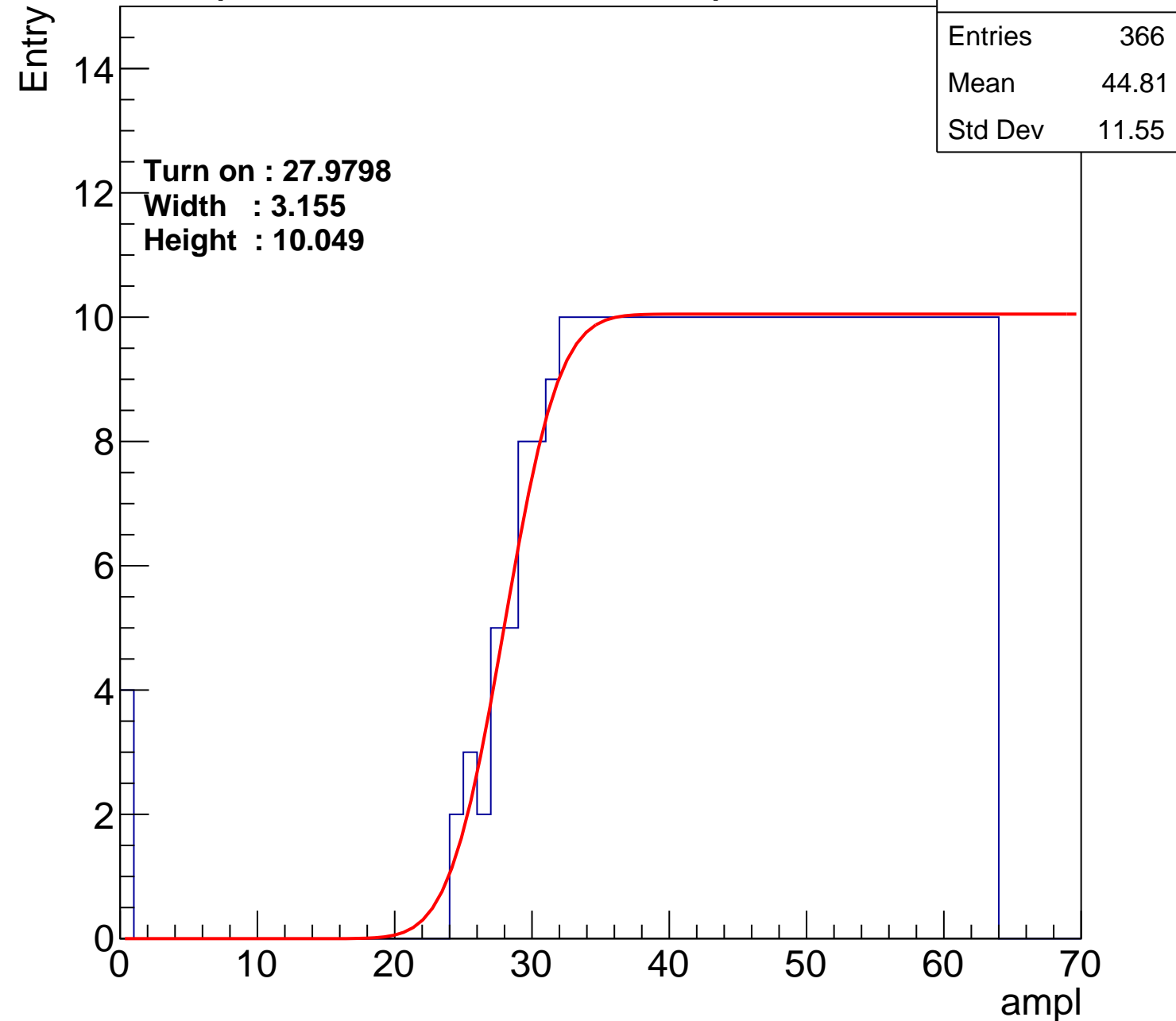
Width : 3.155

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.78
Std Dev	11.55

Turn on : 27.9022

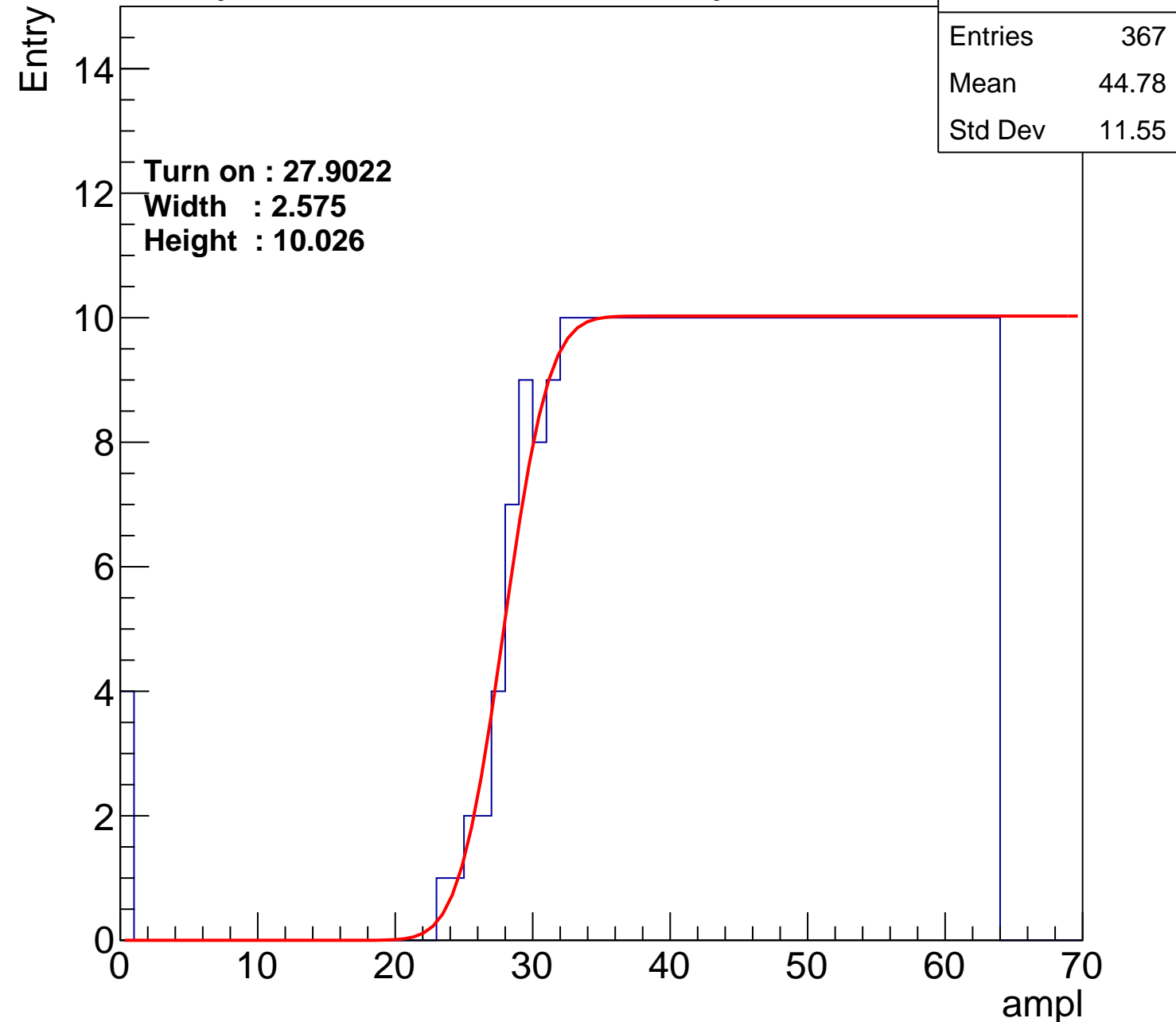
Width : 2.575

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.47
Std Dev	11.22

Turn on : 26.4908

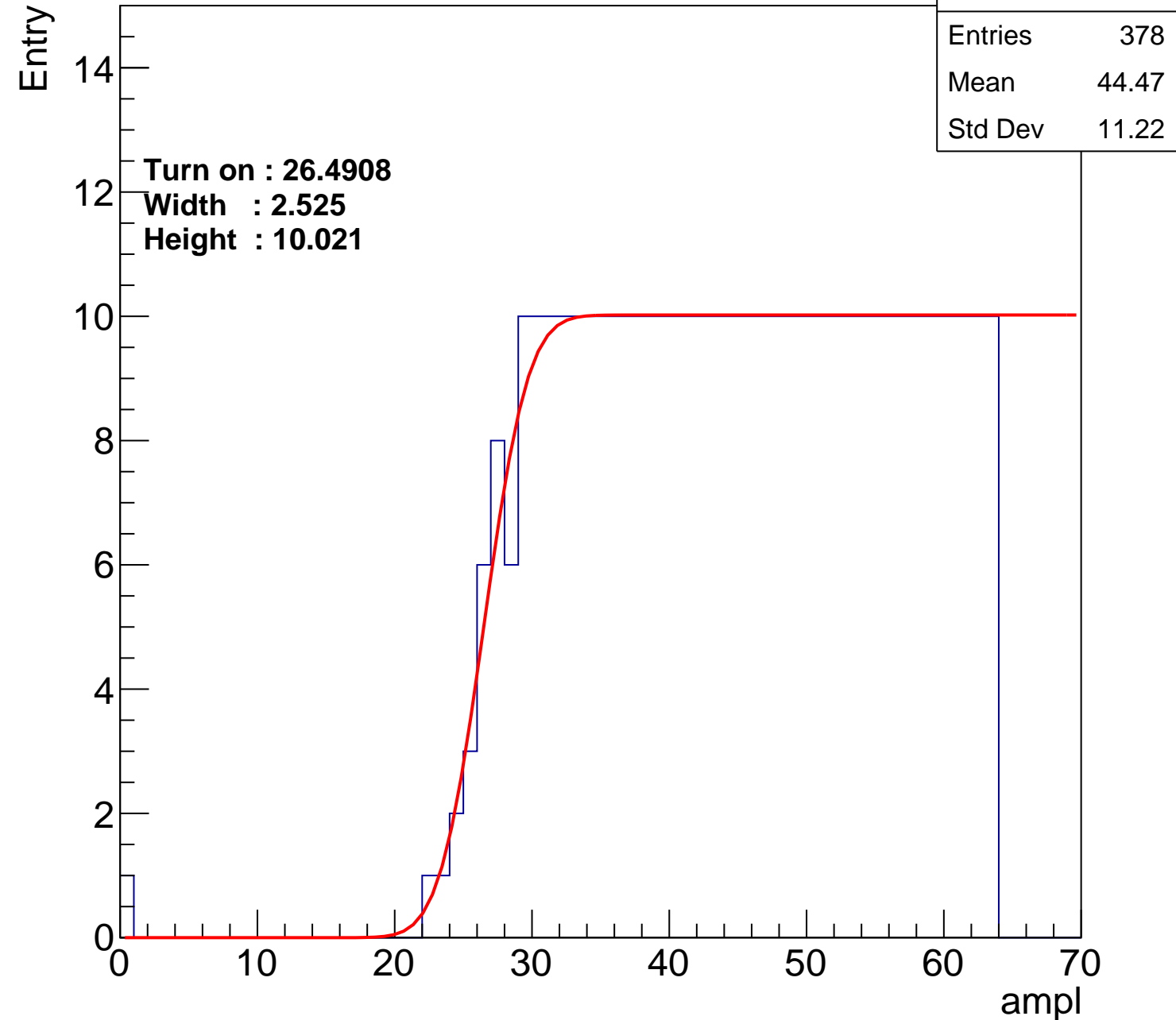
Width : 2.525

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch13

calib_packv5_042523_0143.root, FC#13, port D2

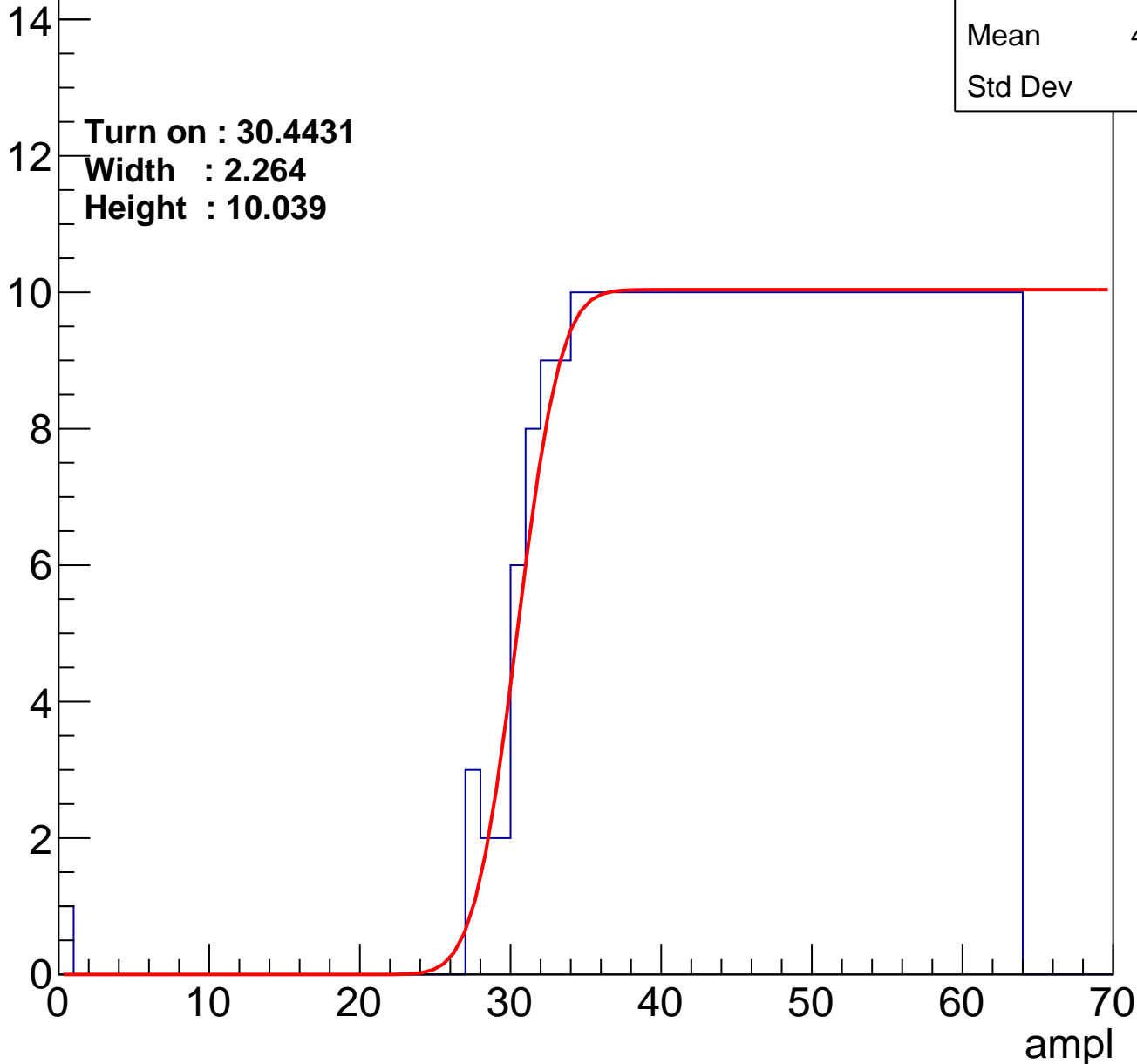
Entries	340
Mean	46.35
Std Dev	10.2

Turn on : 30.4431

Width : 2.264

Height : 10.039

Entry



B1L003S, U11-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.63
Std Dev	10.61

Turn on : 29.0321

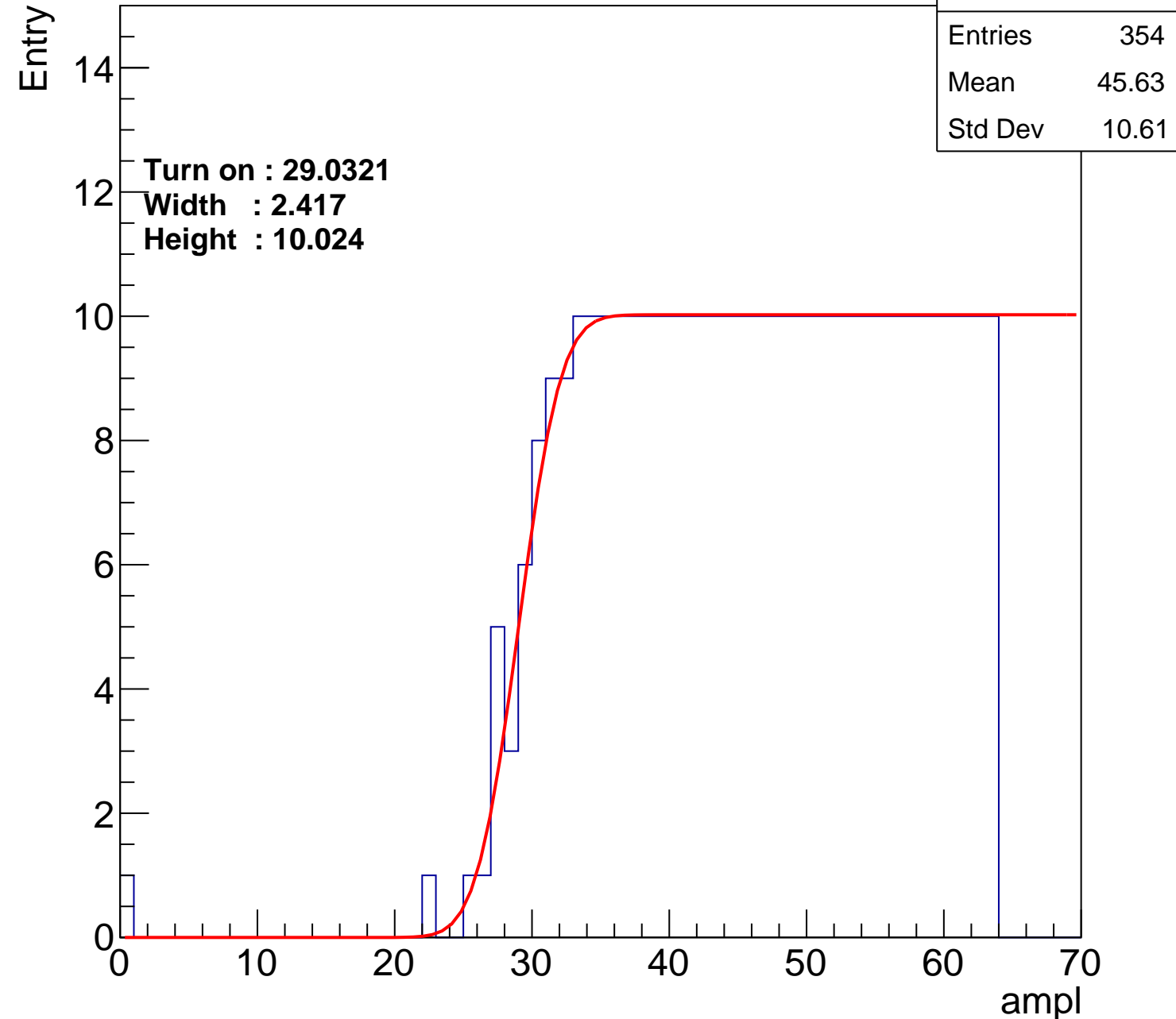
Width : 2.417

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch15

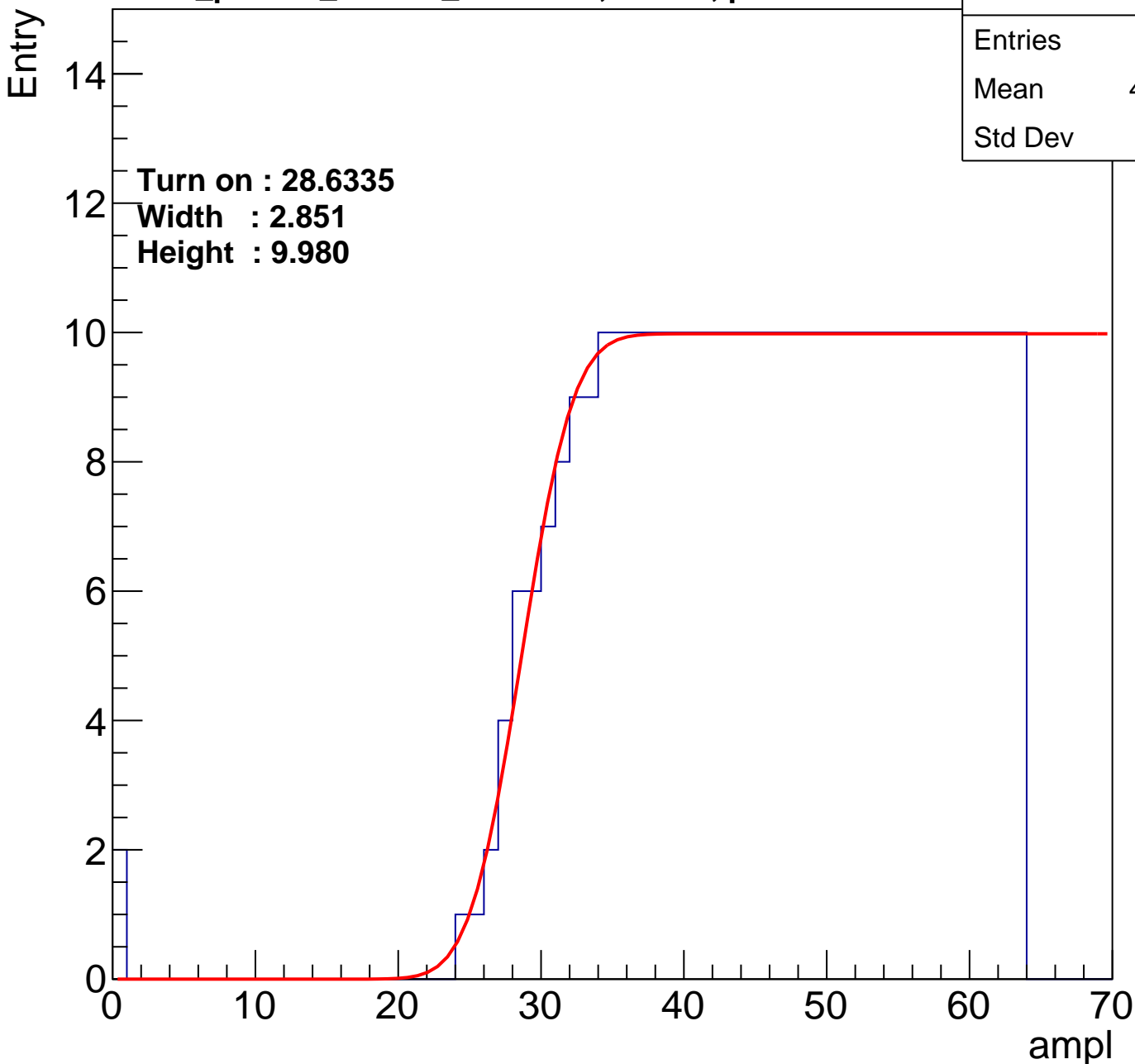
calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.48
Std Dev	10.9

Turn on : 28.6335

Width : 2.851

Height : 9.980



B1L003S, U11-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.28
Std Dev	10.8

Turn on : 28.2099

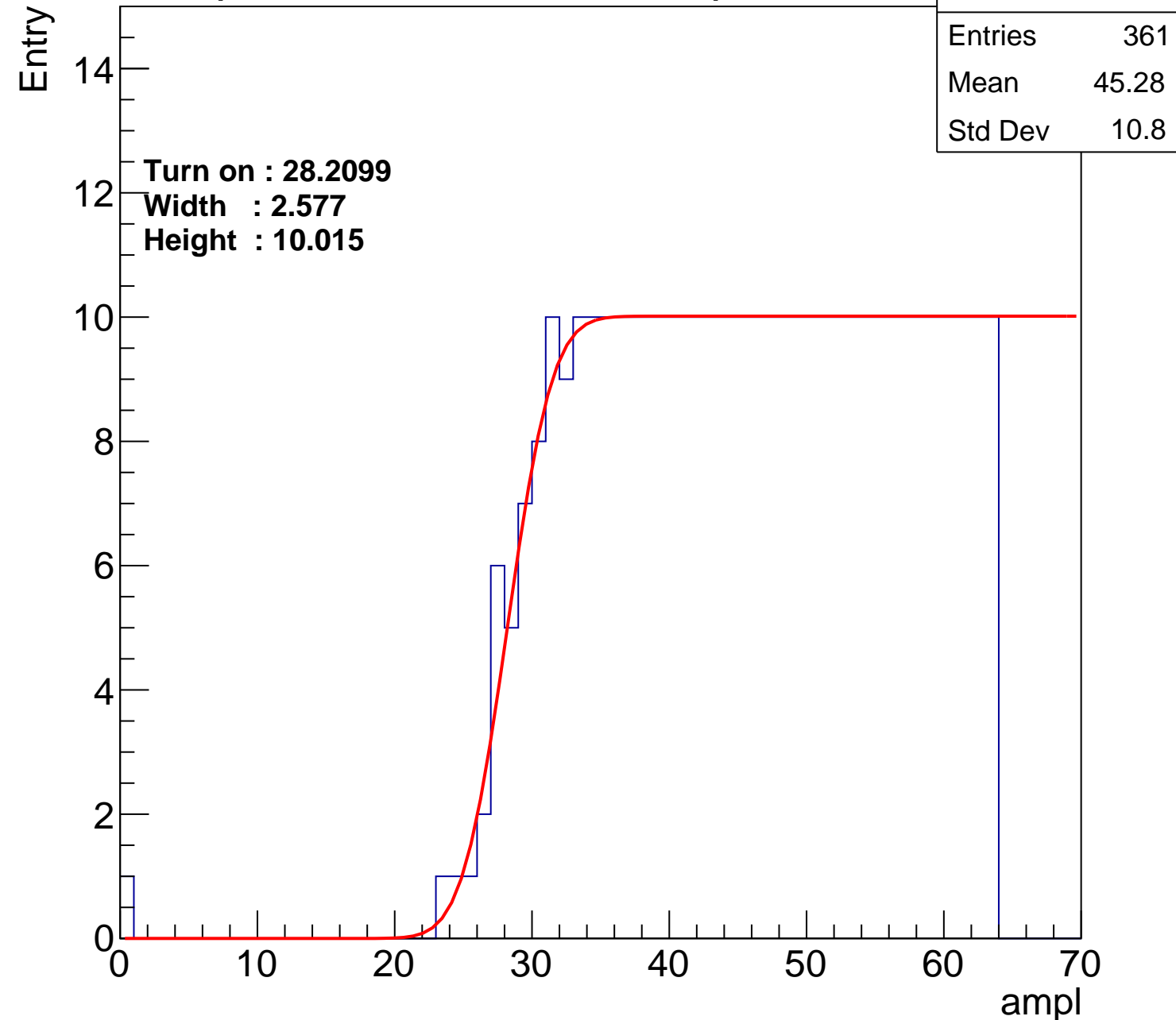
Width : 2.577

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.16
Std Dev	11.09

Turn on : 28.6055

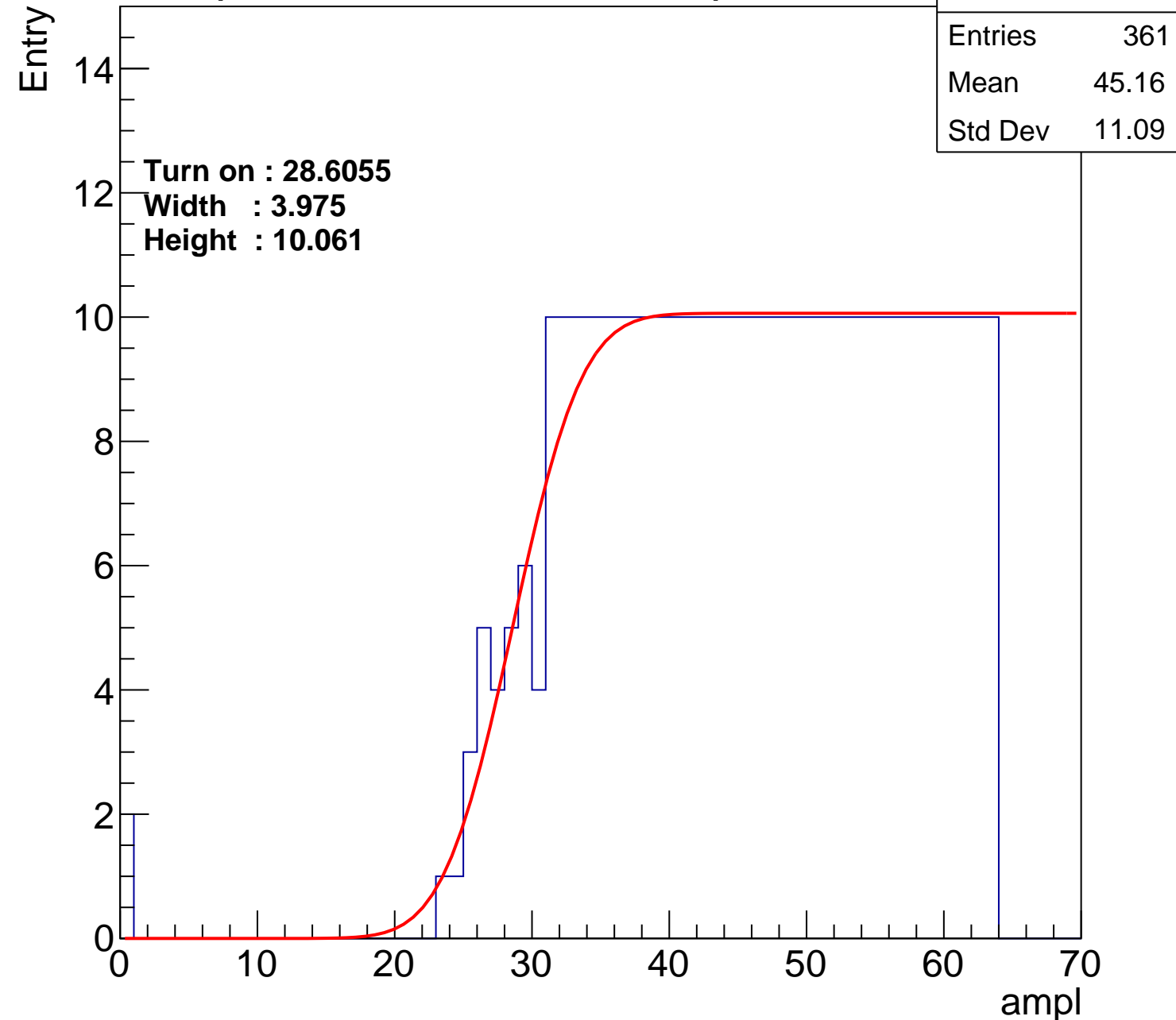
Width : 3.975

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.16
Std Dev	11.04

Turn on : 28.1521

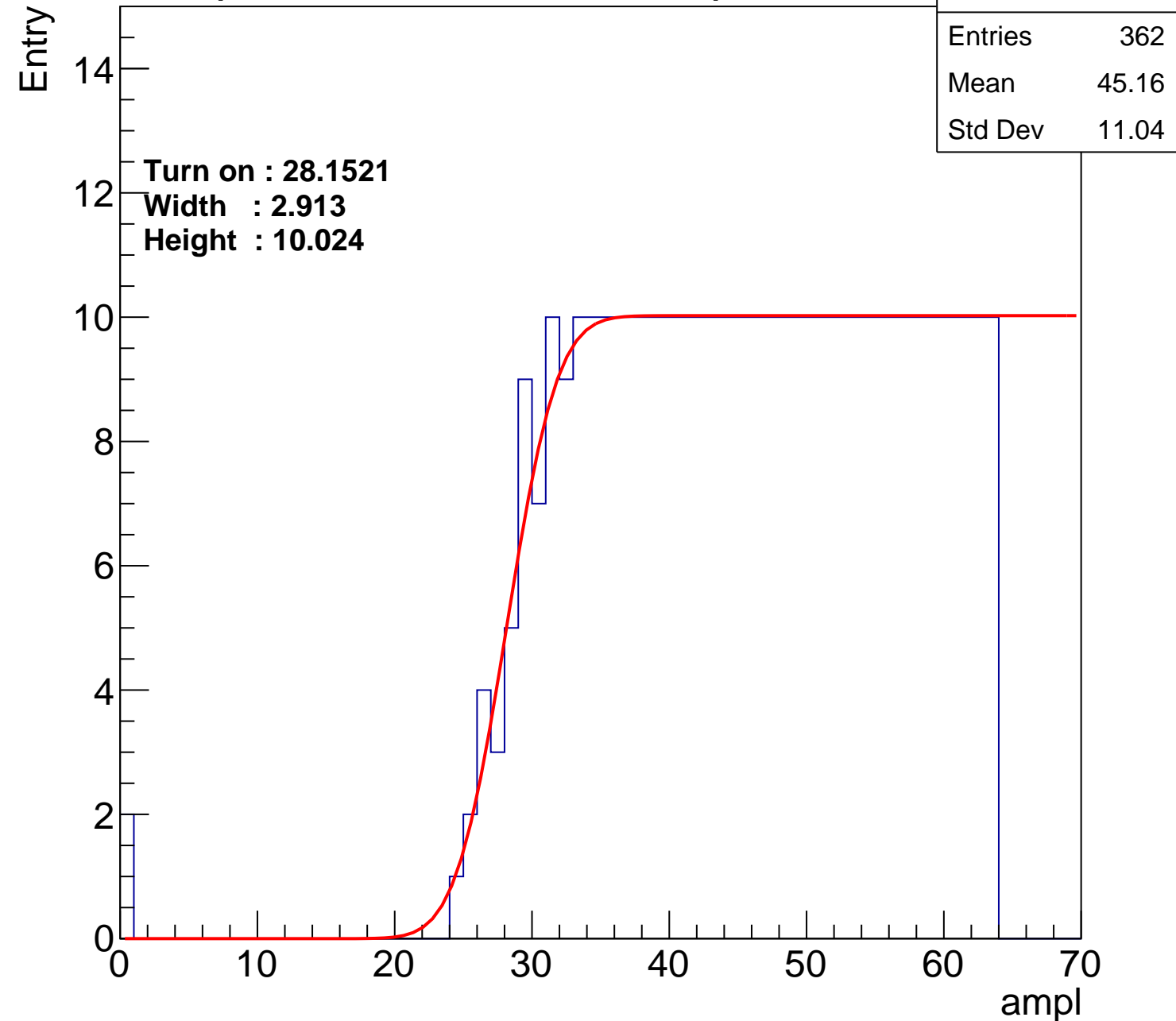
Width : 2.913

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.94
Std Dev	11.04

Turn on : 27.6719

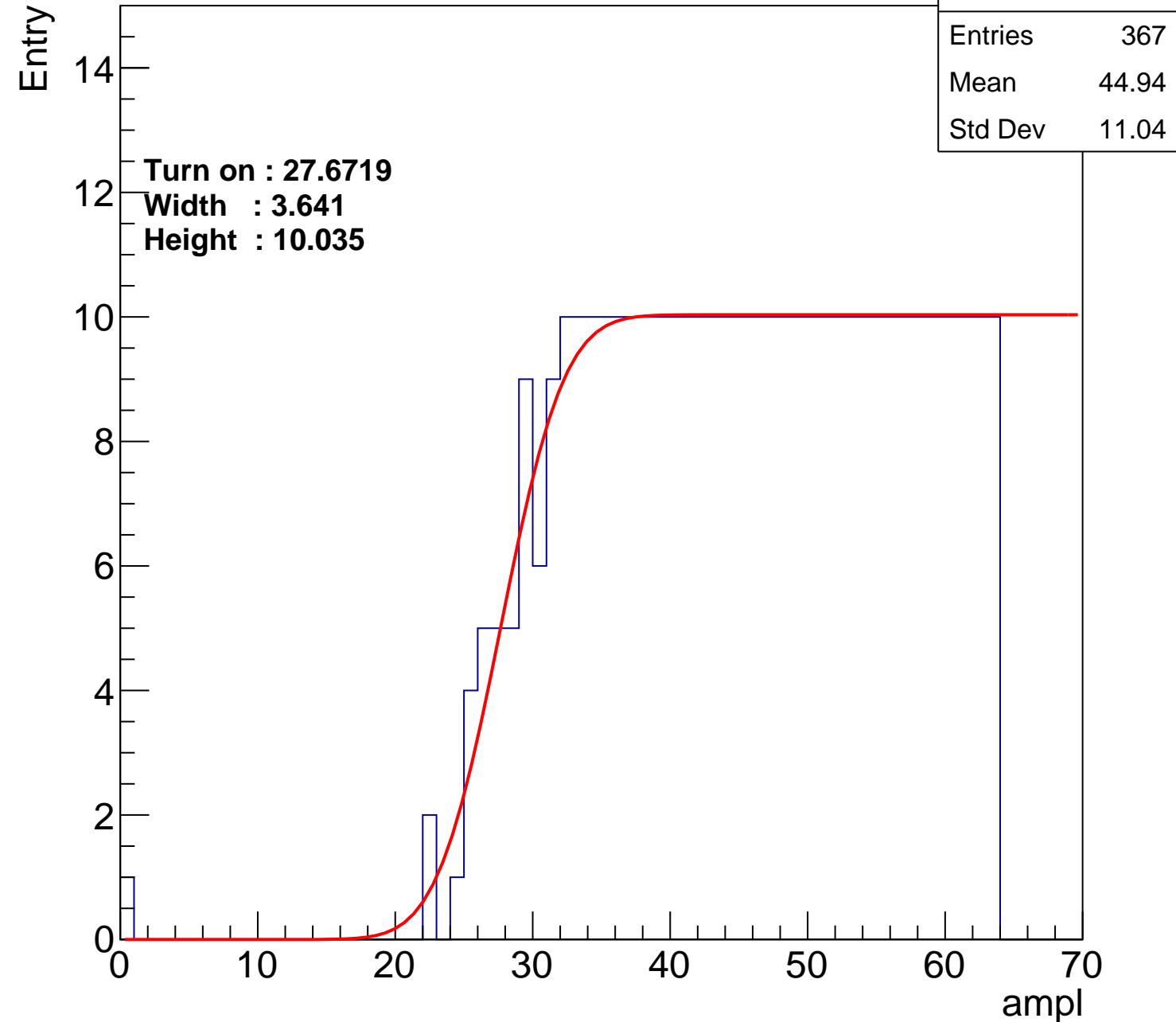
Width : 3.641

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.61
Std Dev	11.14

Turn on : 25.8807

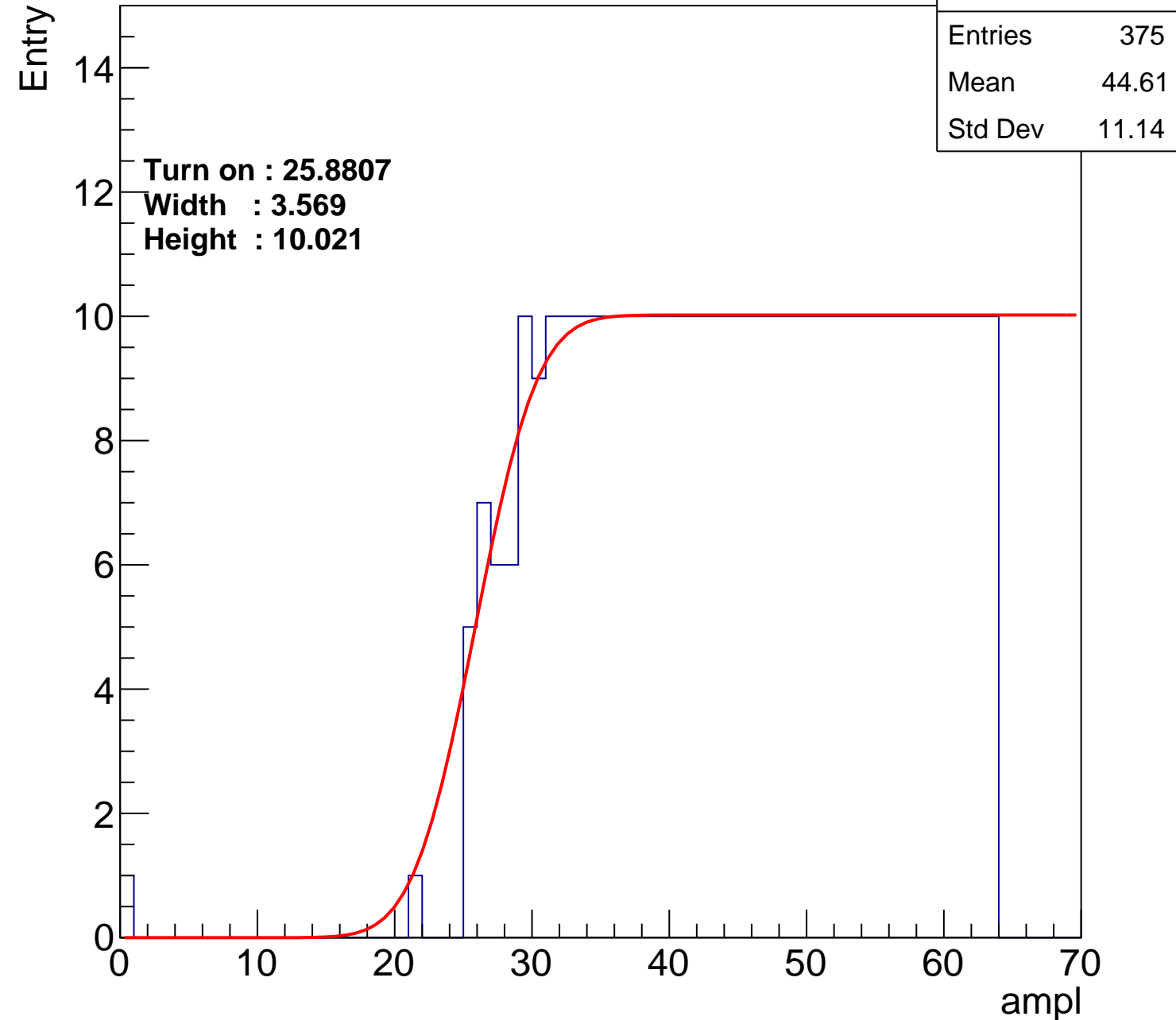
Width : 3.569

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.3
Std Dev	10.77

Turn on : 28.1531

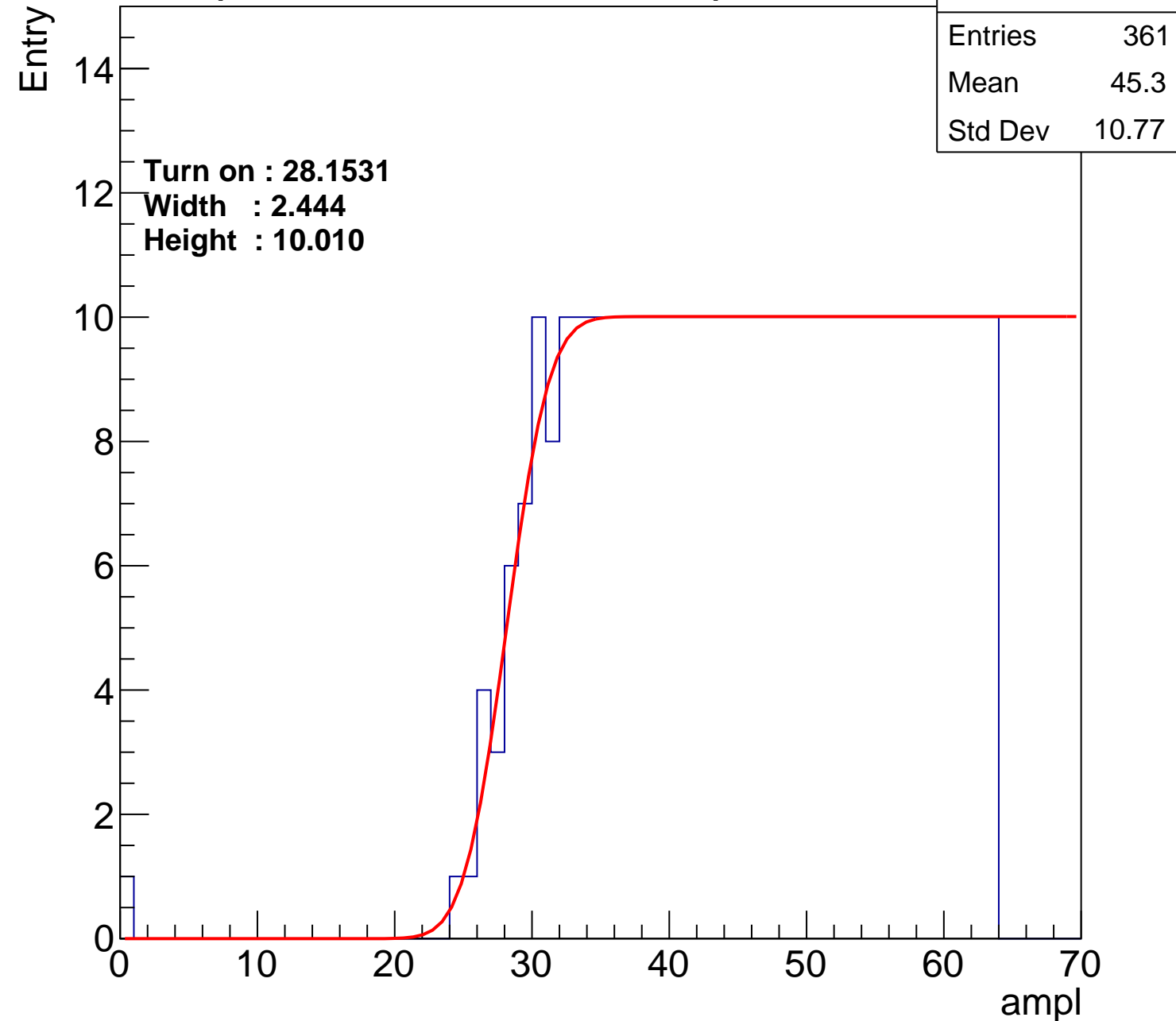
Width : 2.444

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.9915

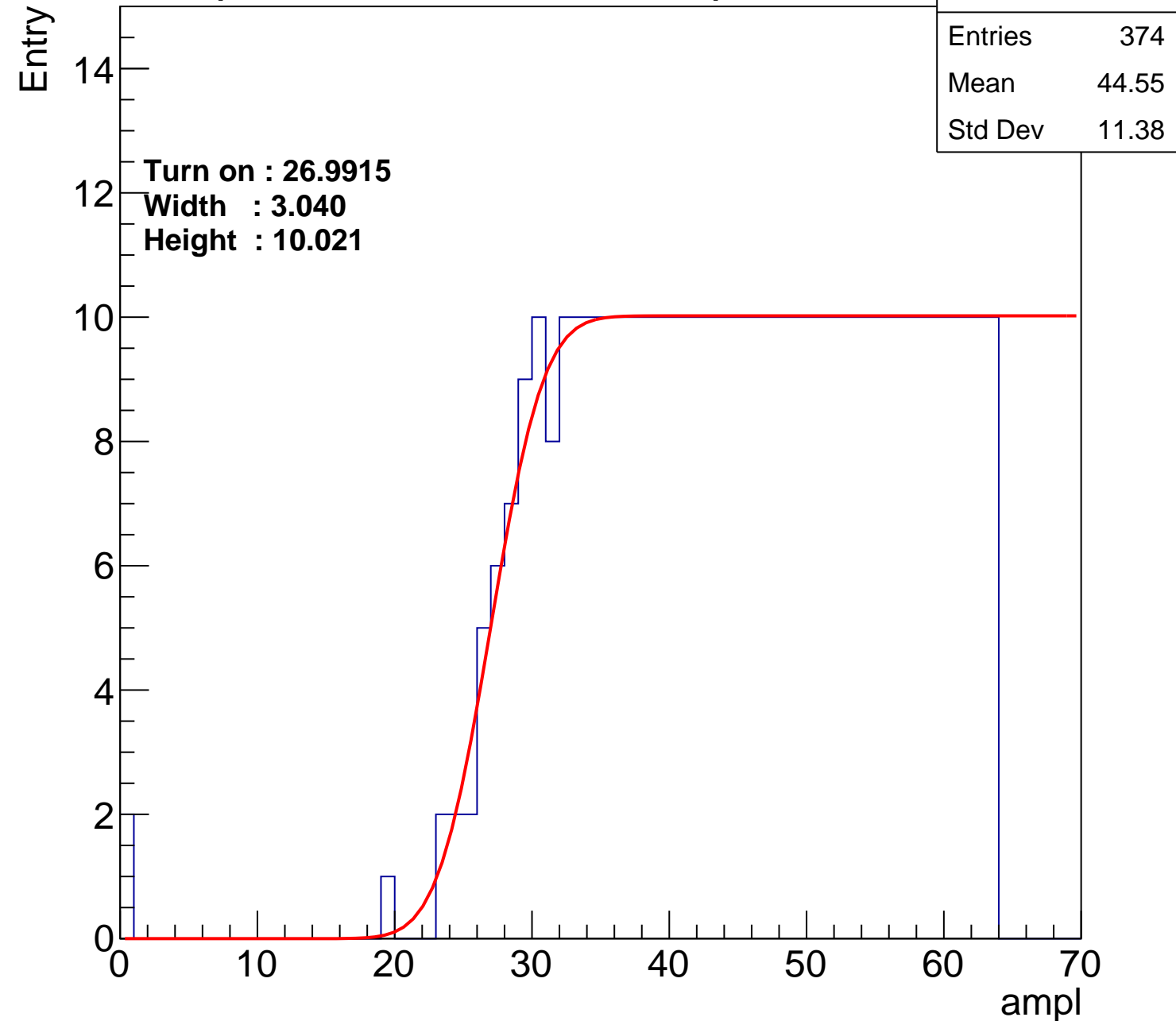
Width : 3.040

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.37
Std Dev	11.33

Turn on : 29.1775

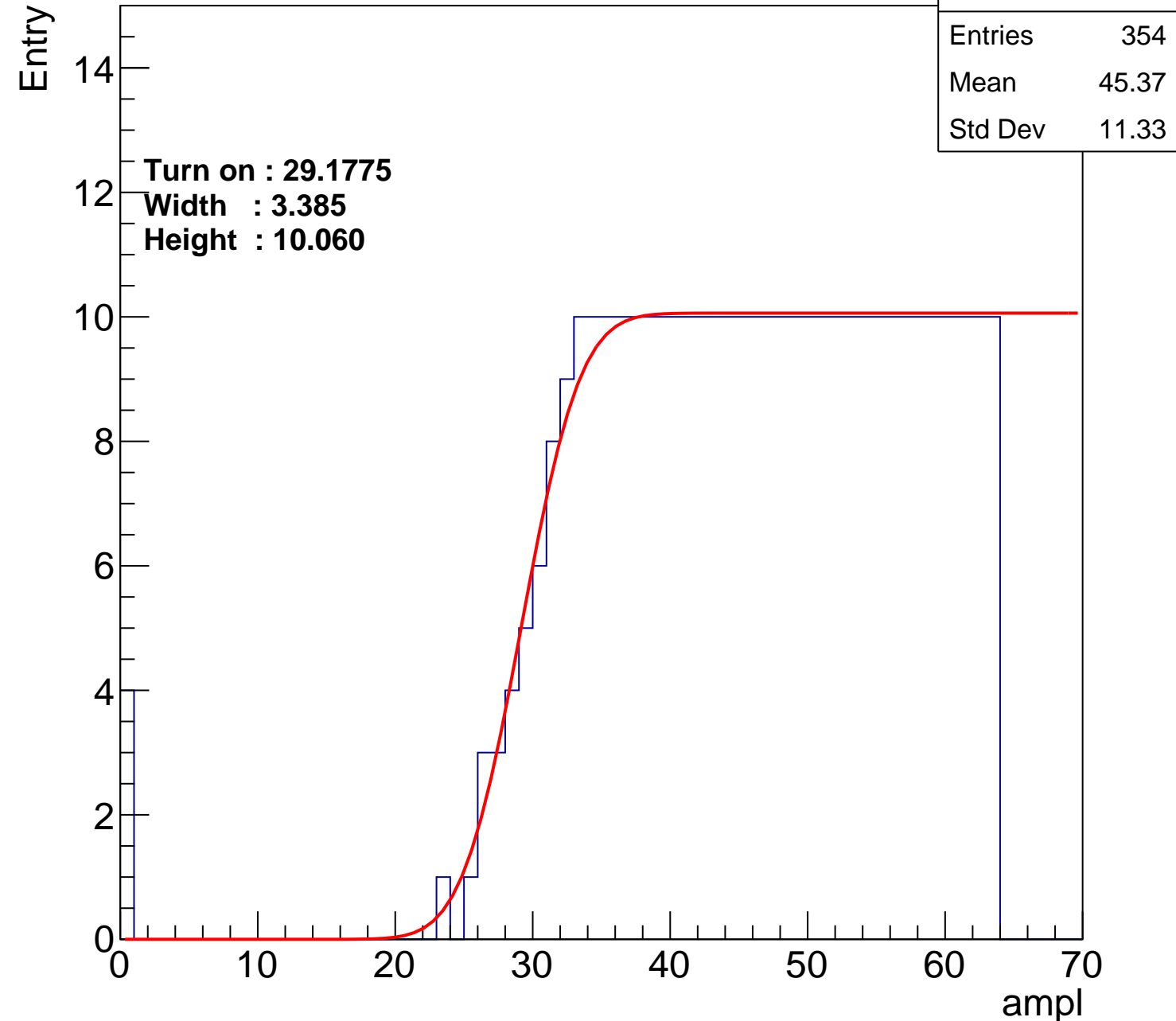
Width : 3.385

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl

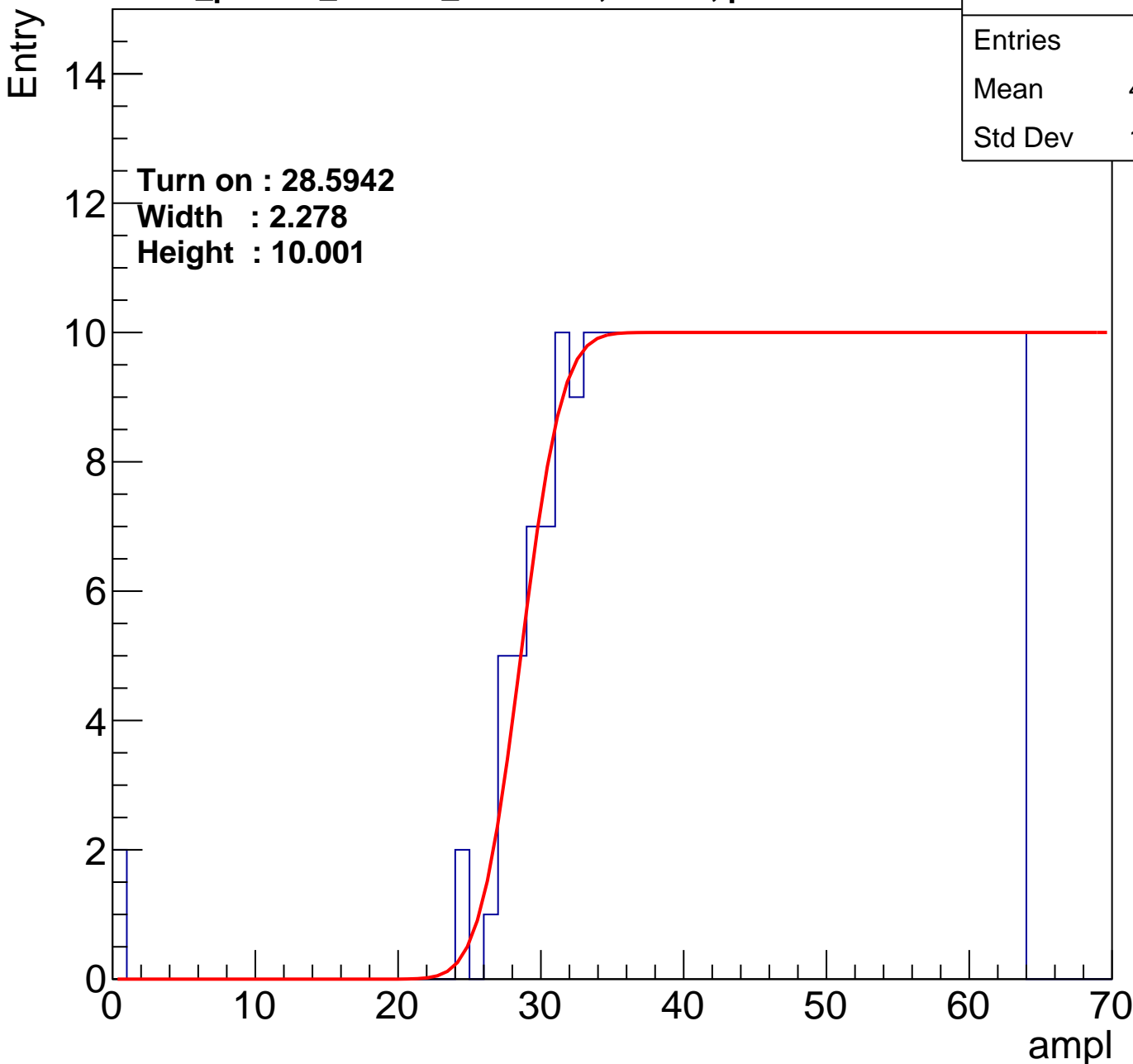


calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.36
Std Dev	10.93

Height : 10.001



B1L003S, U11-ch25

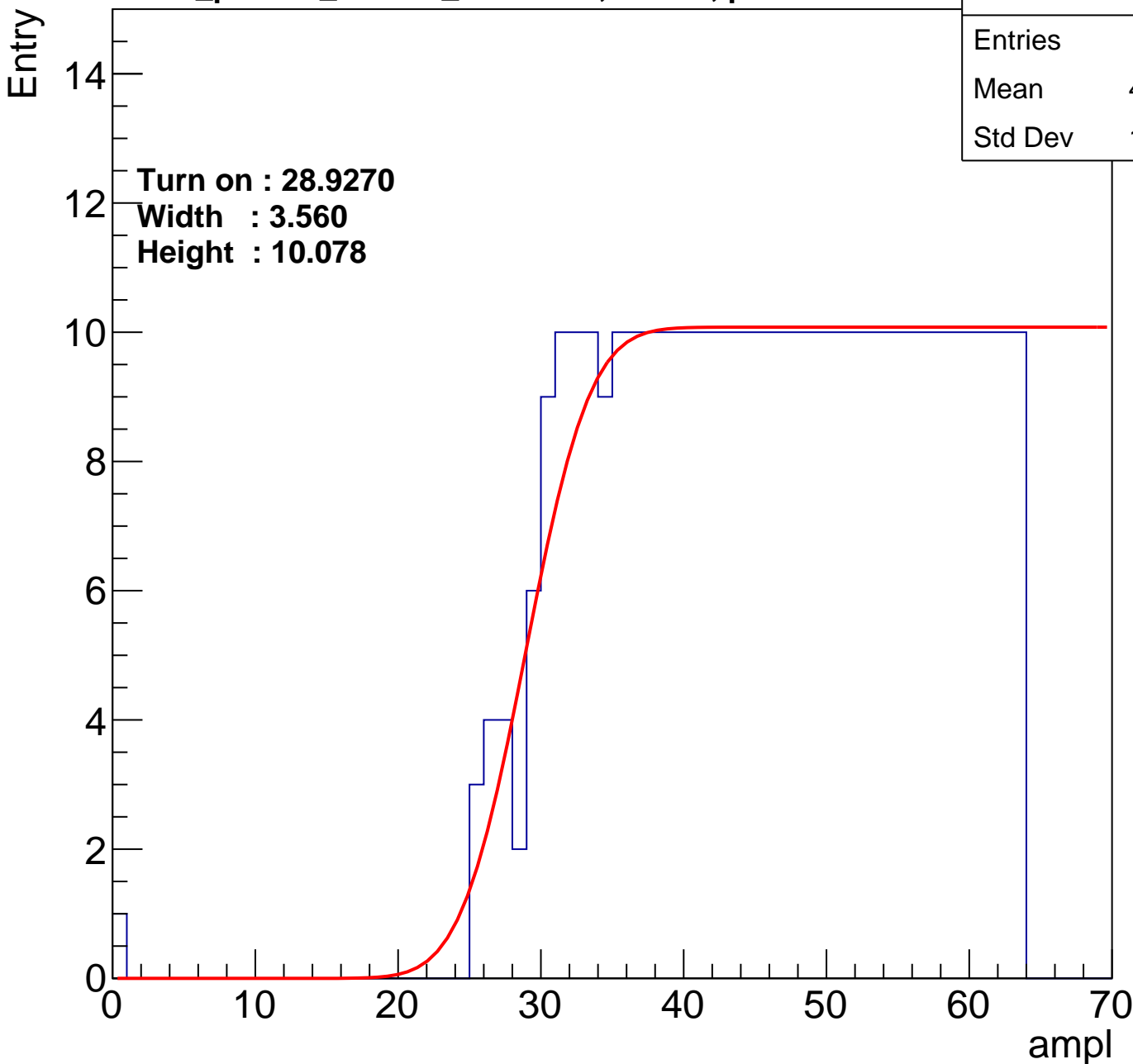
calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.9270

Width : 3.560

Height : 10.078



B1L003S, U11-ch26

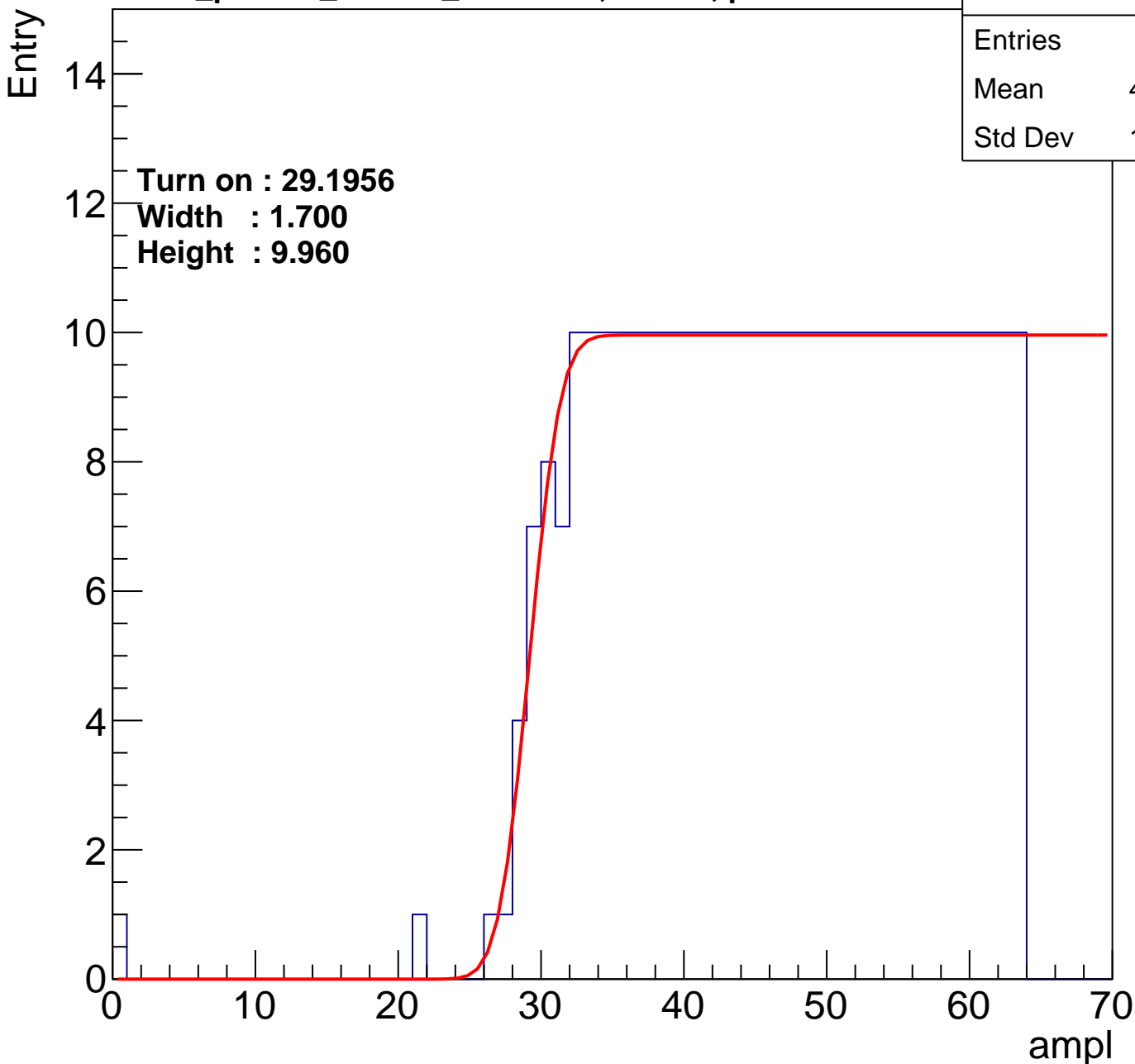
calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.85
Std Dev	10.48

Turn on : 29.1956

Width : 1.700

Height : 9.960



B1L003S, U11-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.36
Std Dev	11.08

Turn on : 28.5830

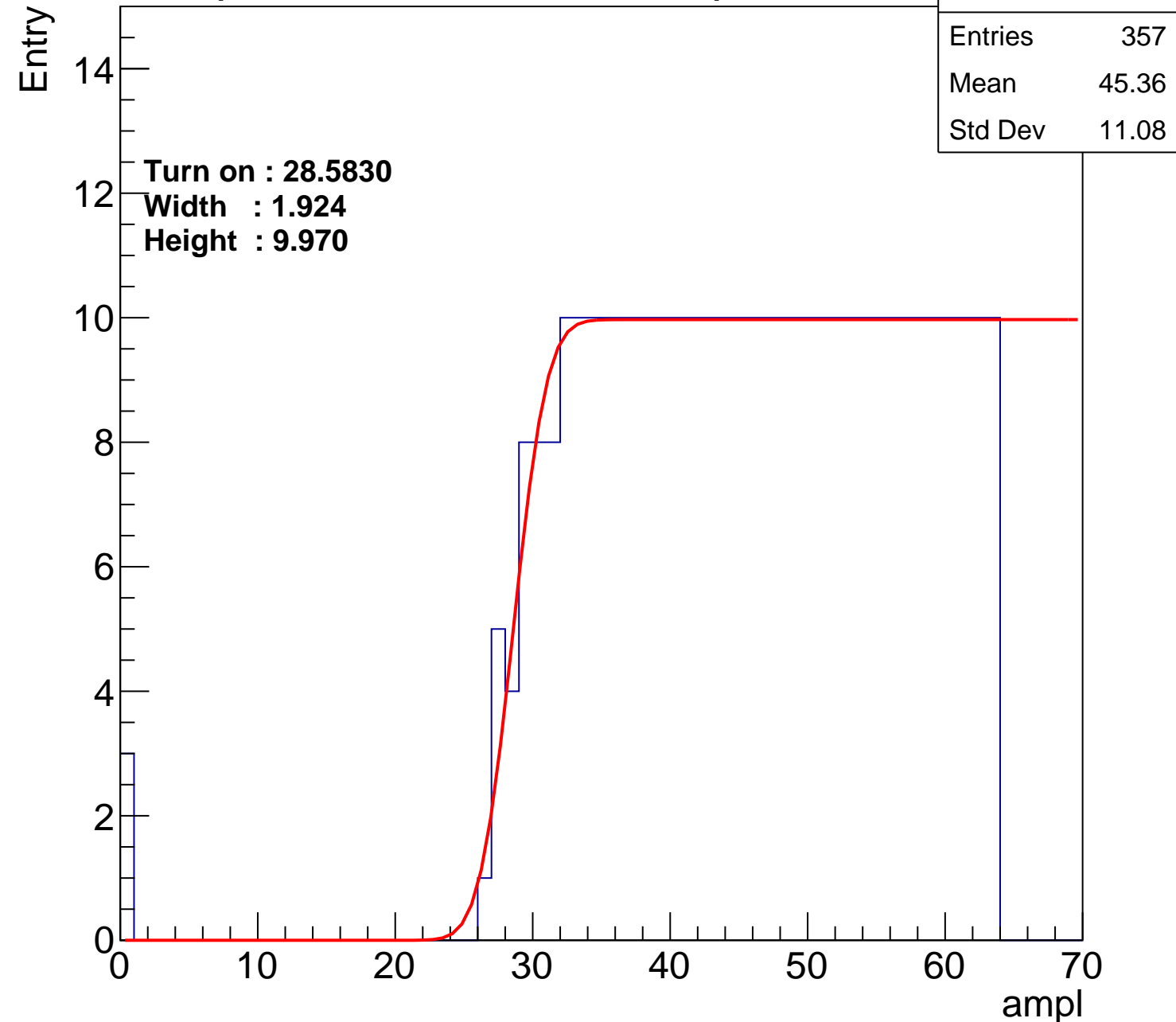
Width : 1.924

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.83
Std Dev	11.34

Turn on : 27.4168

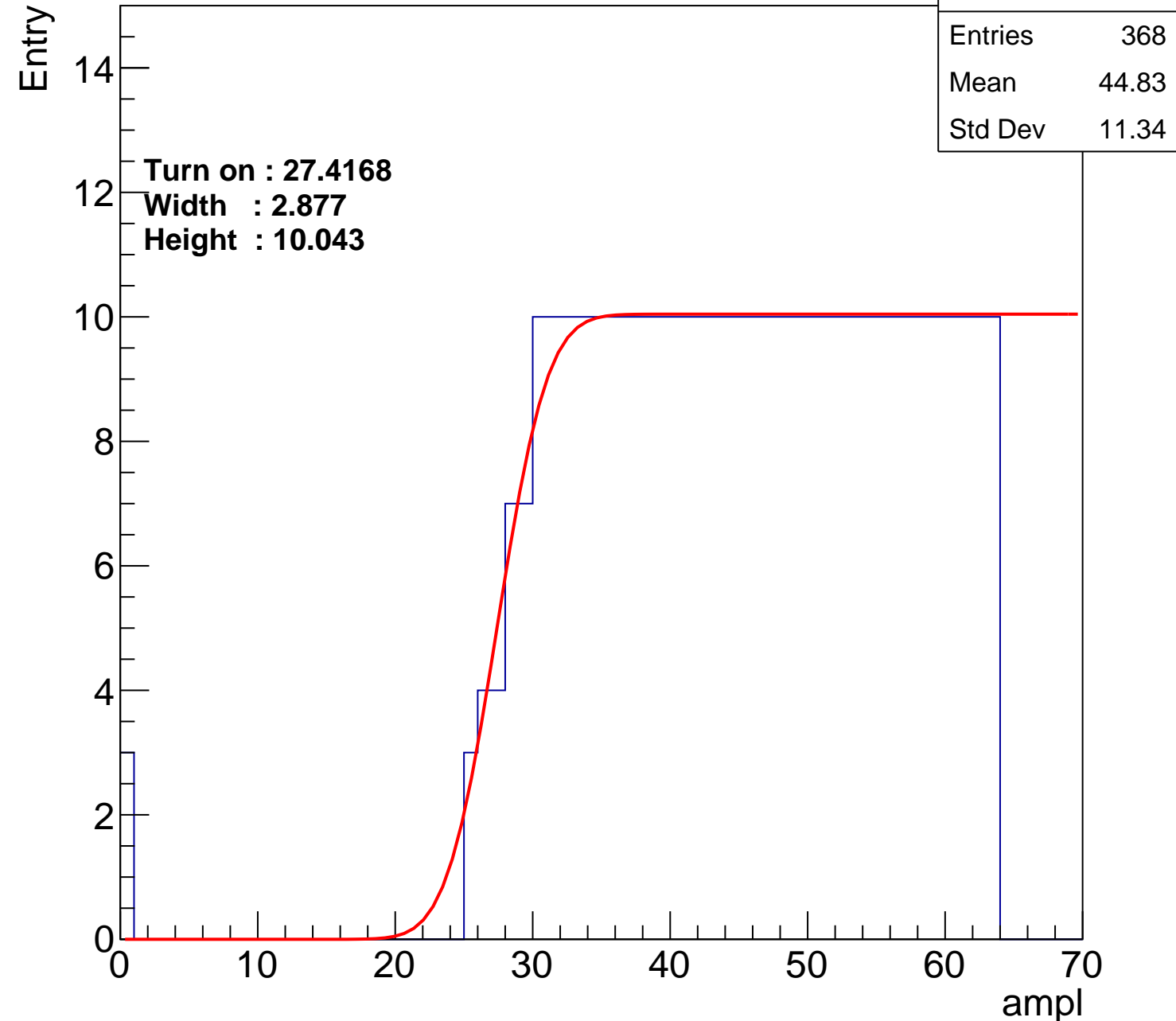
Width : 2.877

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 28.5109

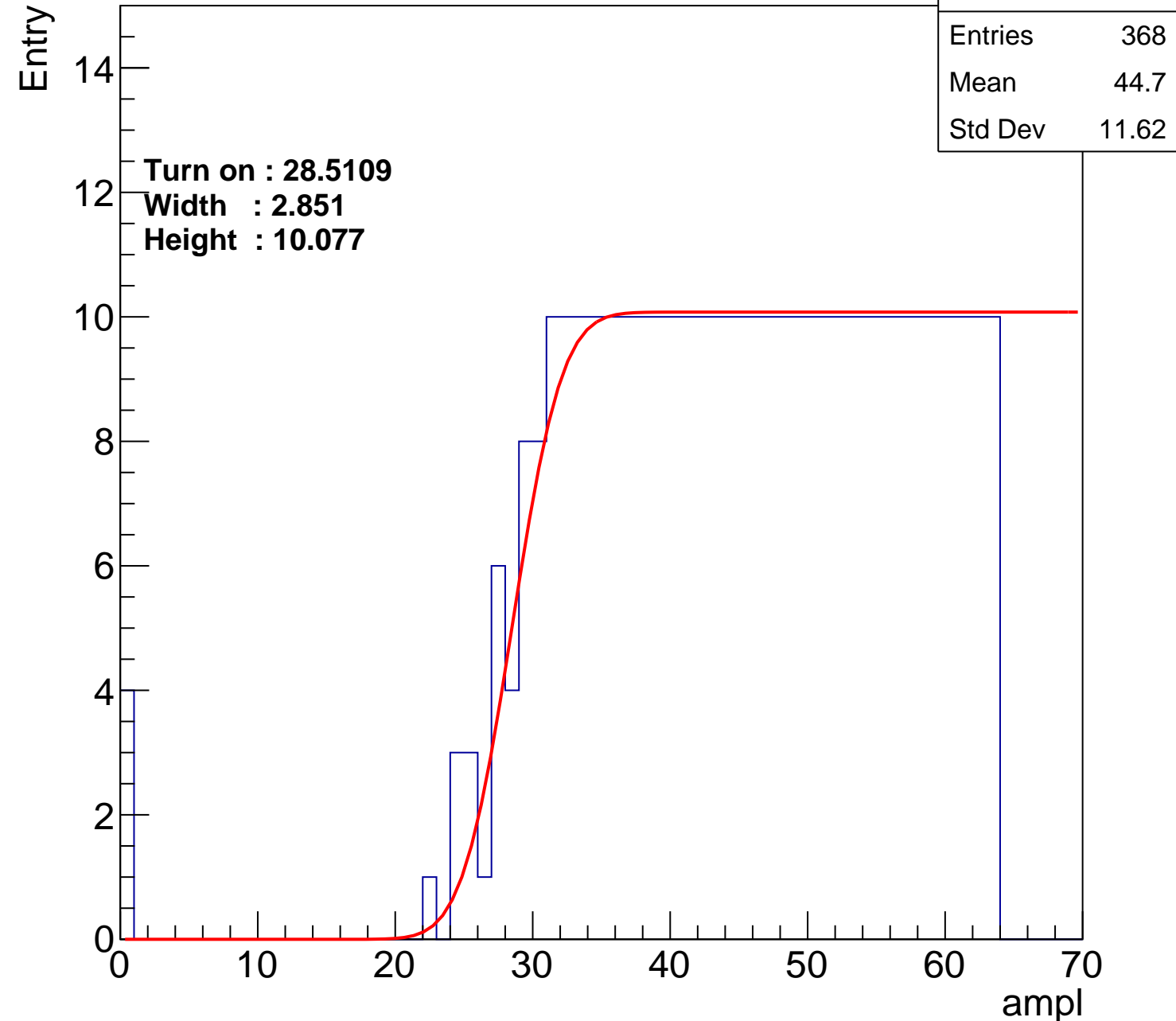
Width : 2.851

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.6003

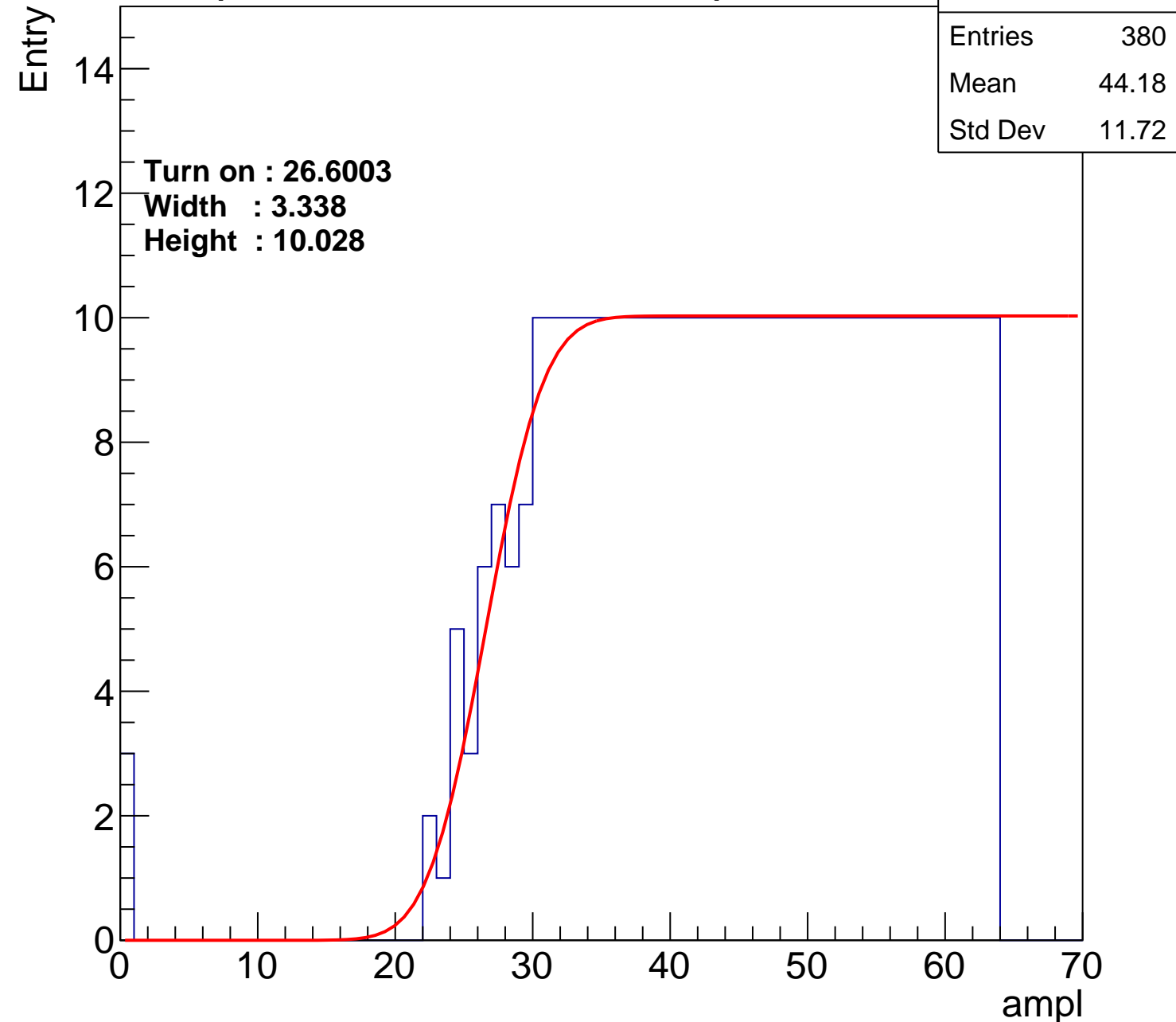
Width : 3.338

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 27.2192

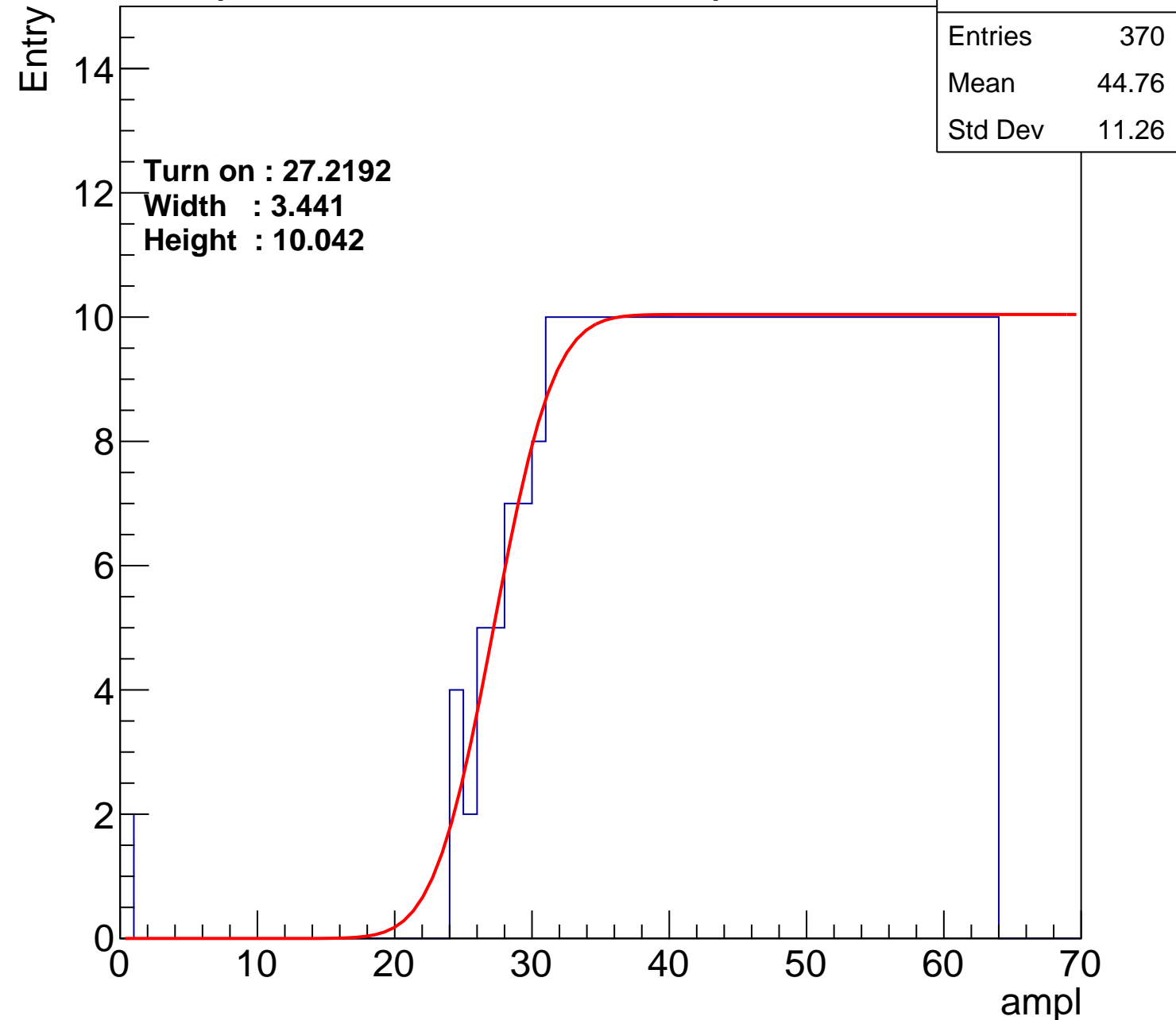
Width : 3.441

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.42
Std Dev	11.31

Turn on : 29.8902

Width : 3.835

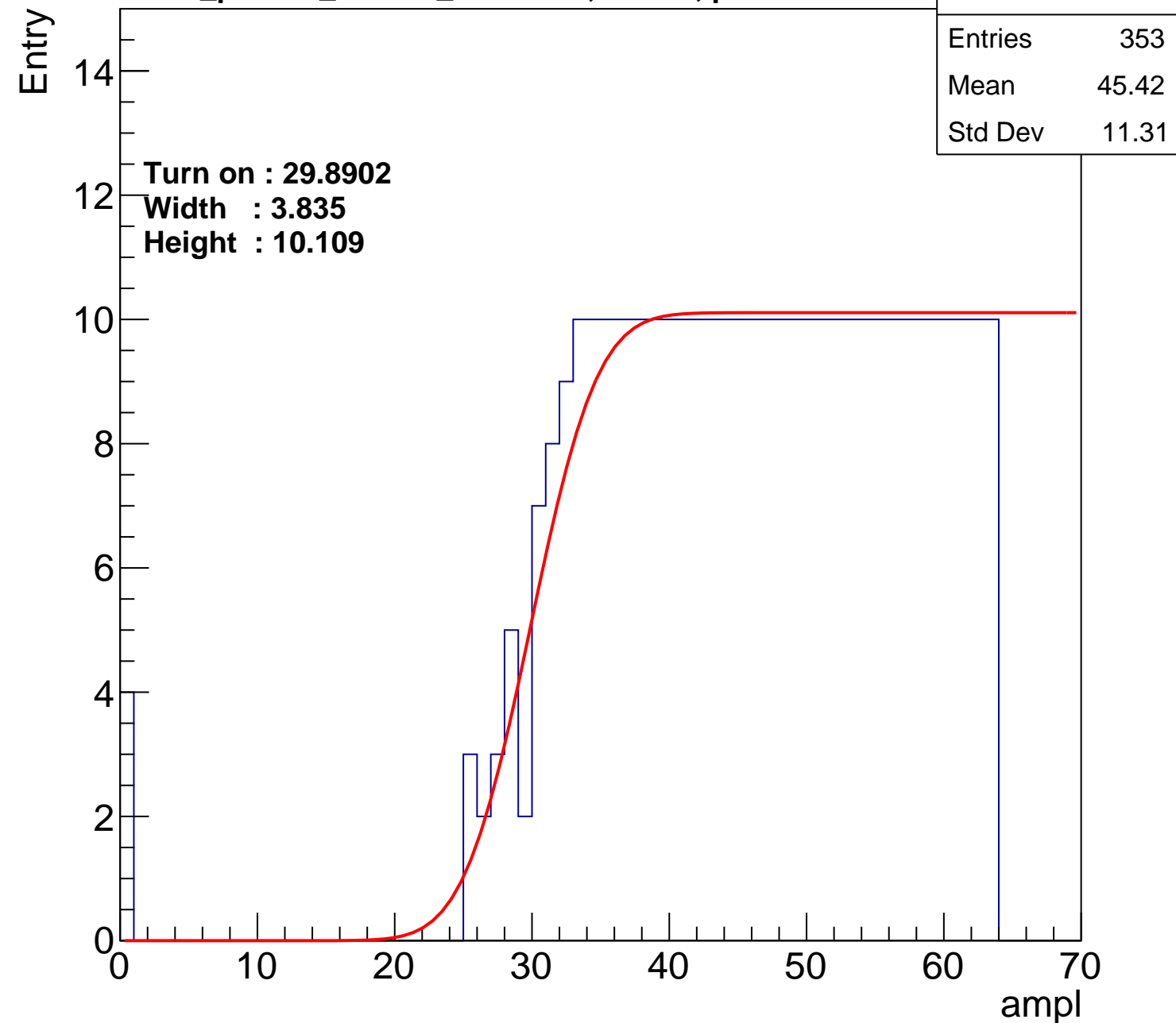
Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U11-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.48
Std Dev	10.91

Turn on : 29.5639

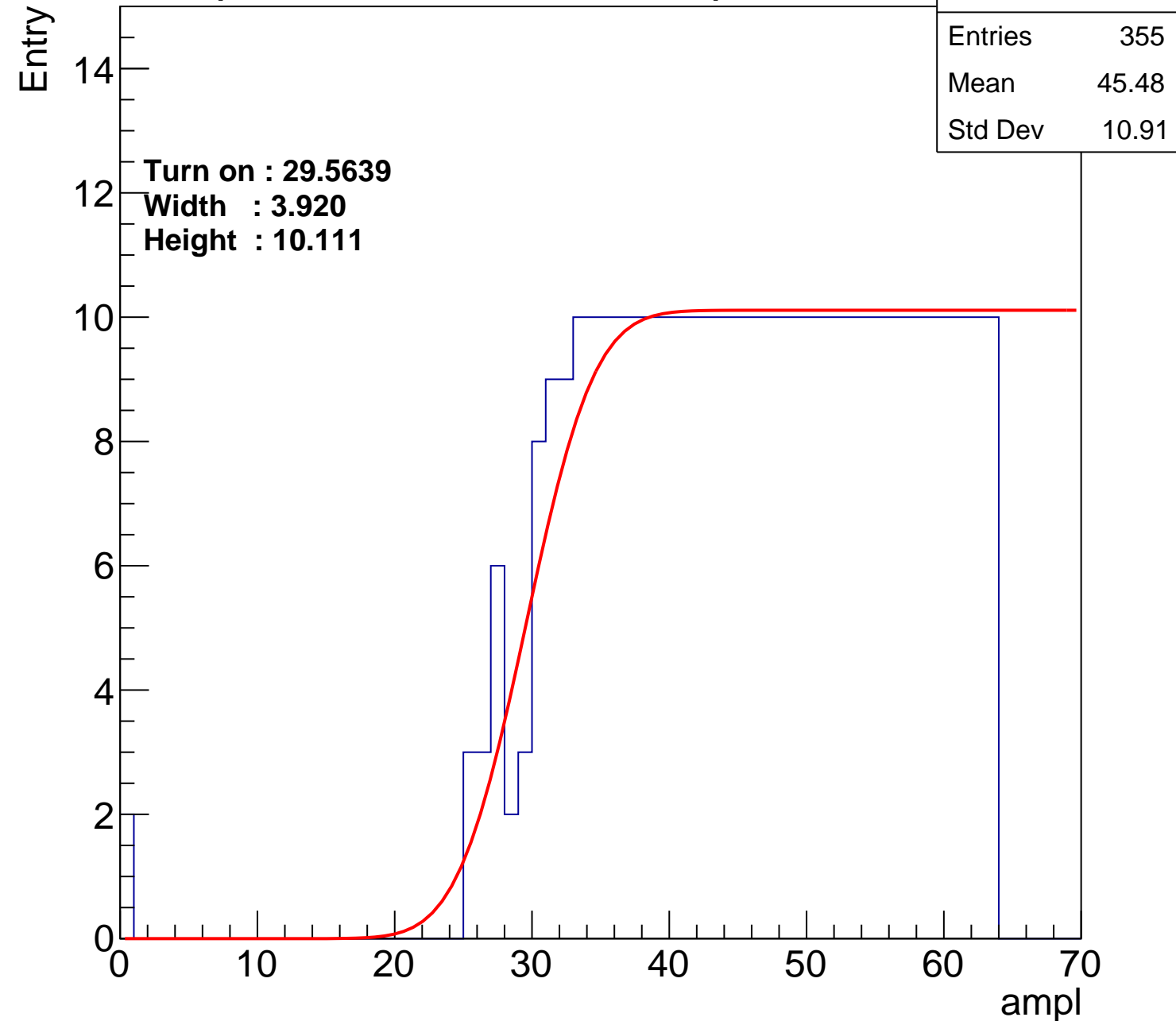
Width : 3.920

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 28.0143

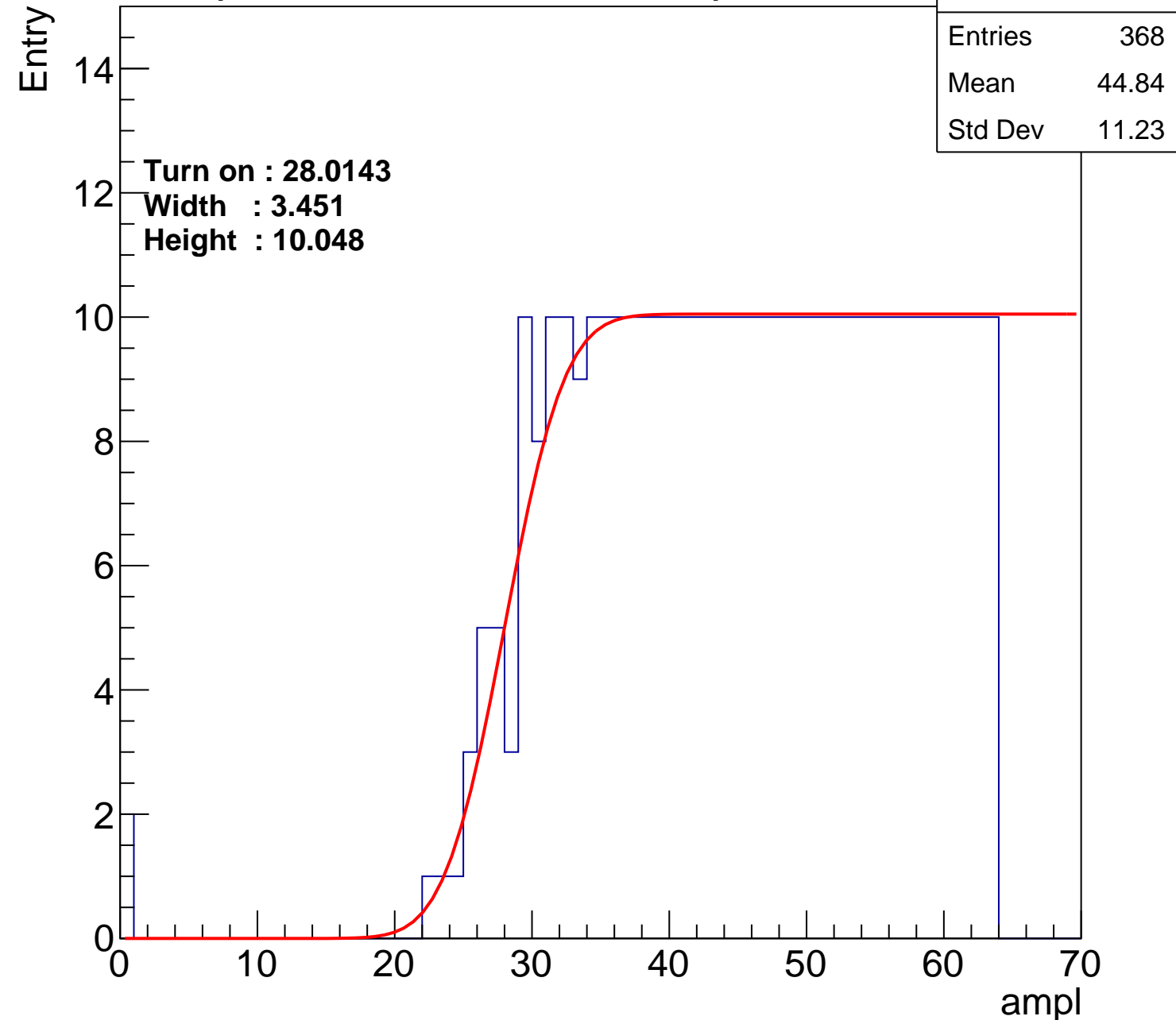
Width : 3.451

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch35

calib_packv5_042523_0143.root, FC#13, port D2

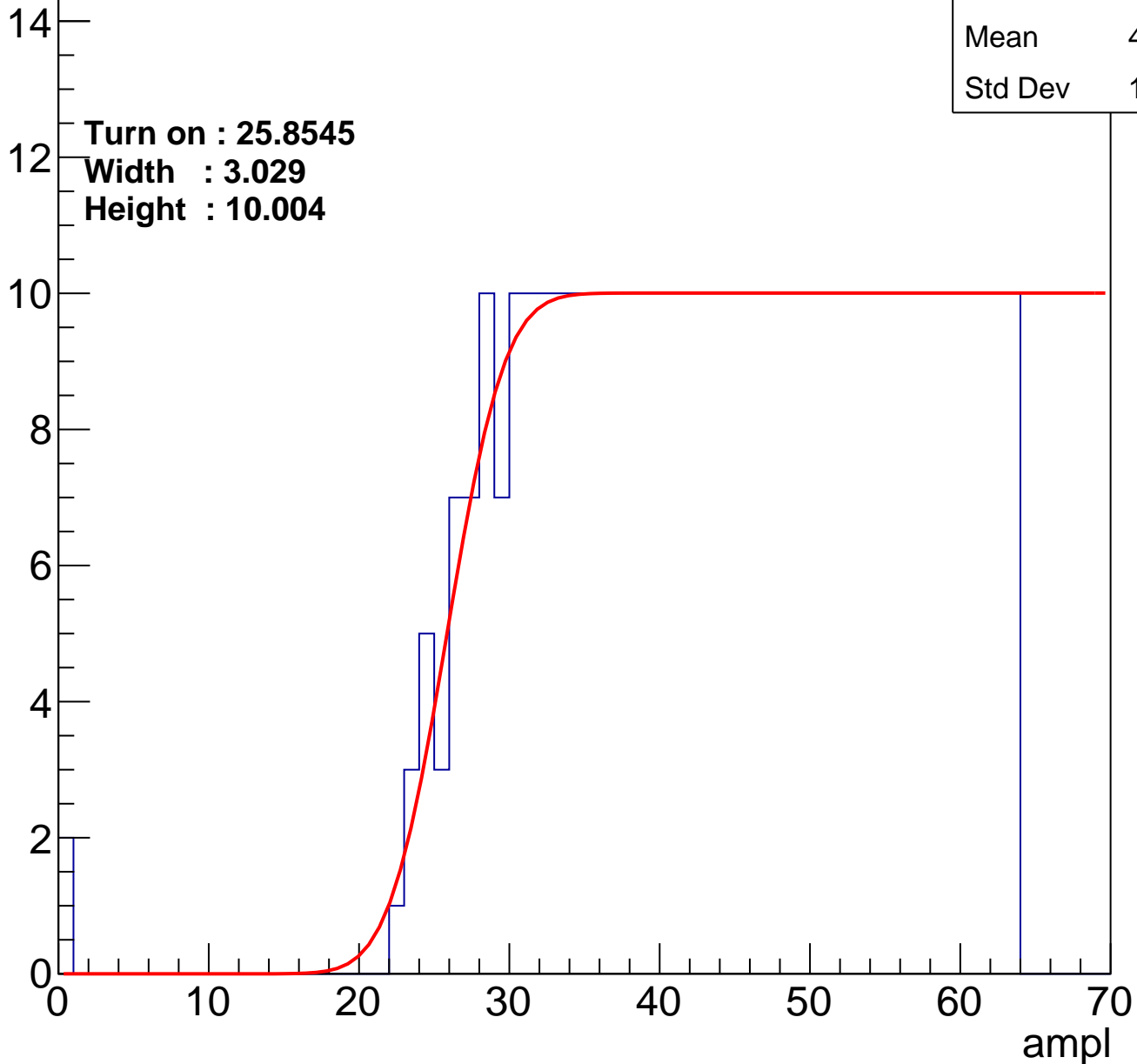
Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.8545

Width : 3.029

Height : 10.004

Entry



B1L003S, U11-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.72
Std Dev	10.79

Turn on : 29.8524

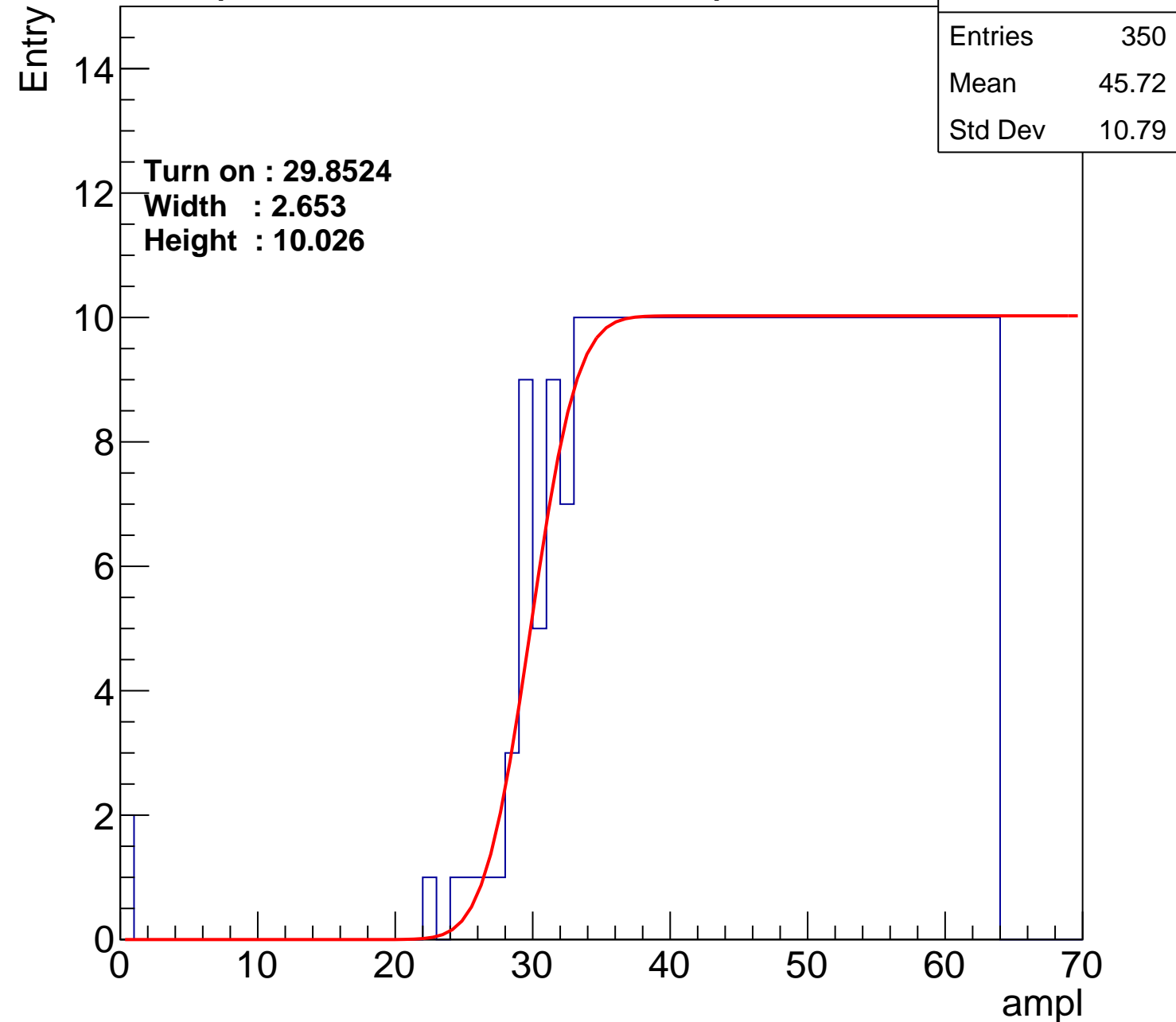
Width : 2.653

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.98
Std Dev	12.08

Turn on : 26.3617

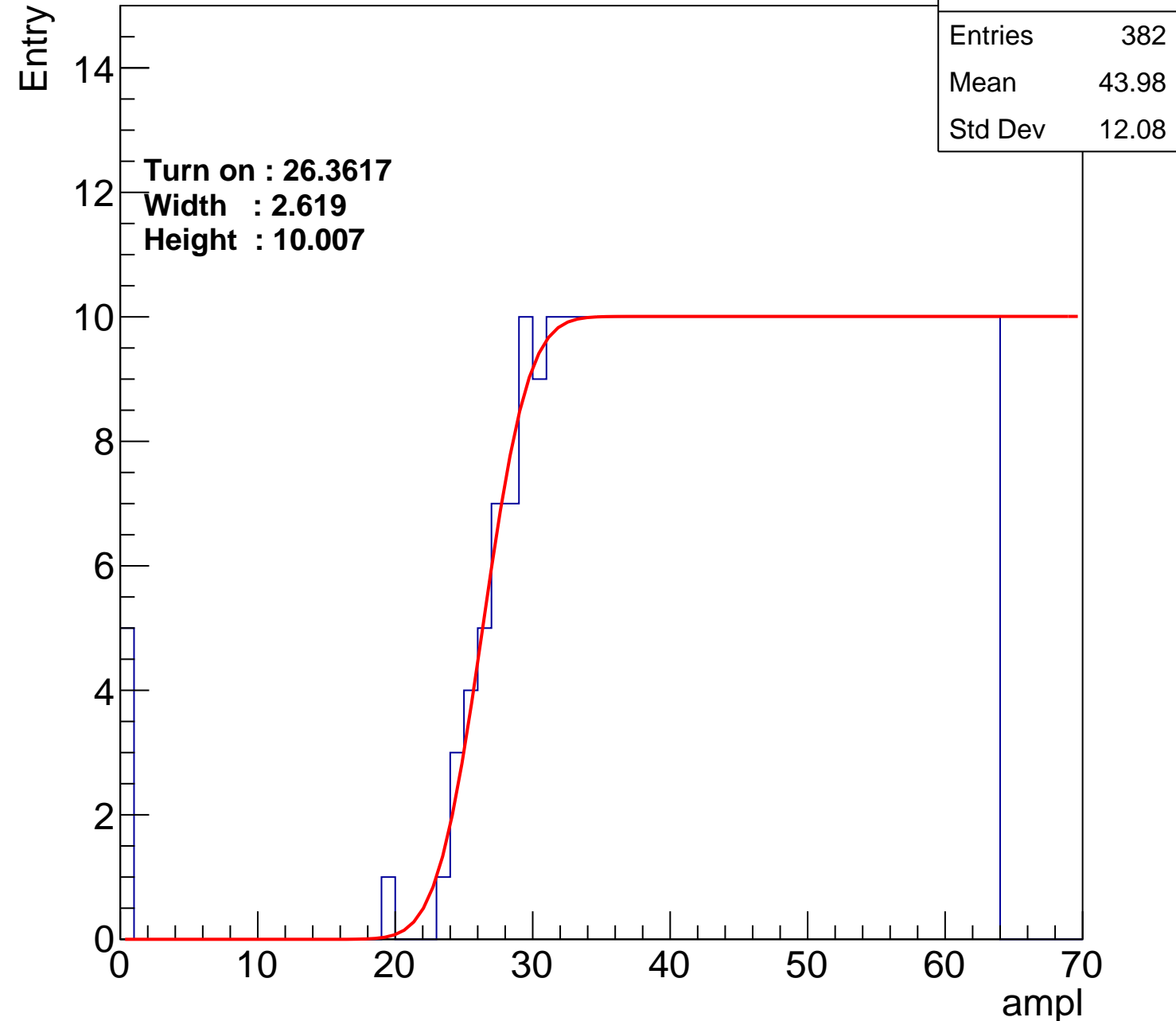
Width : 2.619

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch38

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.72
Std Dev	11.25

Turn on : 26.6378

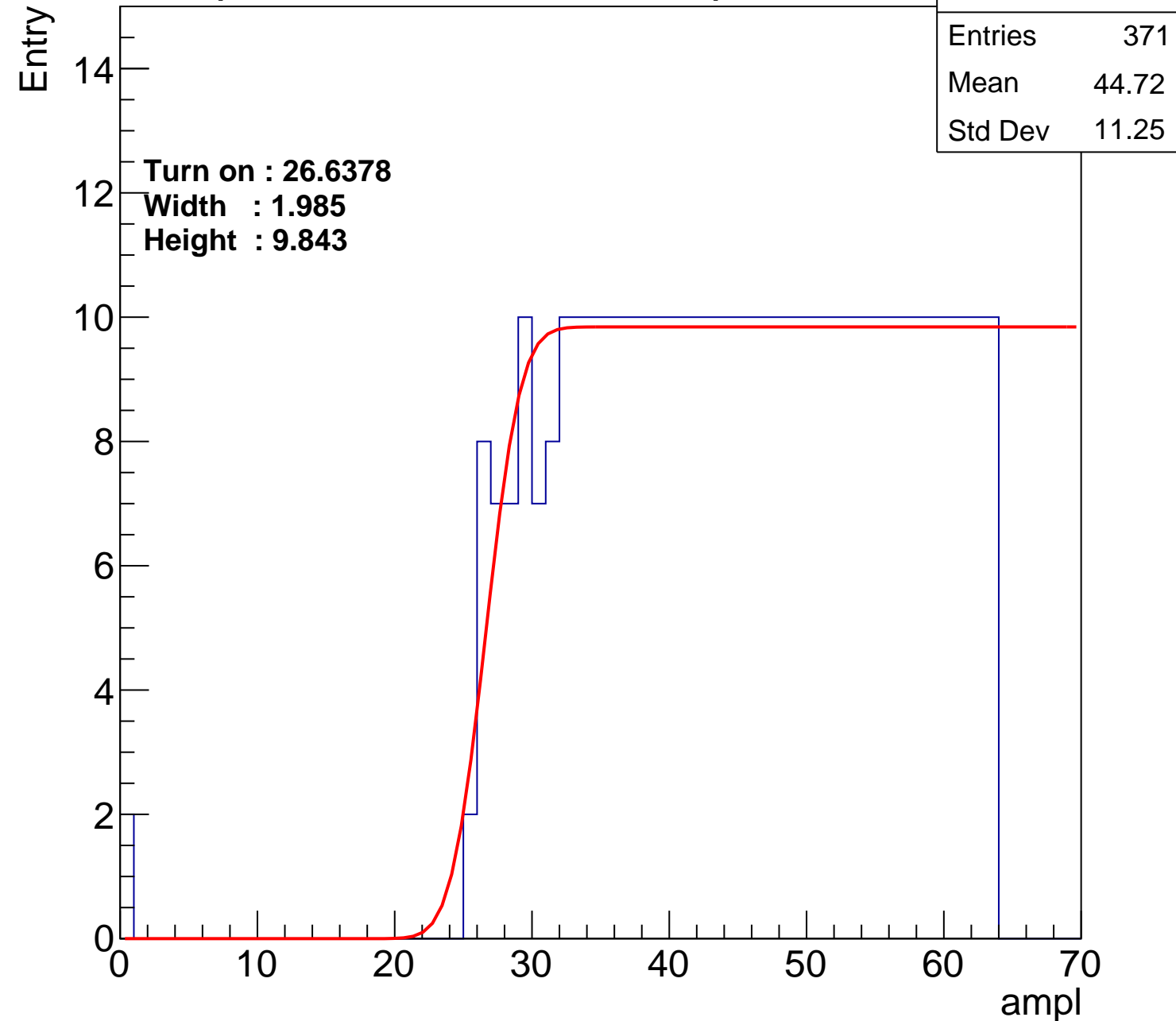
Width : 1.985

Height : 9.843

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch39

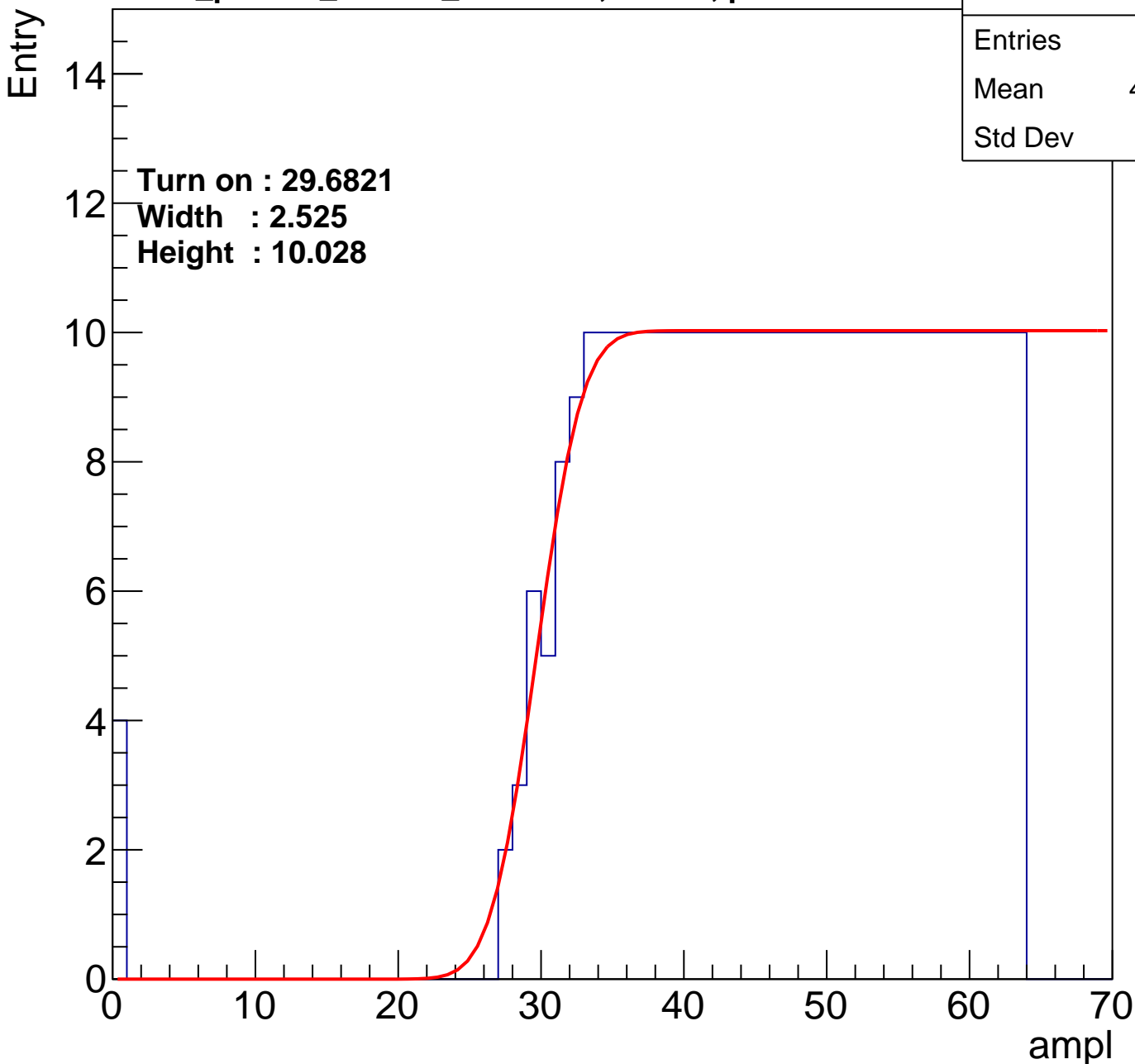
calib_packv5_042523_0143.root, FC#13, port D2

Entries	347
Mean	45.76
Std Dev	11.1

Turn on : 29.6821

Width : 2.525

Height : 10.028



B1L003S, U11-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.68
Std Dev	10.58

Turn on : 28.8485

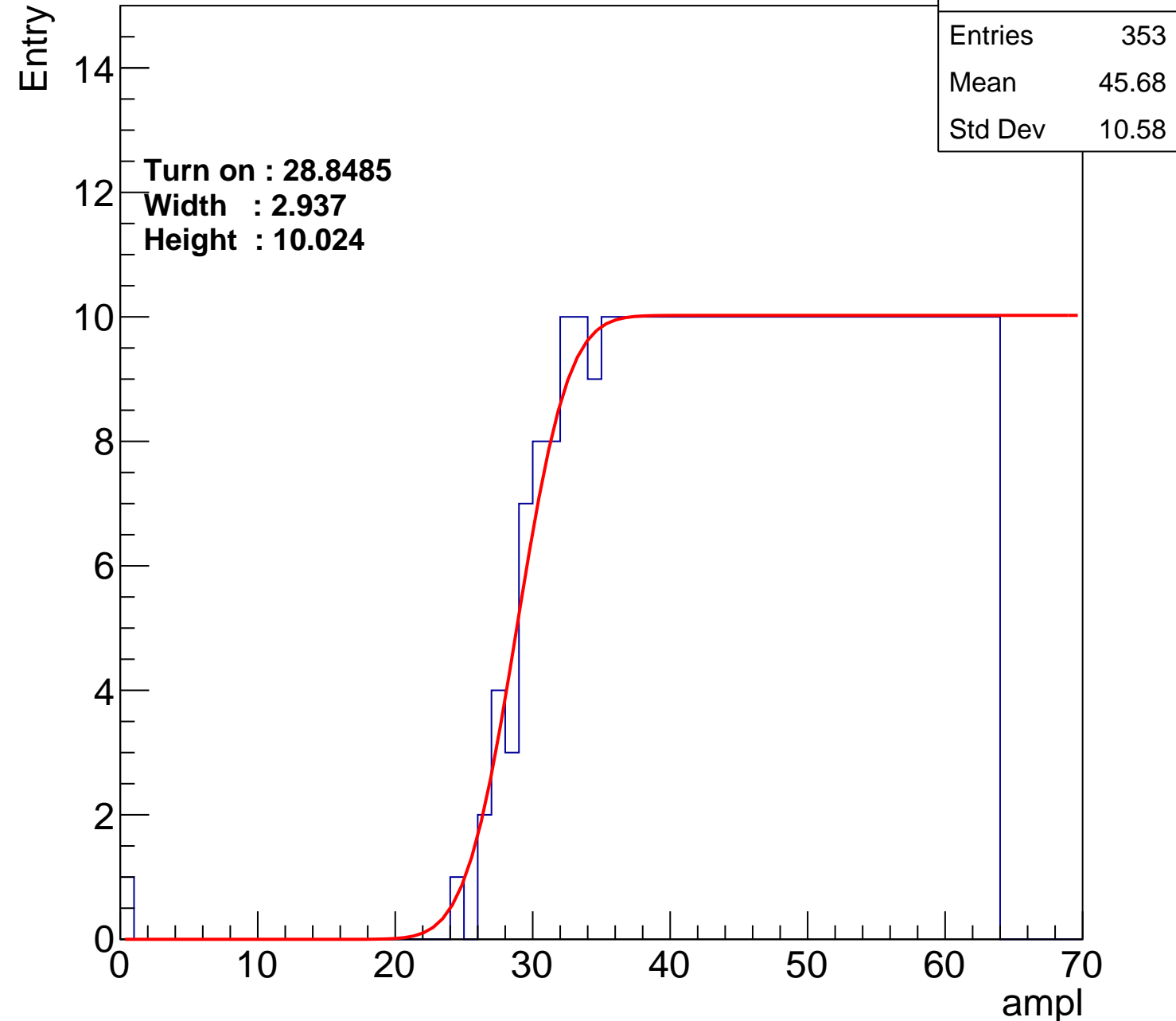
Width : 2.937

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch41

calib_packv5_042523_0143.root, FC#13, port D2

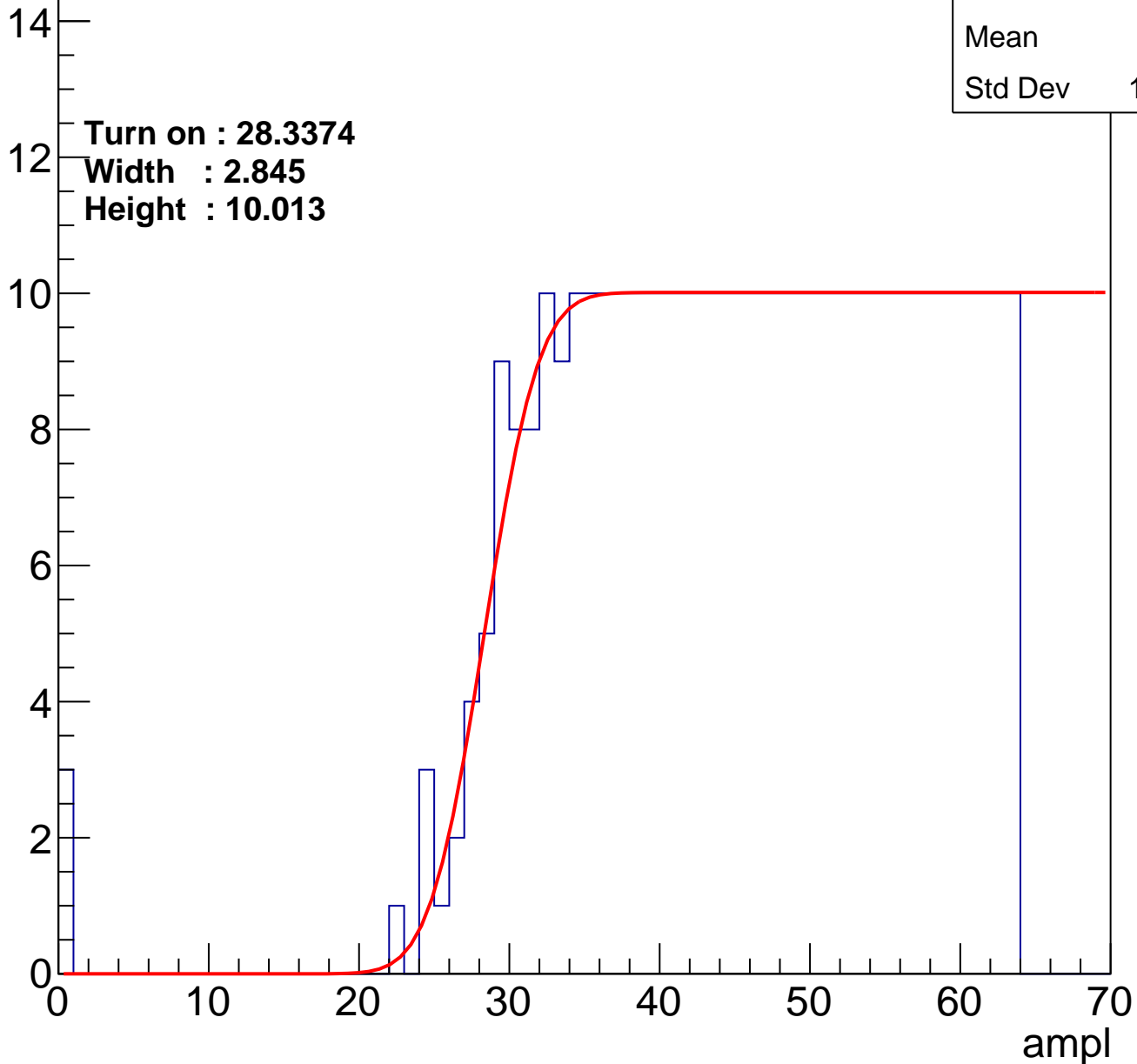
Entries	363
Mean	45
Std Dev	11.33

Turn on : 28.3374

Width : 2.845

Height : 10.013

Entry



B1L003S, U11-ch42

calib_packv5_042523_0143.root, FC#13, port D2

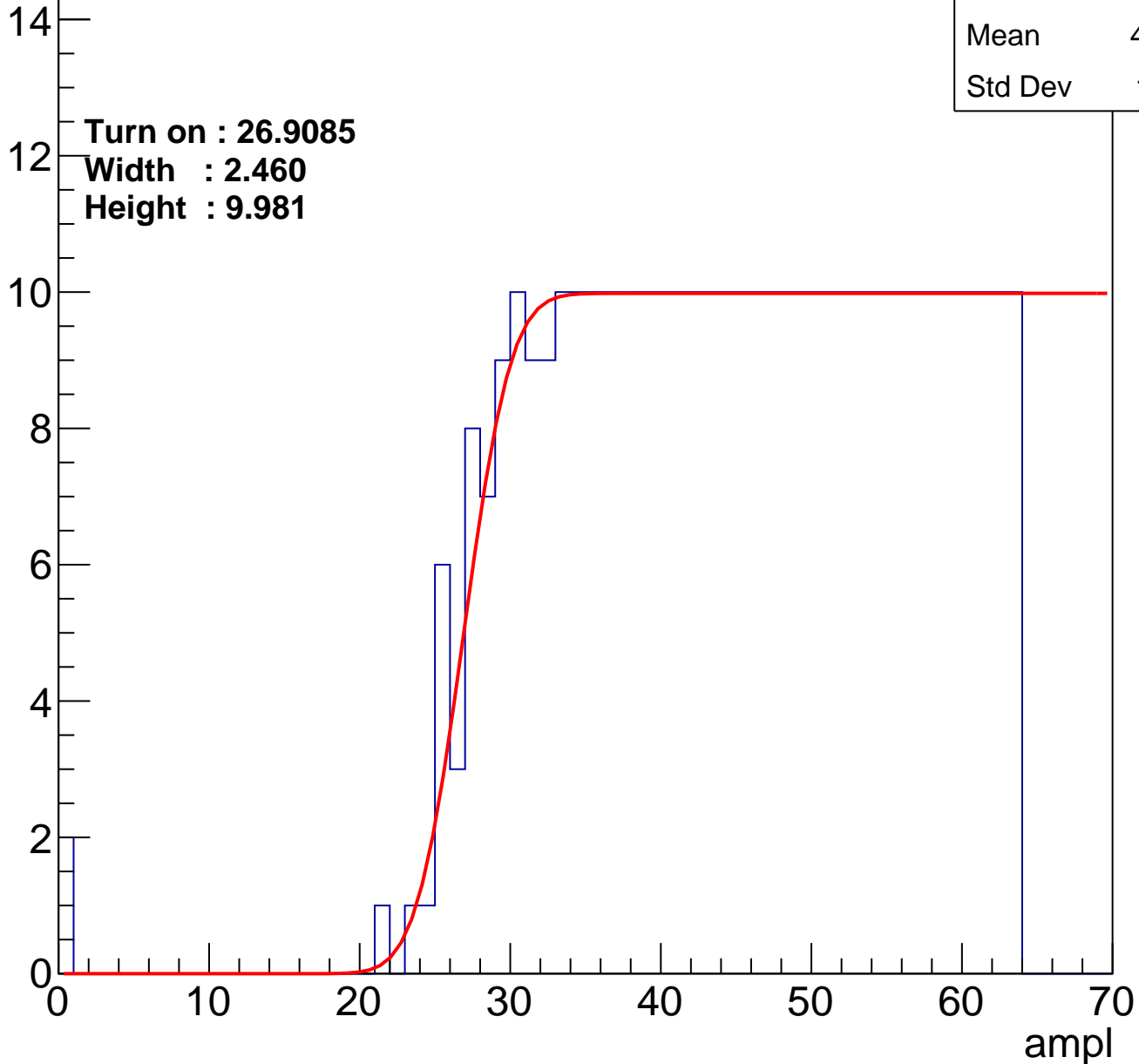
Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.9085

Width : 2.460

Height : 9.981

Entry



B1L003S, U11-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.77
Std Dev	11.43

Turn on : 27.8828

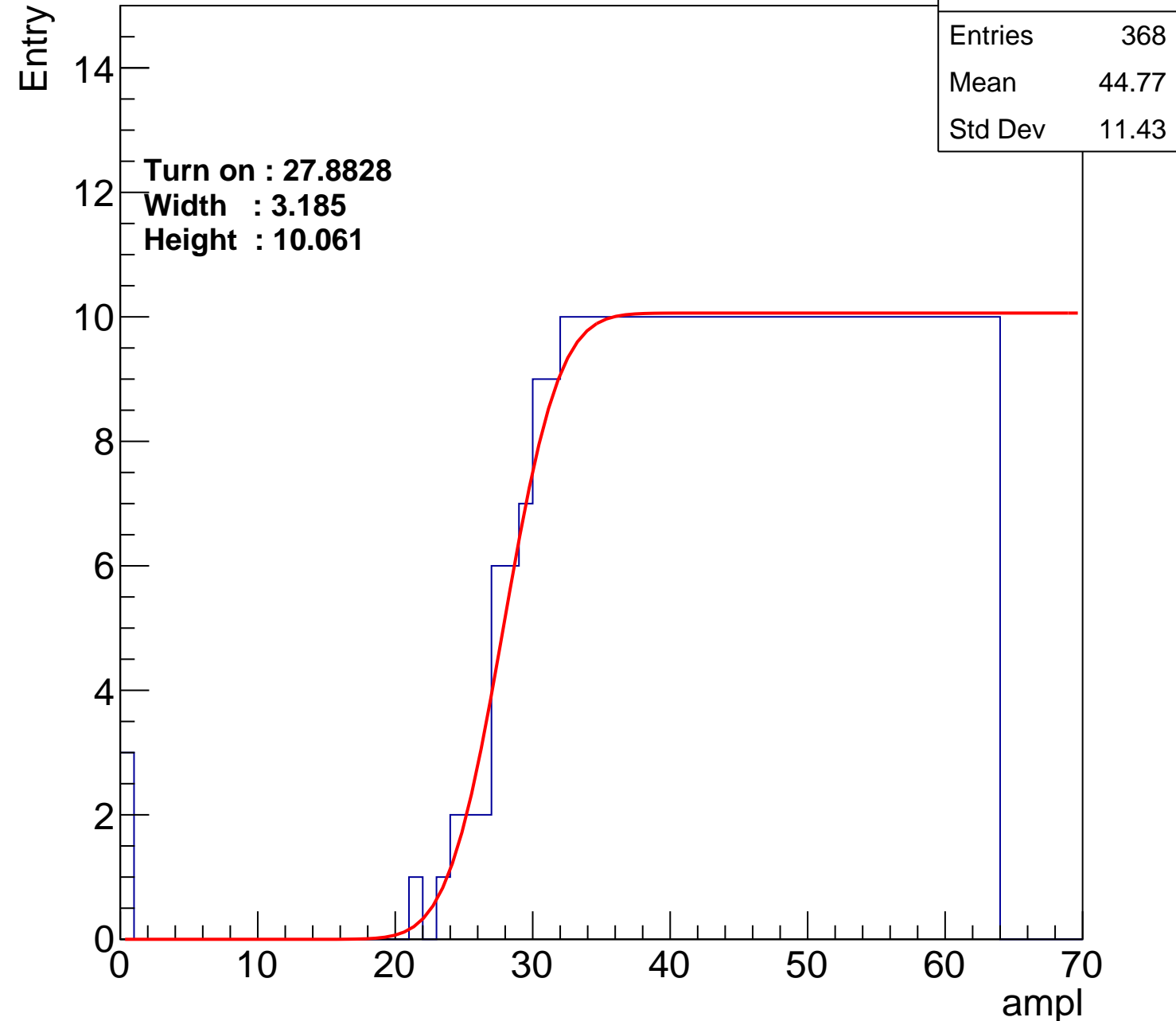
Width : 3.185

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch44

calib_packv5_042523_0143.root, FC#13, port D2

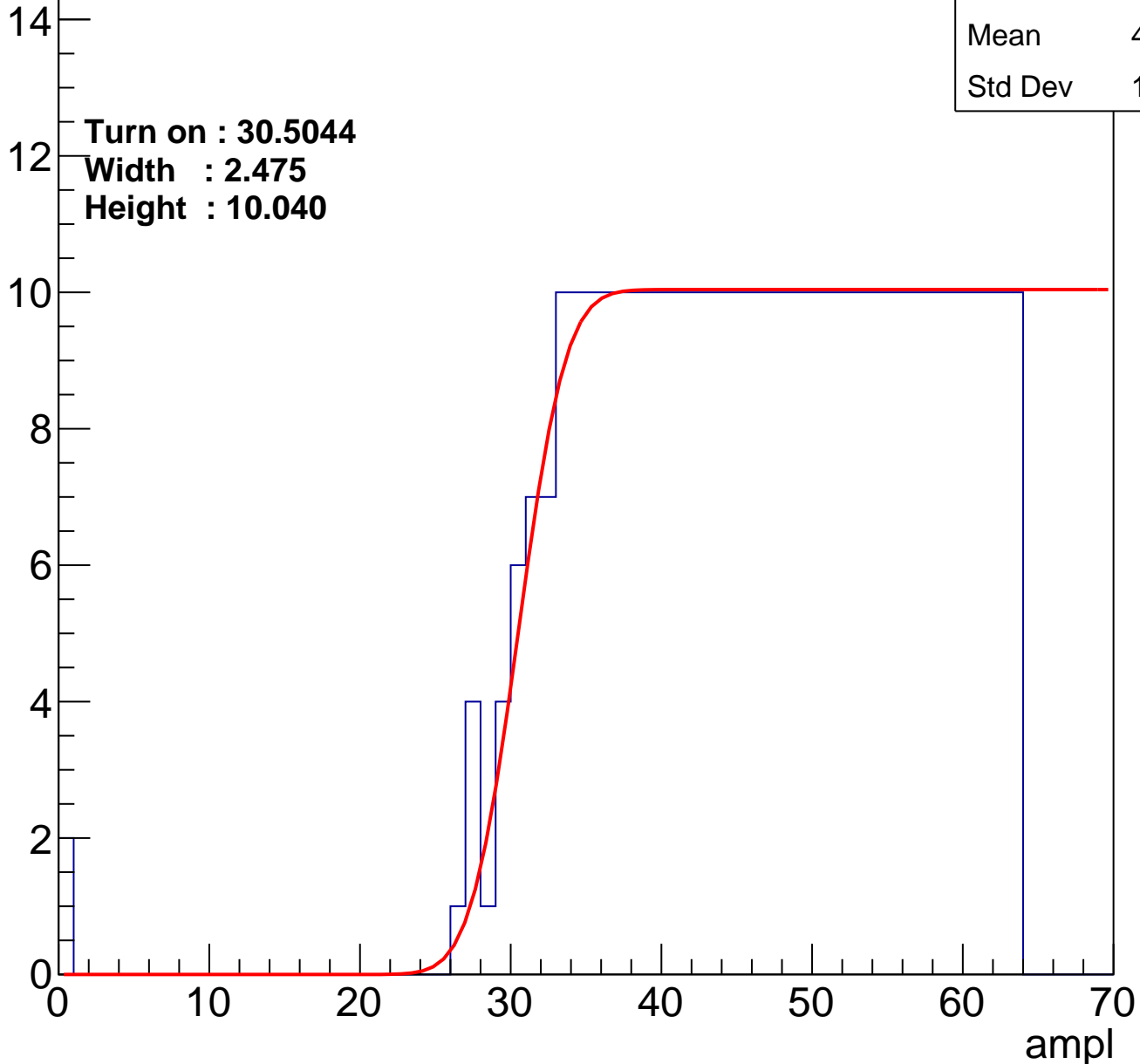
Entries	342
Mean	46.14
Std Dev	10.55

Turn on : 30.5044

Width : 2.475

Height : 10.040

Entry



B1L003S, U11-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 26.7594

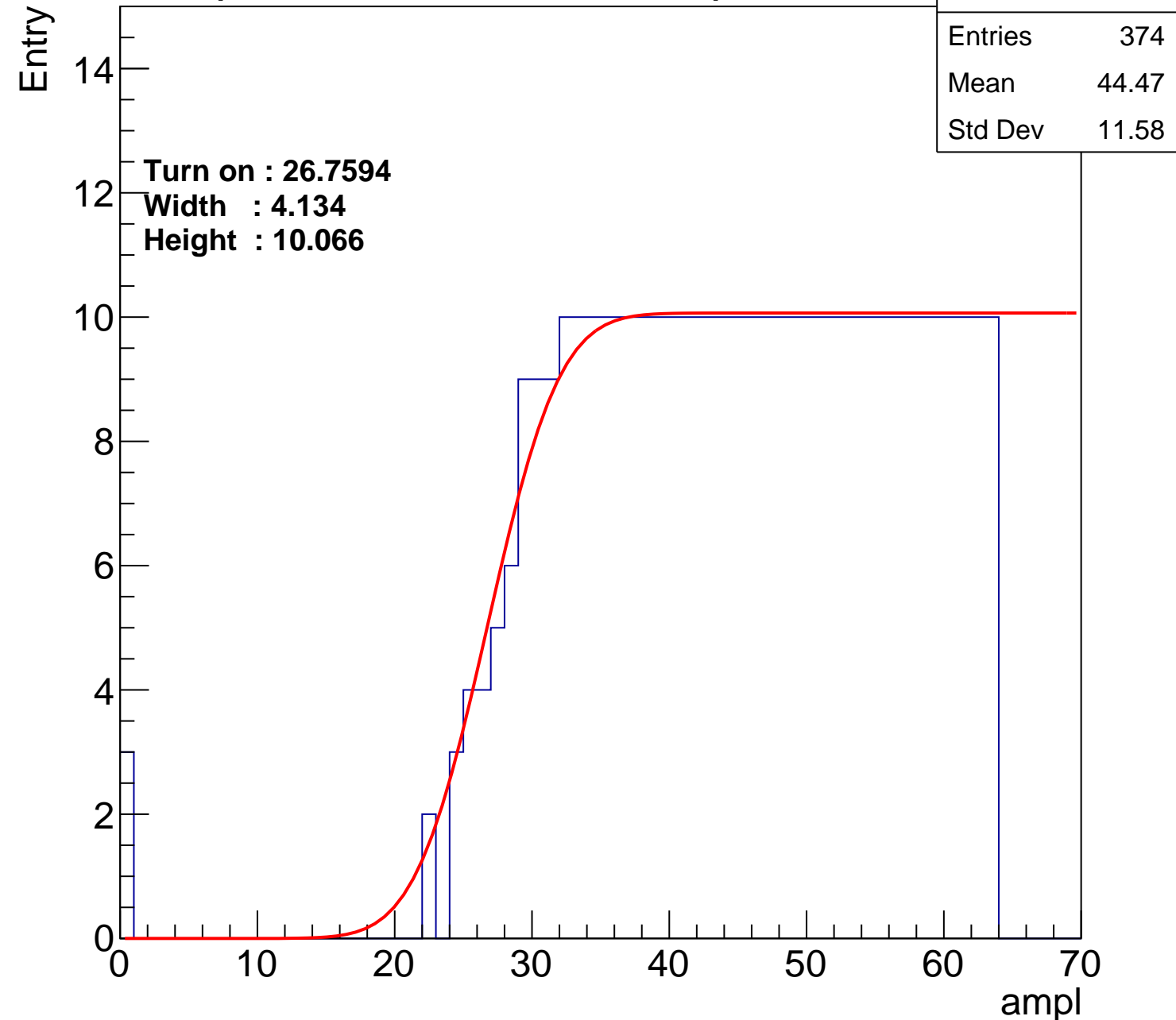
Width : 4.134

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.83
Std Dev	11.86

Turn on : 25.7594

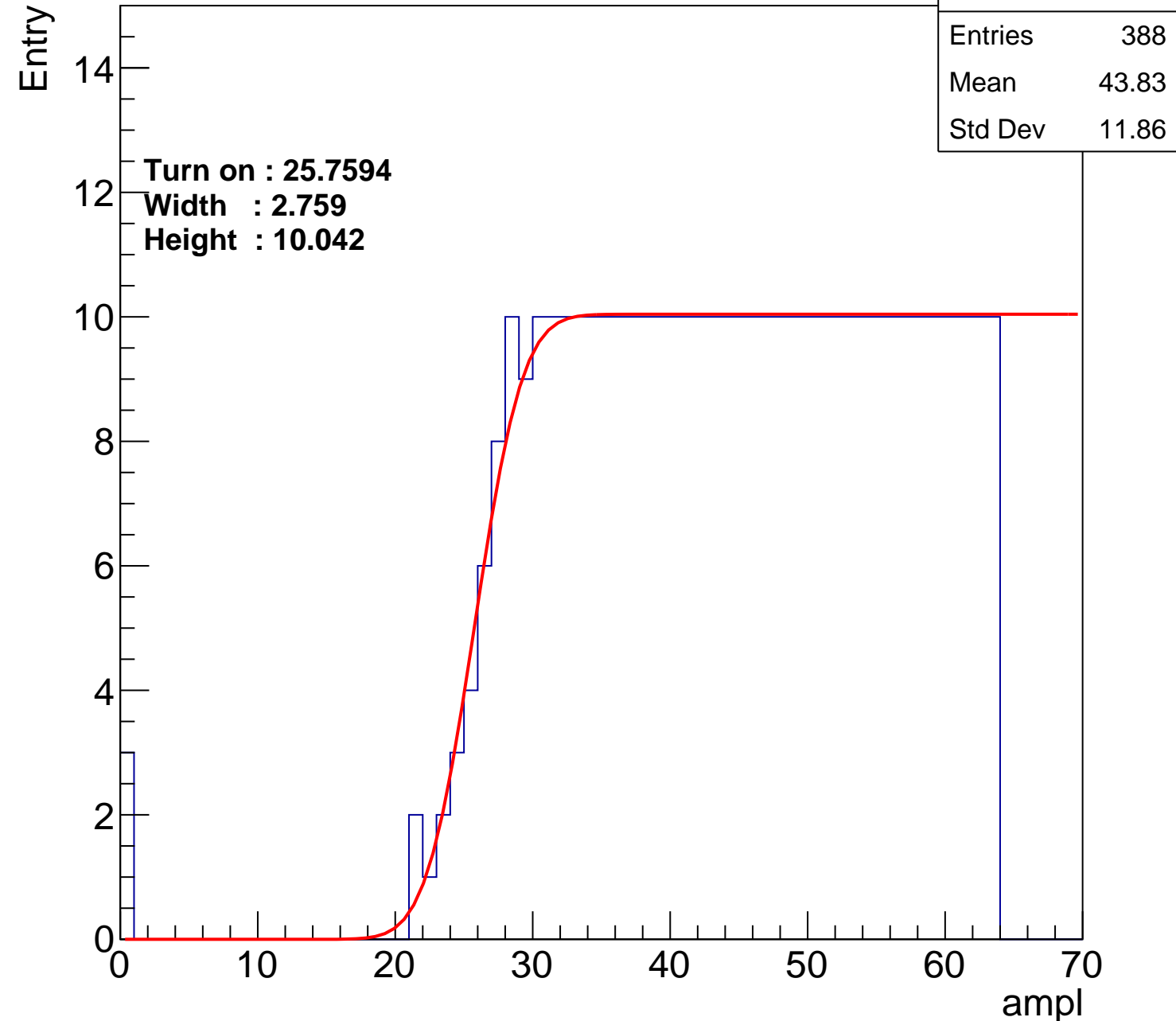
Width : 2.759

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.06
Std Dev	11.09

Turn on : 27.8976

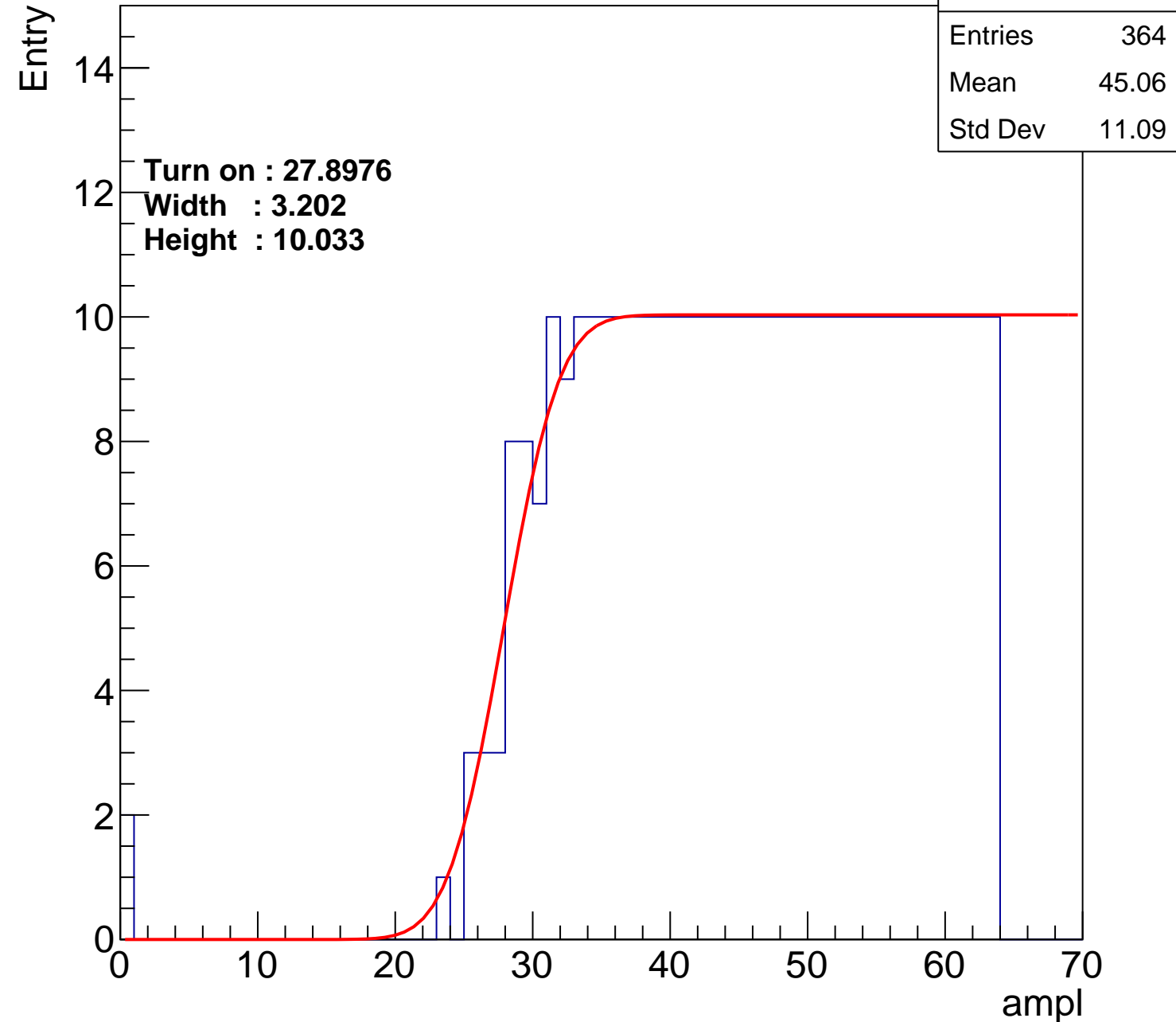
Width : 3.202

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.47
Std Dev	11.53

Turn on : 26.6049

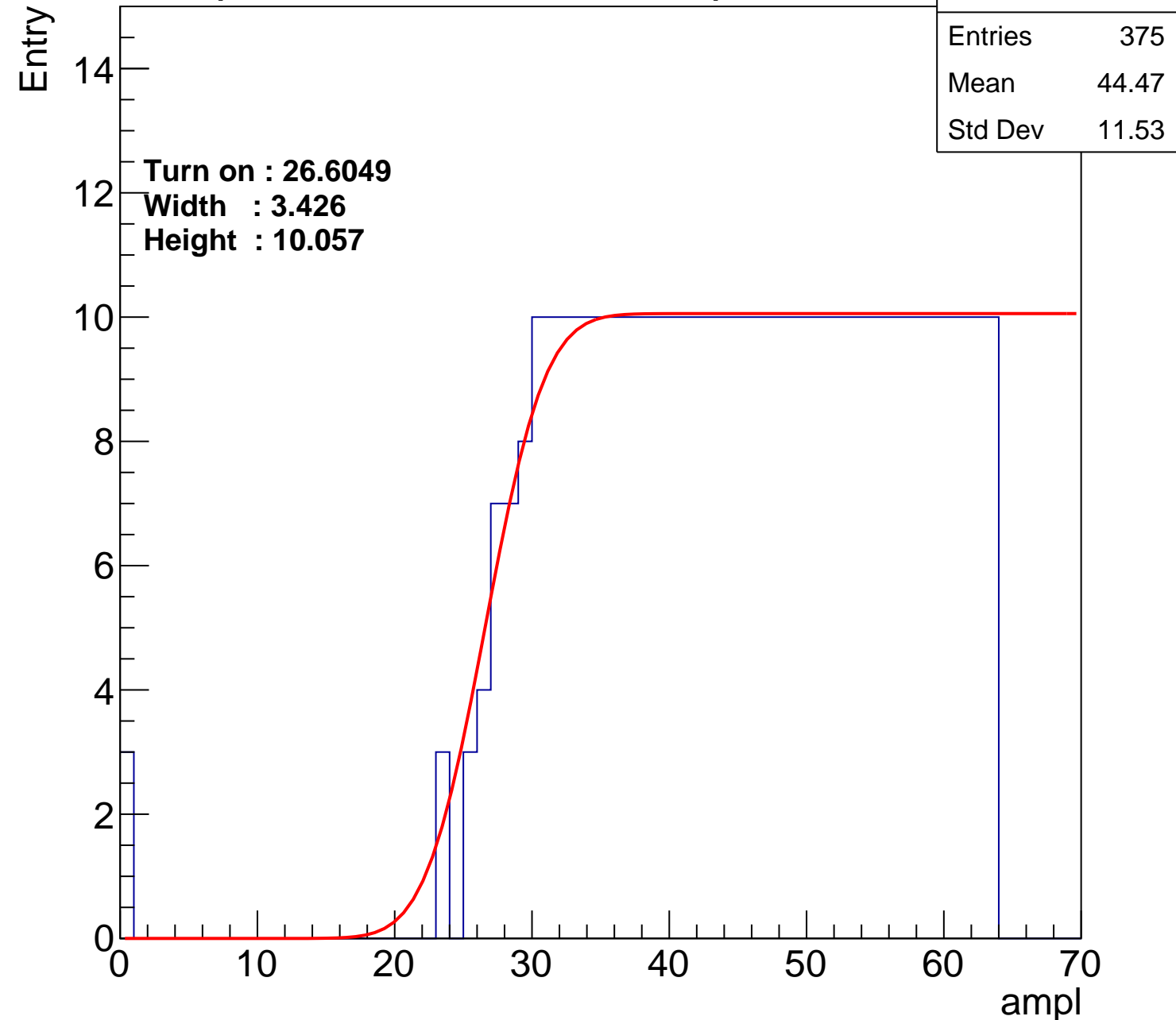
Width : 3.426

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.39
Std Dev	11.09

Turn on : 28.7185

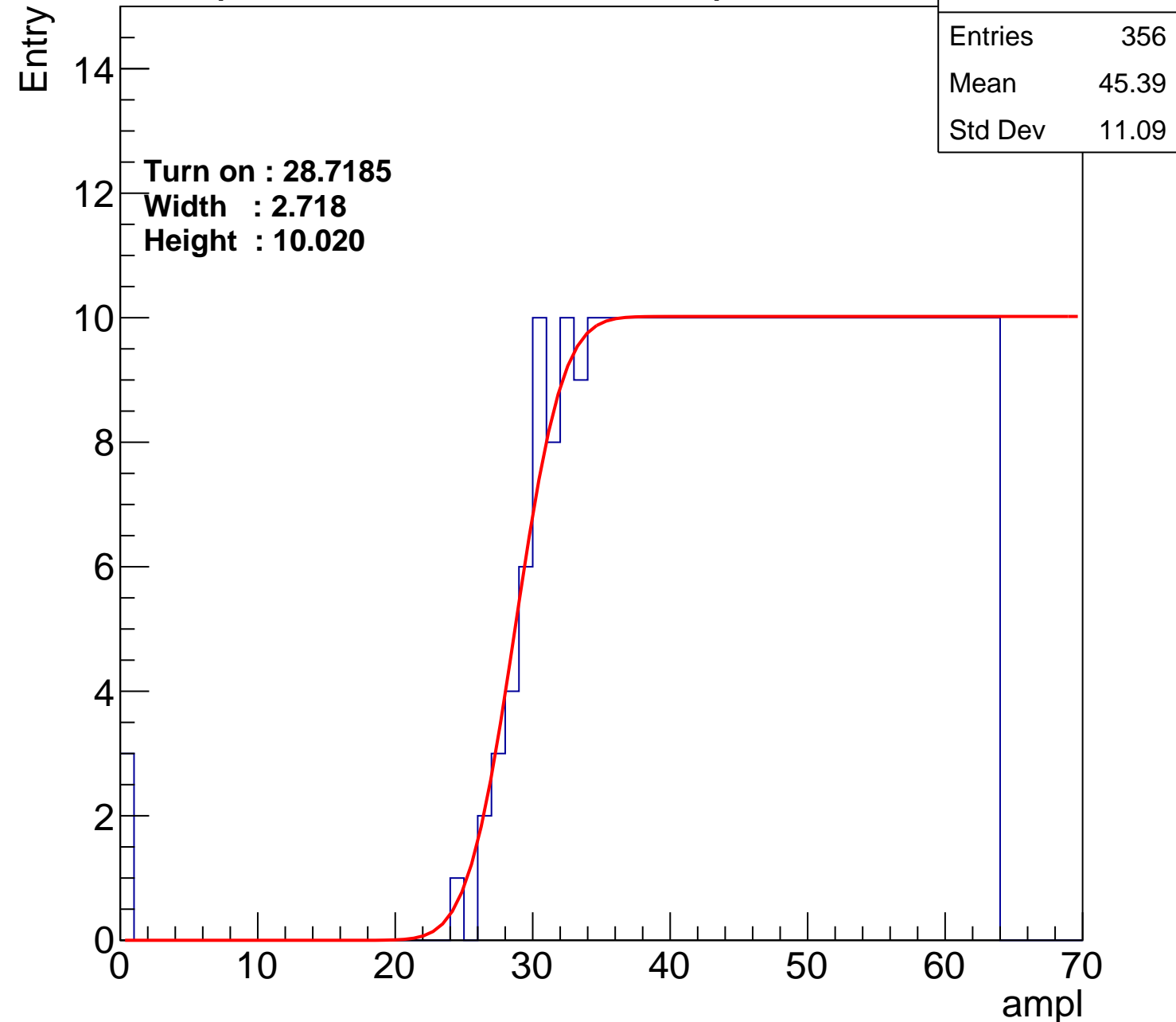
Width : 2.718

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 26.3906

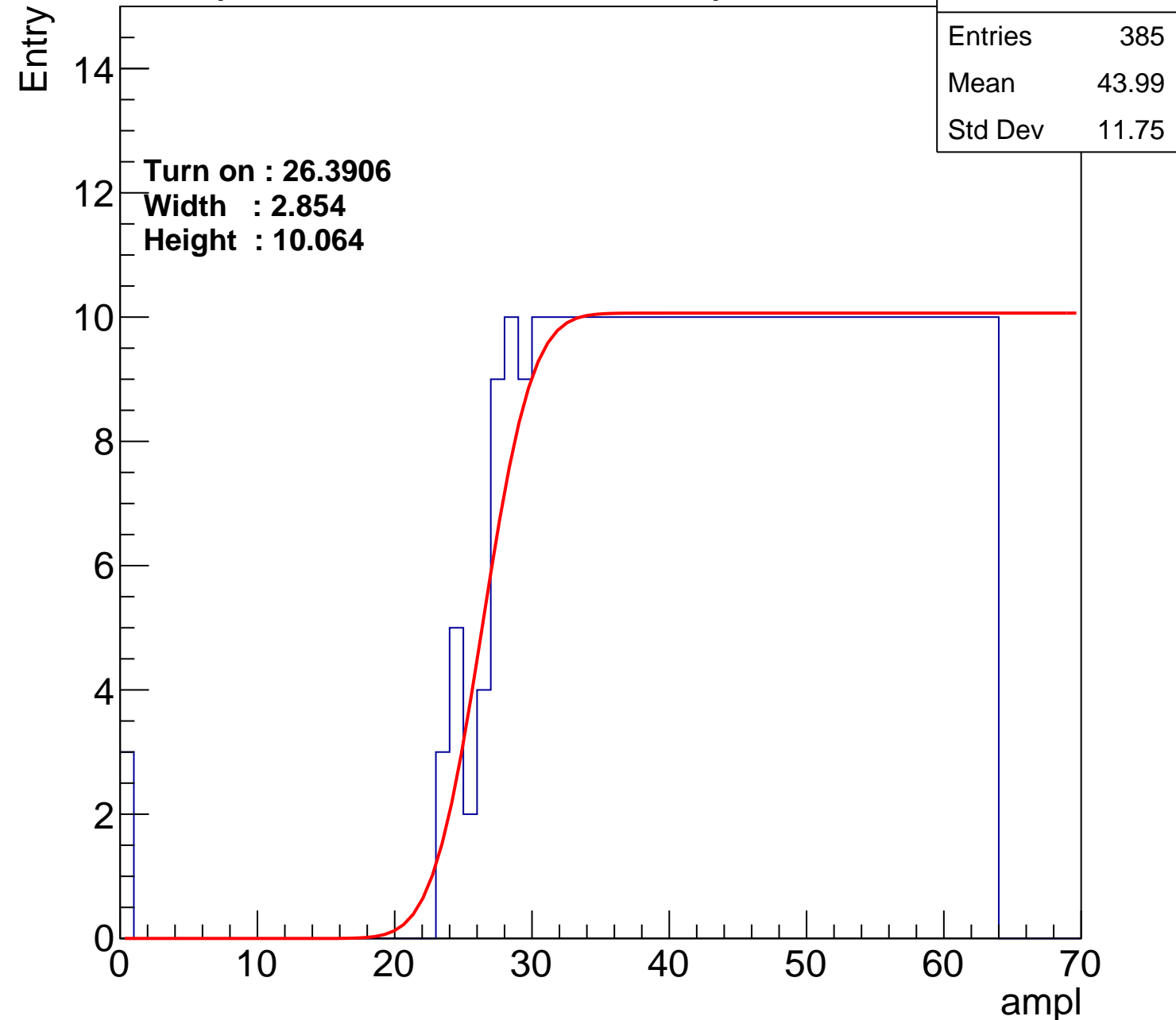
Width : 2.854

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch51

calib_packv5_042523_0143.root, FC#13, port D2

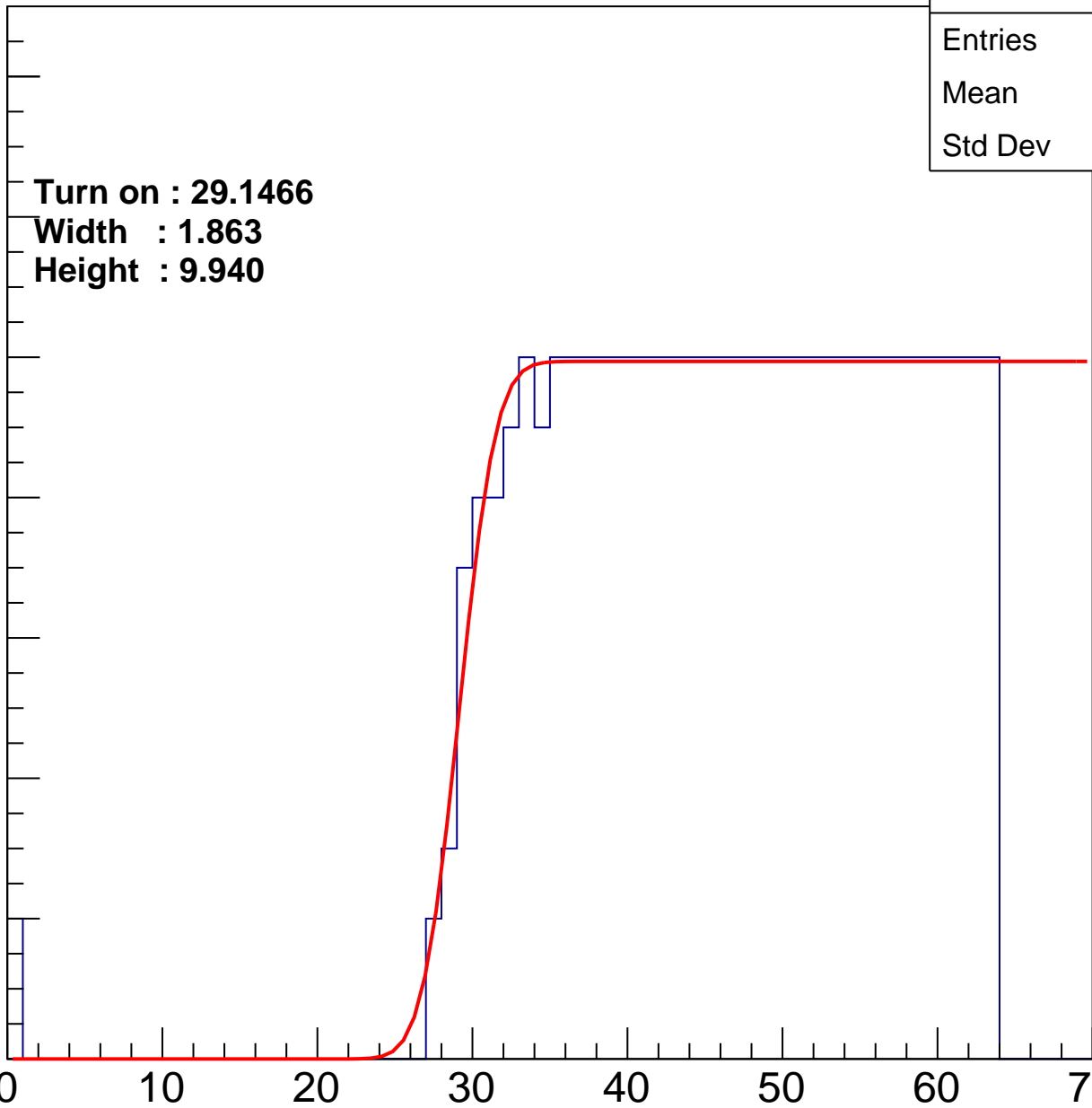
Entry

14
12
10
8
6
4
2
0

Turn on : 29.1466
Width : 1.863
Height : 9.940

Entries	348
Mean	45.87
Std Dev	10.65

ampl



B1L003S, U11-ch52

calib_packv5_042523_0143.root, FC#13, port D2

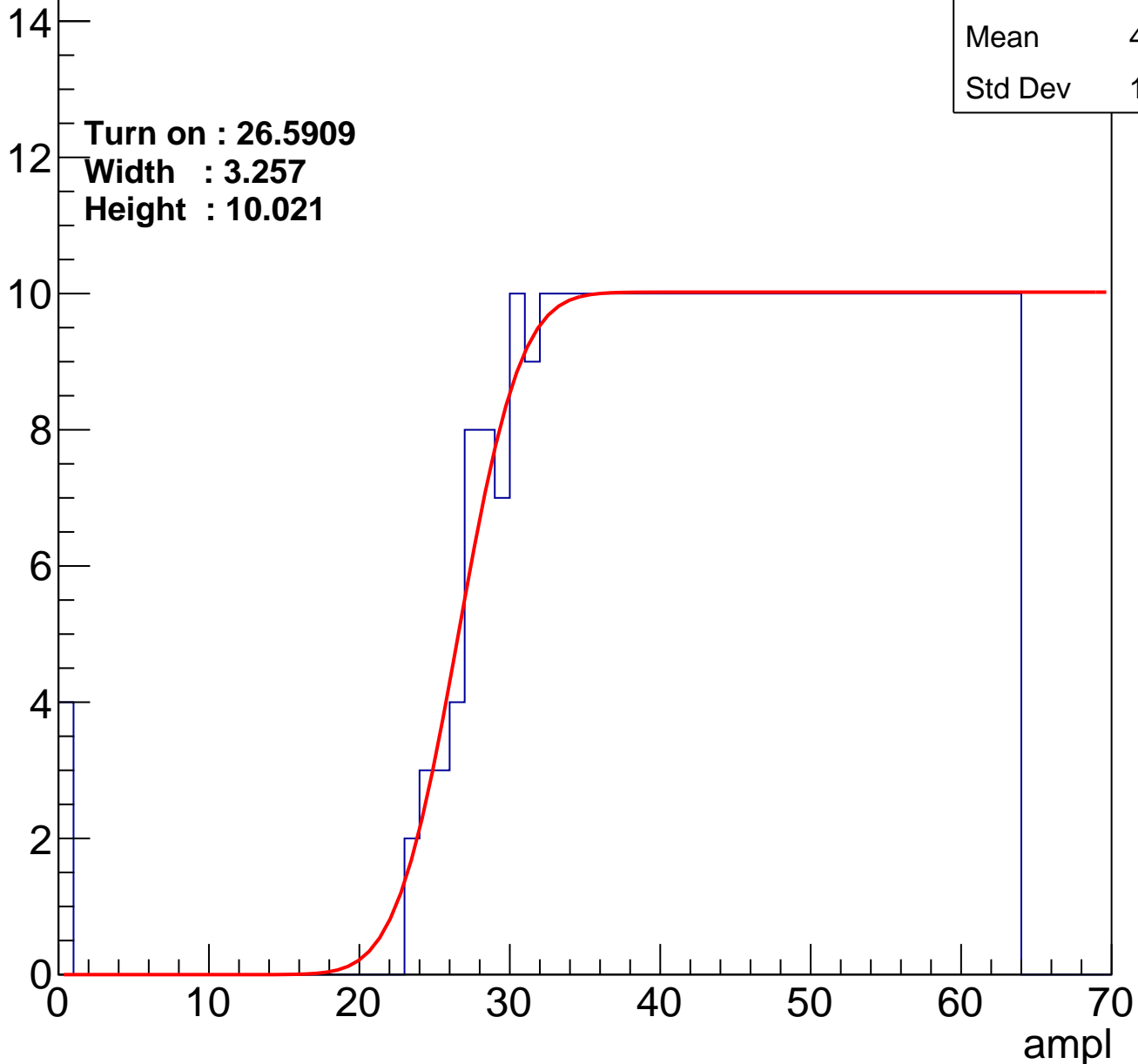
Entries	378
Mean	44.23
Std Dev	11.82

Turn on : 26.5909

Width : 3.257

Height : 10.021

Entry



B1L003S, U11-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.58
Std Dev	11.47

Turn on : 27.3673

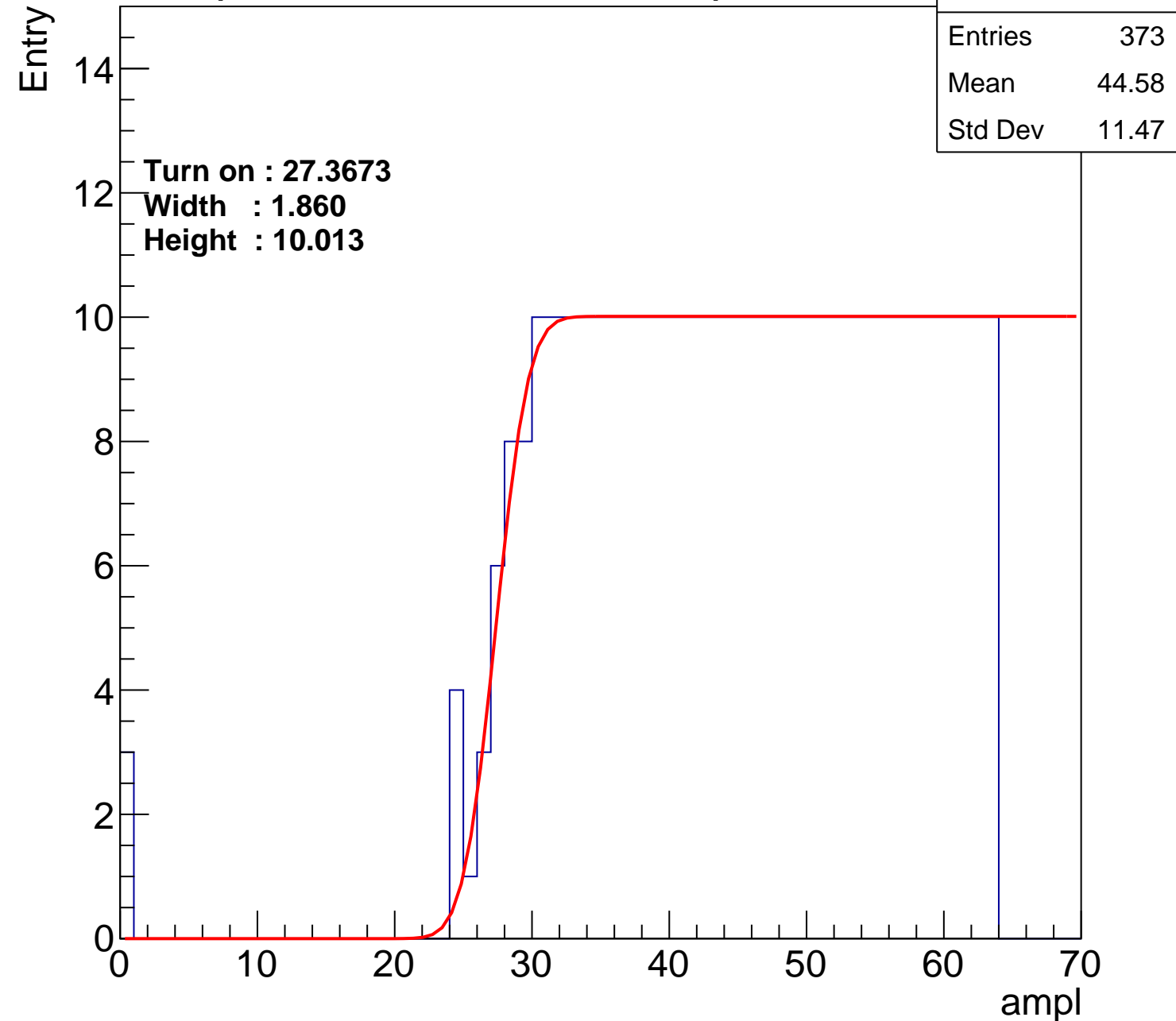
Width : 1.860

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.61
Std Dev	10.65

Turn on : 28.6885

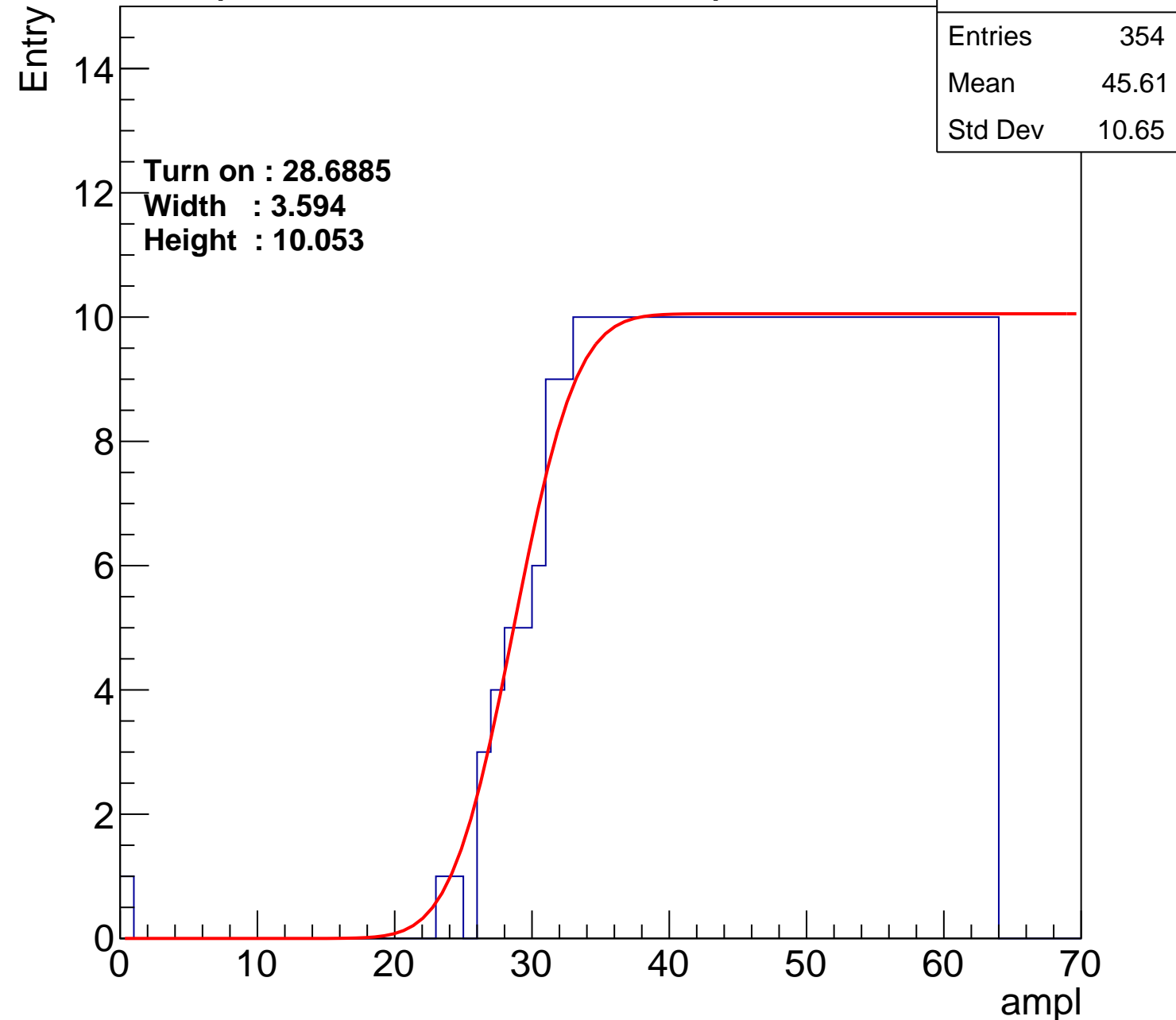
Width : 3.594

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch55

calib_packv5_042523_0143.root, FC#13, port D2

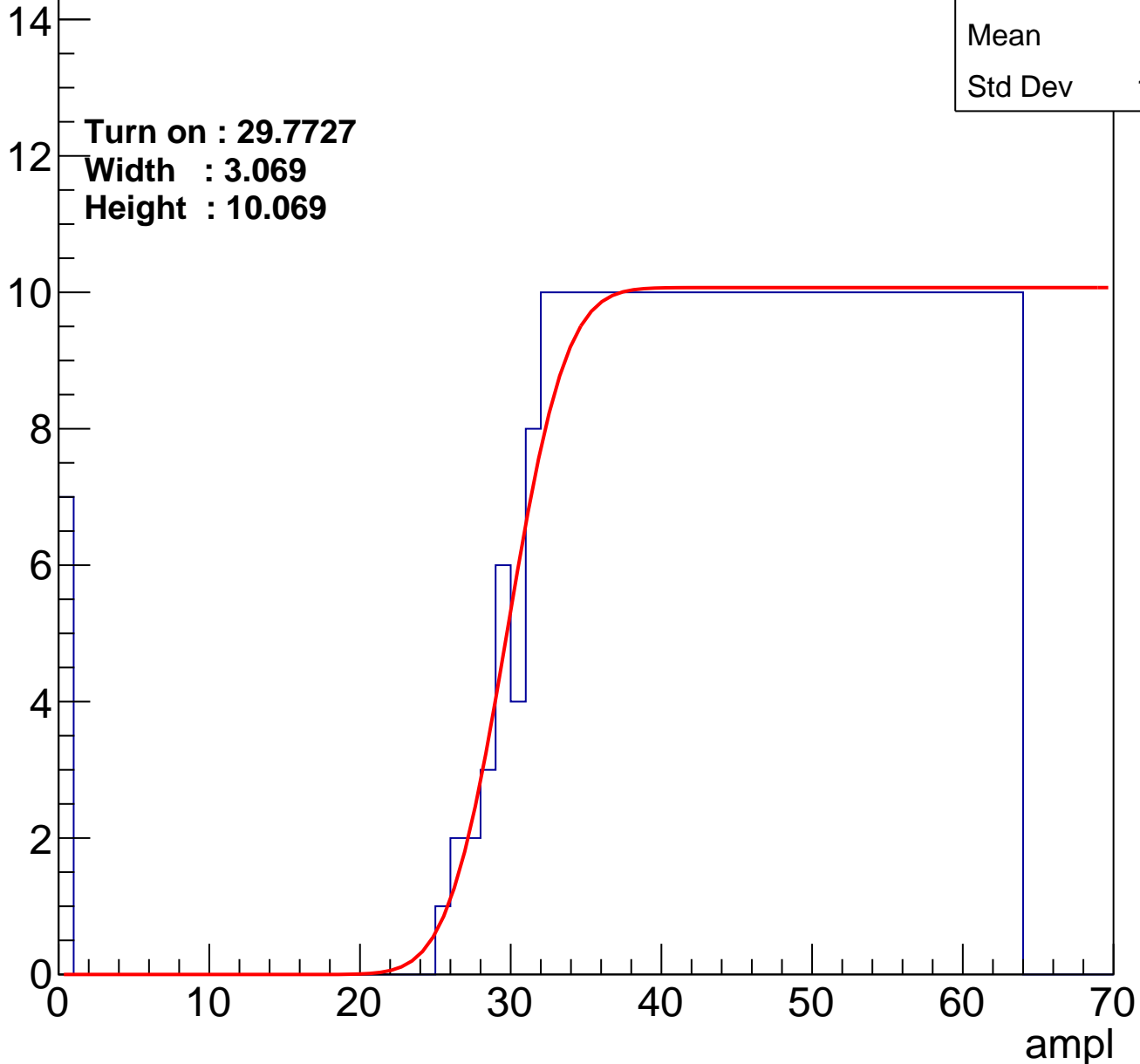
Entries	353
Mean	45.2
Std Dev	11.91

Turn on : 29.7727

Width : 3.069

Height : 10.069

Entry



B1L003S, U11-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.1
Std Dev	11.04

Turn on : 28.2358

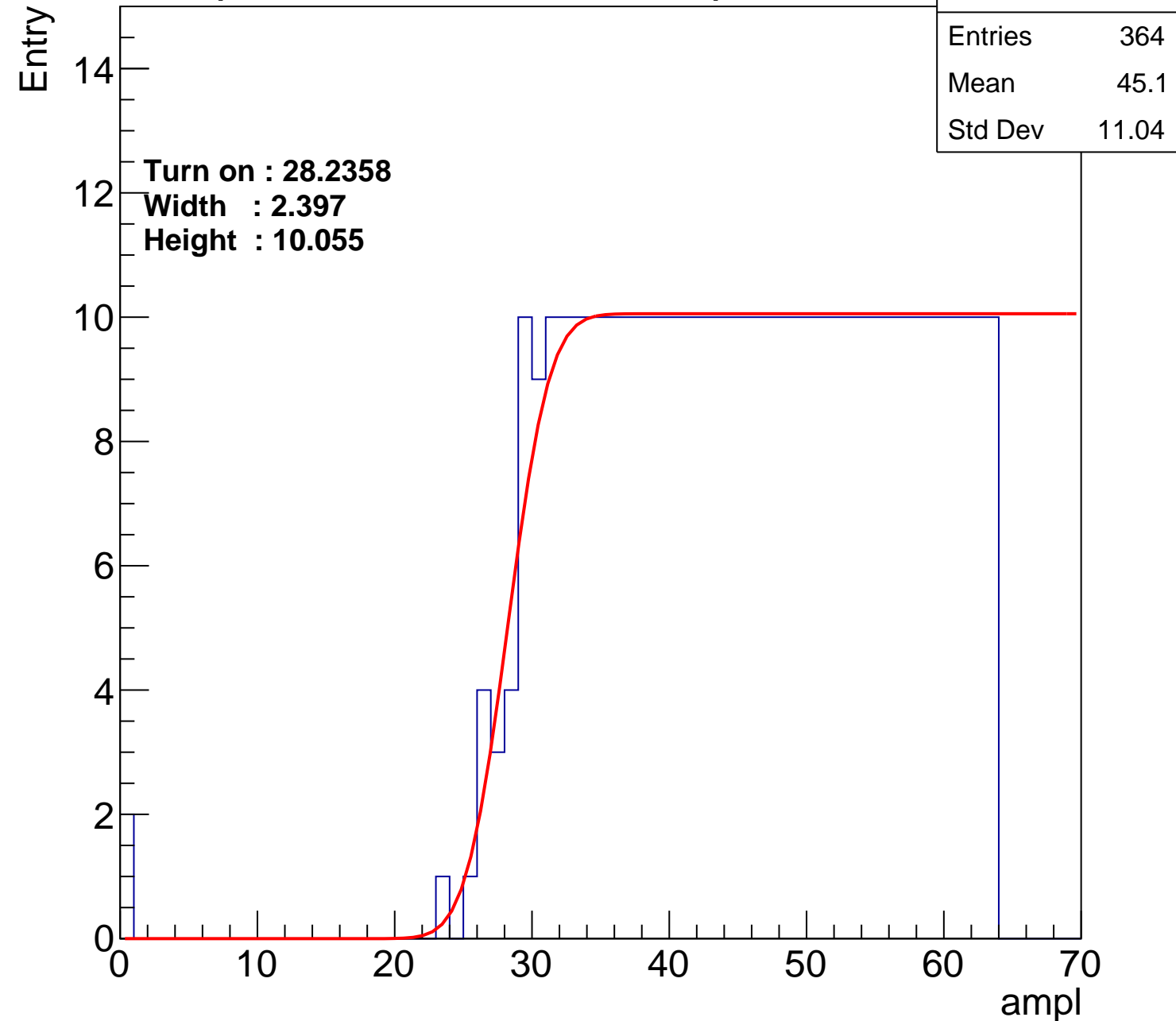
Width : 2.397

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.25
Std Dev	11.2

Turn on : 29.2311

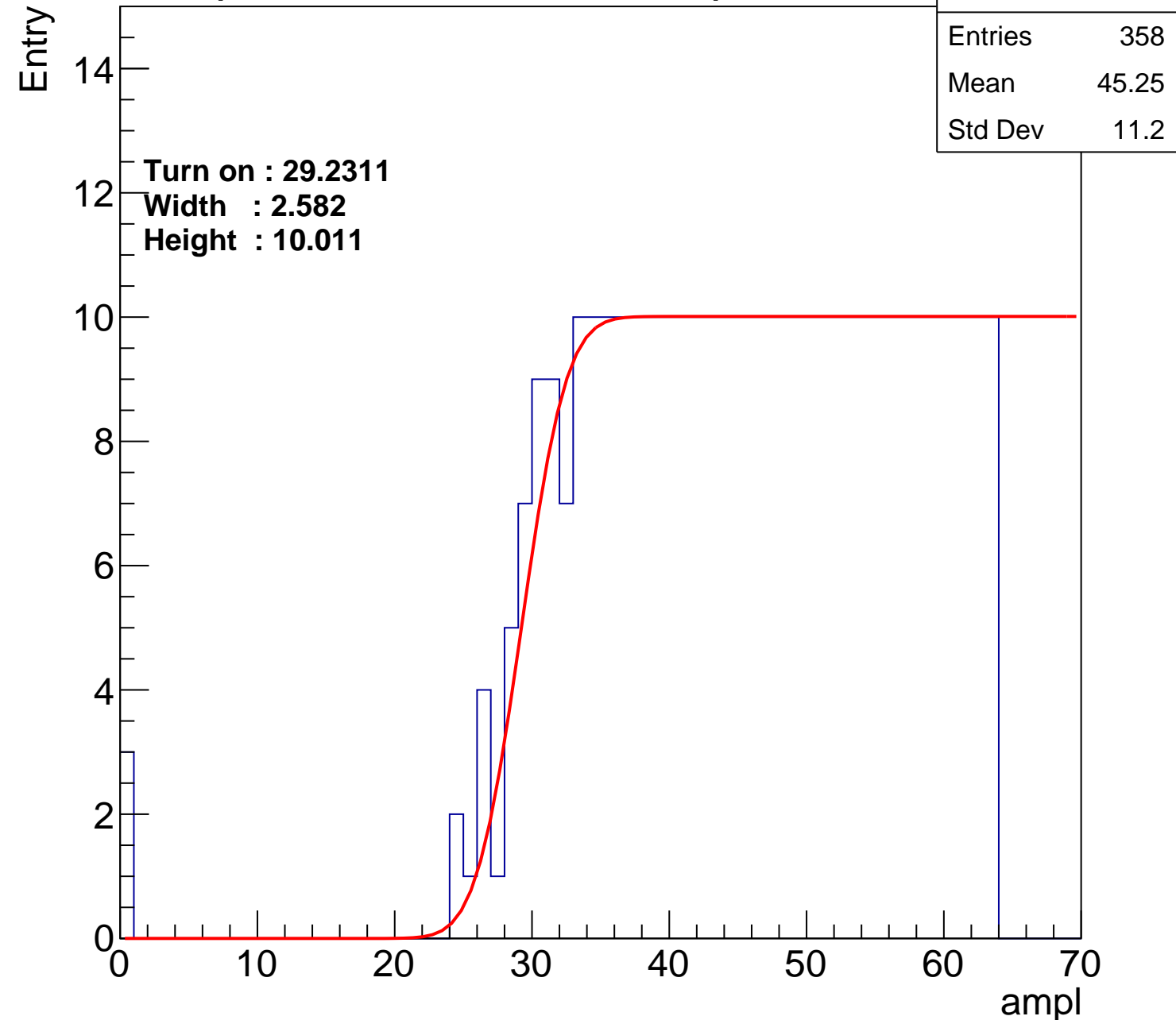
Width : 2.582

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.96
Std Dev	11.15

Turn on : 27.7547

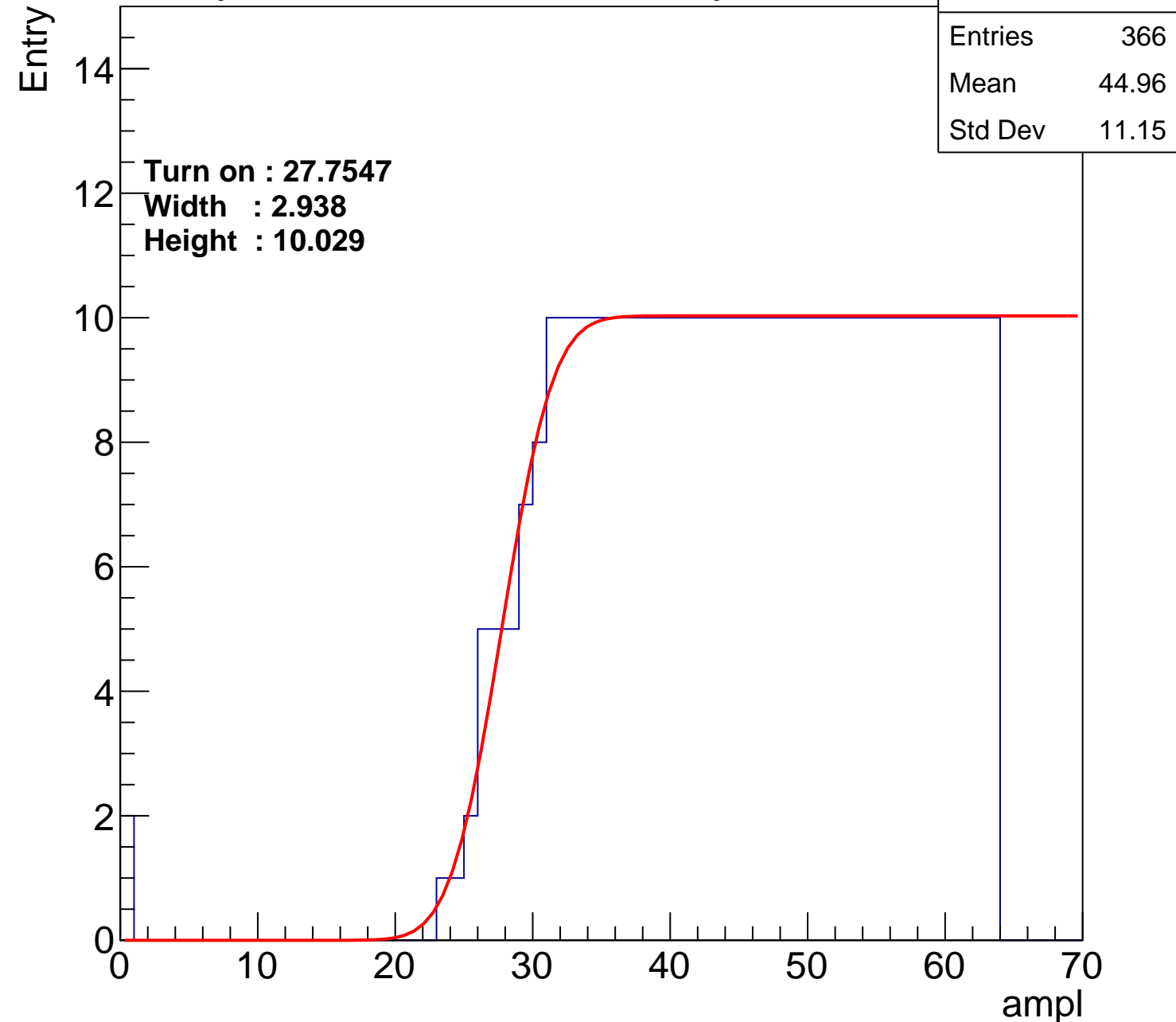
Width : 2.938

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch59

calib_packv5_042523_0143.root, FC#13, port D2

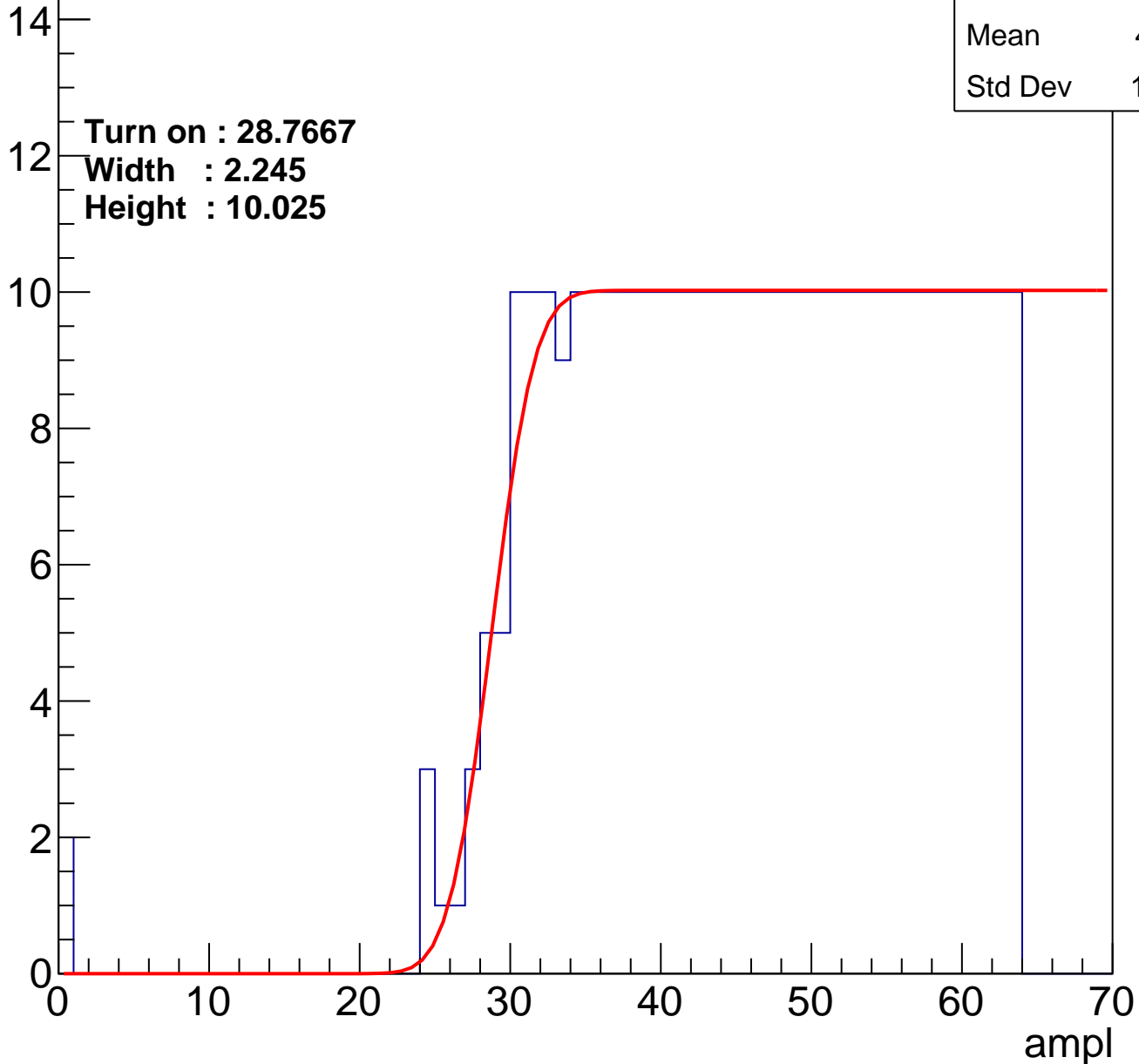
Entries	359
Mean	45.31
Std Dev	10.96

Turn on : 28.7667

Width : 2.245

Height : 10.025

Entry



B1L003S, U11-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.8770

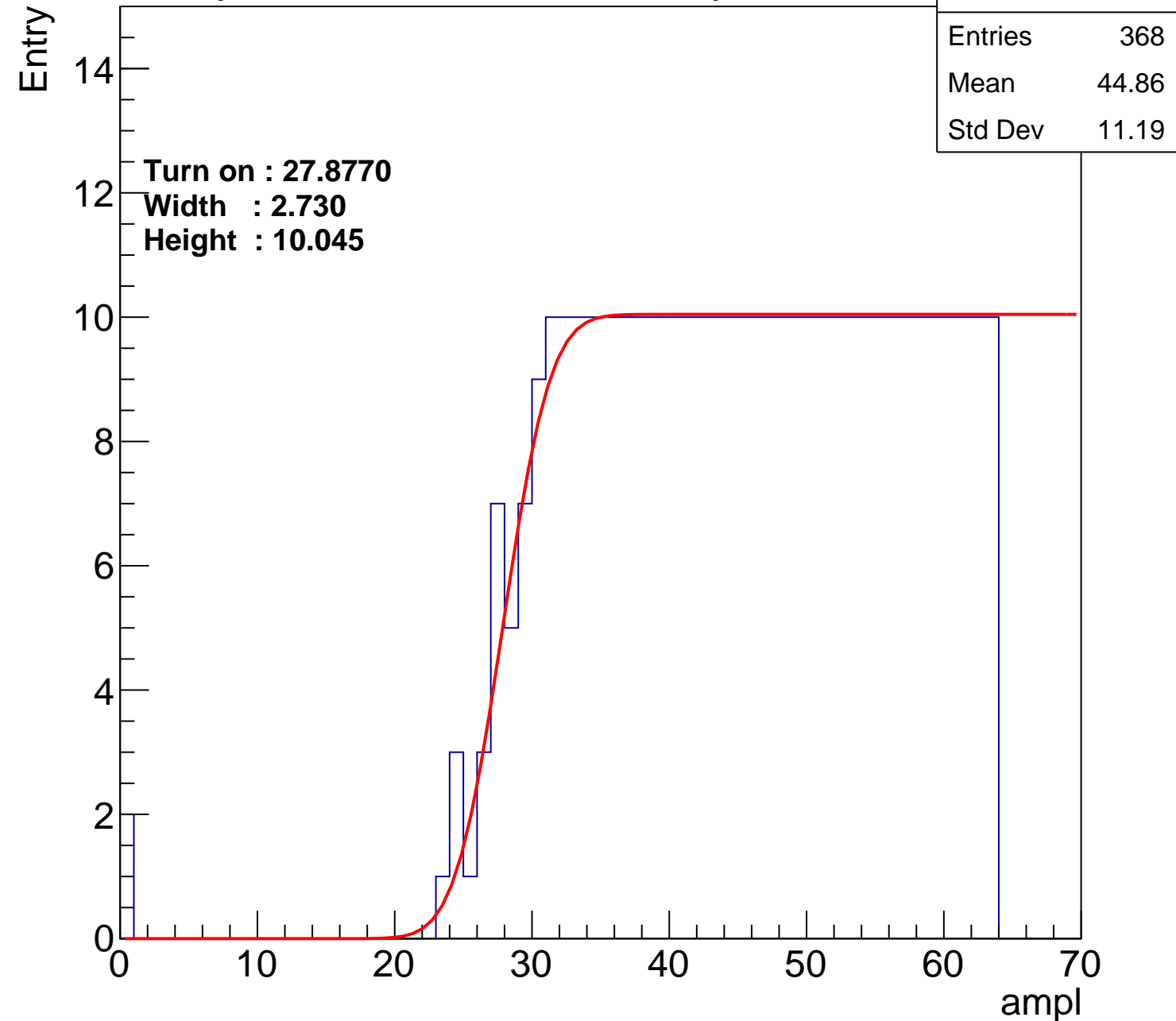
Width : 2.730

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch61

calib_packv5_042523_0143.root, FC#13, port D2

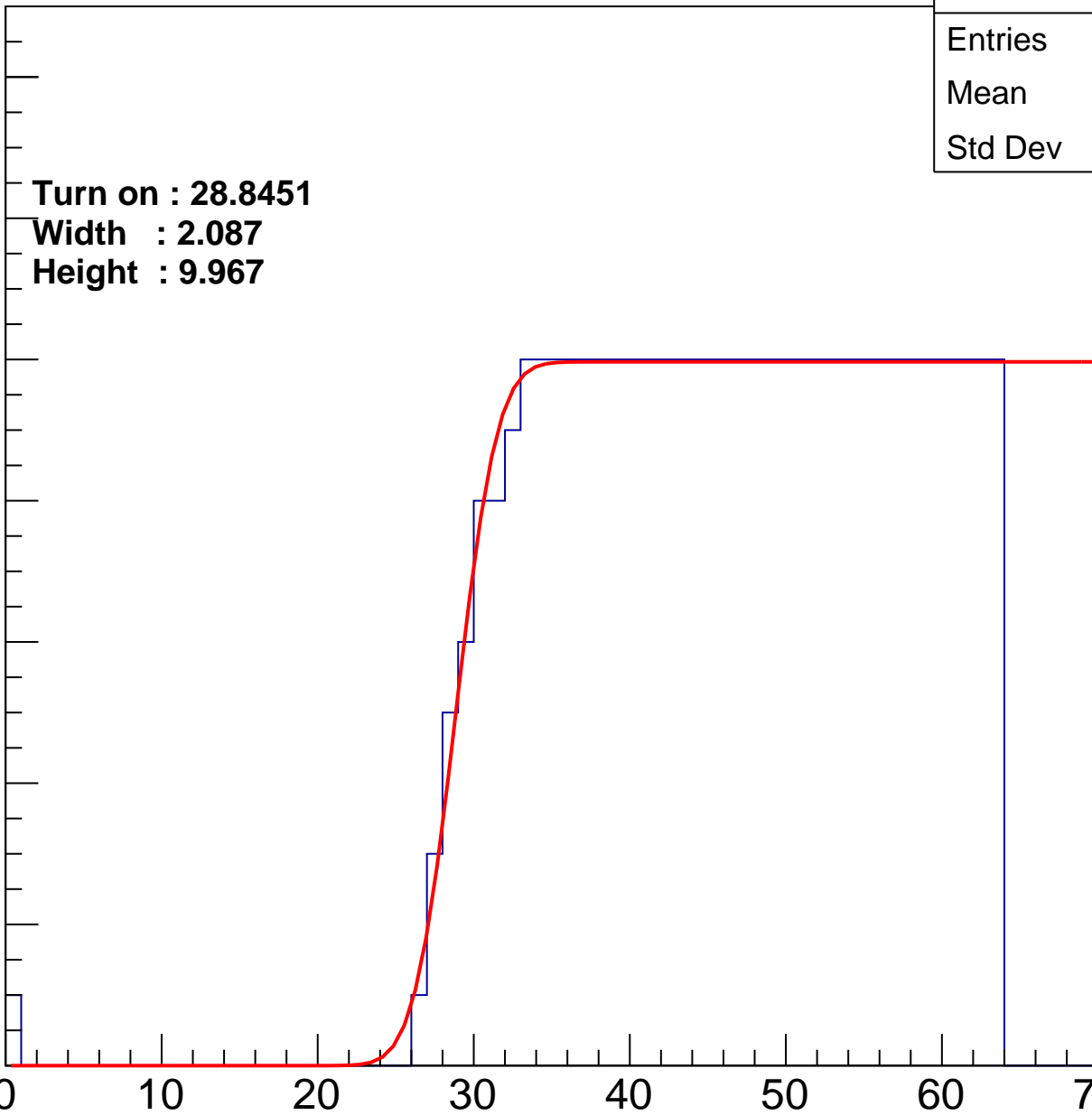
Entry

14
12
10
8
6
4
2
0

Turn on : 28.8451
Width : 2.087
Height : 9.967

Entries	351
Mean	45.8
Std Dev	10.49

ampl



B1L003S, U11-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.84
Std Dev	11.75

Turn on : 28.2862

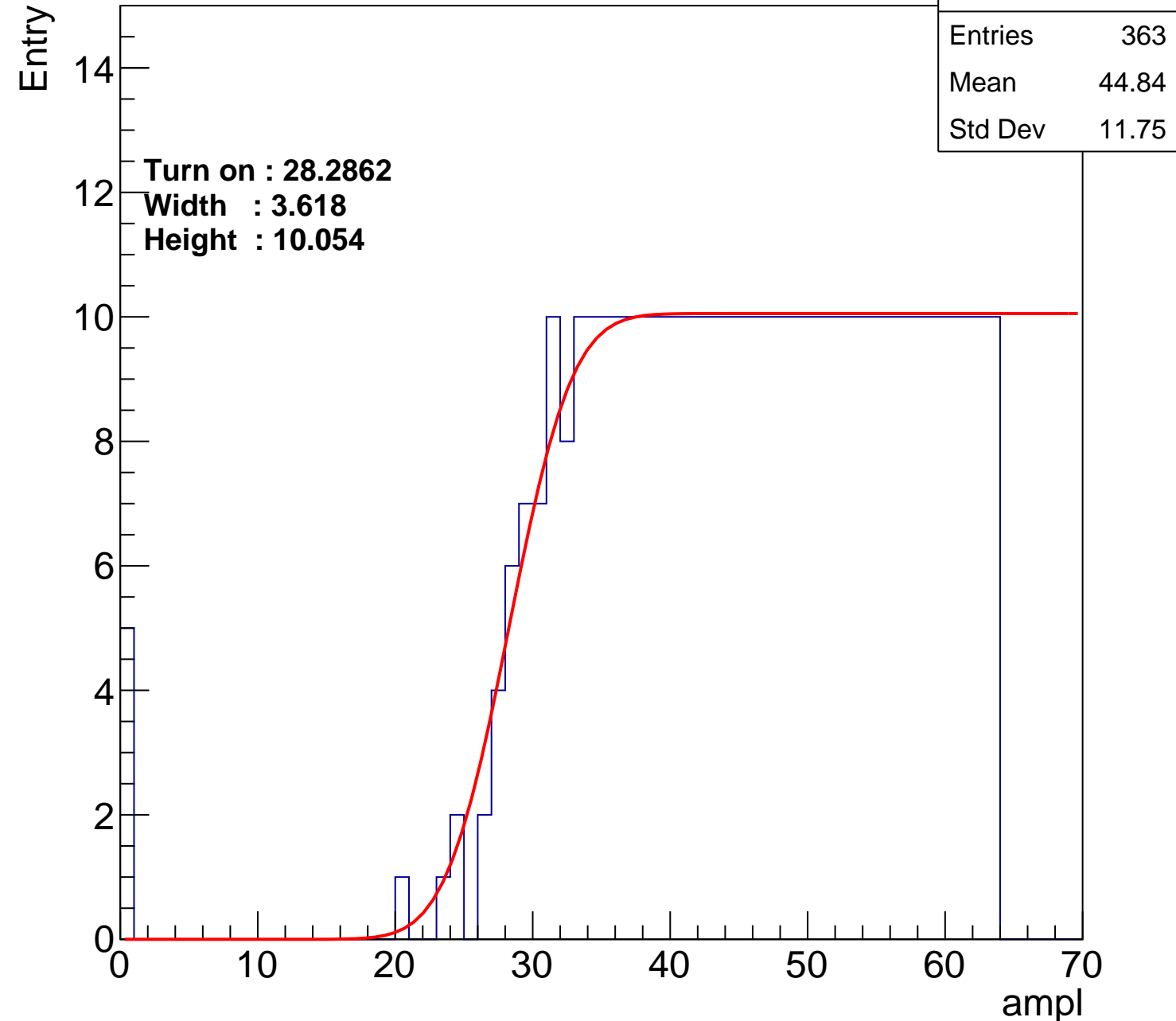
Width : 3.618

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.06
Std Dev	11.39

Turn on : 28.2944

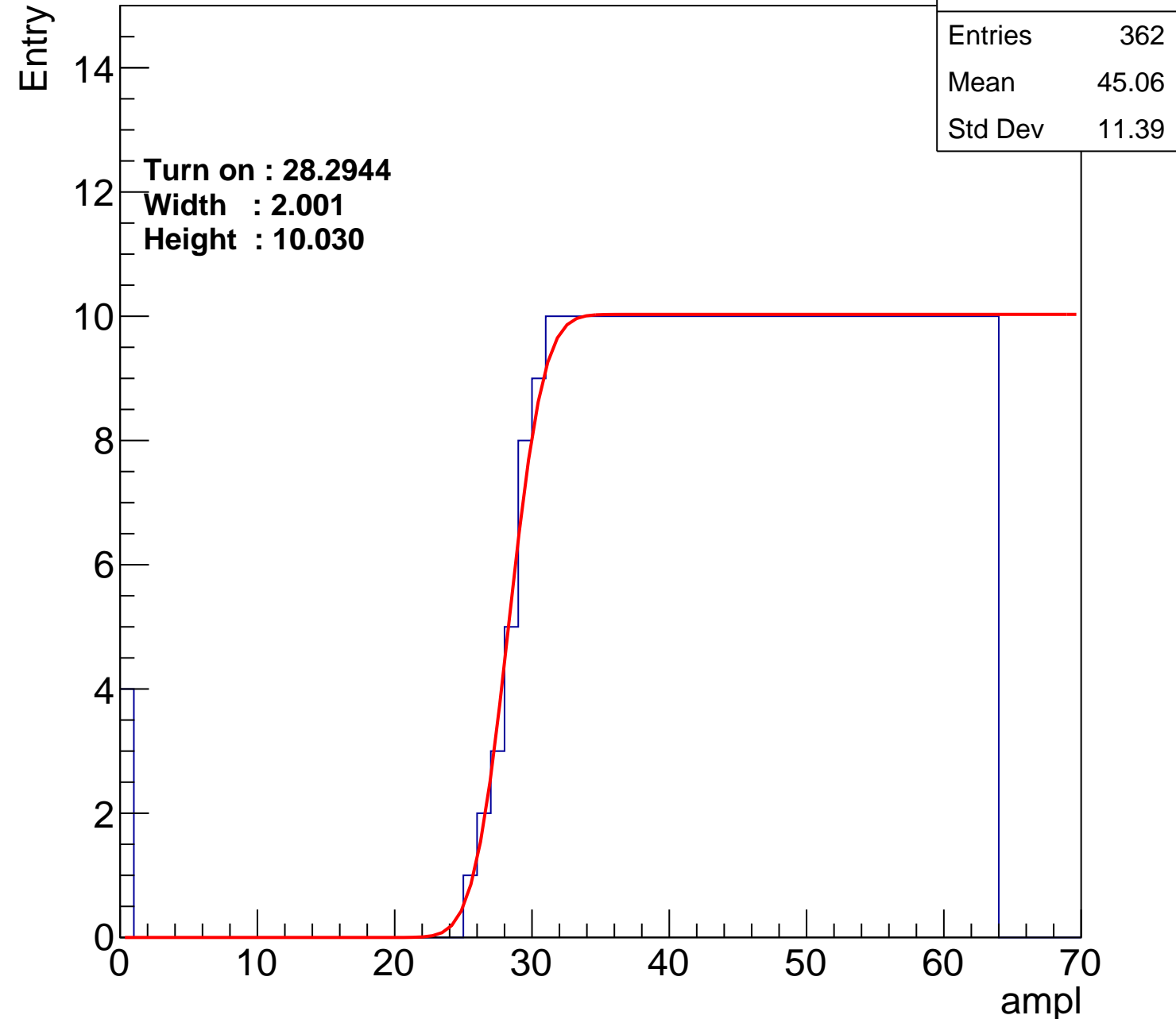
Width : 2.001

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch64

calib_packv5_042523_0143.root, FC#13, port D2

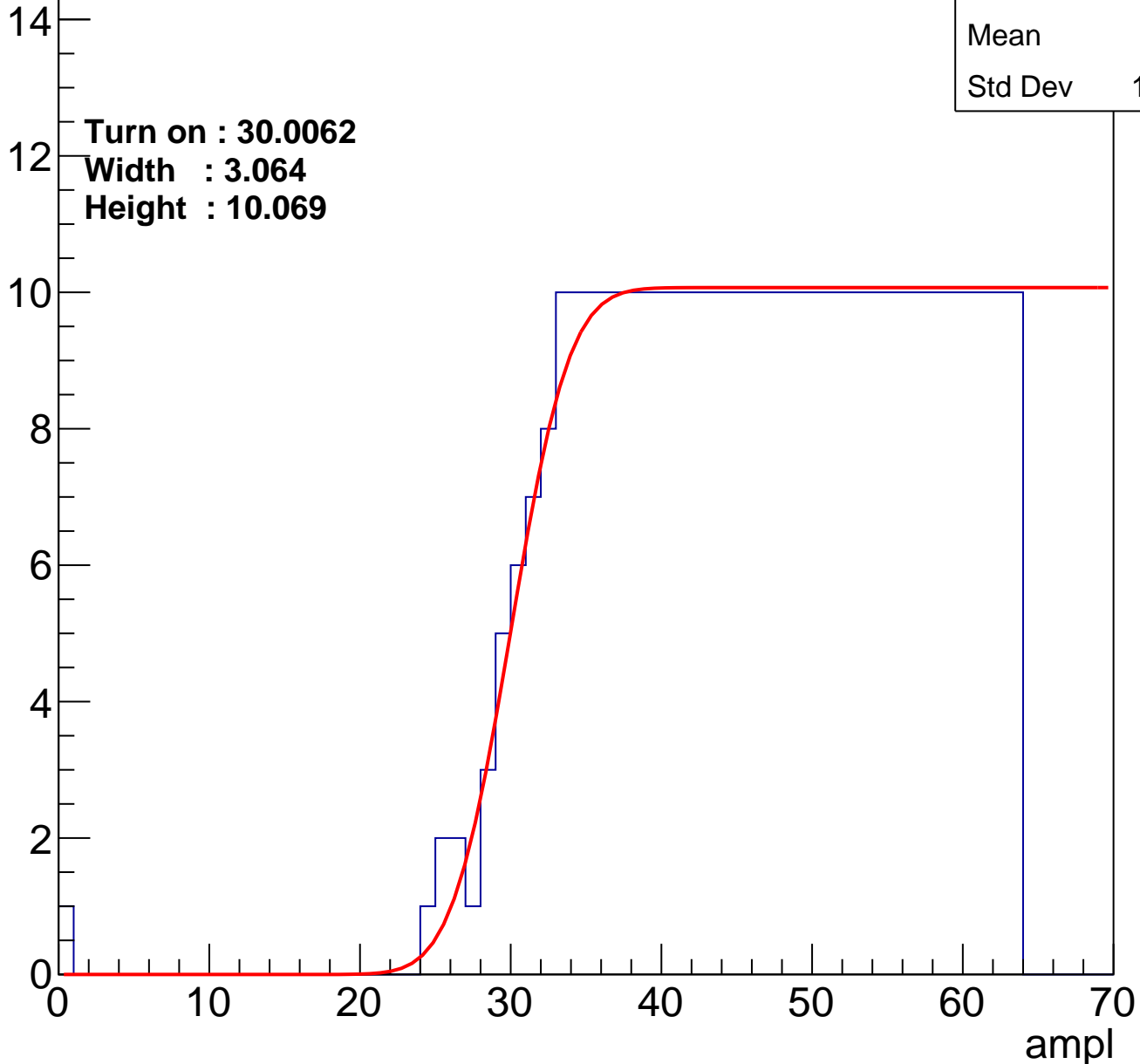
Entries	346
Mean	46
Std Dev	10.45

Turn on : 30.0062

Width : 3.064

Height : 10.069

Entry



B1L003S, U11-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.49
Std Dev	11.54

Turn on : 26.6901

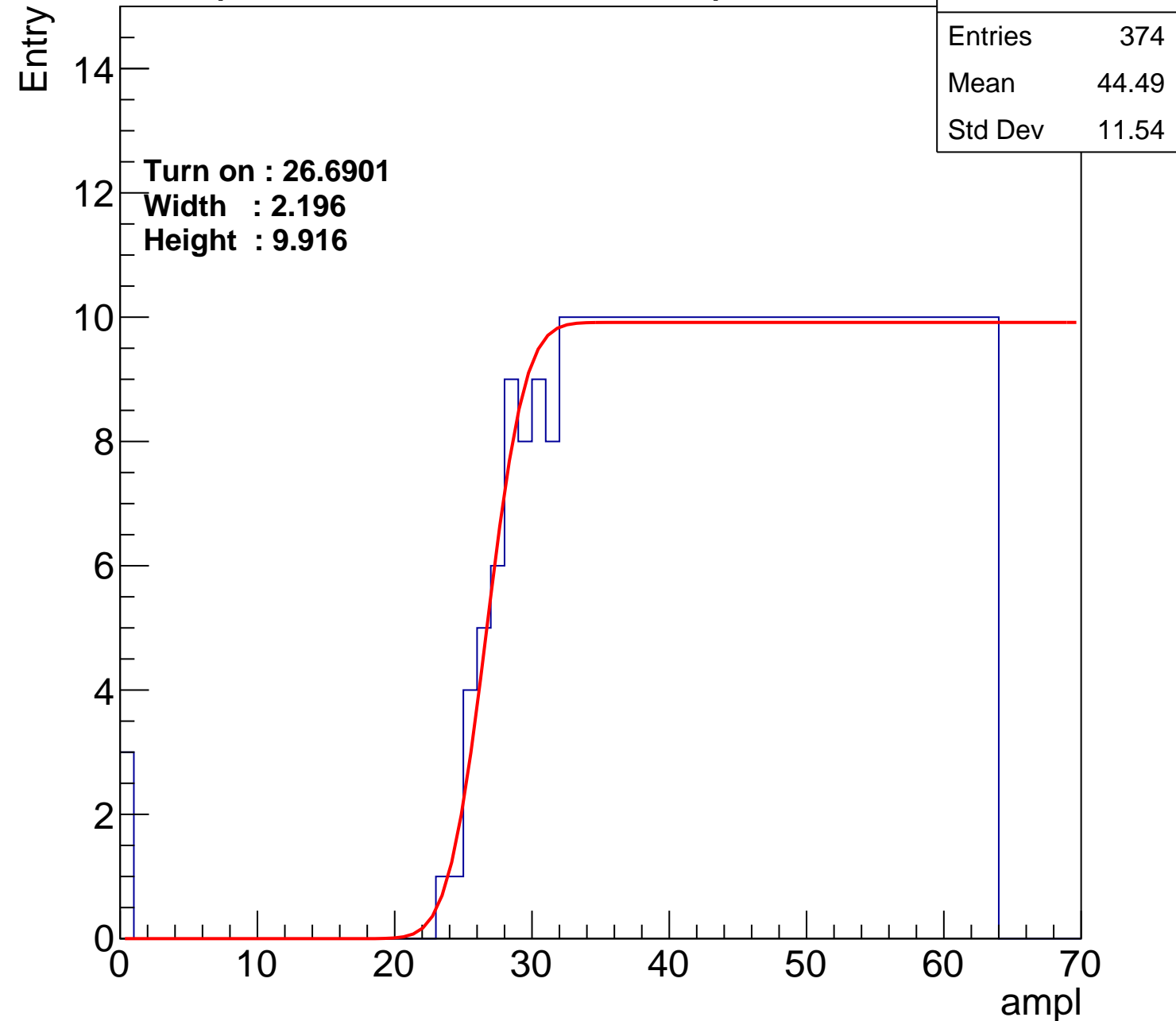
Width : 2.196

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.19
Std Dev	11.04

Turn on : 27.8203

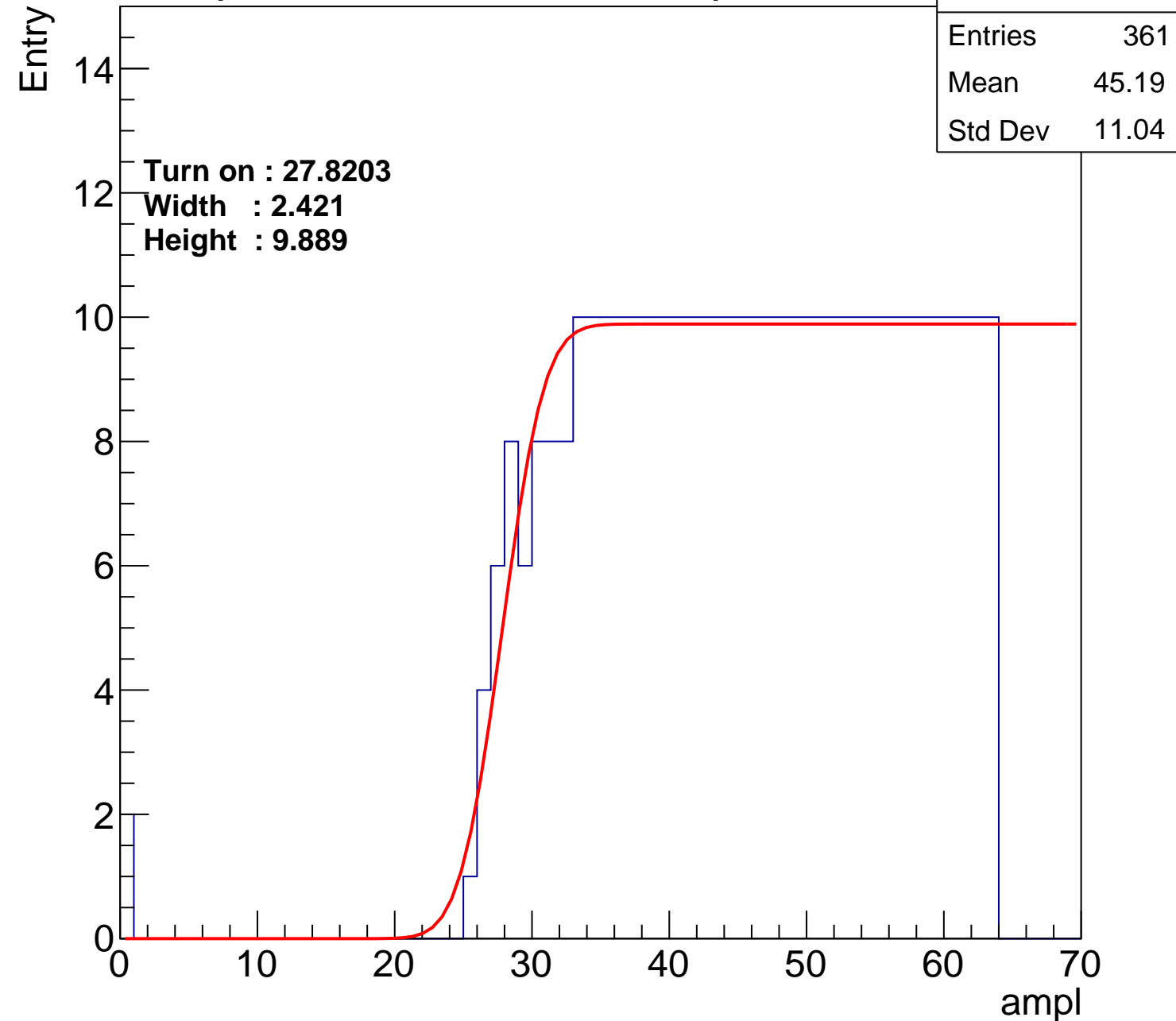
Width : 2.421

Height : 9.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.59
Std Dev	11.63

Turn on : 27.9305

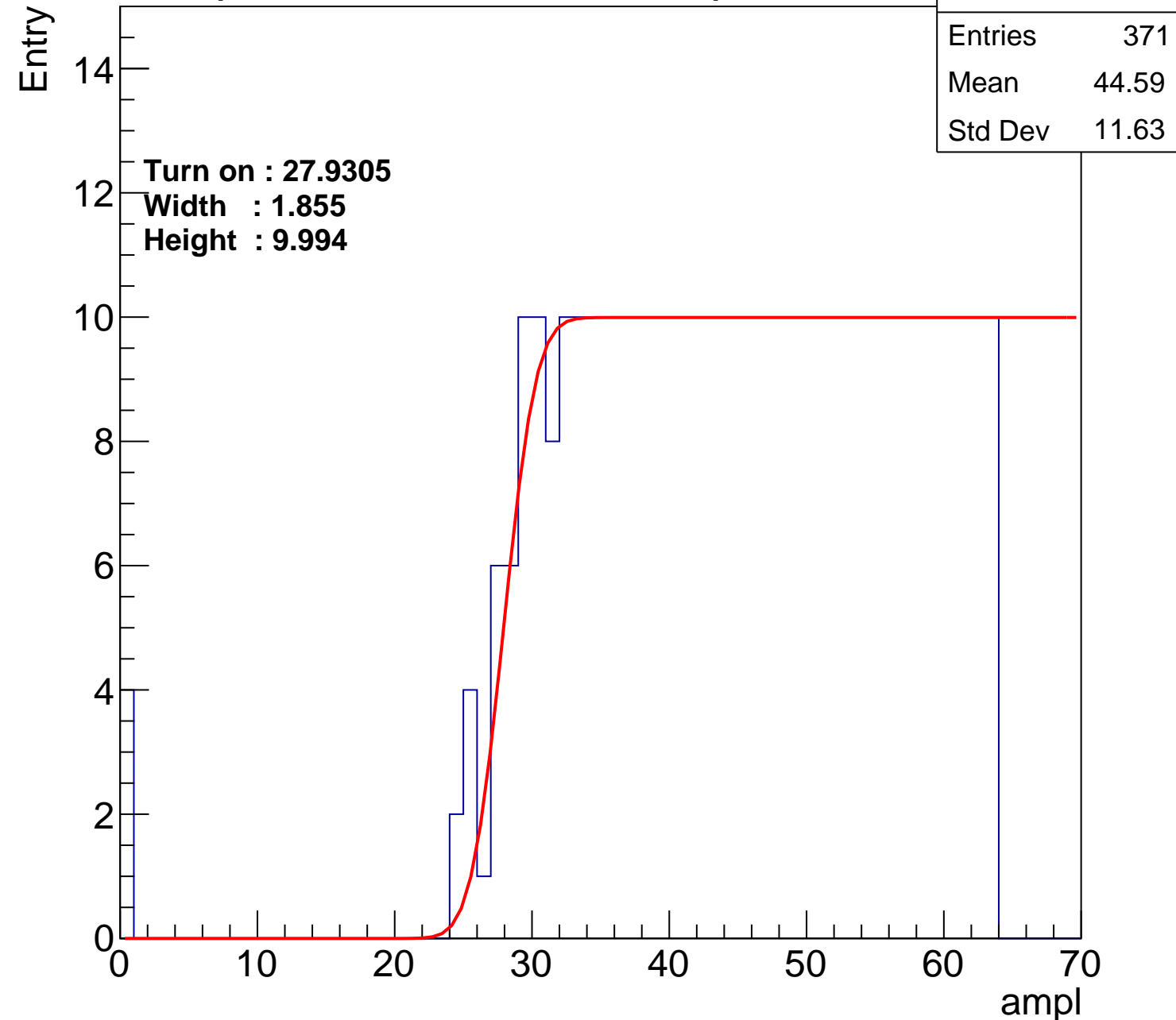
Width : 1.855

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.9867

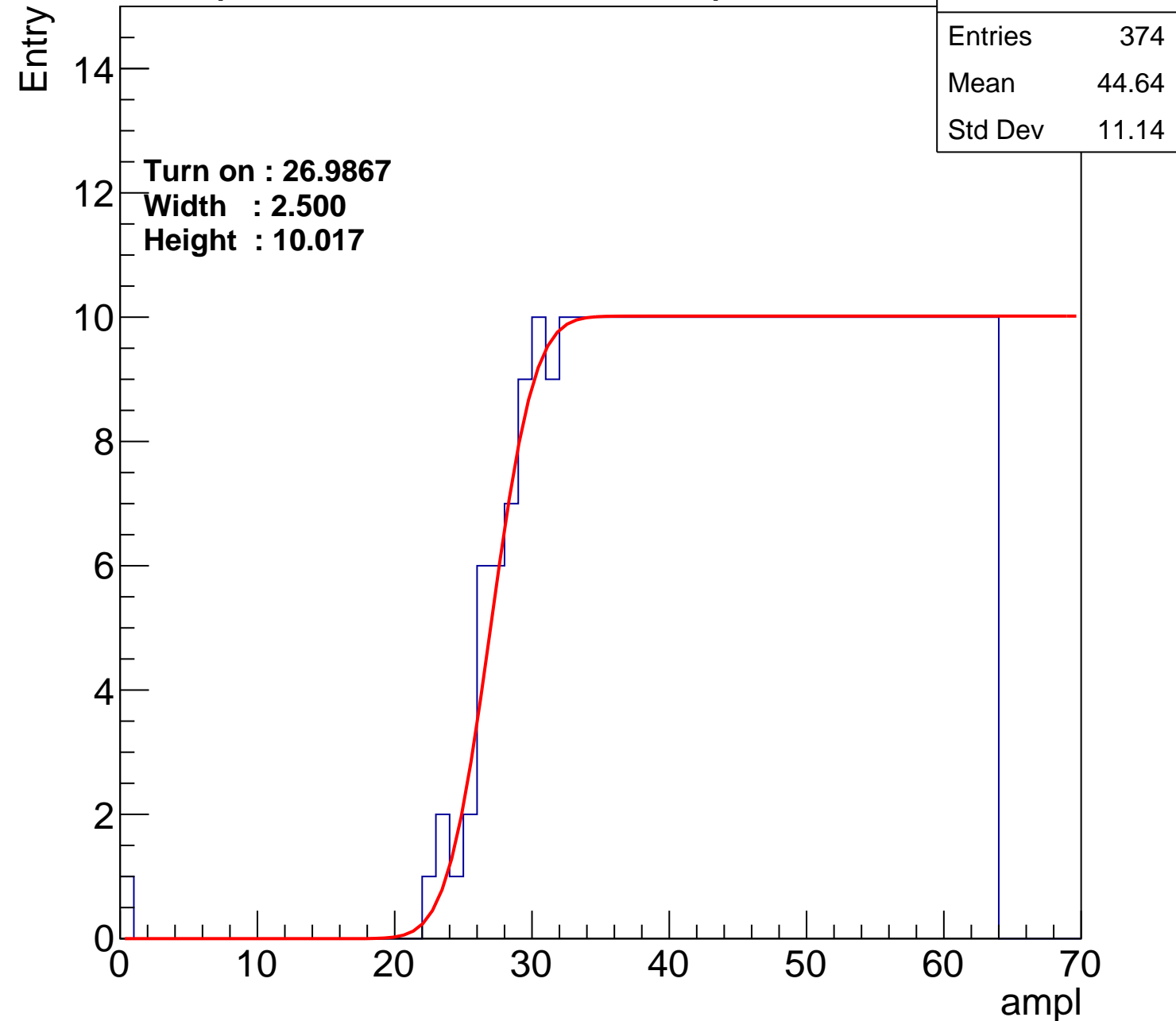
Width : 2.500

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.95
Std Dev	11.49

Turn on : 28.1298

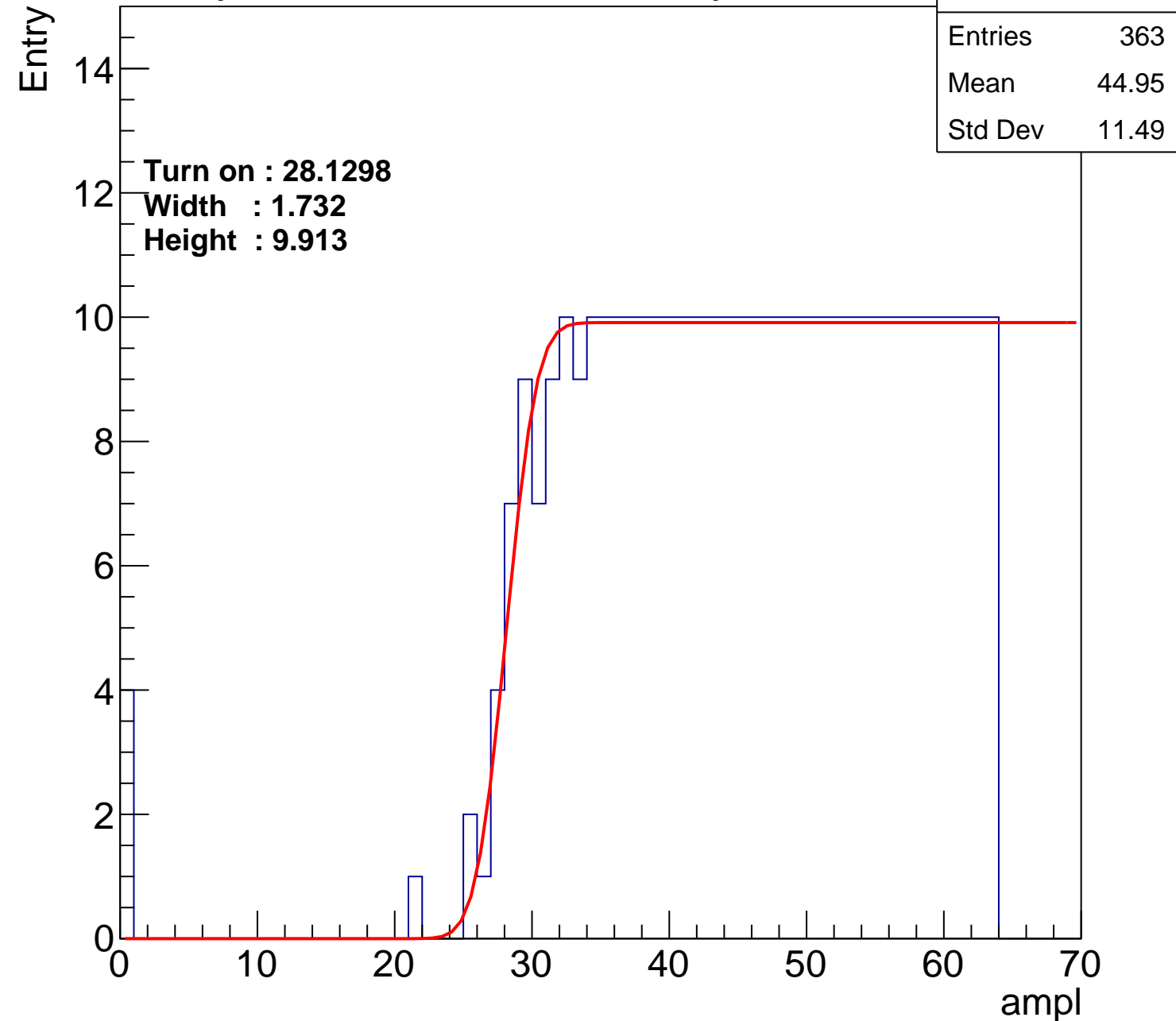
Width : 1.732

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.95
Std Dev	11.3

Turn on : 28.1381

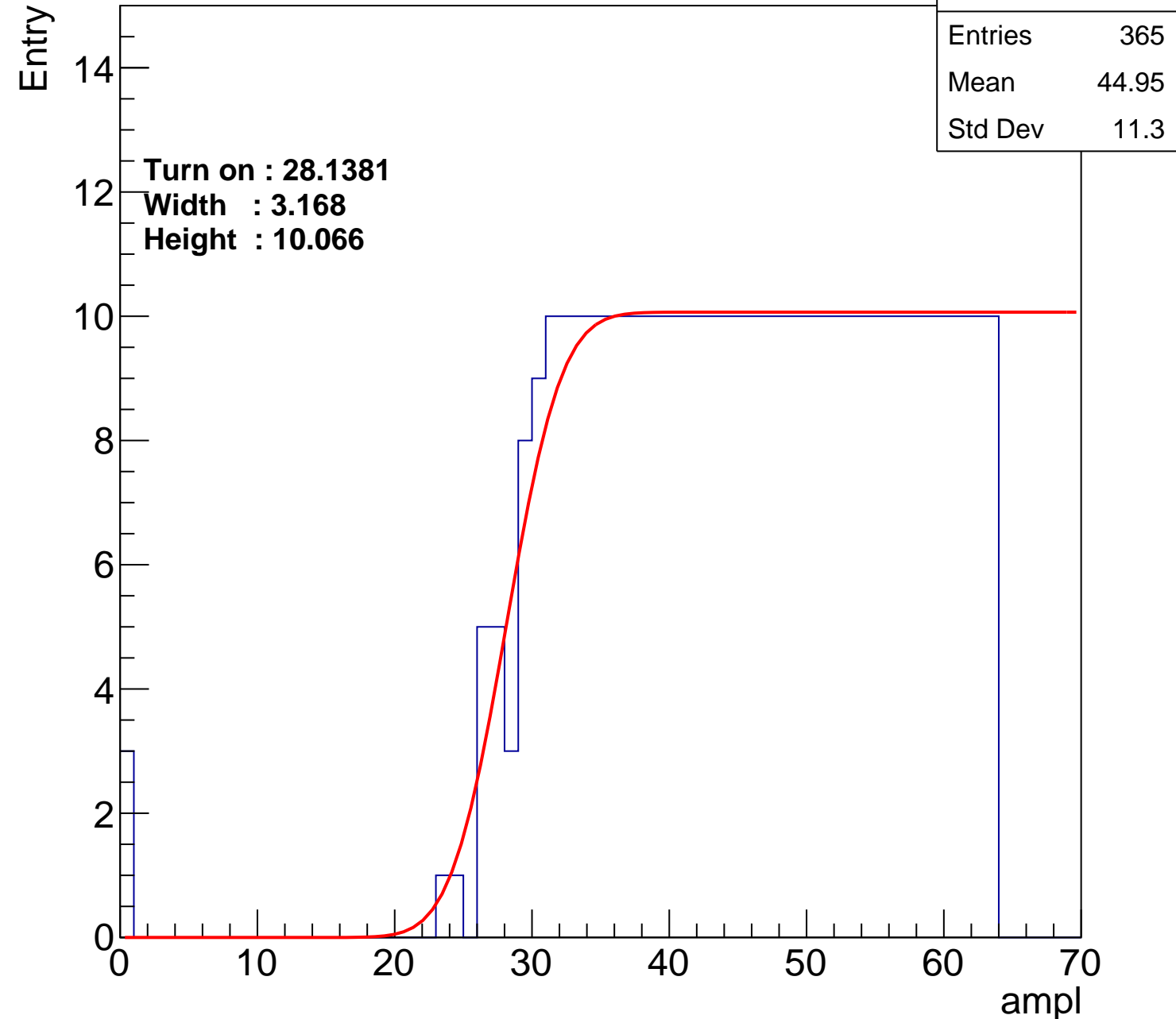
Width : 3.168

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.8
Std Dev	11.05

Turn on : 27.2556

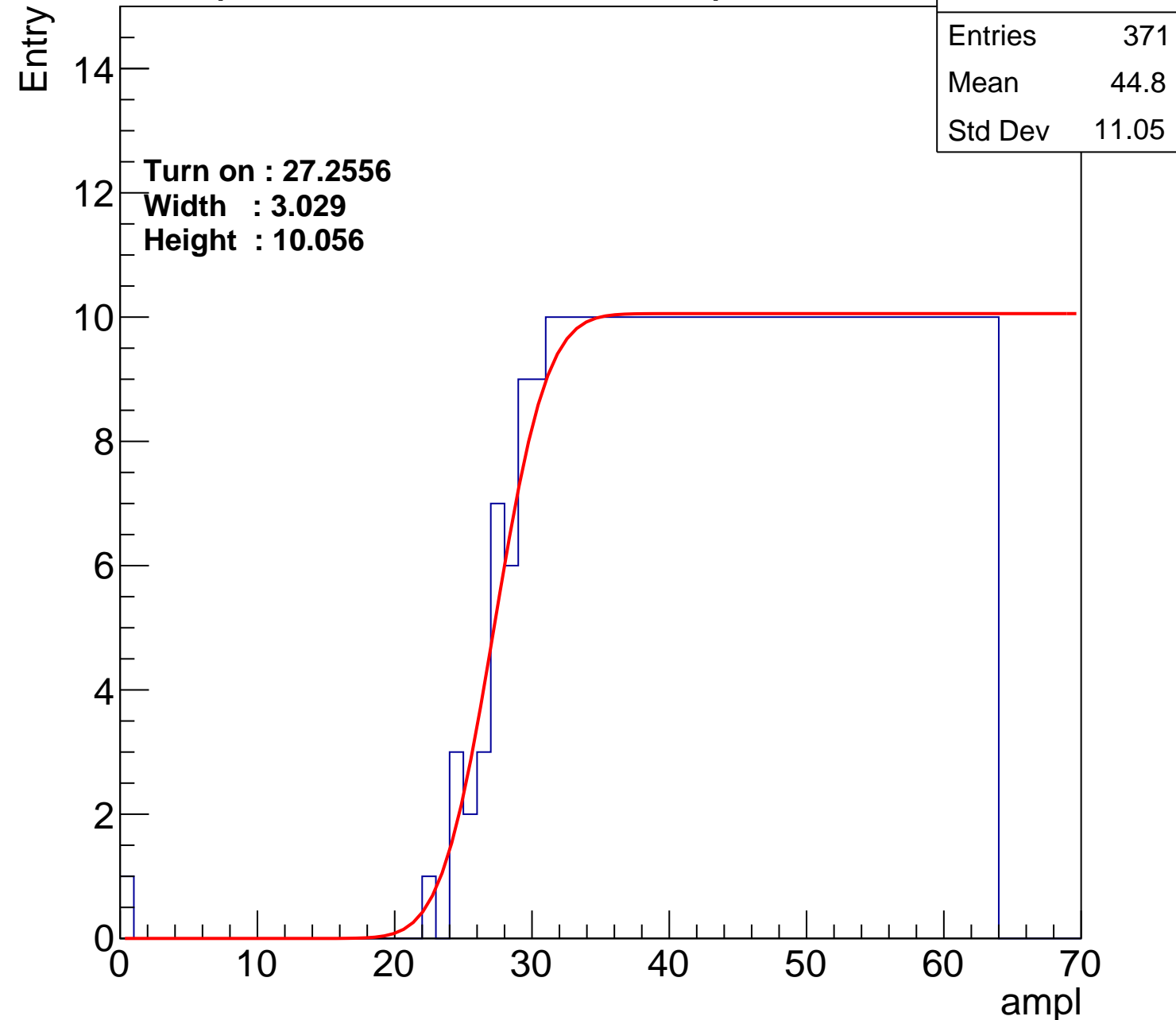
Width : 3.029

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.96
Std Dev	11.34

Turn on : 29.0622

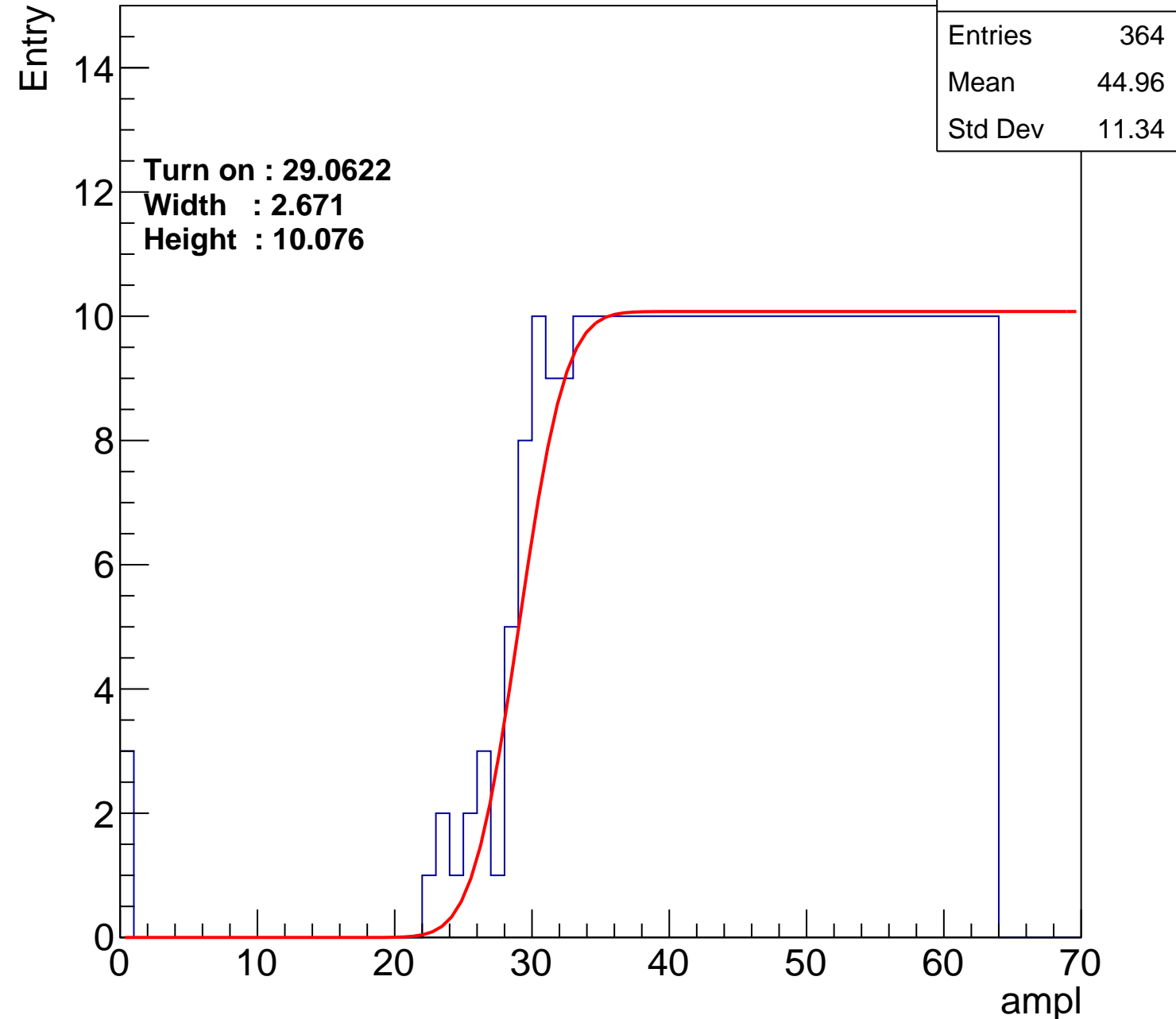
Width : 2.671

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.9868

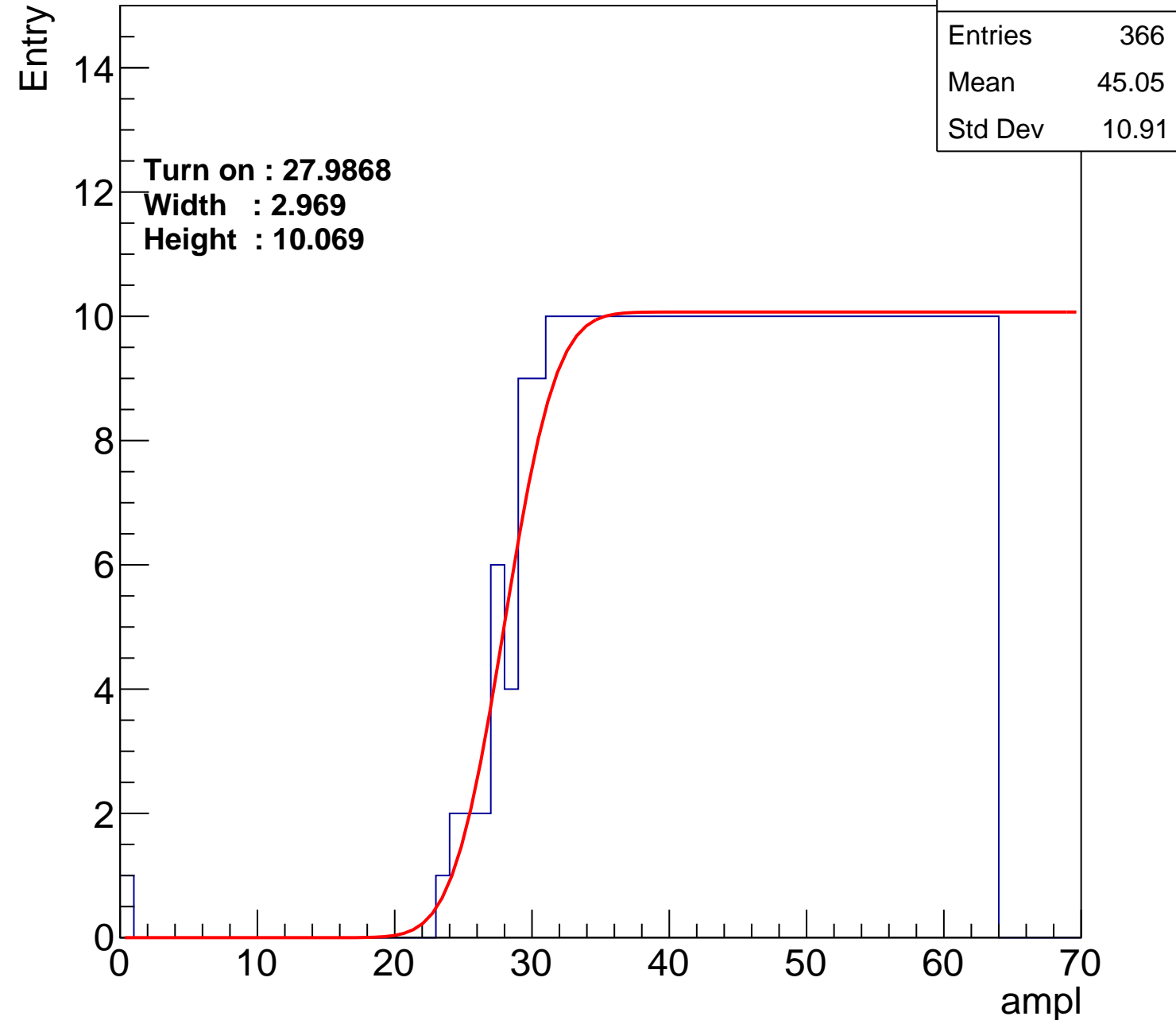
Width : 2.969

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.49
Std Dev	12.19

Turn on : 25.3418

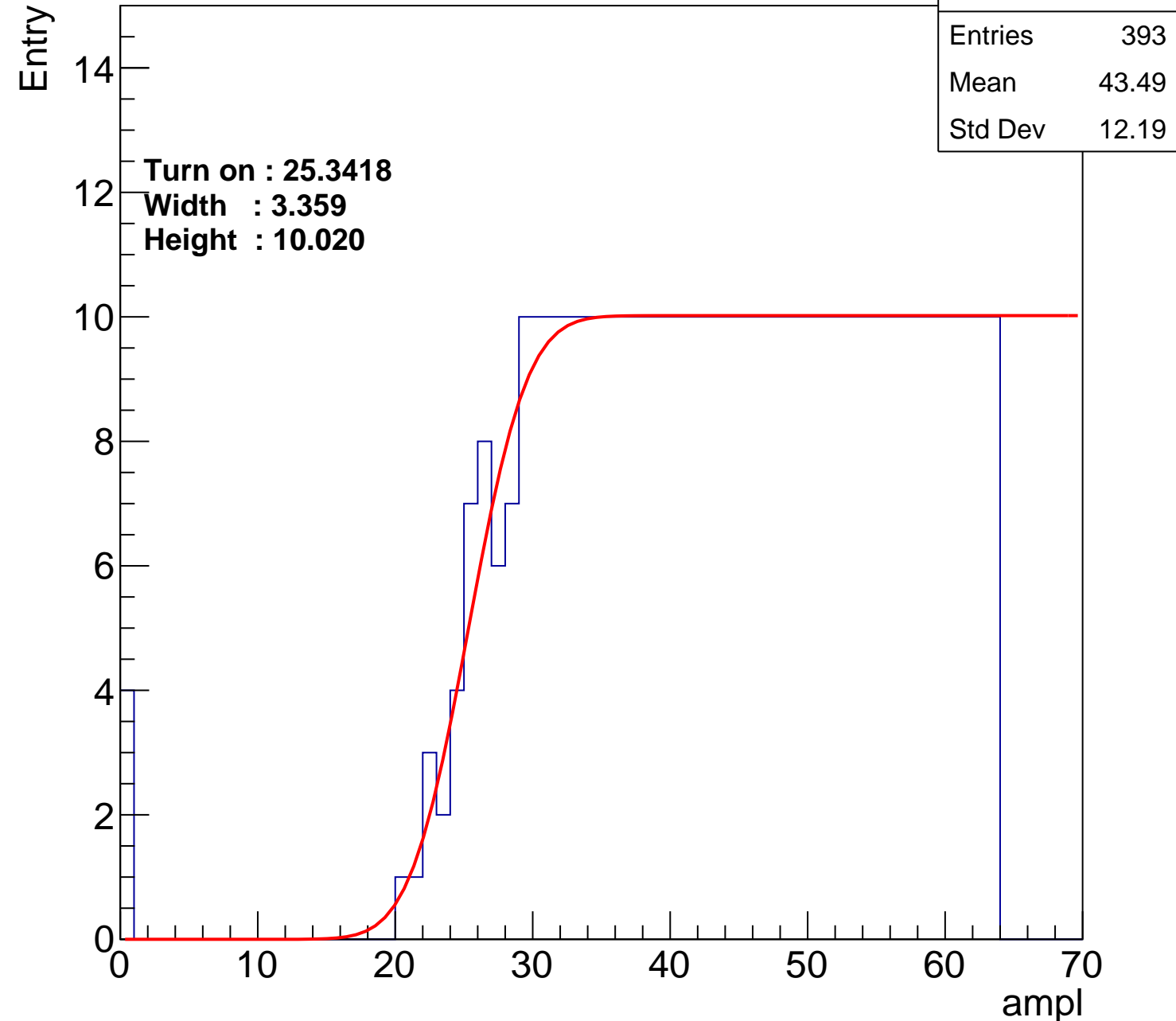
Width : 3.359

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.85
Std Dev	11.99

Turn on : 26.1771

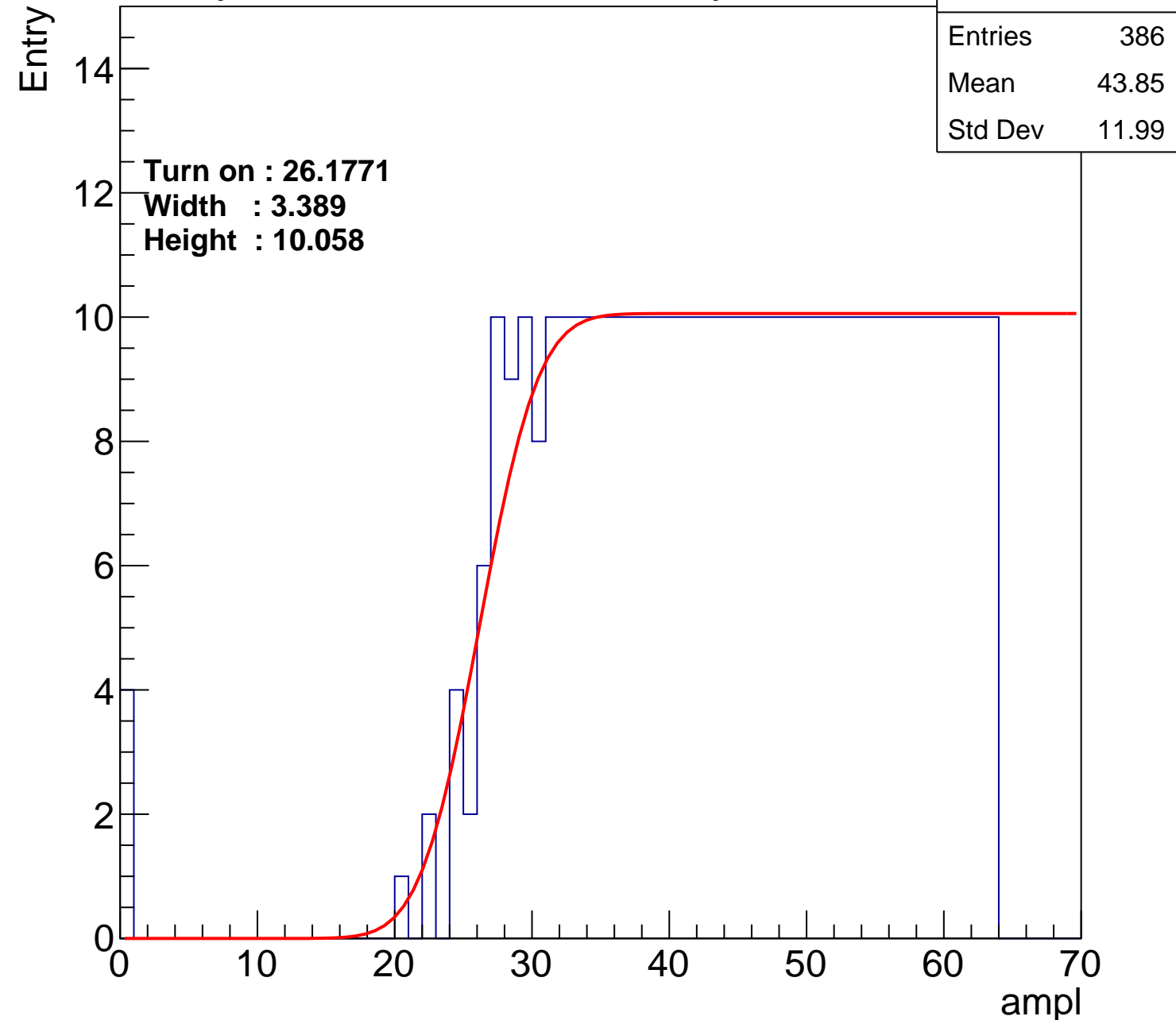
Width : 3.389

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch76

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.49
Std Dev	11.69

Turn on : 27.5994

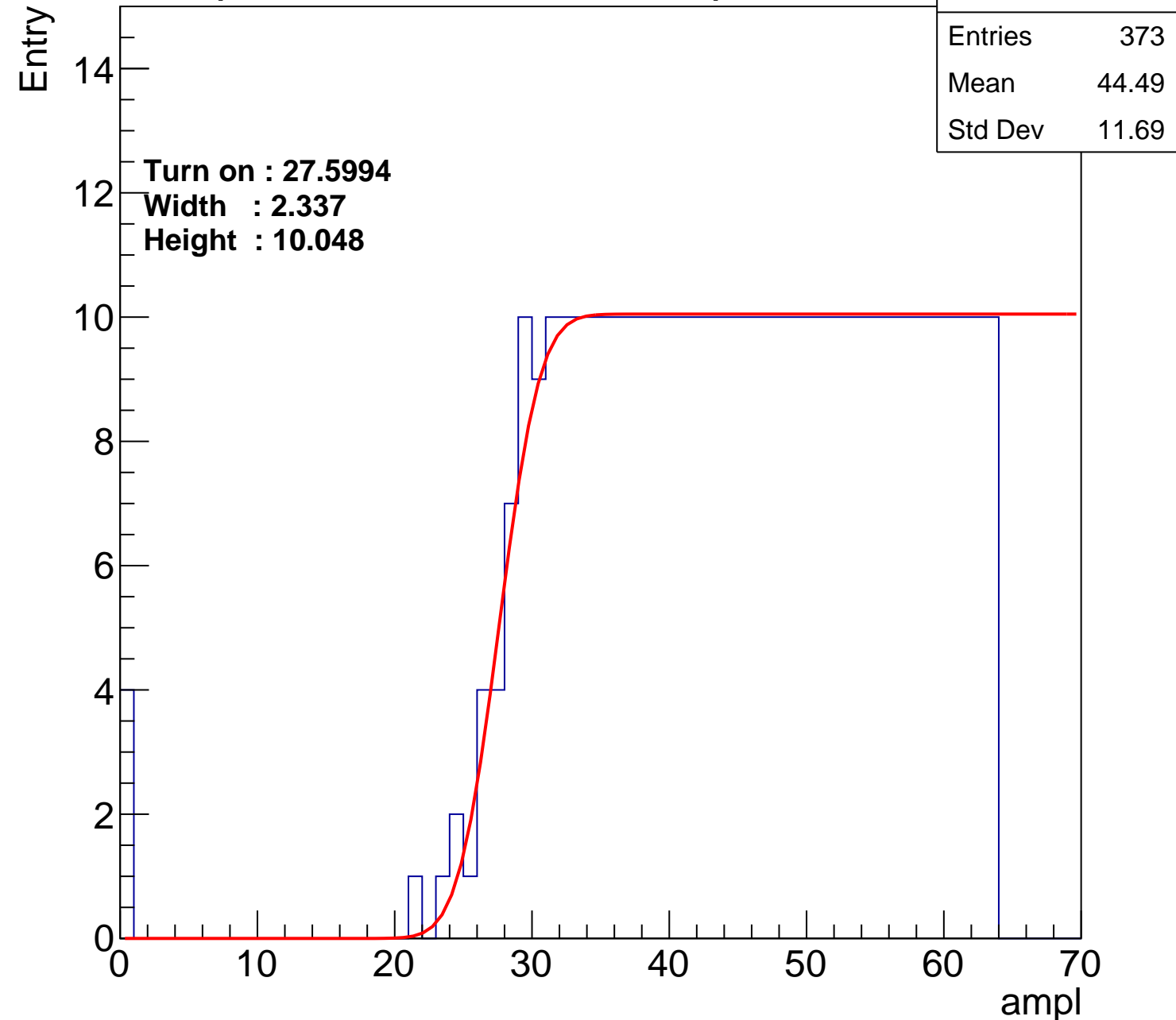
Width : 2.337

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.13
Std Dev	11.63

Turn on : 28.4327

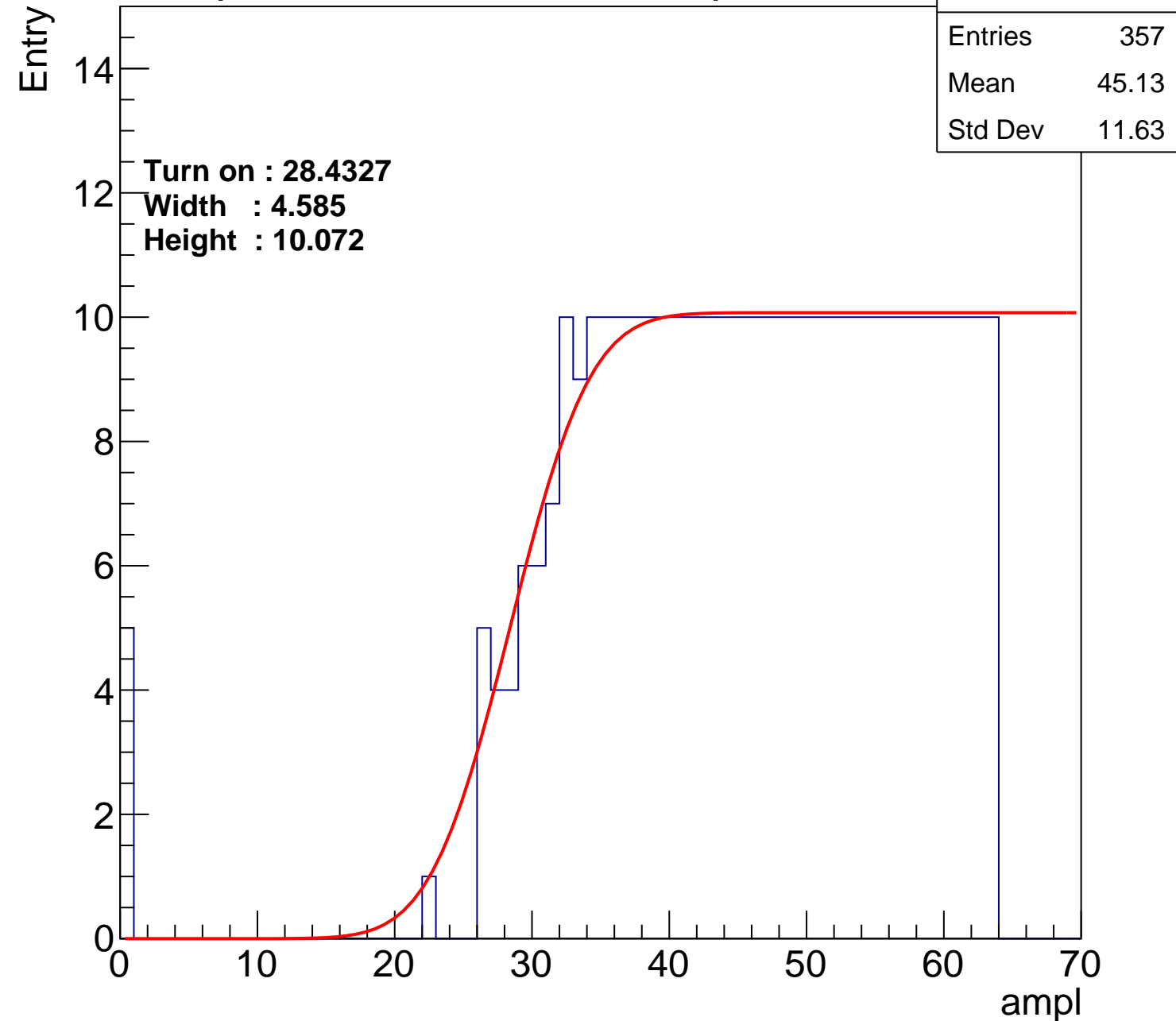
Width : 4.585

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch78

calib_packv5_042523_0143.root, FC#13, port D2

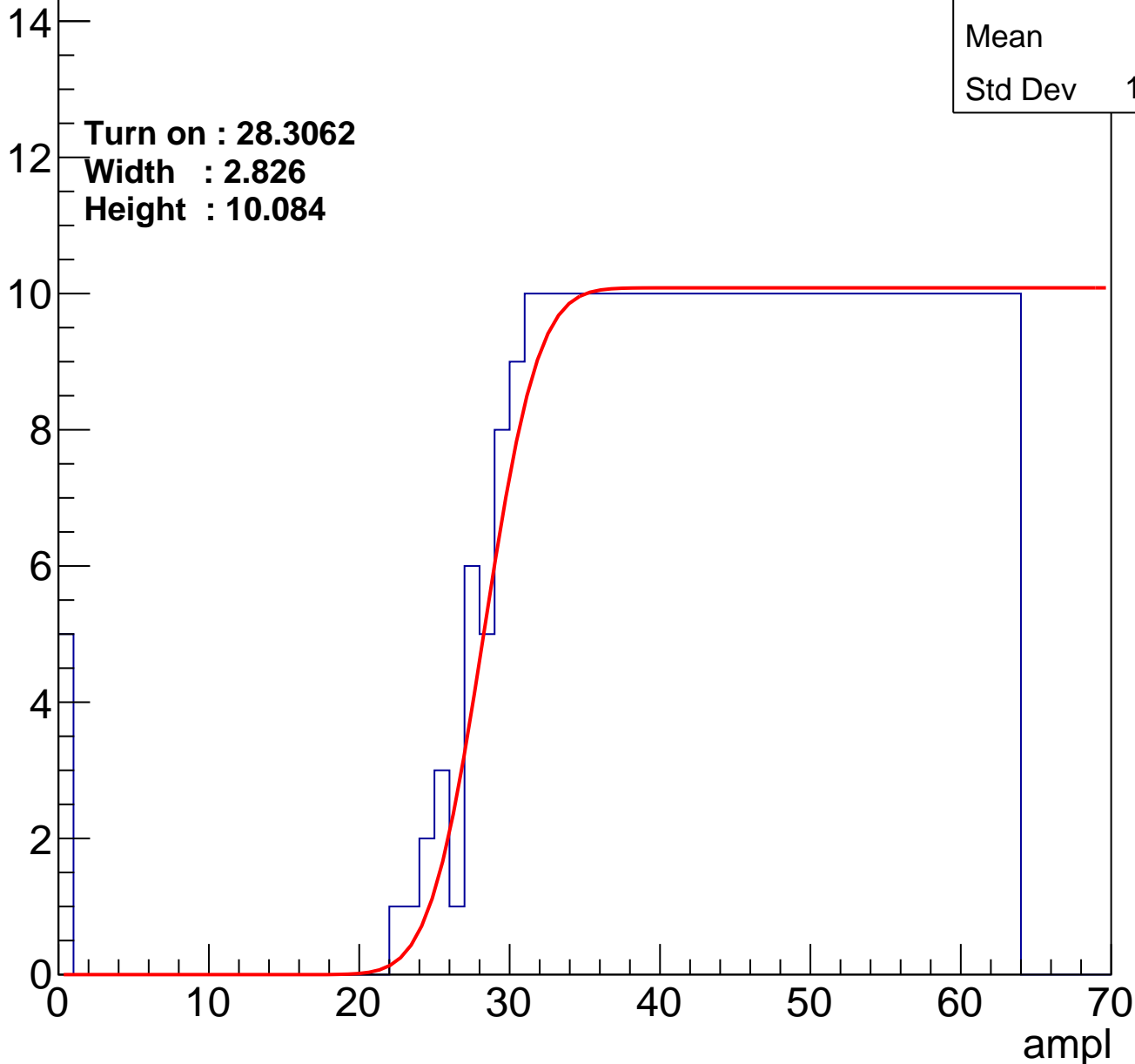
Entry

Entries	371
Mean	44.5
Std Dev	11.86

Turn on : 28.3062

Width : 2.826

Height : 10.084



B1L003S, U11-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.92
Std Dev	11.2

Turn on : 27.5812

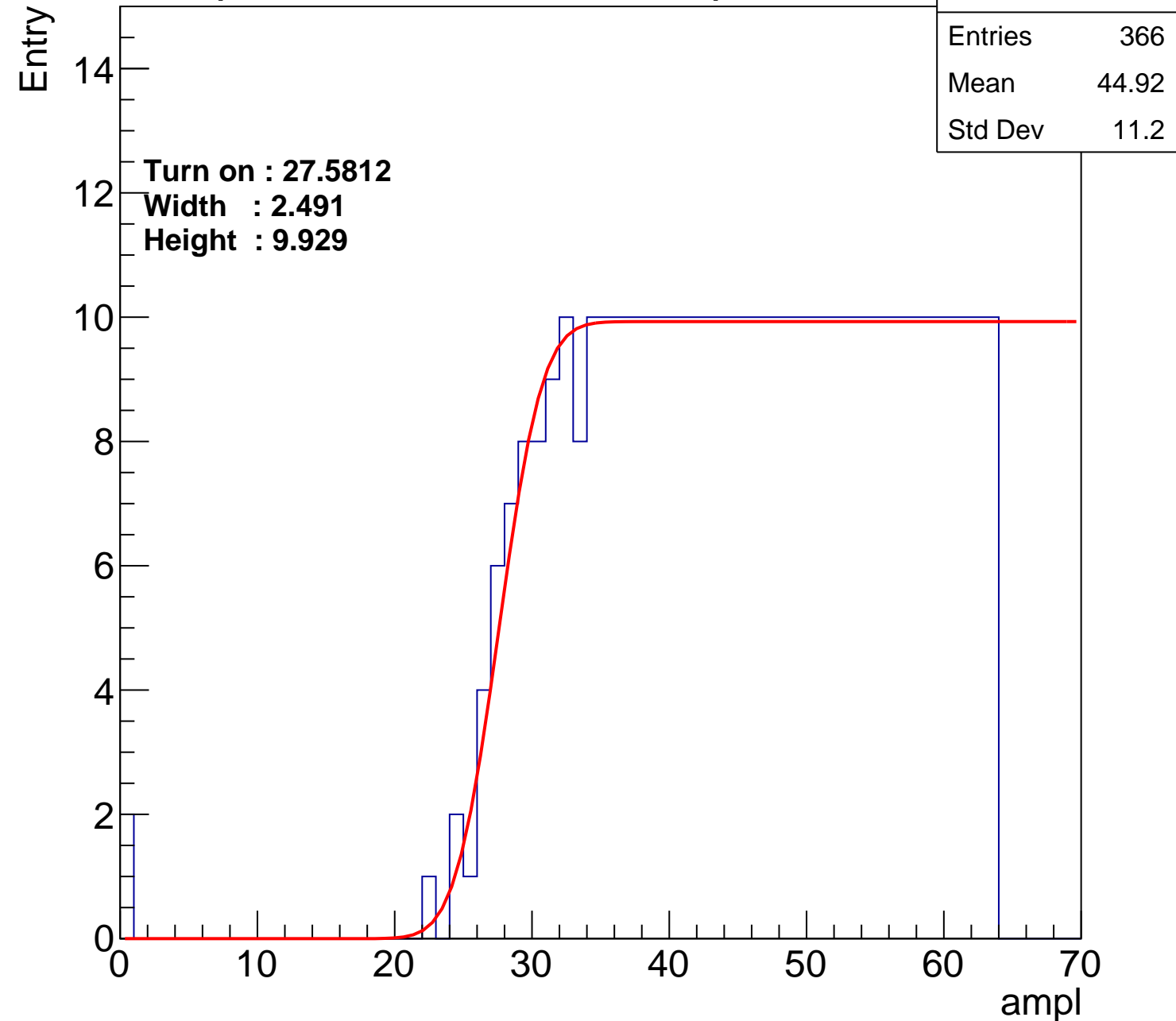
Width : 2.491

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch80

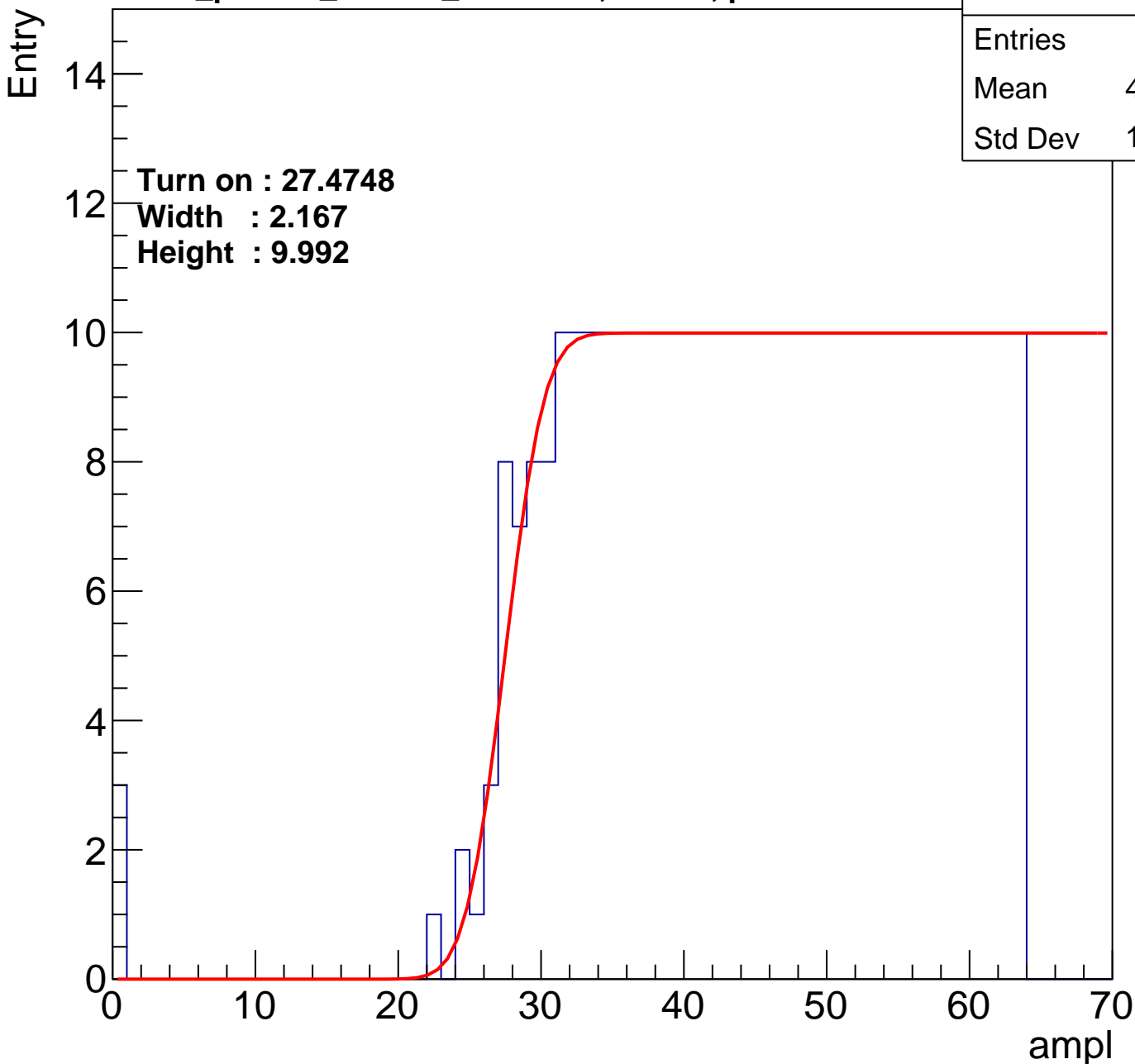
calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.65
Std Dev	11.45

Turn on : 27.4748

Width : 2.167

Height : 9.992



B1L003S, U11-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.89
Std Dev	10.96

Turn on : 27.3788

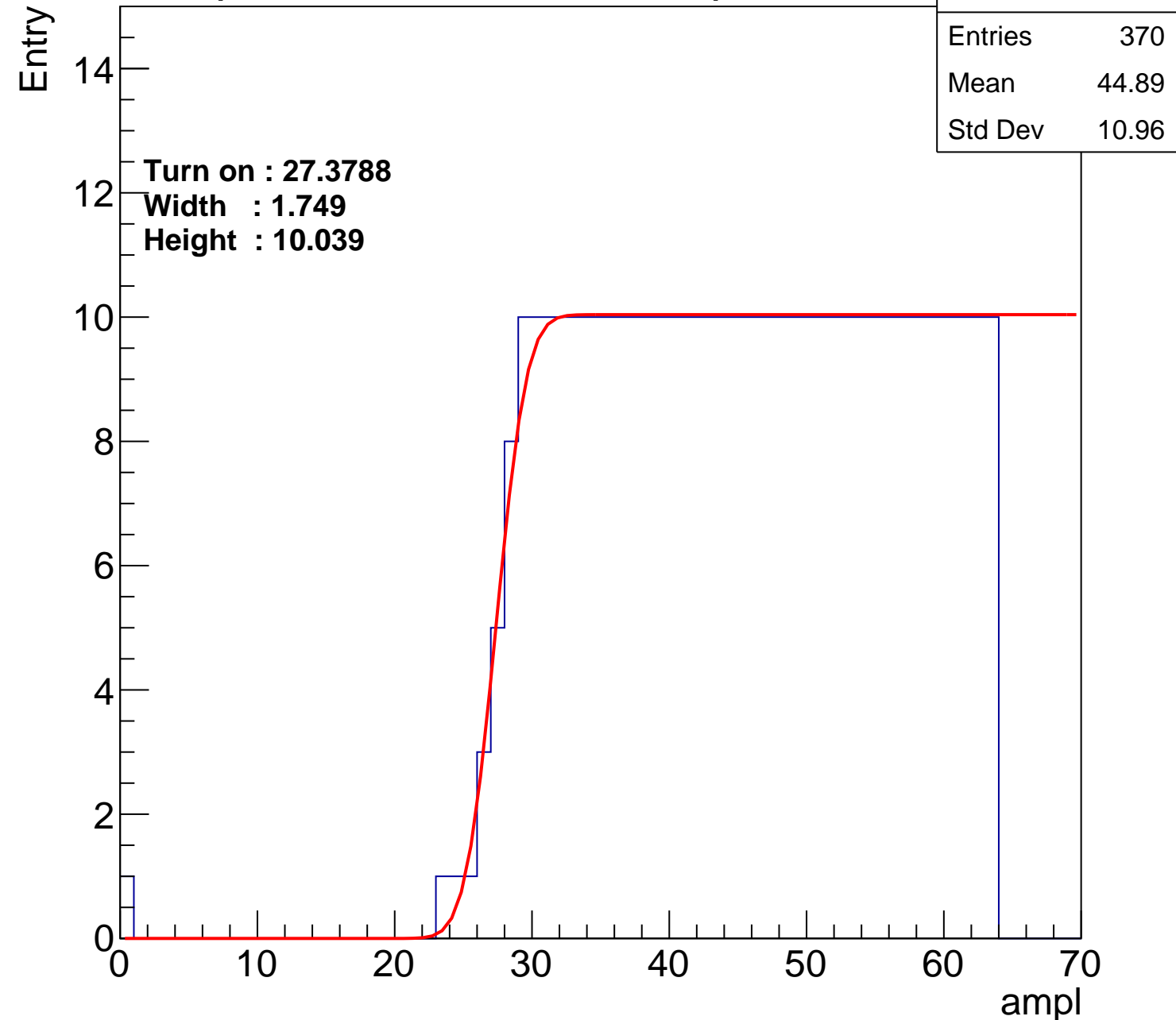
Width : 1.749

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.19
Std Dev	10.99

Turn on : 27.9918

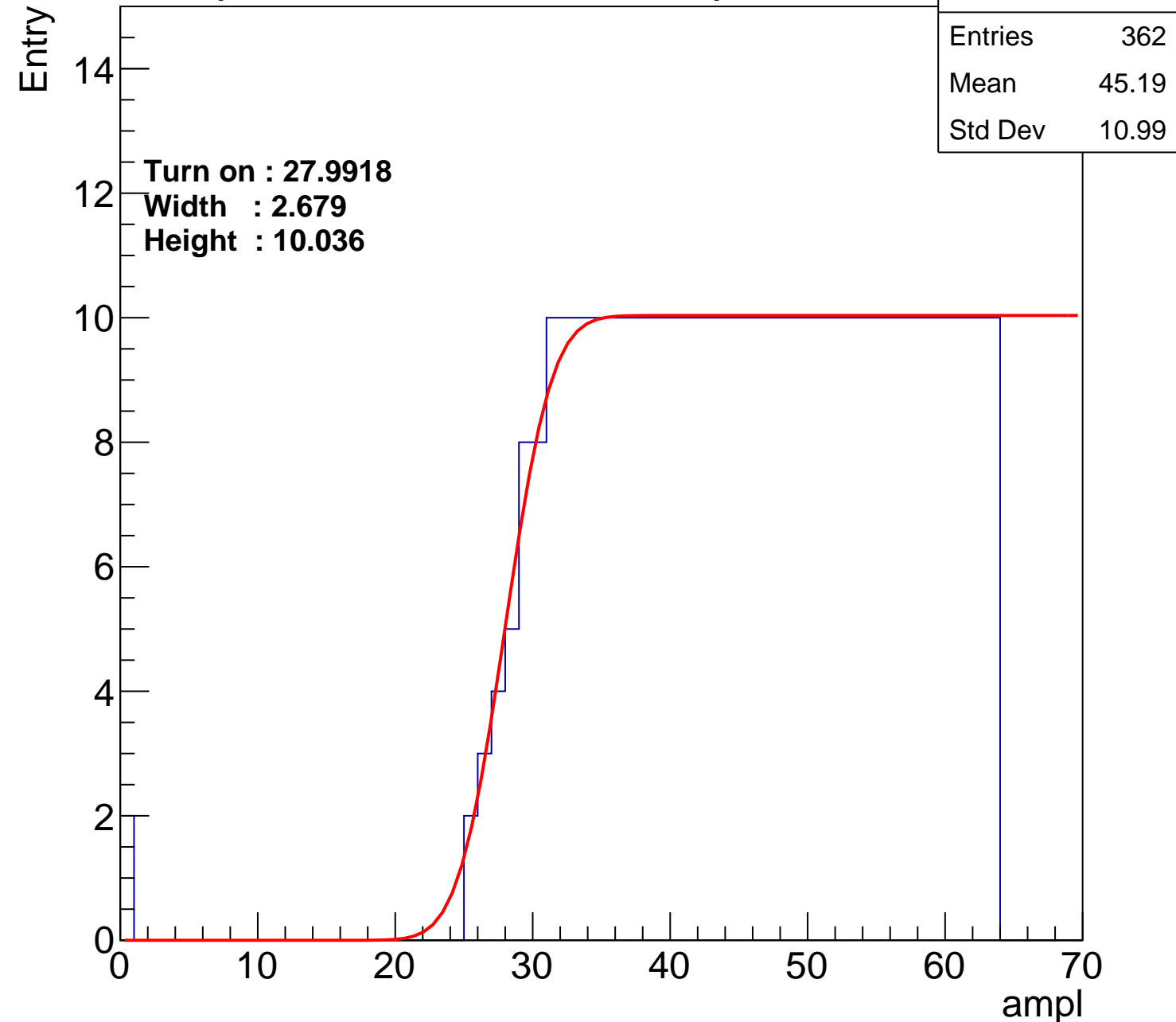
Width : 2.679

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.85
Std Dev	11.01

Turn on : 27.0375

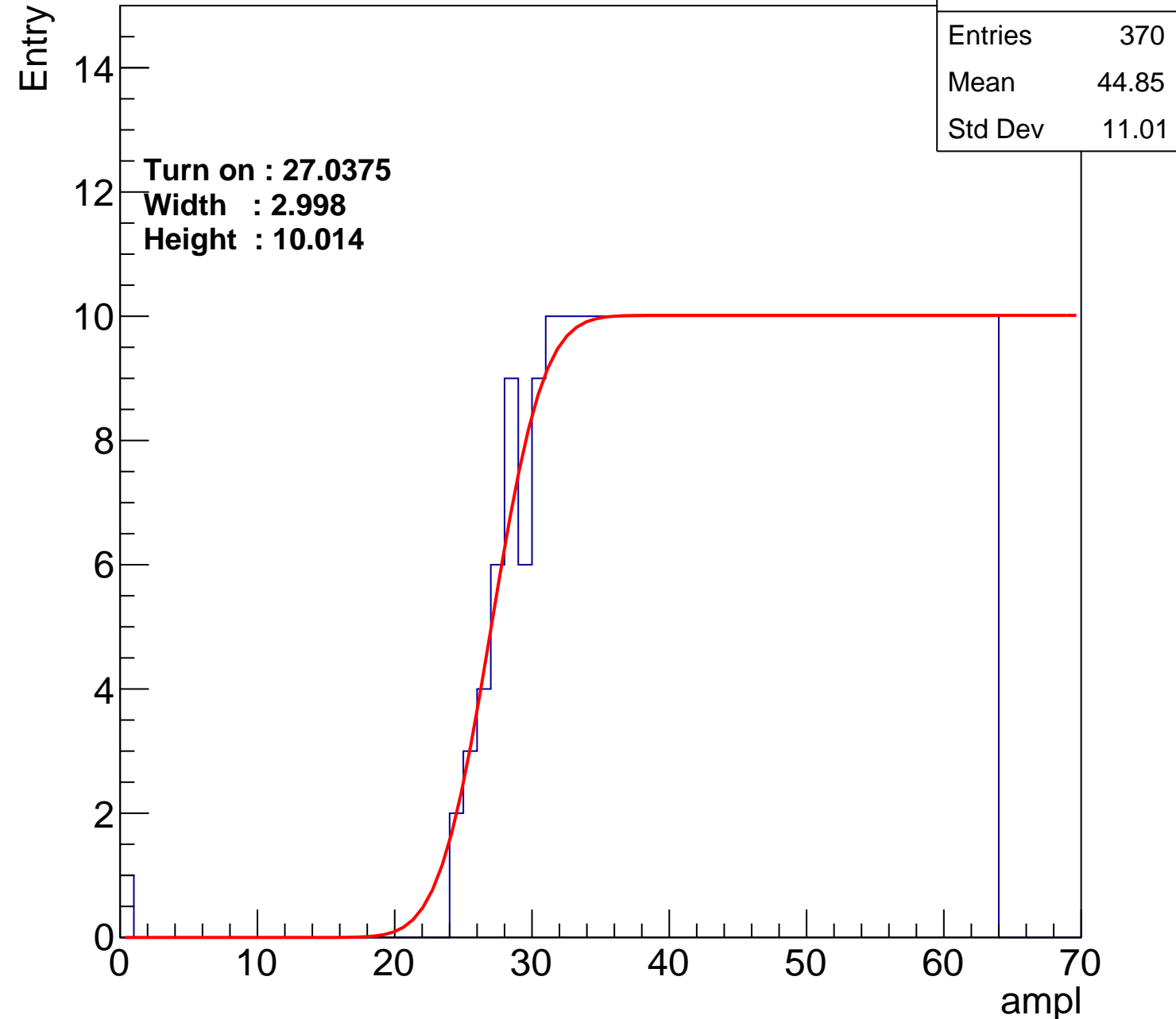
Width : 2.998

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.9911

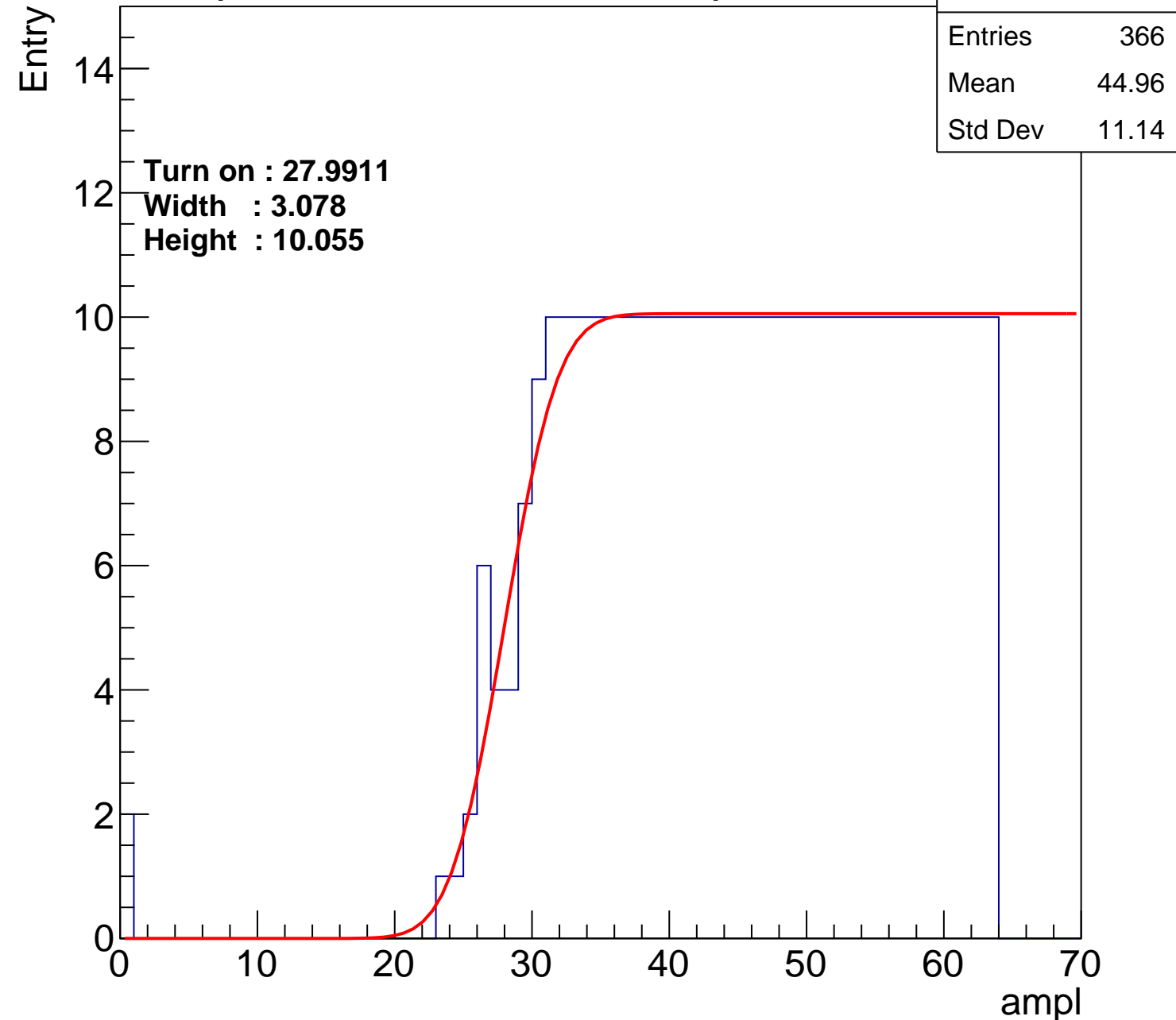
Width : 3.078

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch85

calib_packv5_042523_0143.root, FC#13, port D2

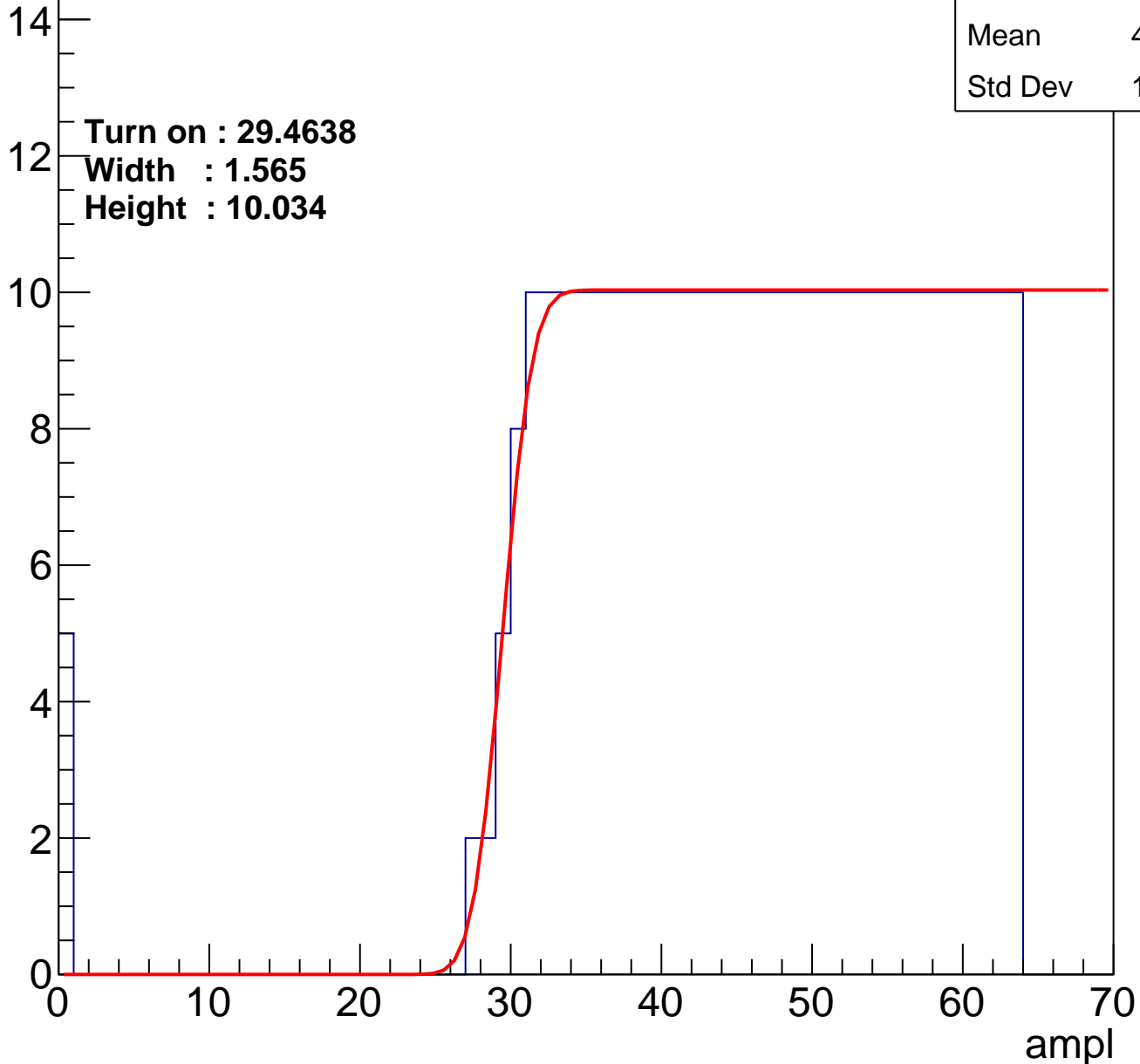
Entries	352
Mean	45.47
Std Dev	11.38

Turn on : 29.4638

Width : 1.565

Height : 10.034

Entry



B1L003S, U11-ch86

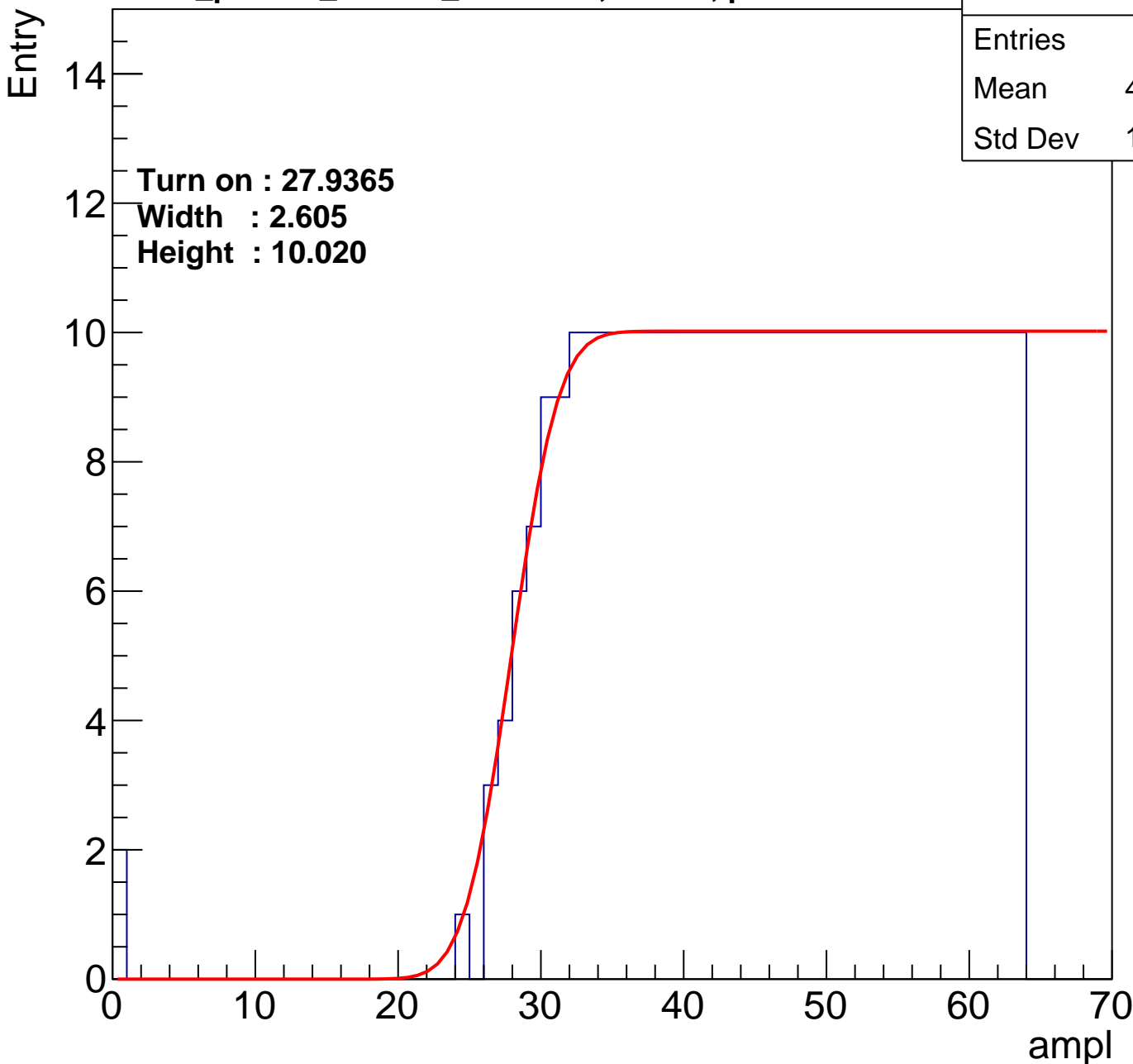
calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.24
Std Dev	10.97

Turn on : 27.9365

Width : 2.605

Height : 10.020



B1L003S, U11-ch87

calib_packv5_042523_0143.root, FC#13, port D2

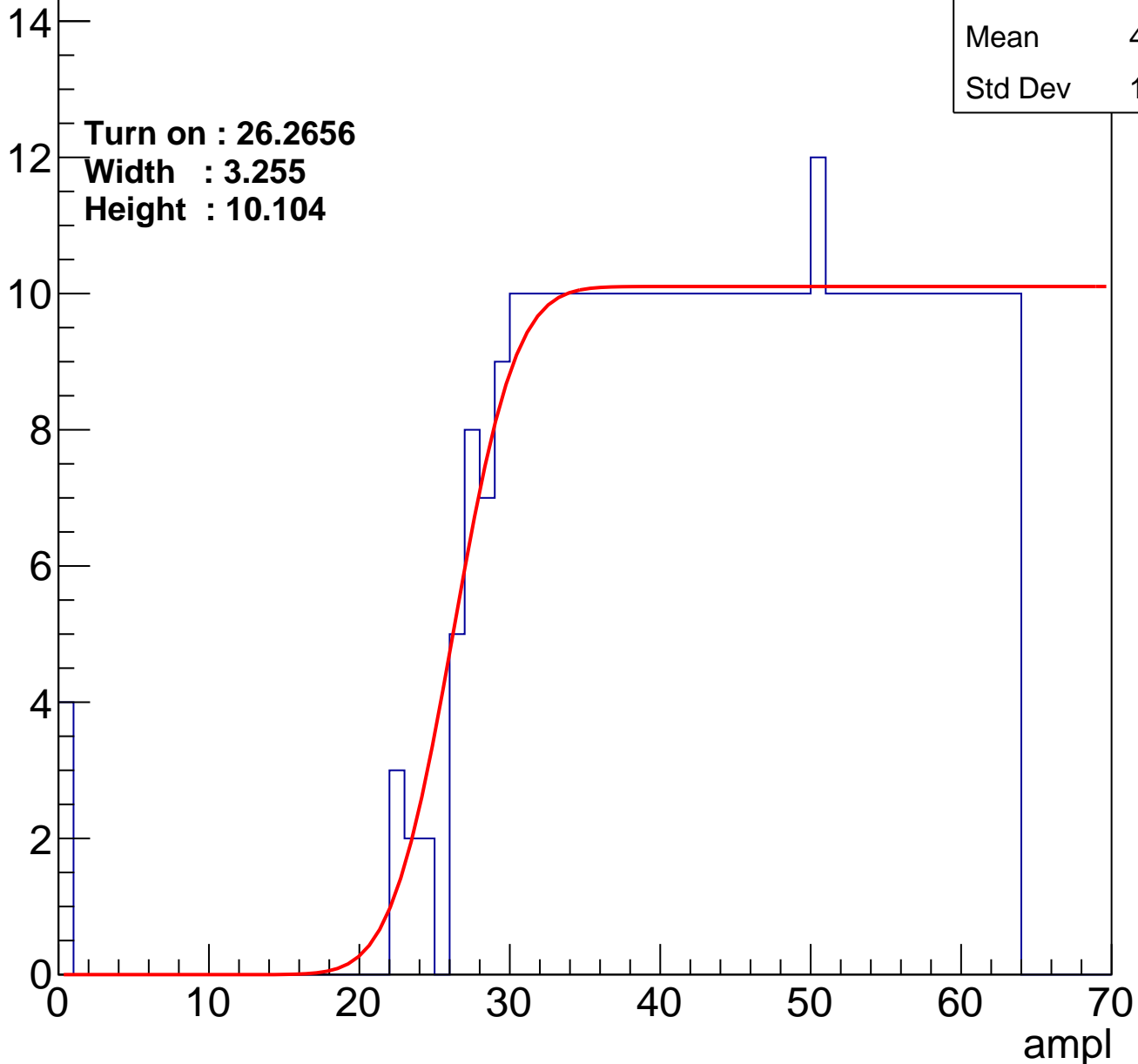
Entries	382
Mean	44.17
Std Dev	11.84

Turn on : 26.2656

Width : 3.255

Height : 10.104

Entry



B1L003S, U11-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.6
Std Dev	11.48

Turn on : 27.4823

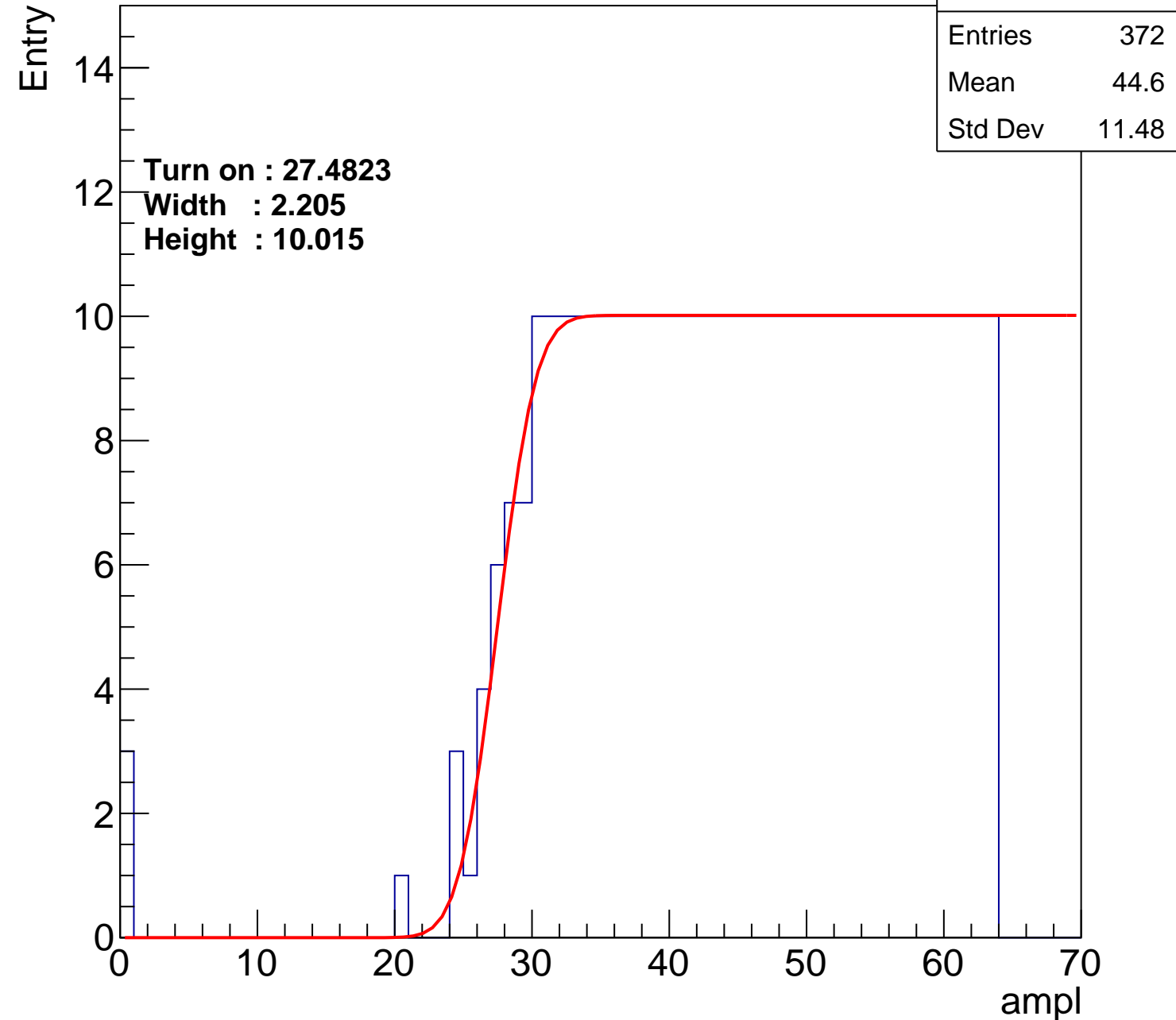
Width : 2.205

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch89

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.78
Std Dev	12.16

Turn on : 25.8848

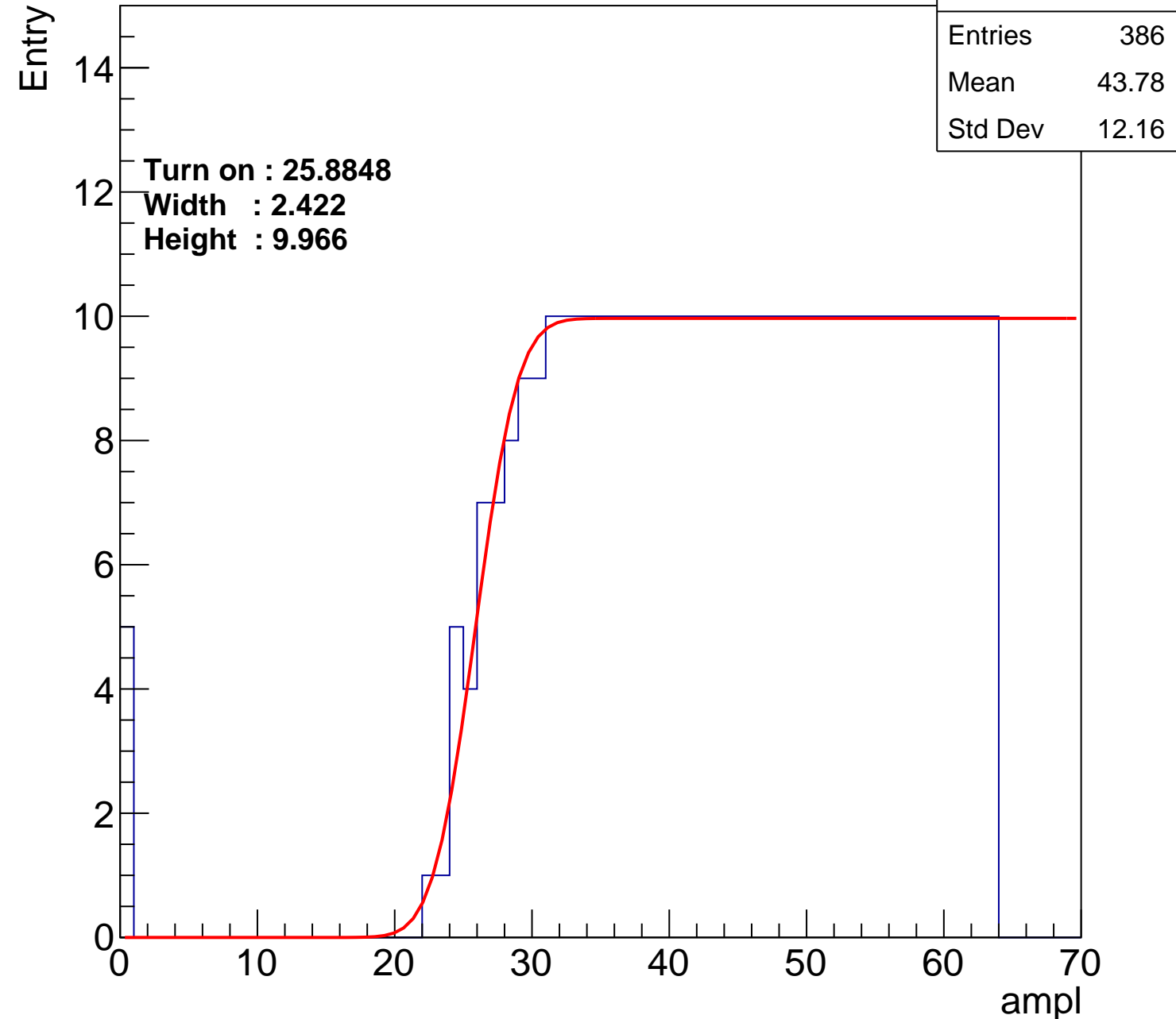
Width : 2.422

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.96
Std Dev	11.19

Turn on : 27.8347

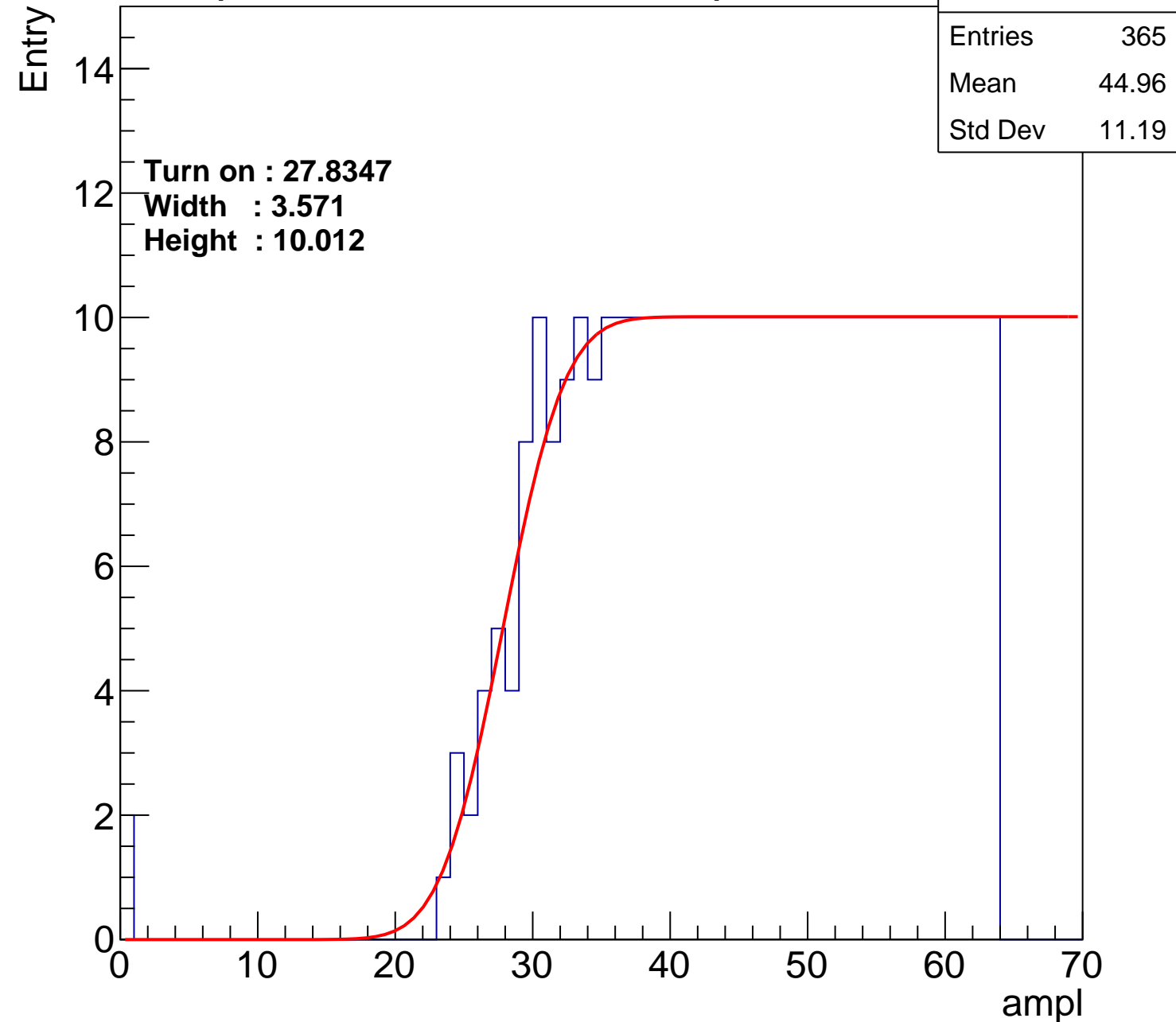
Width : 3.571

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch91

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.11
Std Dev	10.87

Turn on : 28.3413

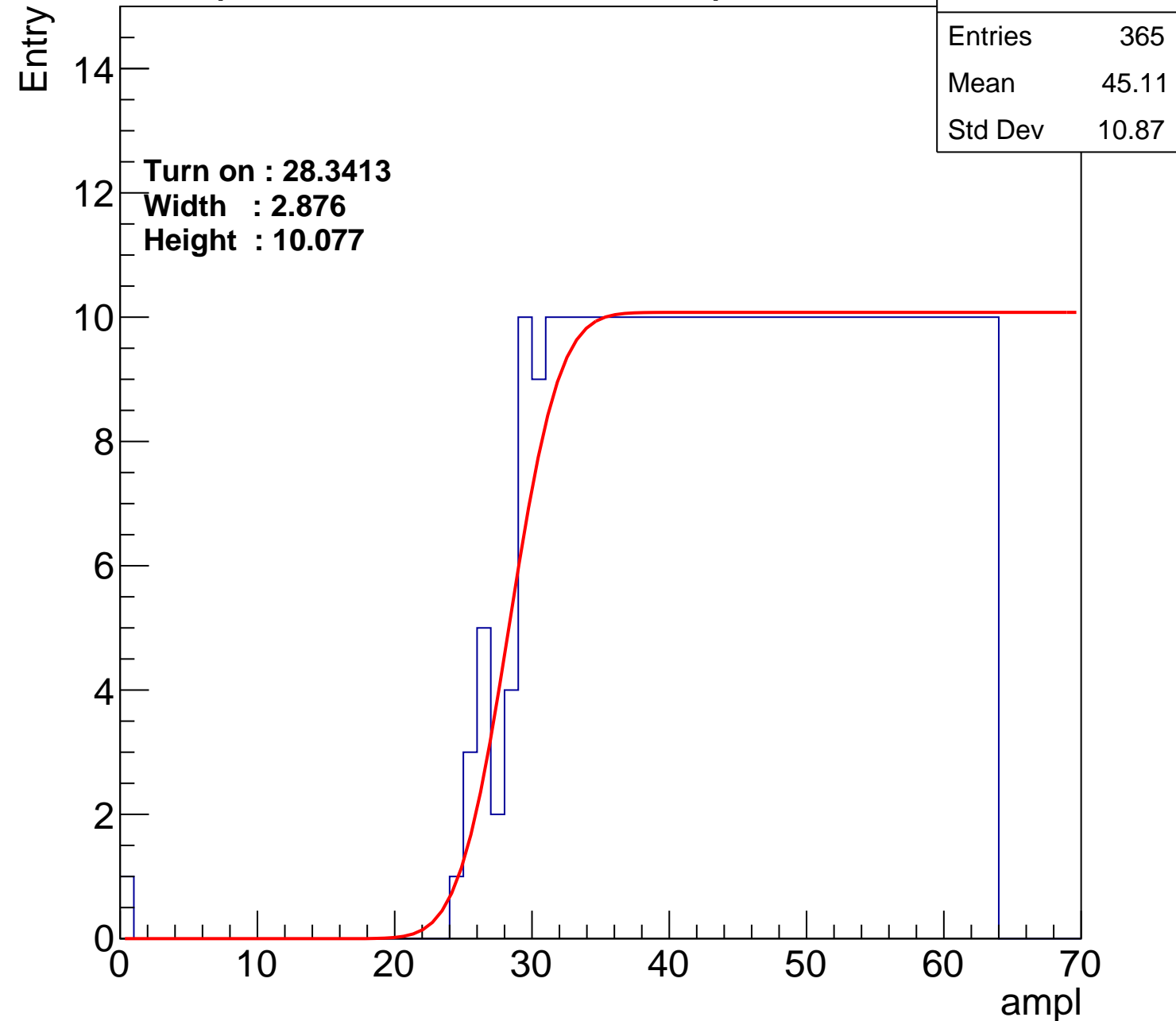
Width : 2.876

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.9208

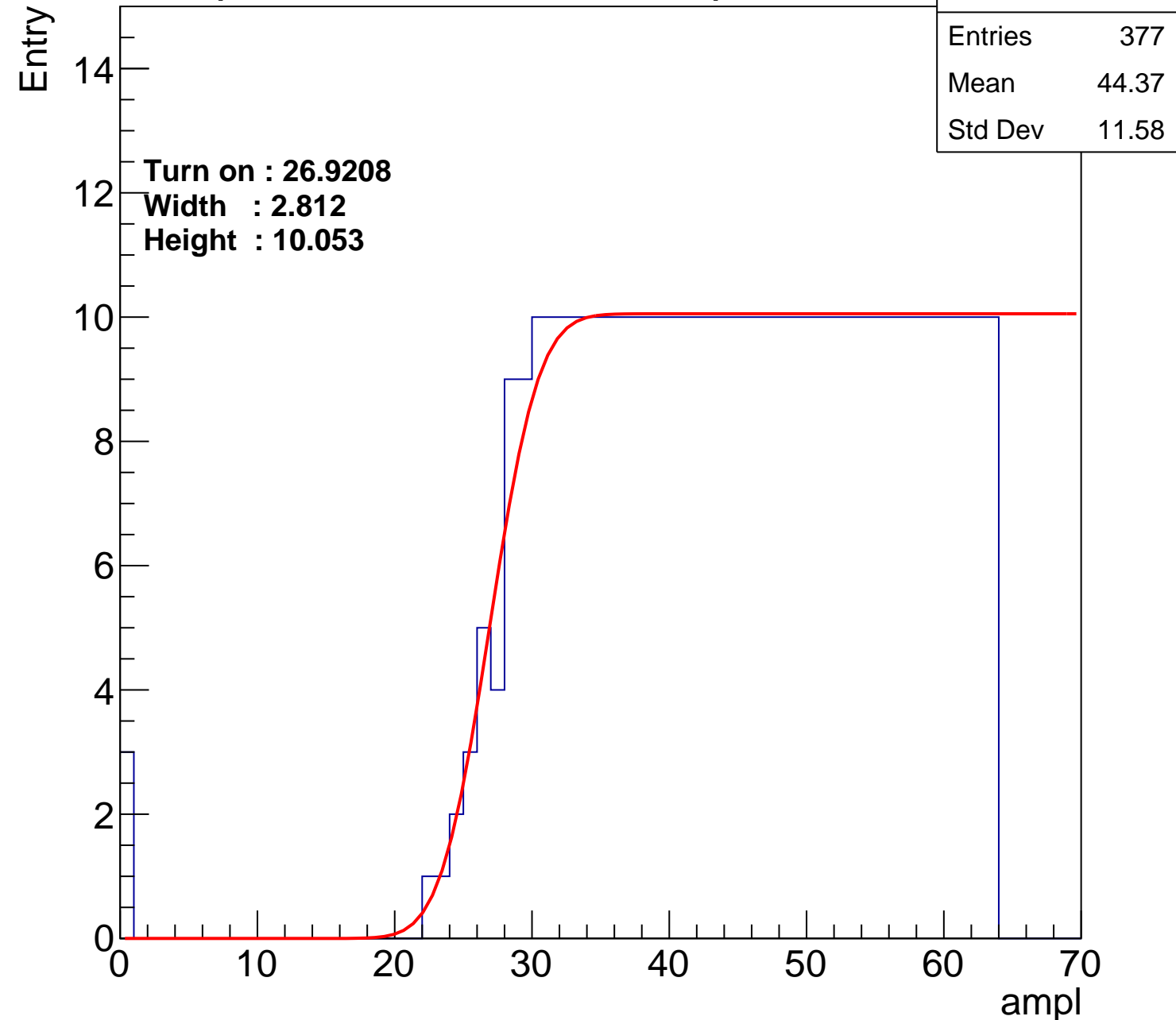
Width : 2.812

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.02
Std Dev	11.25

Turn on : 27.6420

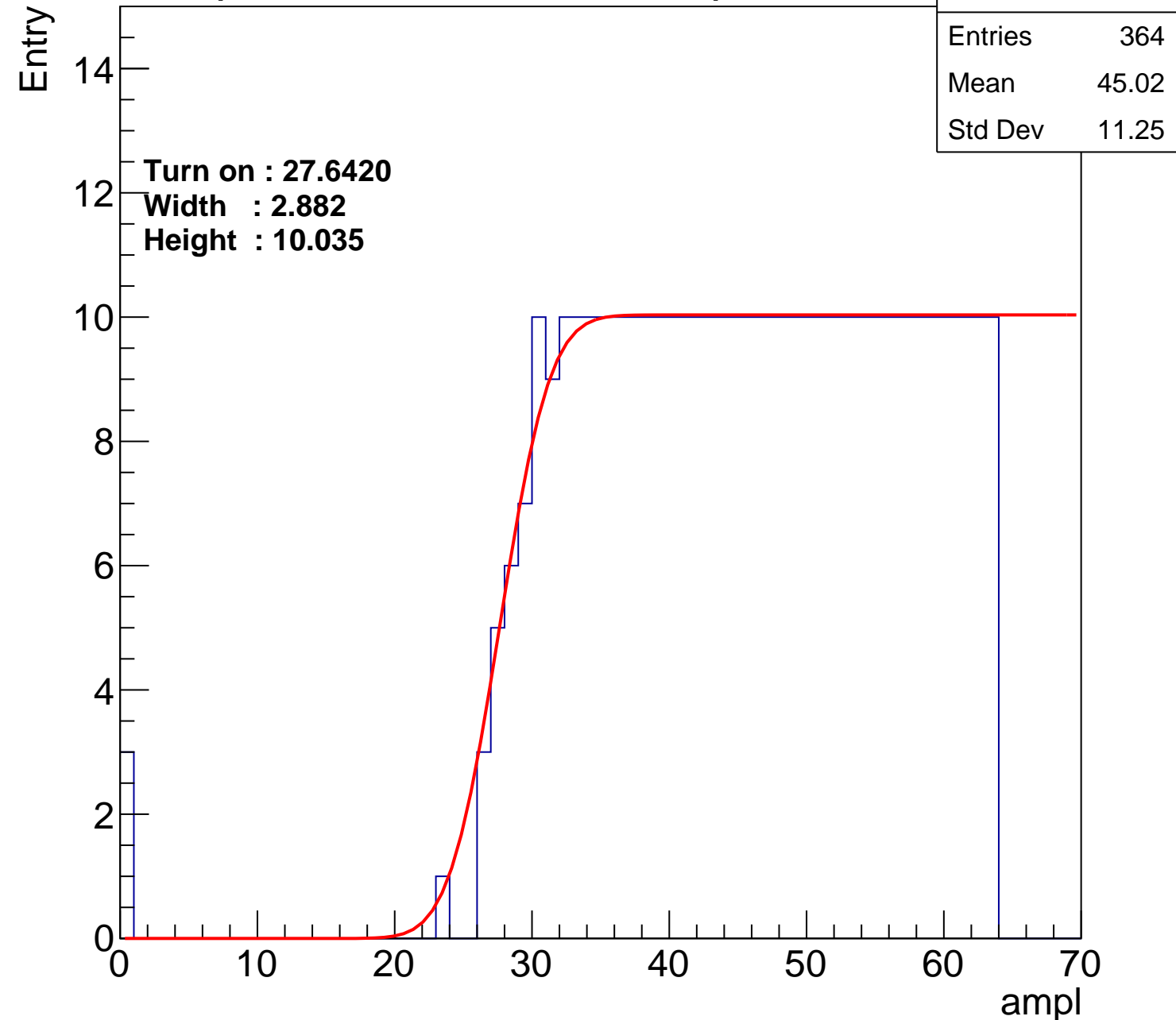
Width : 2.882

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch94

calib_packv5_042523_0143.root, FC#13, port D2

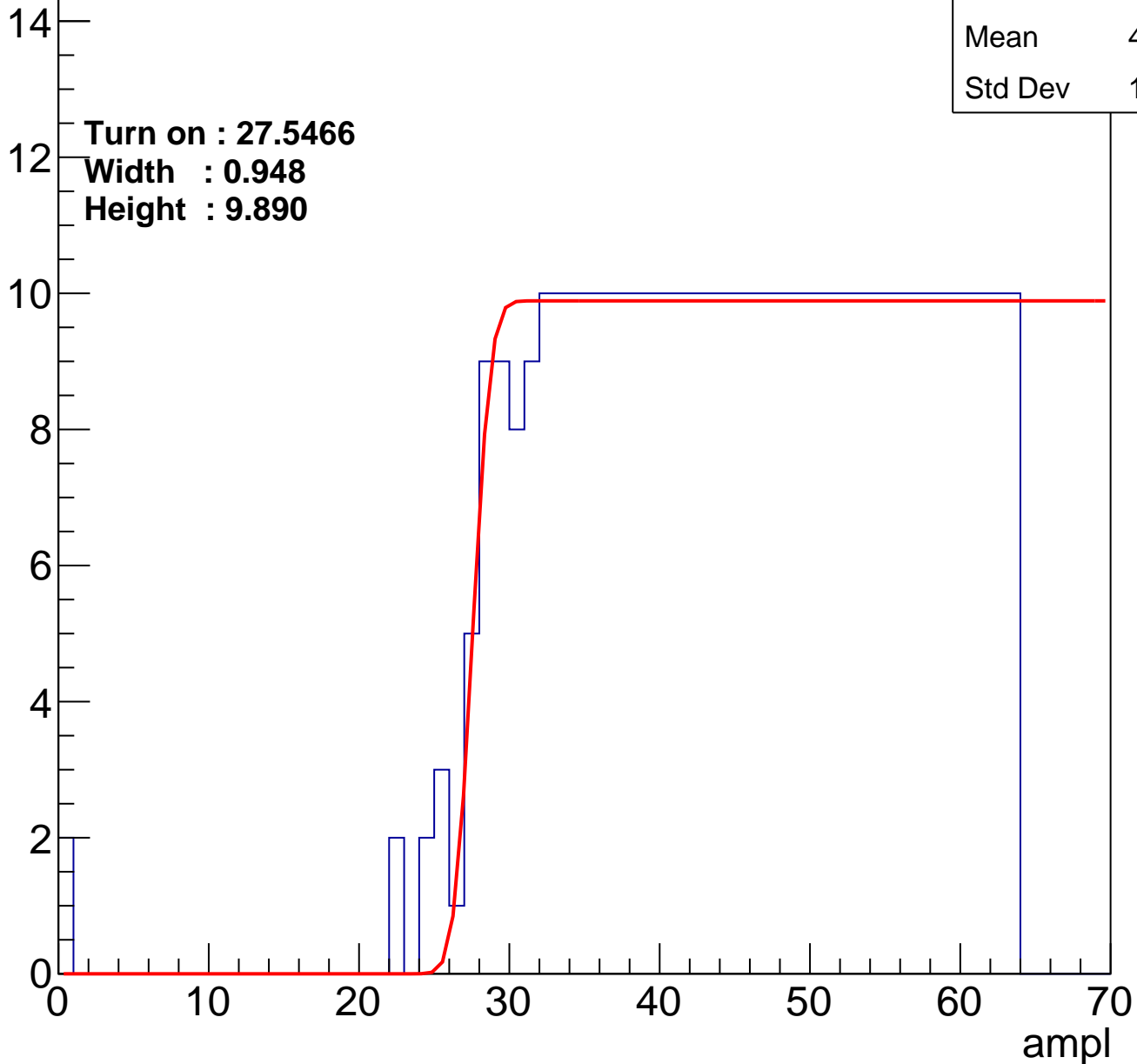
Entry

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 27.5466

Width : 0.948

Height : 9.890



B1L003S, U11-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.87
Std Dev	12.01

Turn on : 26.0263

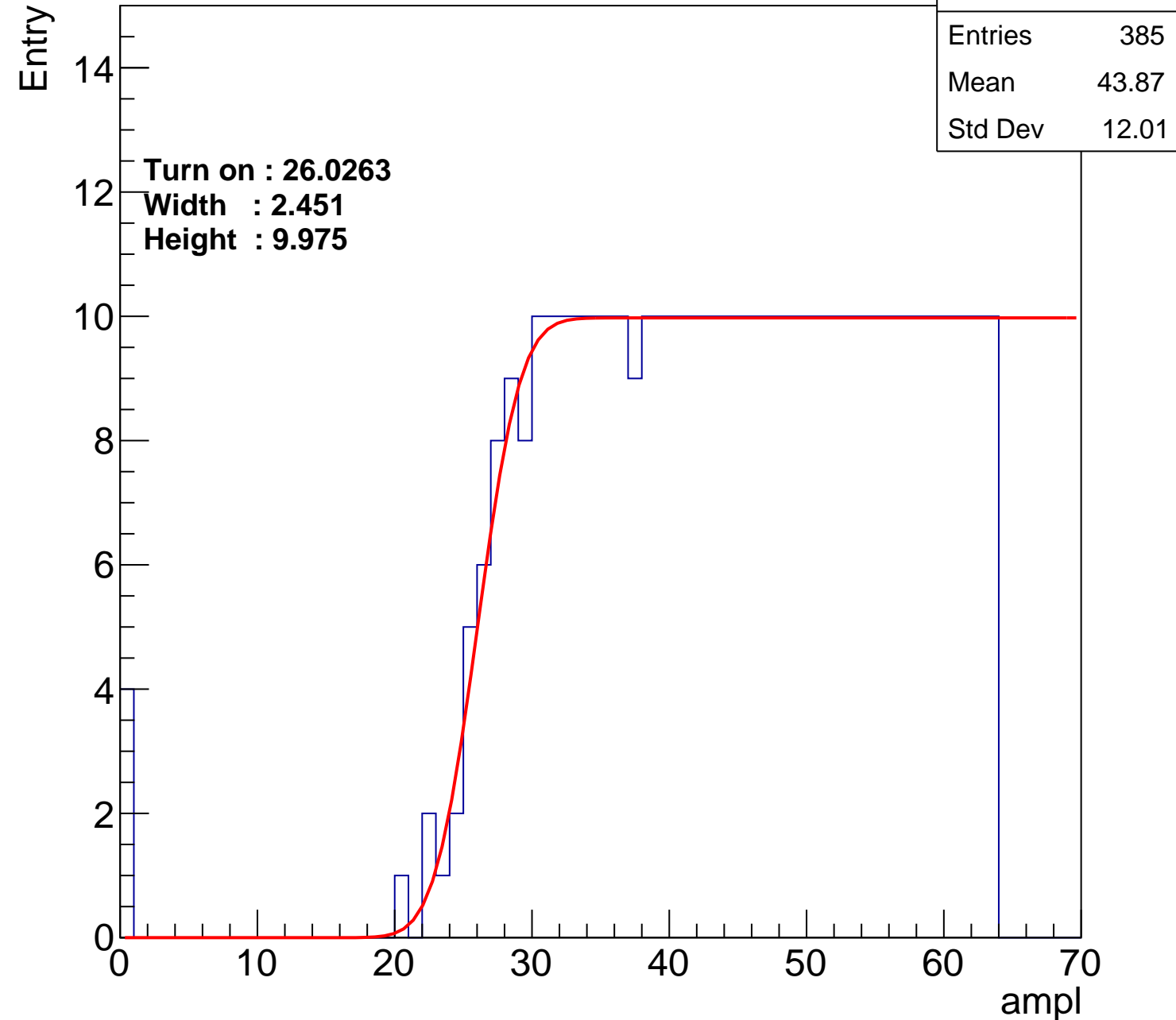
Width : 2.451

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entry

450
400
350
300
250
200
150
100
50
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	24948
Mean	31.66
Std Dev	18.46

ampl

B1L003S, U11-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.2578

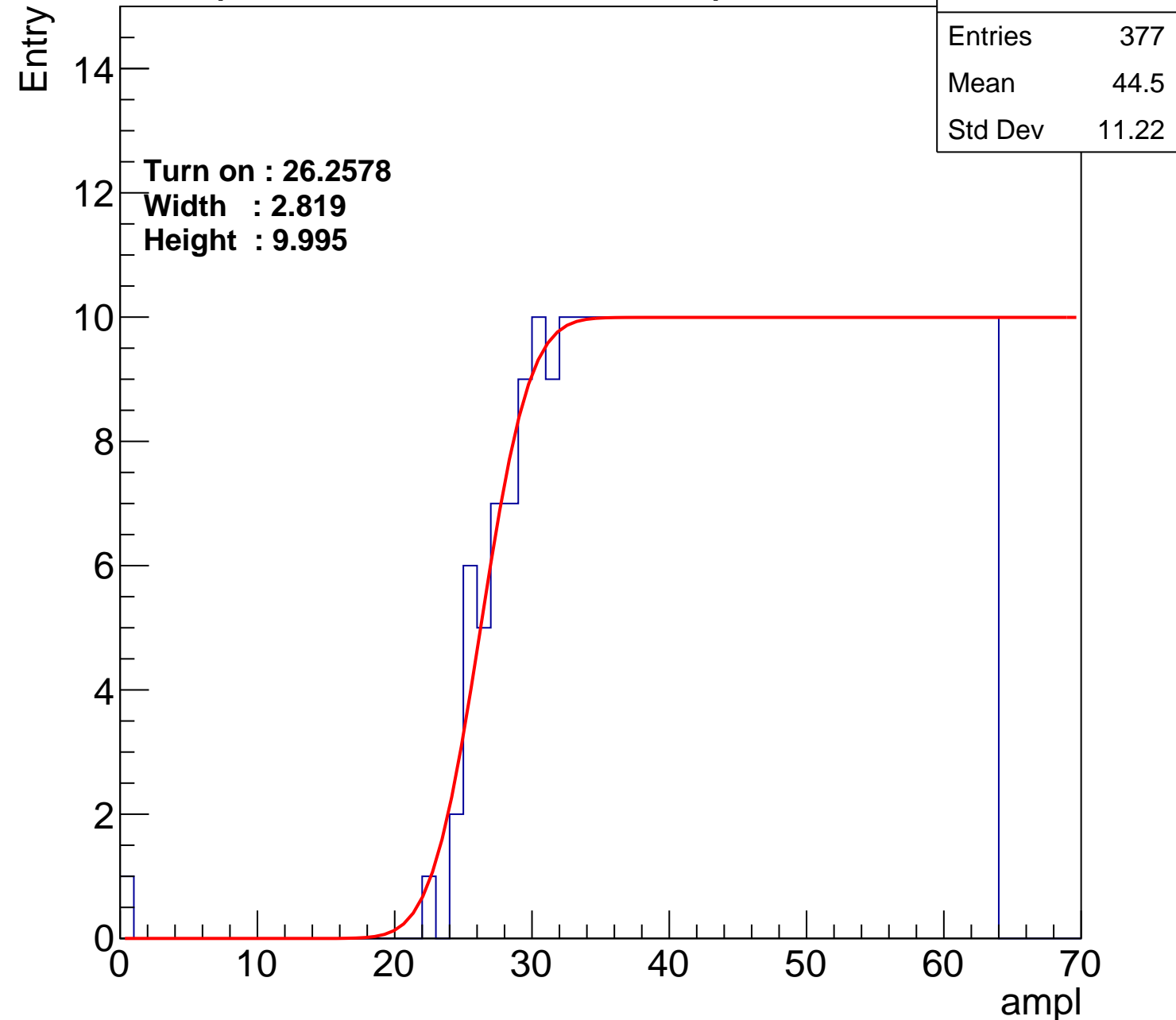
Width : 2.819

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch98

calib_packv5_042523_0143.root, FC#13, port D2

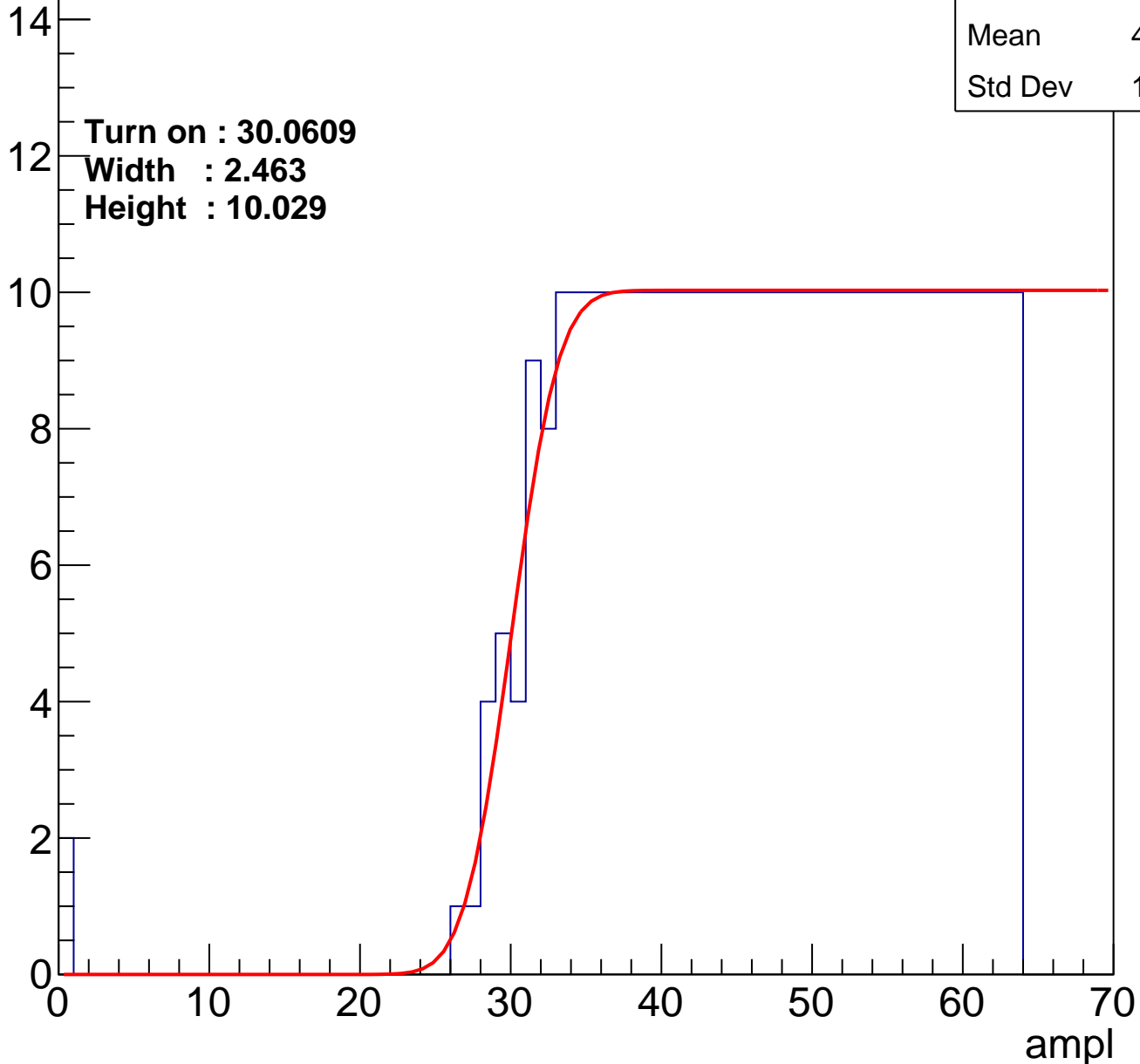
Entries	344
Mean	46.06
Std Dev	10.56

Turn on : 30.0609

Width : 2.463

Height : 10.029

Entry



B1L003S, U11-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 27.8680

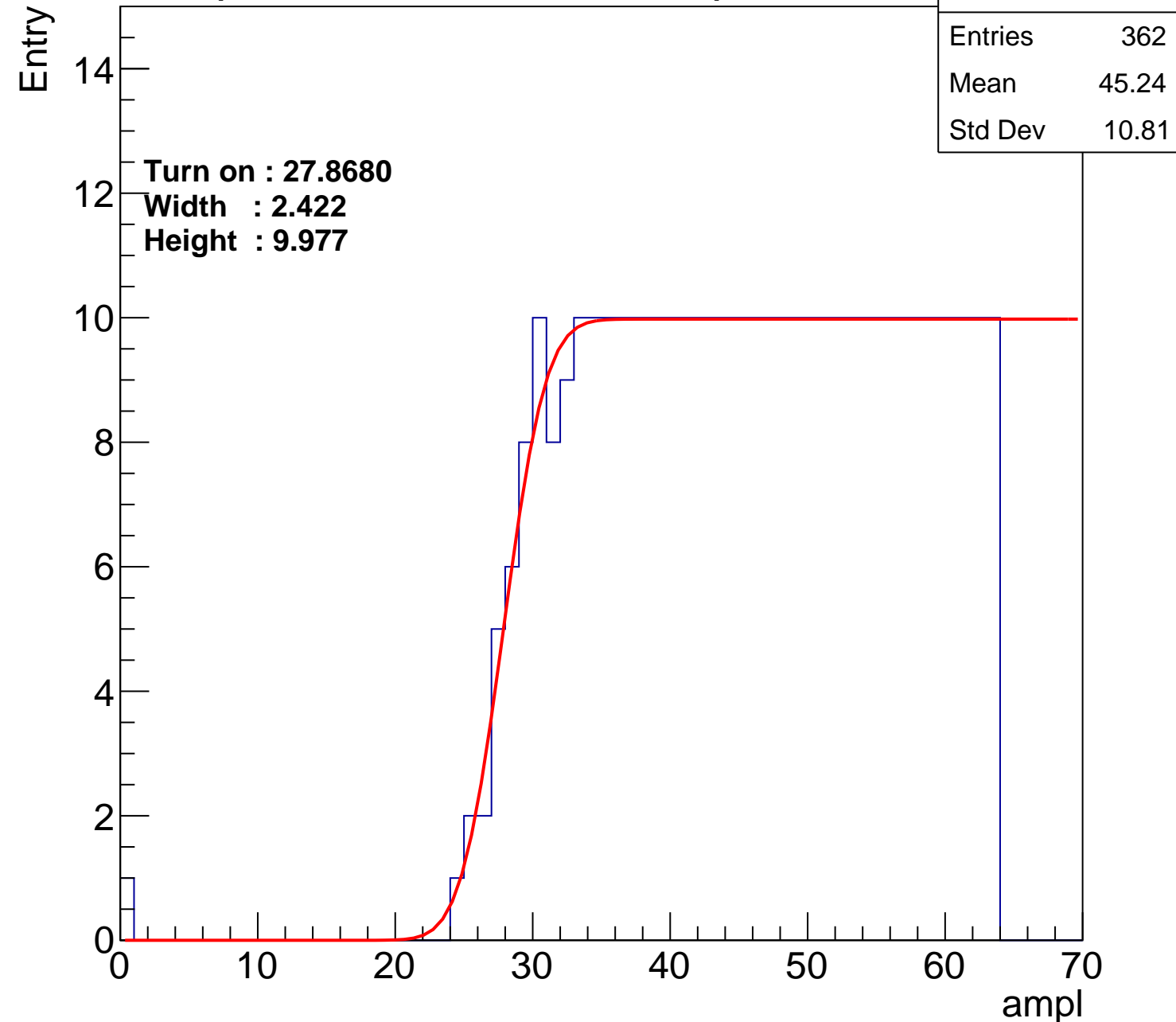
Width : 2.422

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	348
Mean	45.78
Std Dev	10.91

Turn on : 30.0738

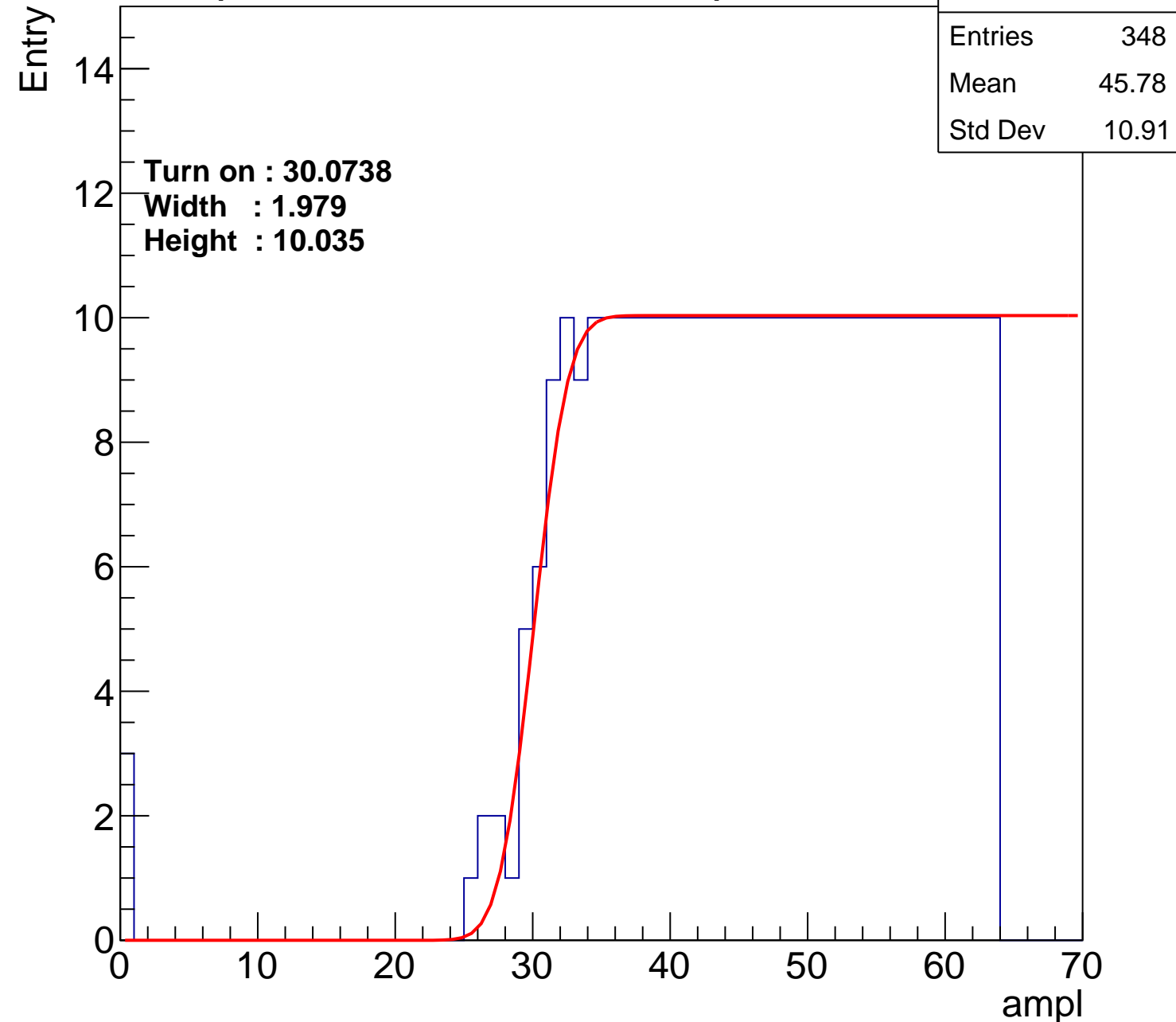
Width : 1.979

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.22
Std Dev	10.84

Turn on : 27.9947

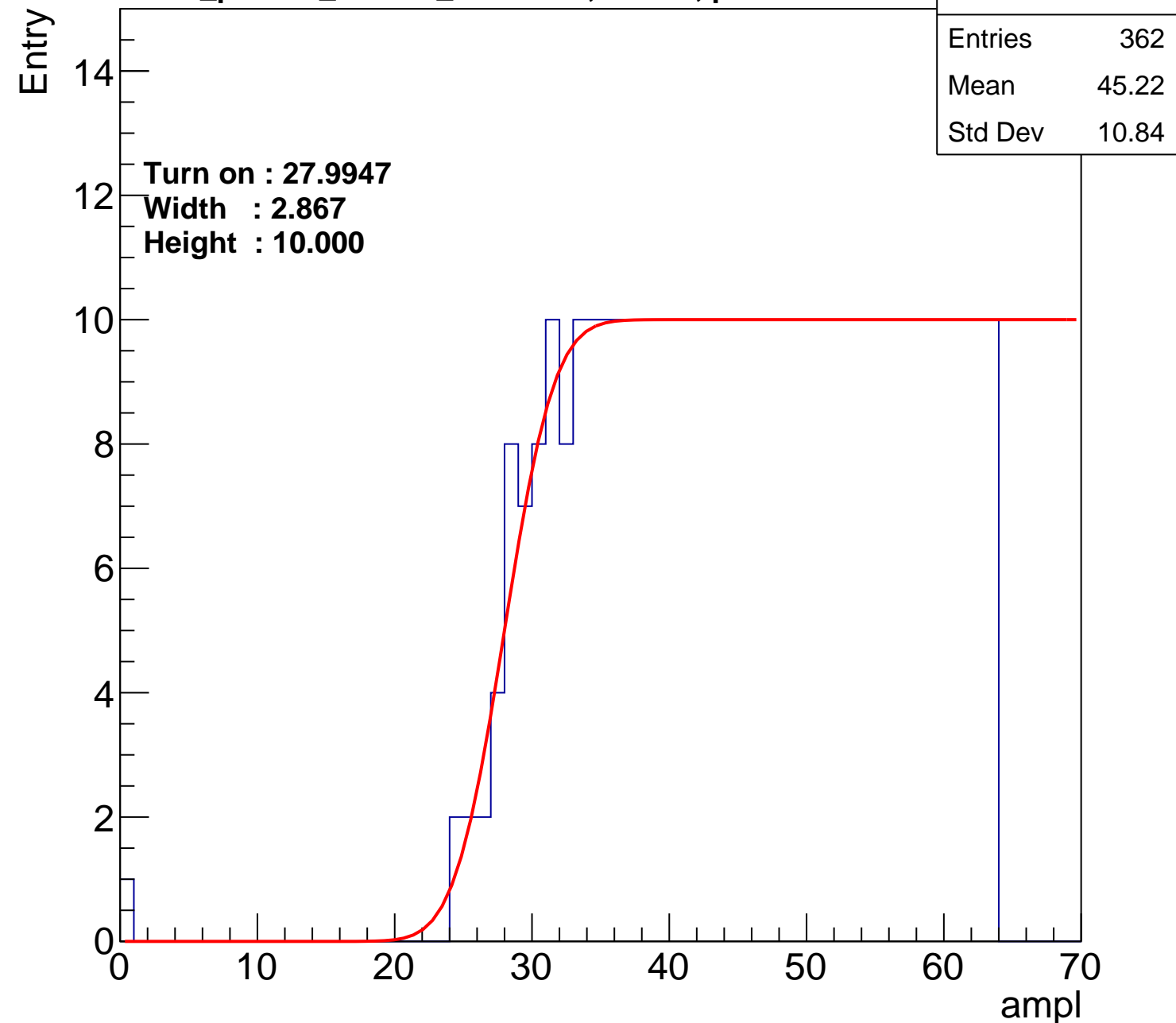
Width : 2.867

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch102

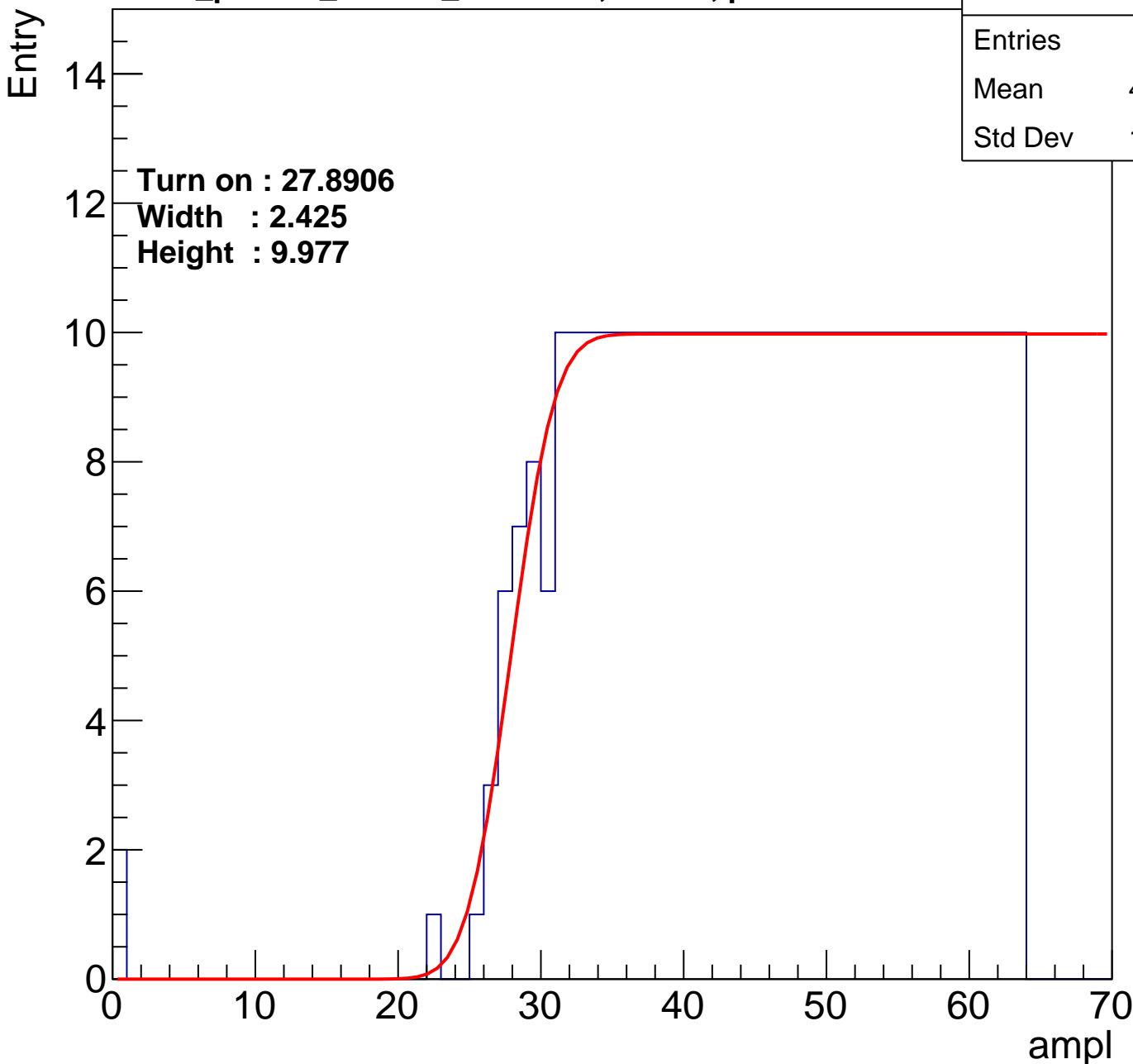
calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.8906

Width : 2.425

Height : 9.977



B1L003S, U11-ch103

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 28.4427

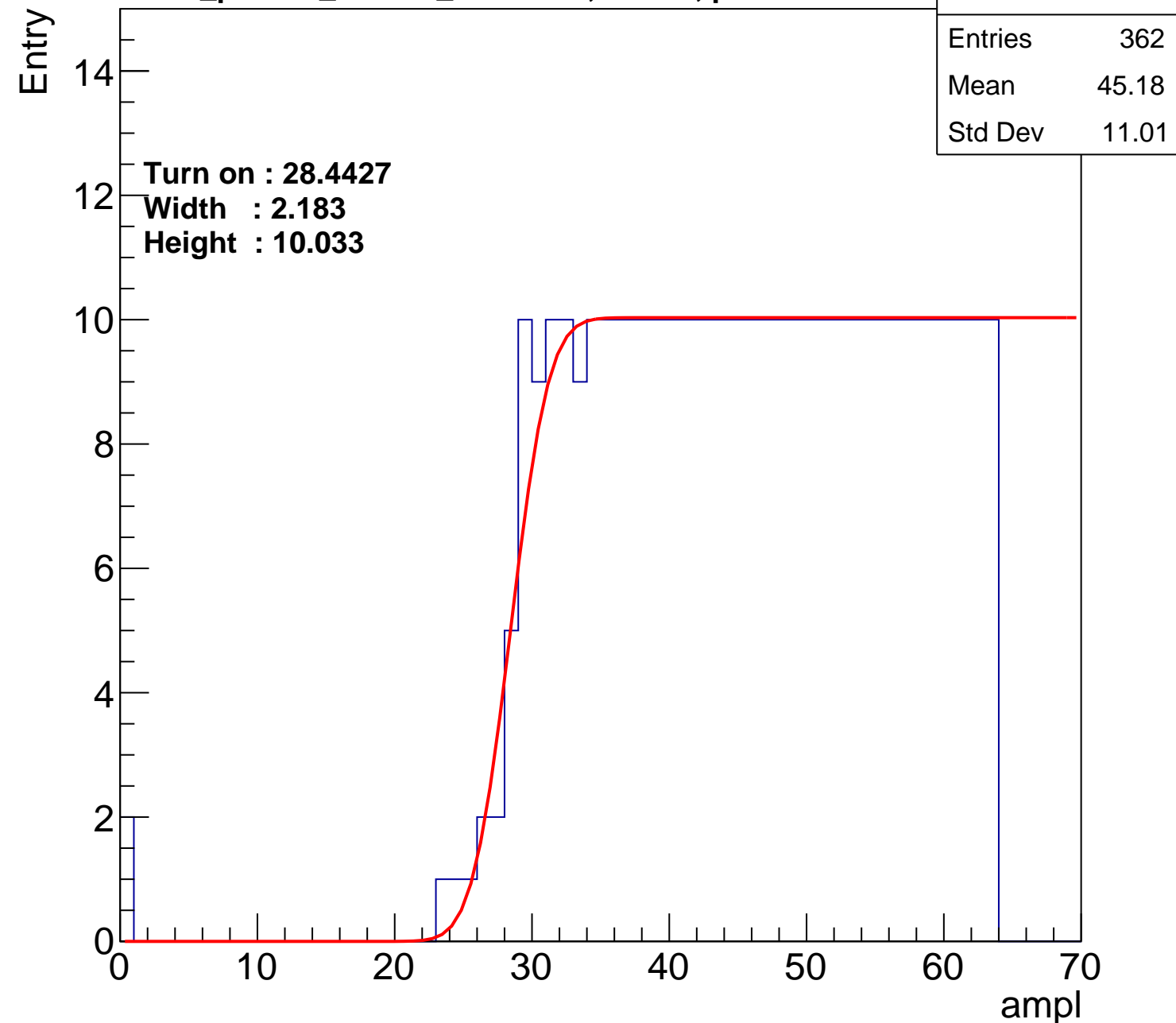
Width : 2.183

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch104

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.09
Std Dev	11.25

Turn on : 28.4630

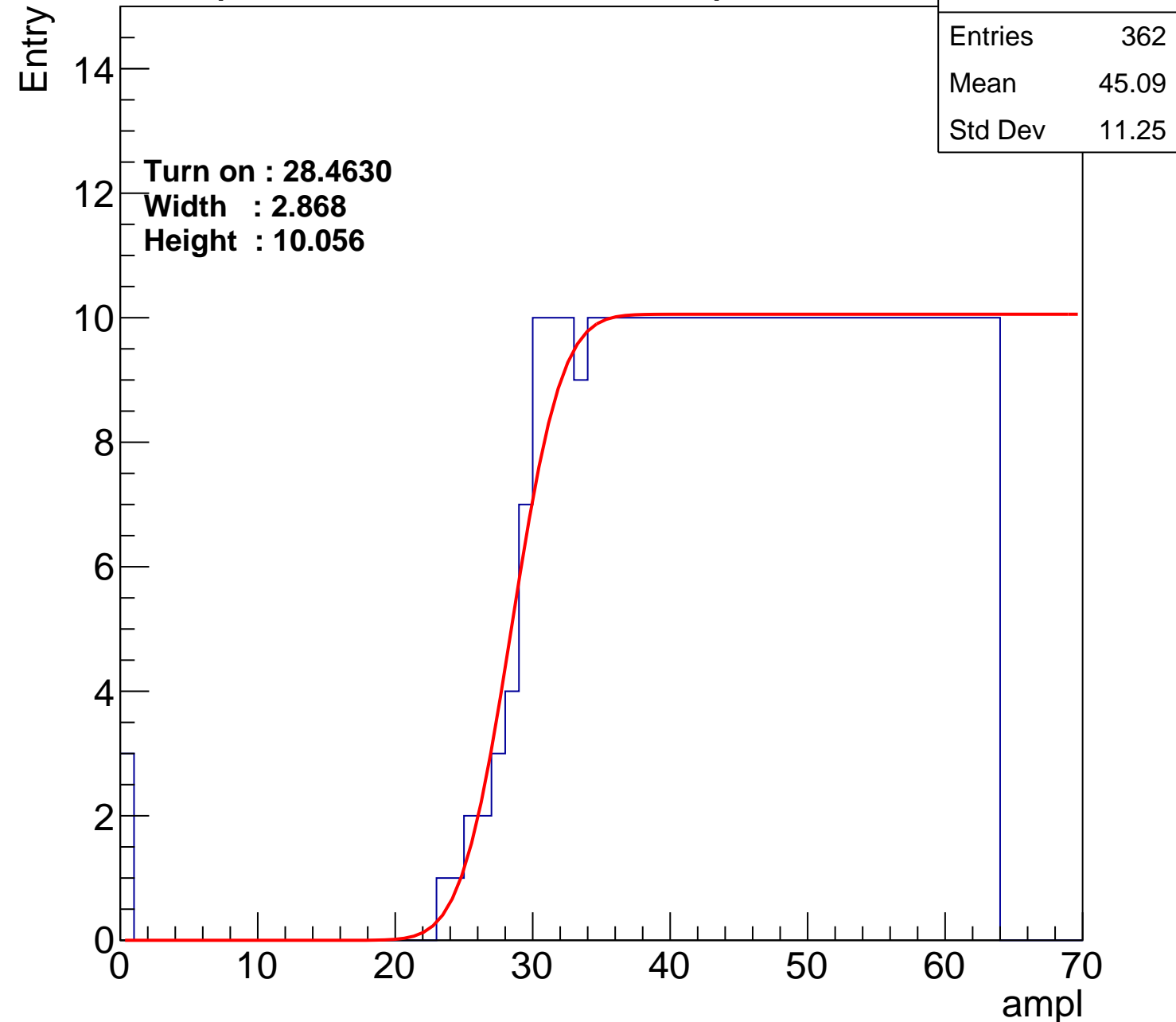
Width : 2.868

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.35
Std Dev	10.73

Turn on : 28.0868

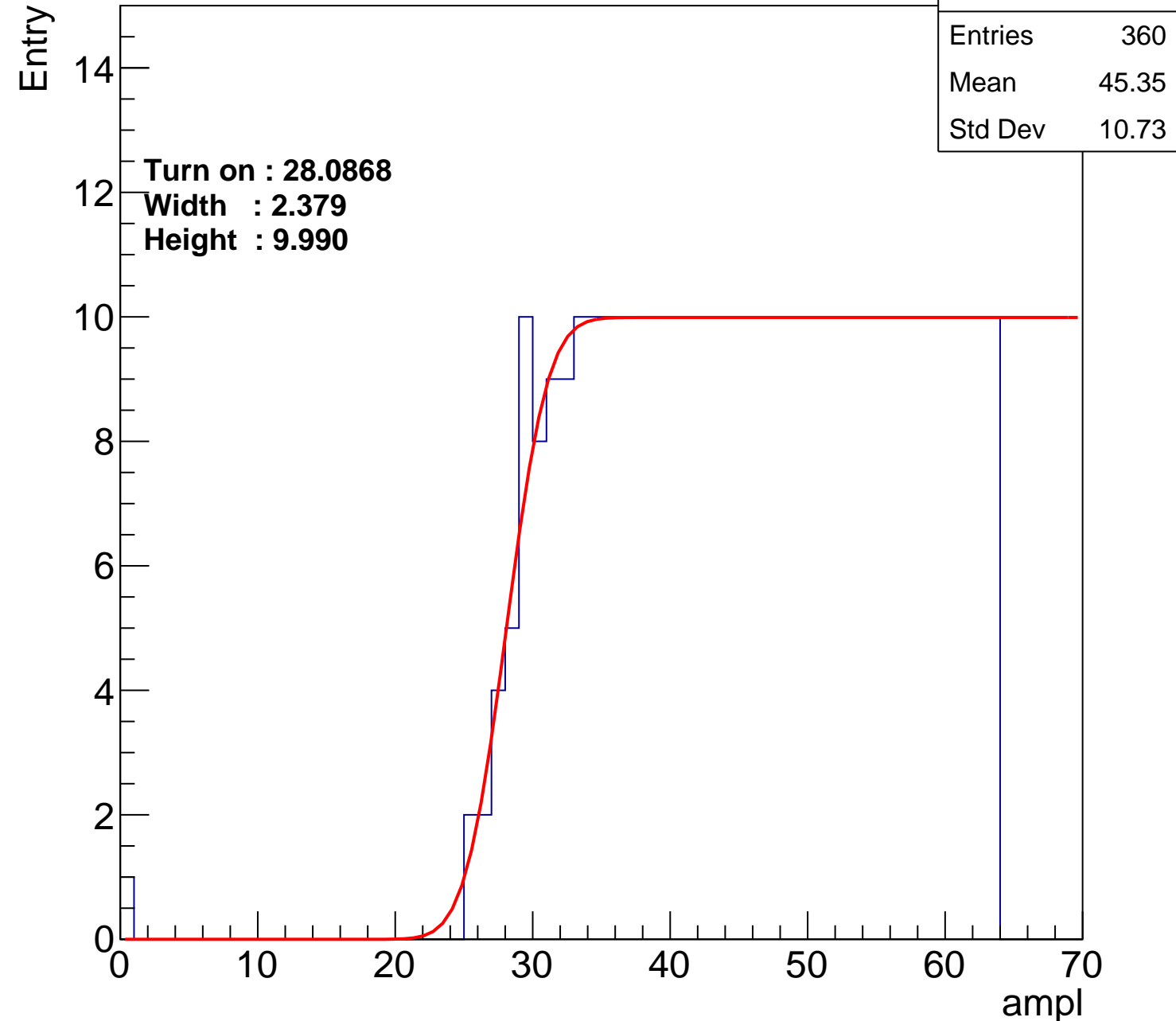
Width : 2.379

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch106

calib_packv5_042523_0143.root, FC#13, port D2

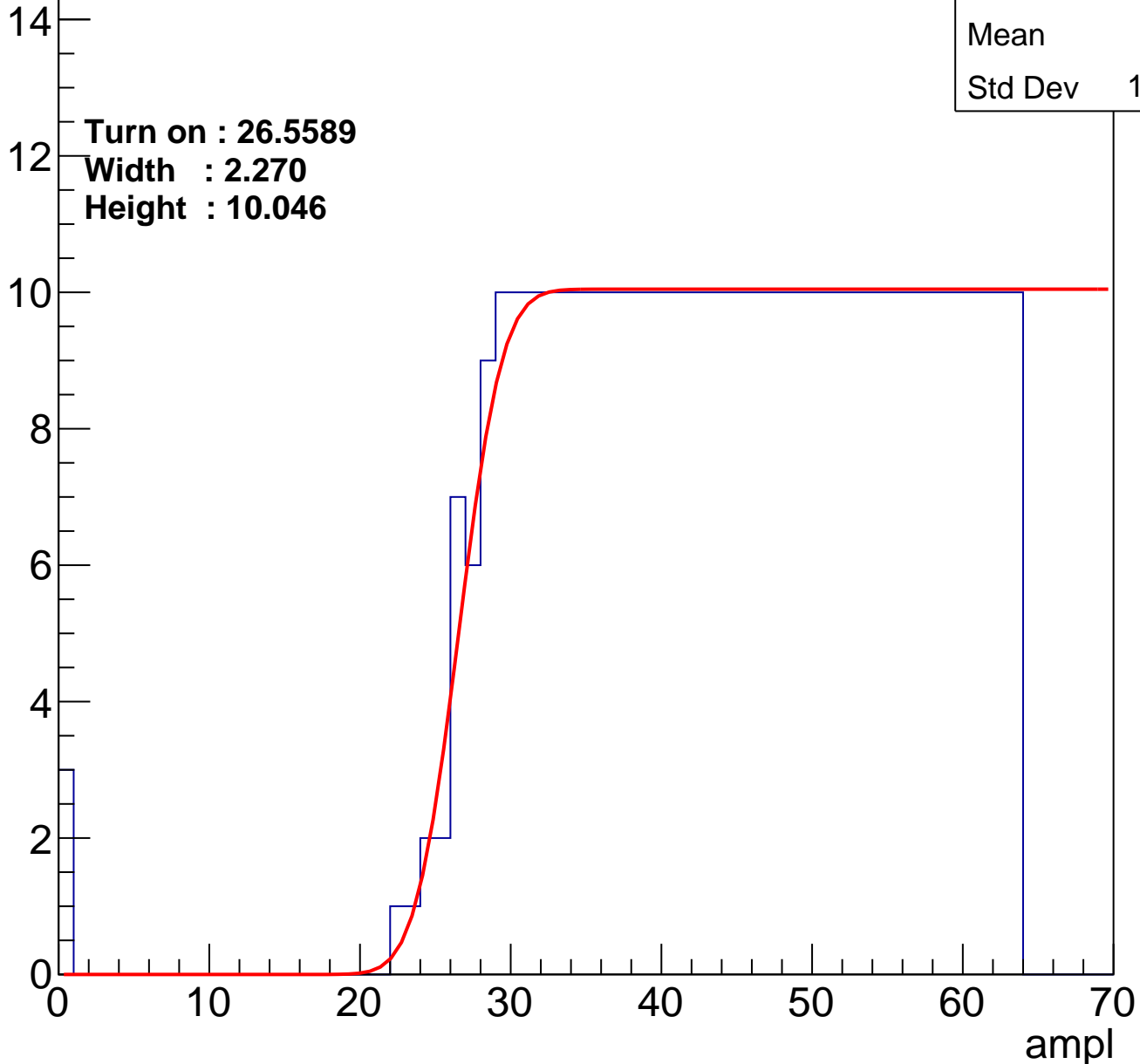
Entries	381
Mean	44.2
Std Dev	11.64

Turn on : 26.5589

Width : 2.270

Height : 10.046

Entry



B1L003S, U11-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.26
Std Dev	11.19

Turn on : 28.8603

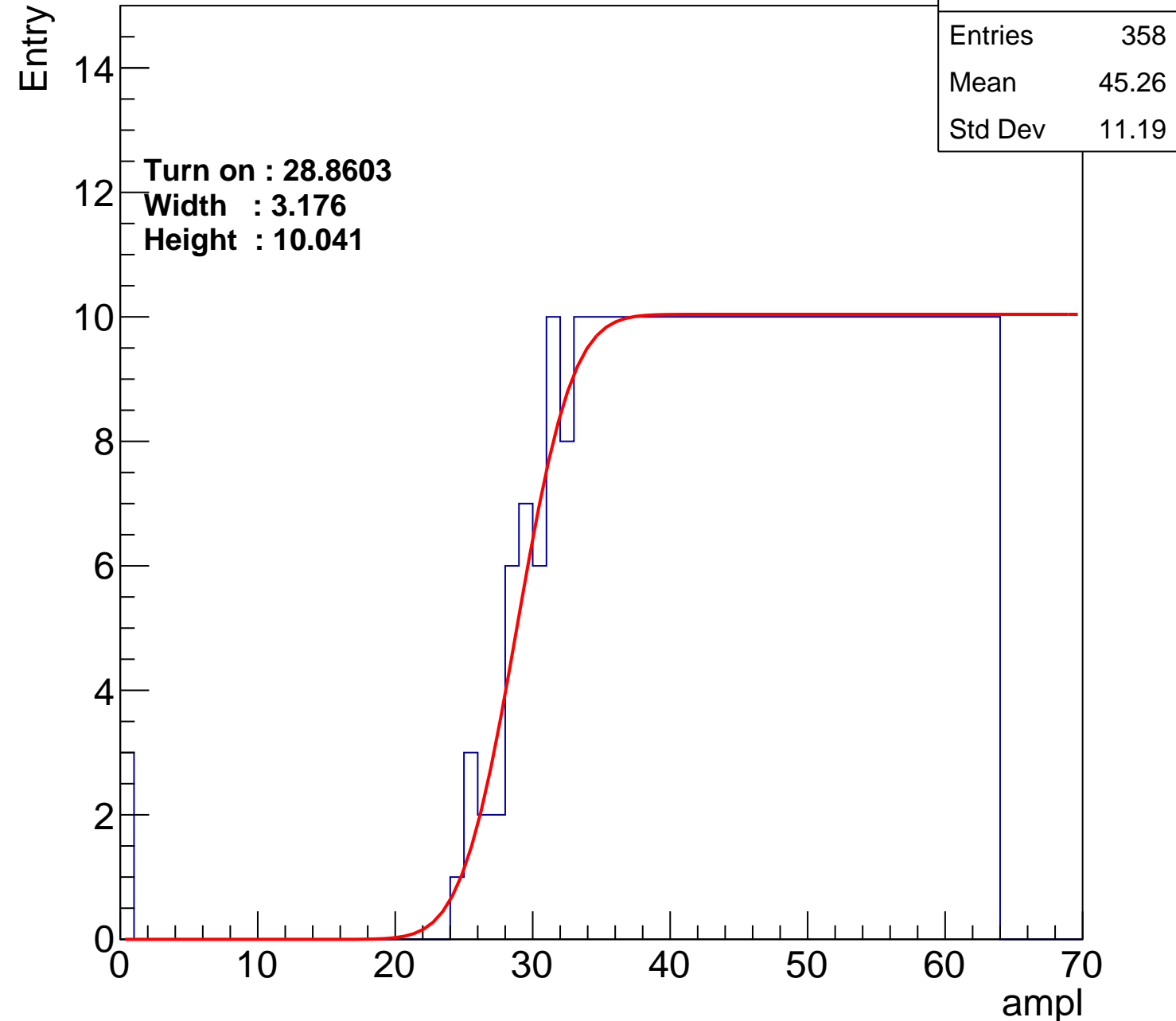
Width : 3.176

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.43
Std Dev	11.03

Turn on : 28.7829

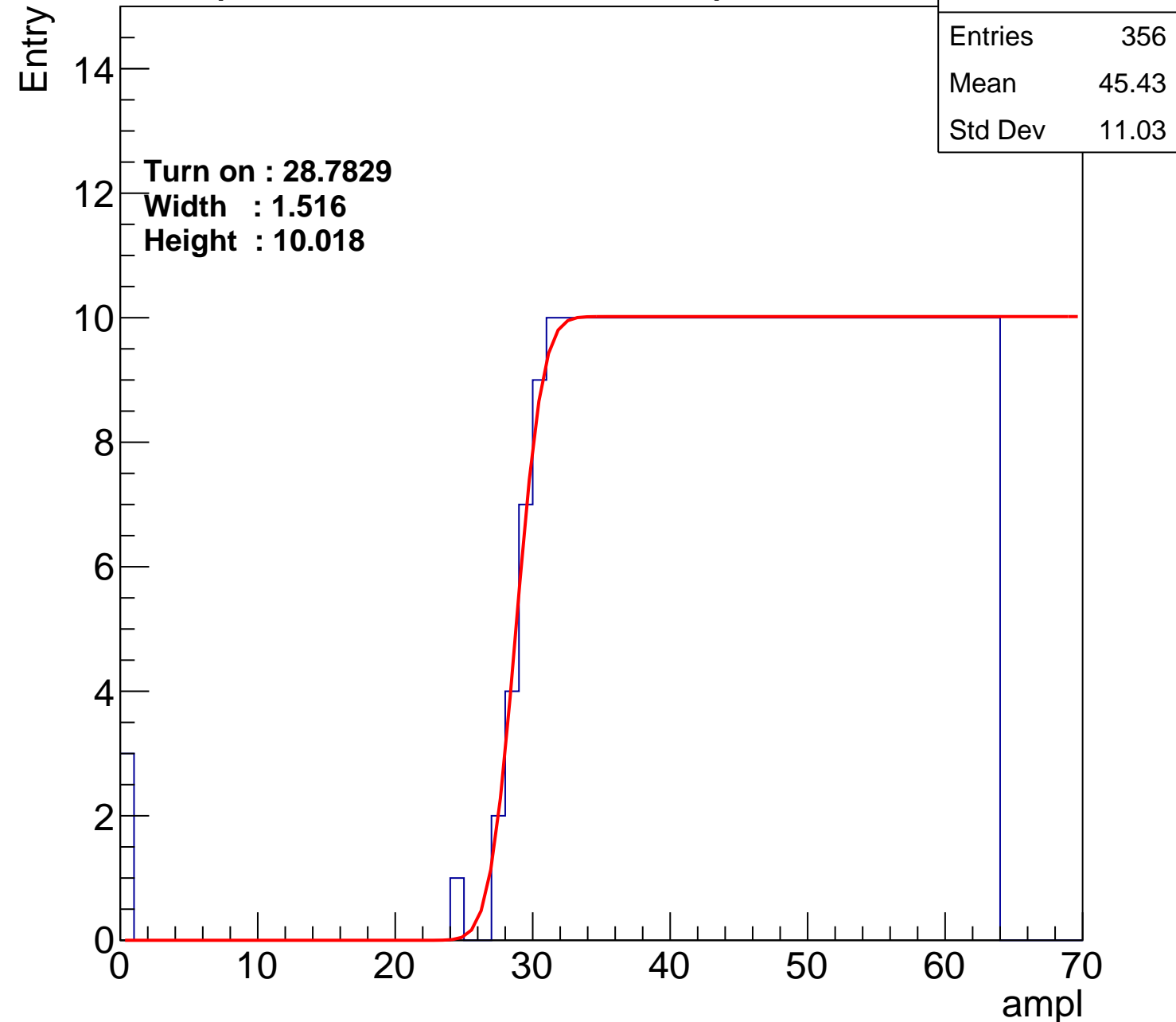
Width : 1.516

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.12
Std Dev	10.84

Turn on : 27.7171

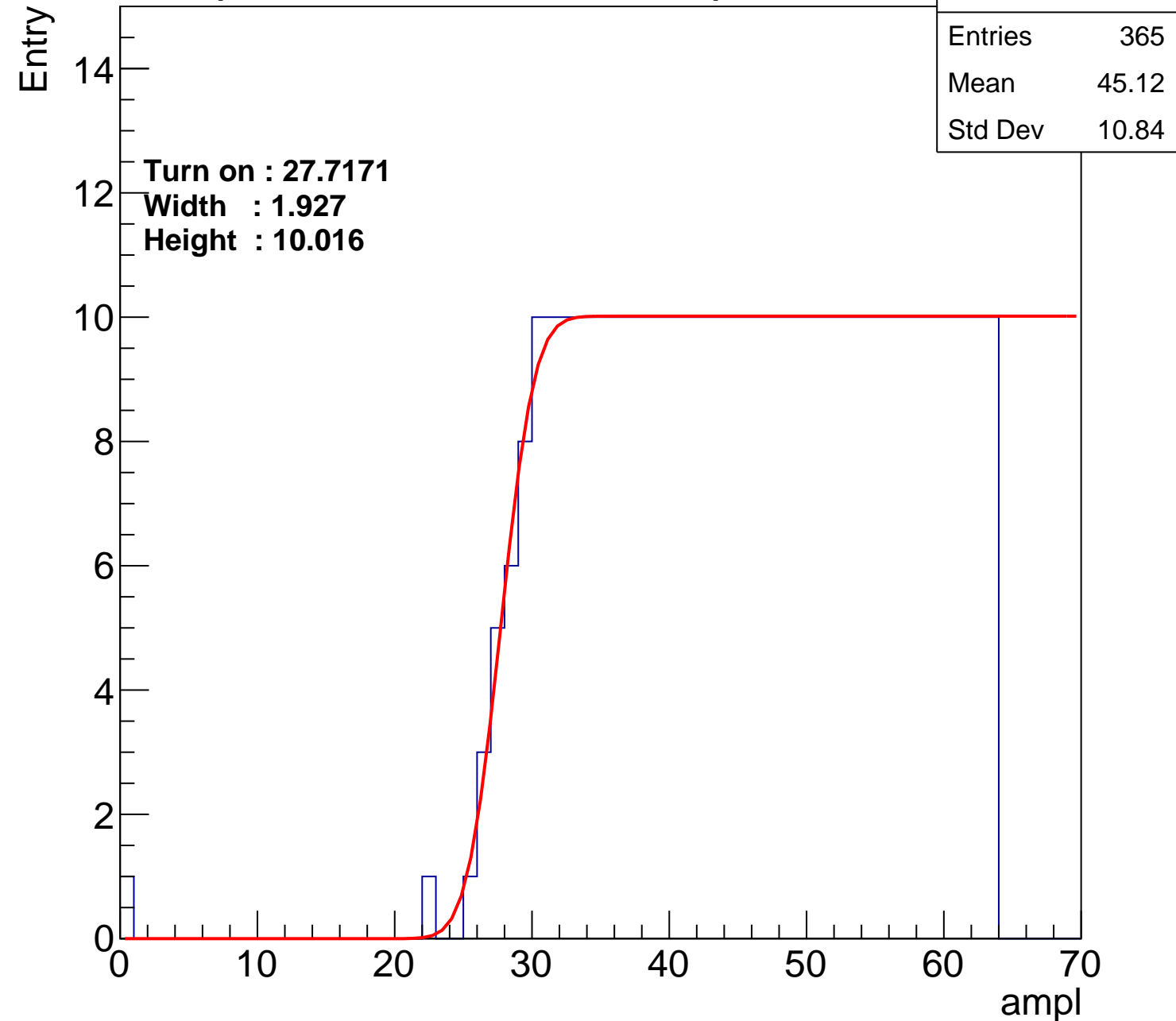
Width : 1.927

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.94
Std Dev	11.51

Turn on : 28.0641

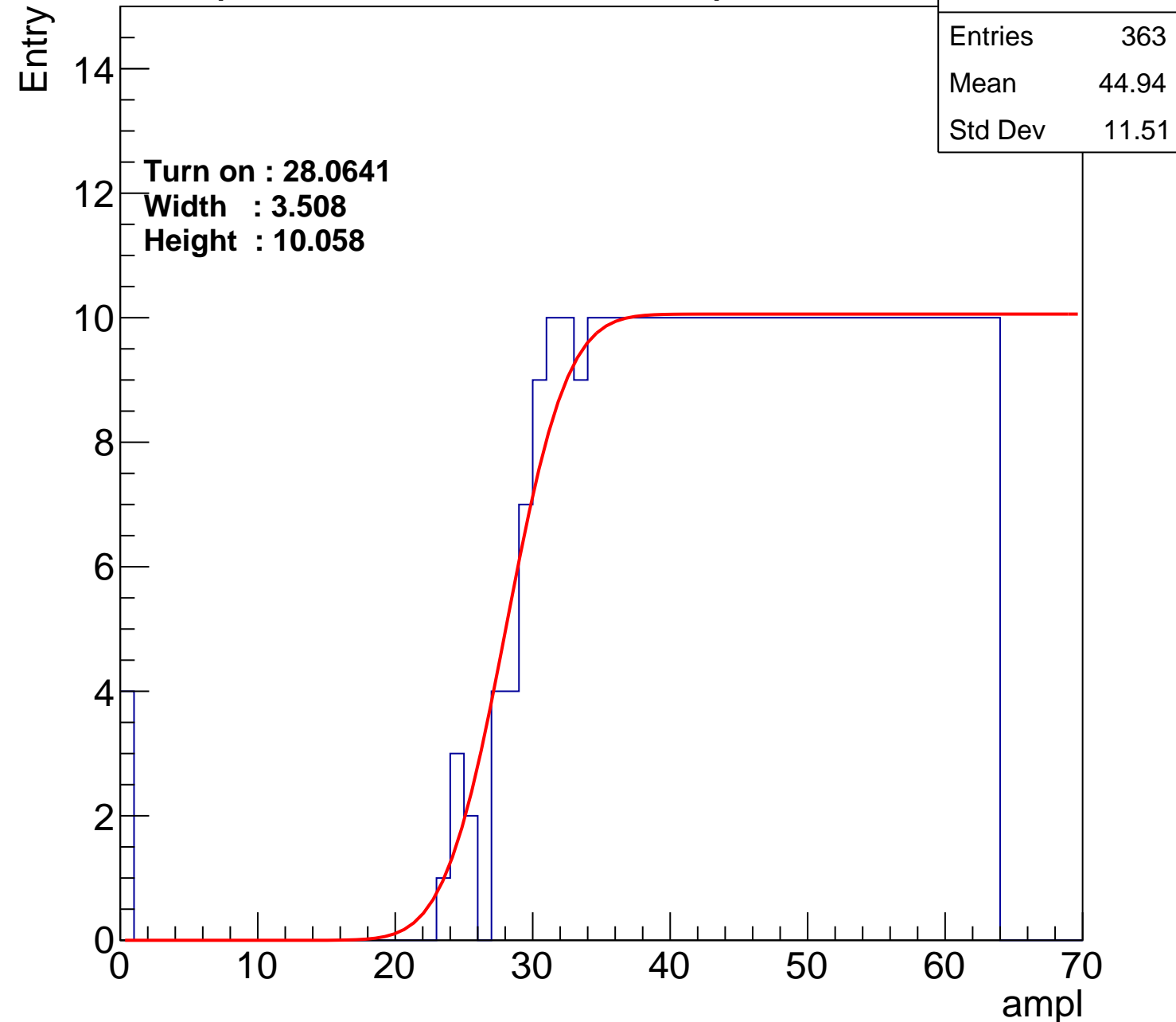
Width : 3.508

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.35
Std Dev	10.74

Turn on : 28.2175

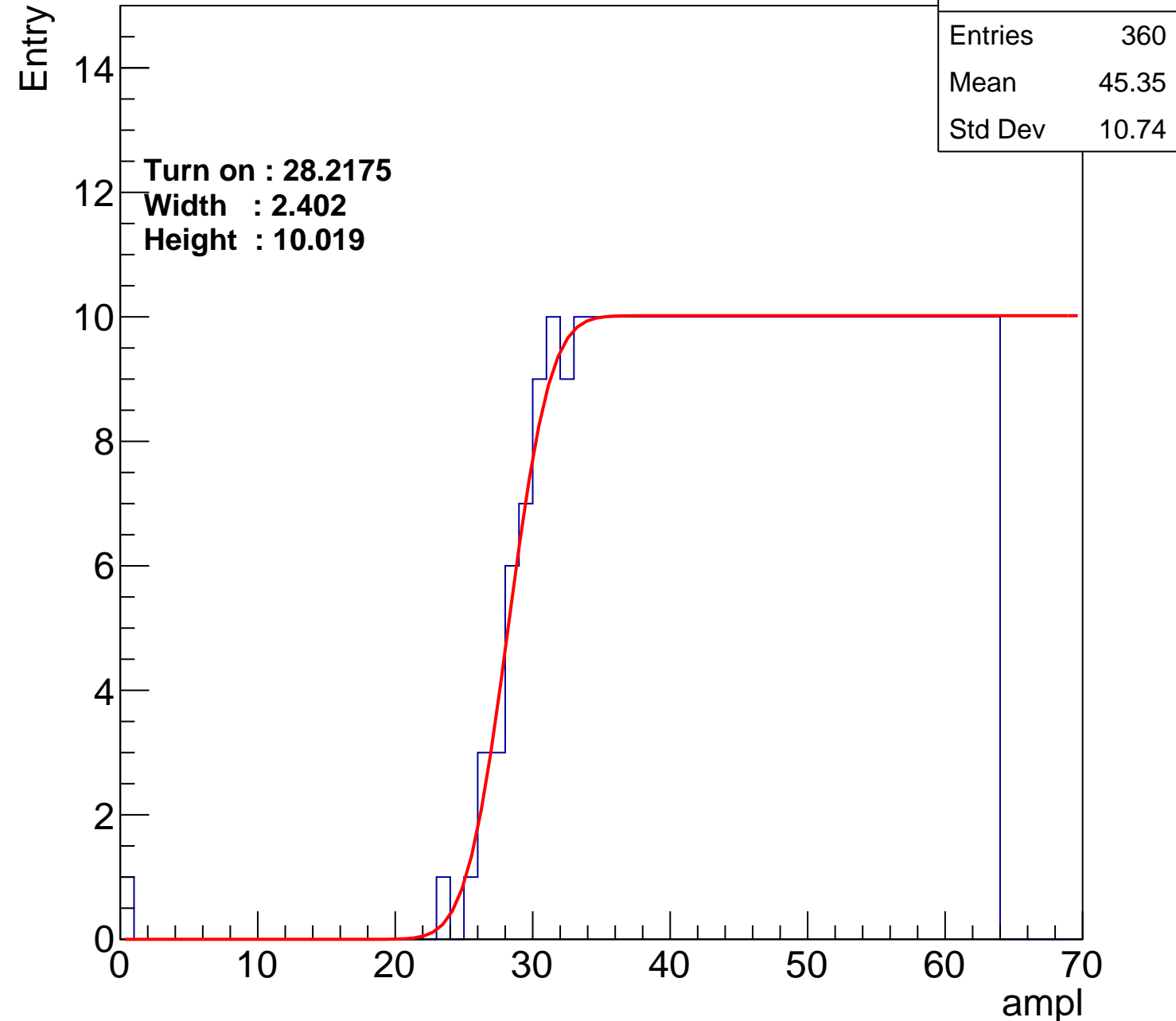
Width : 2.402

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.37
Std Dev	12.6

Turn on : 25.4148

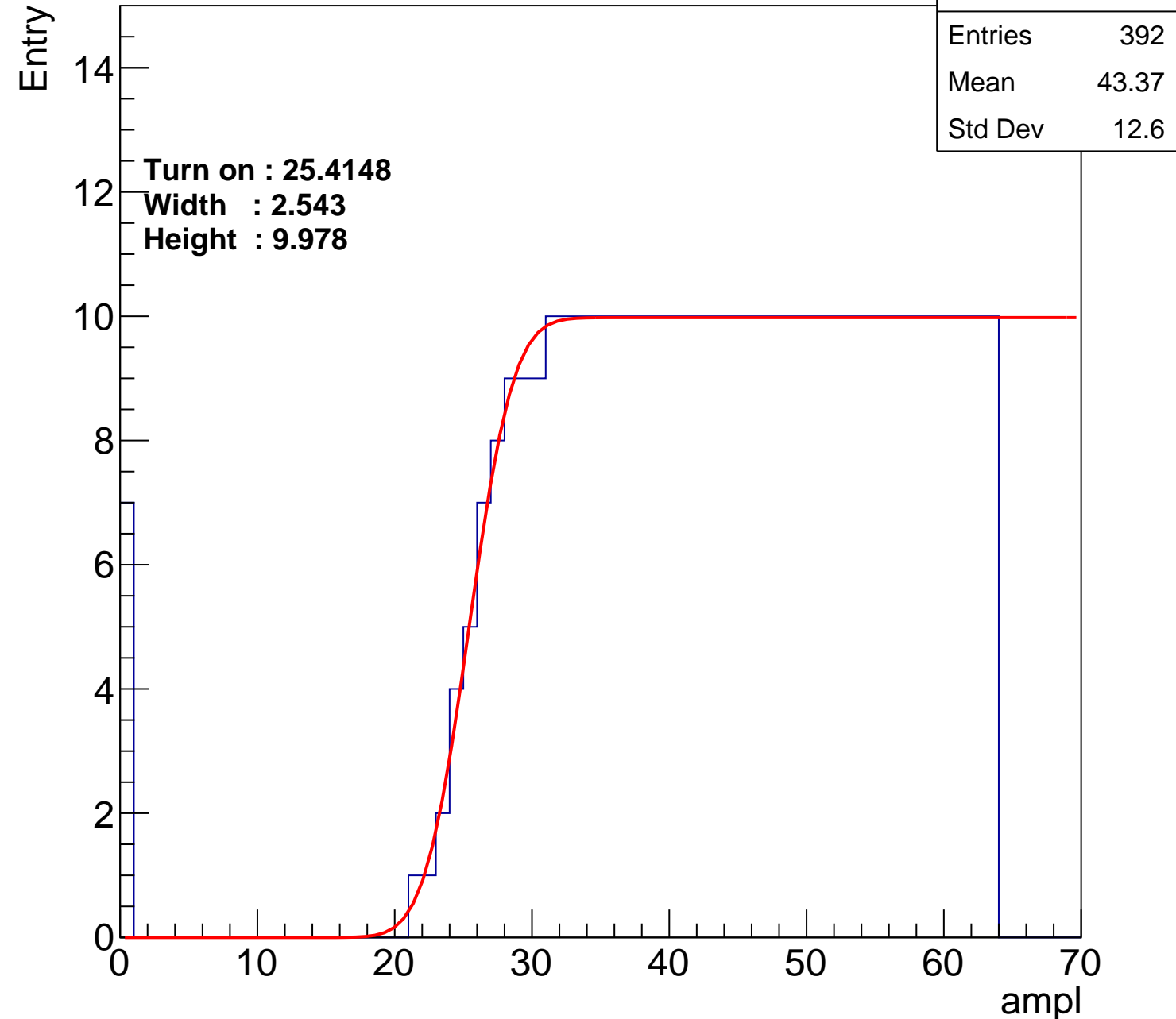
Width : 2.543

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.82
Std Dev	12.03

Turn on : 25.8977

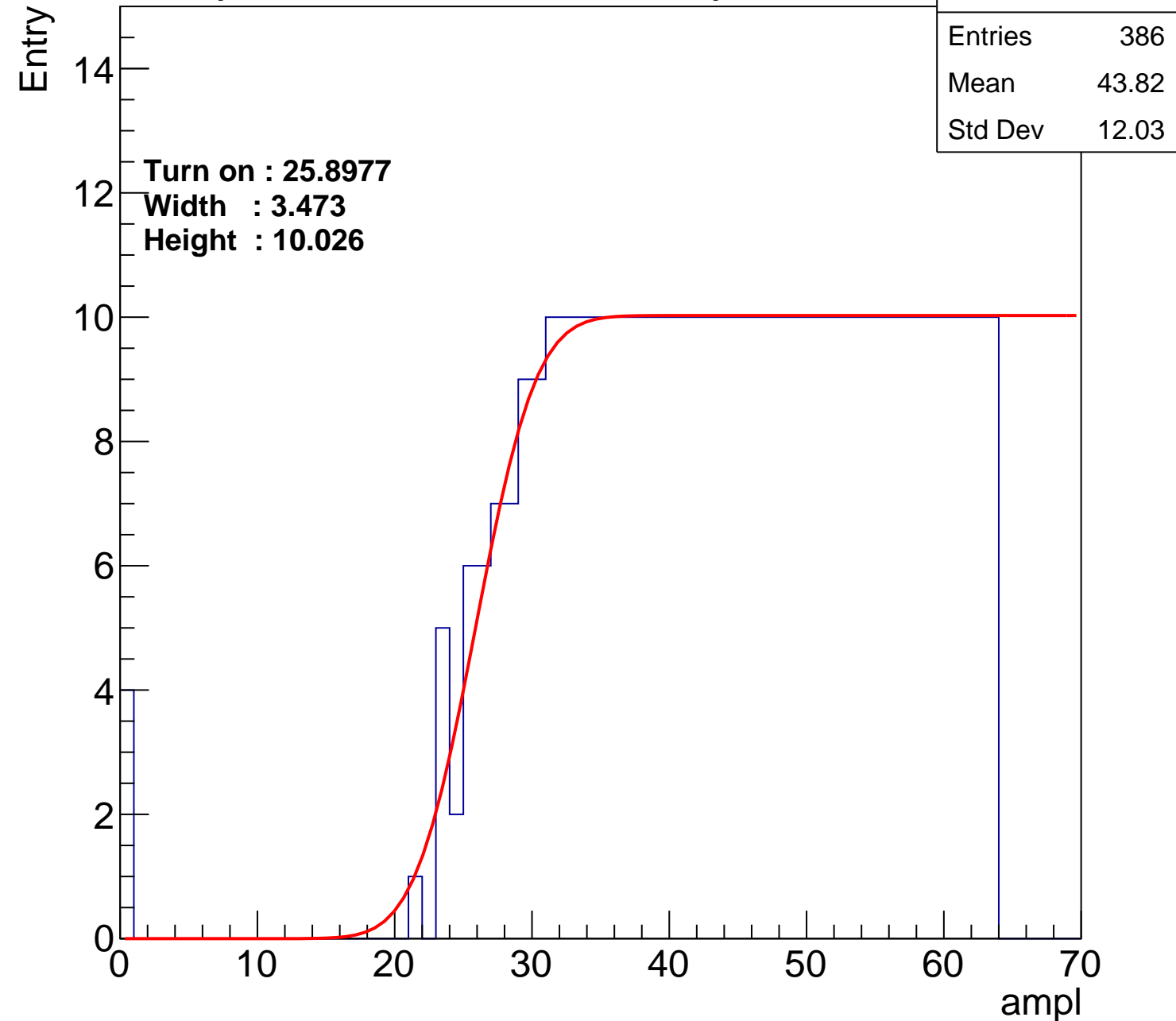
Width : 3.473

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch114

calib_packv5_042523_0143.root, FC#13, port D2

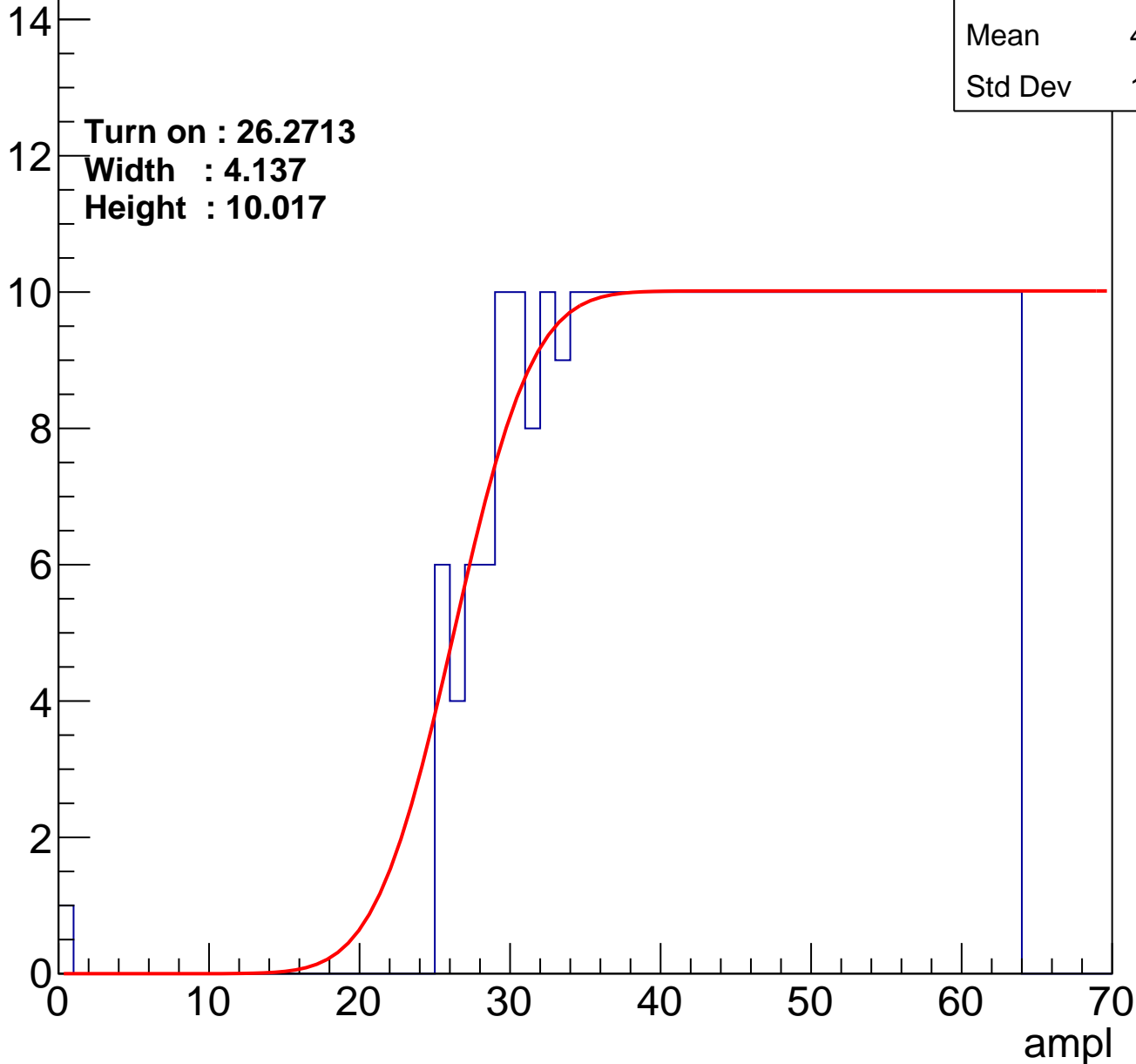
Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 26.2713

Width : 4.137

Height : 10.017

Entry



B1L003S, U11-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.82
Std Dev	11.98

Turn on : 25.7730

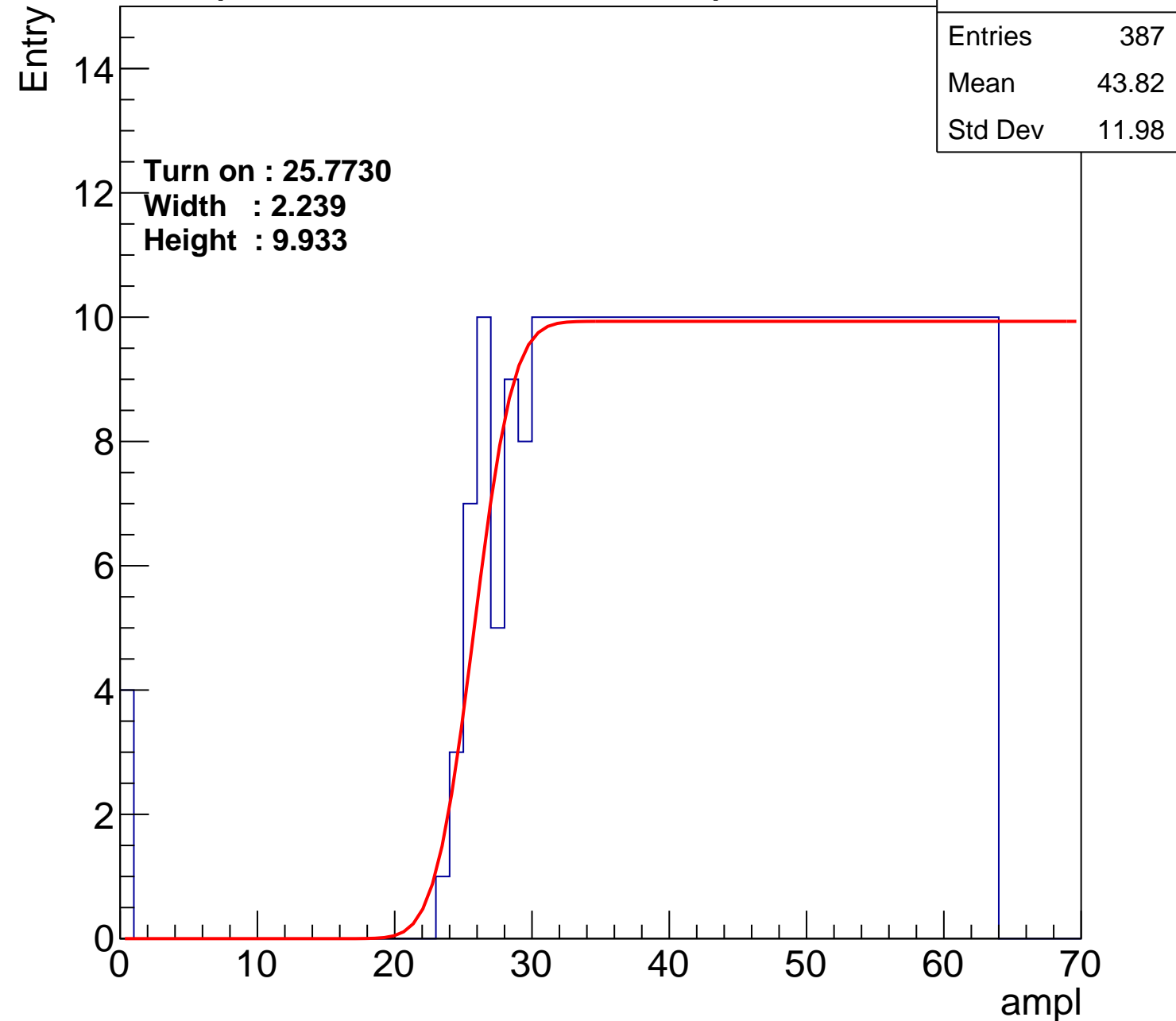
Width : 2.239

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 26.7738

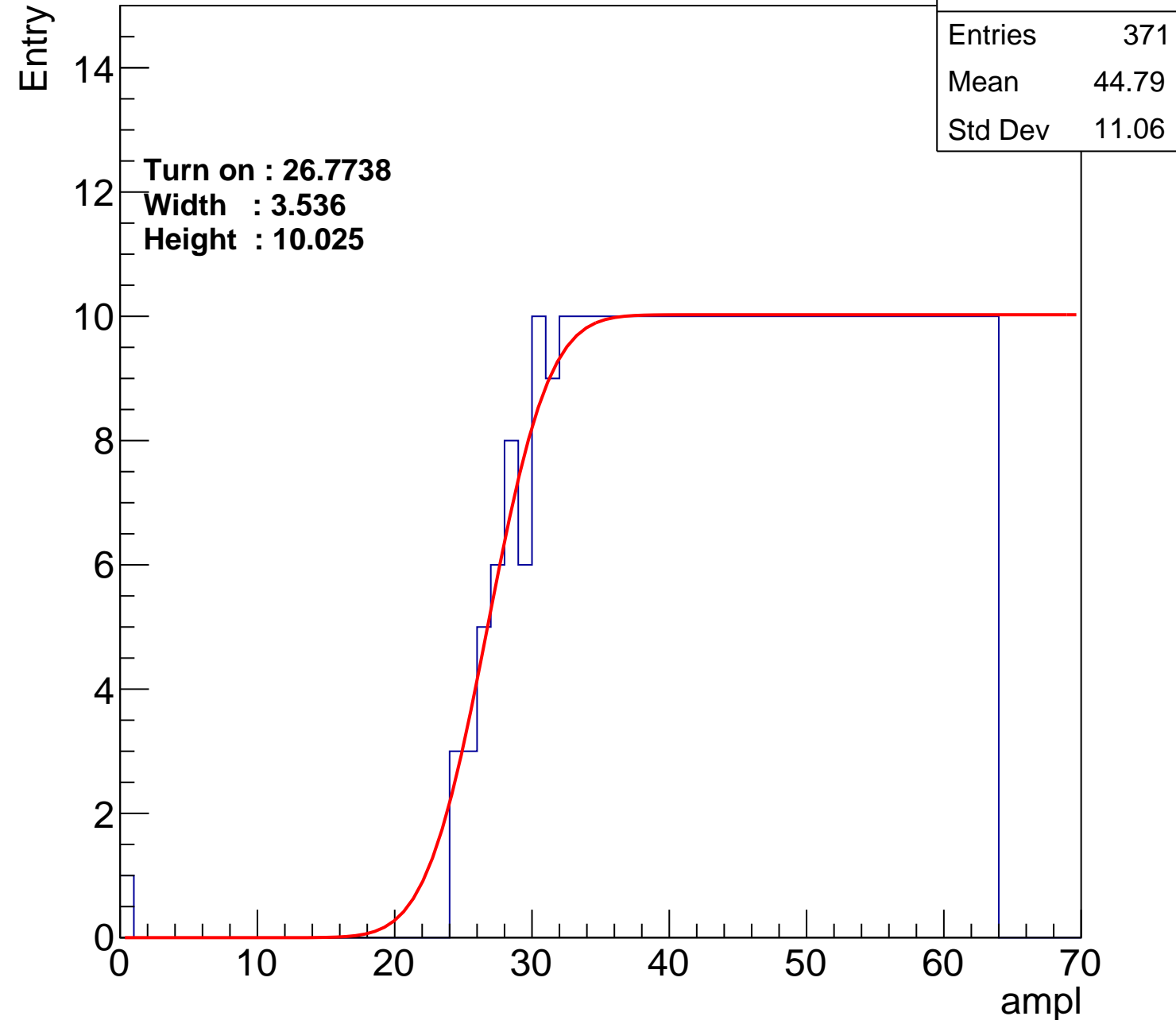
Width : 3.536

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.8848

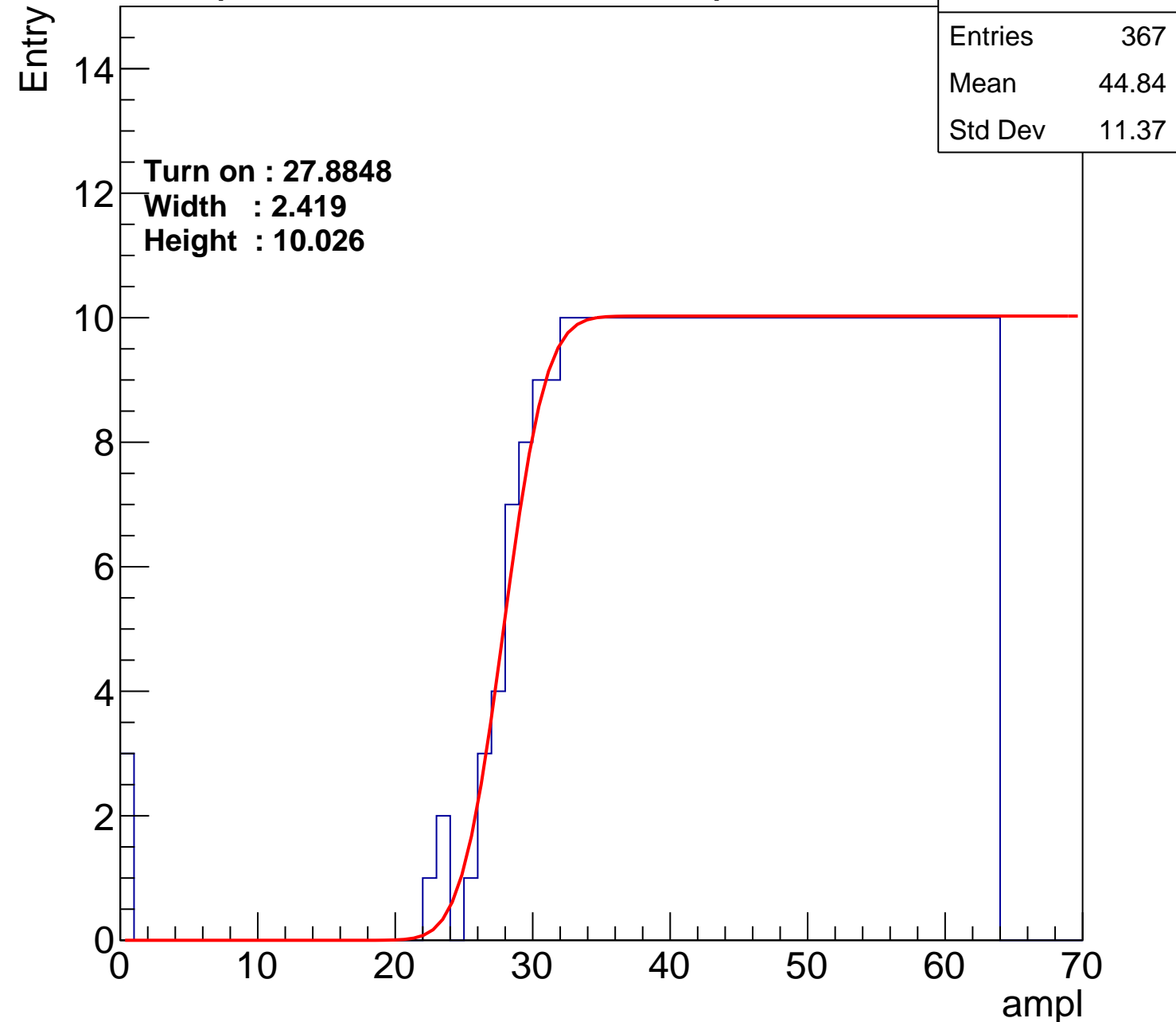
Width : 2.419

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.67
Std Dev	11.47

Turn on : 27.5557

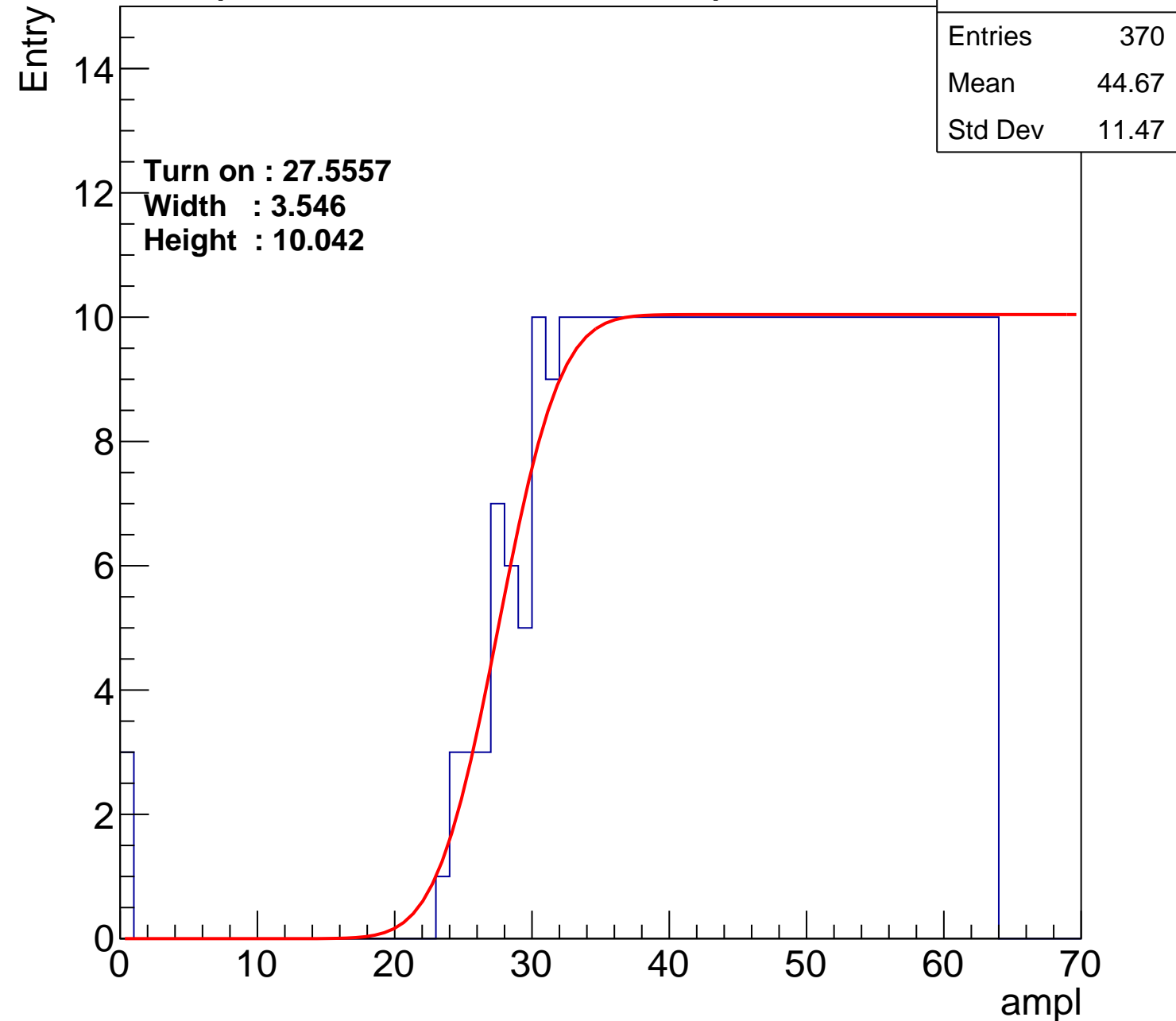
Width : 3.546

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch119

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.52
Std Dev	11.51

Turn on : 27.2137

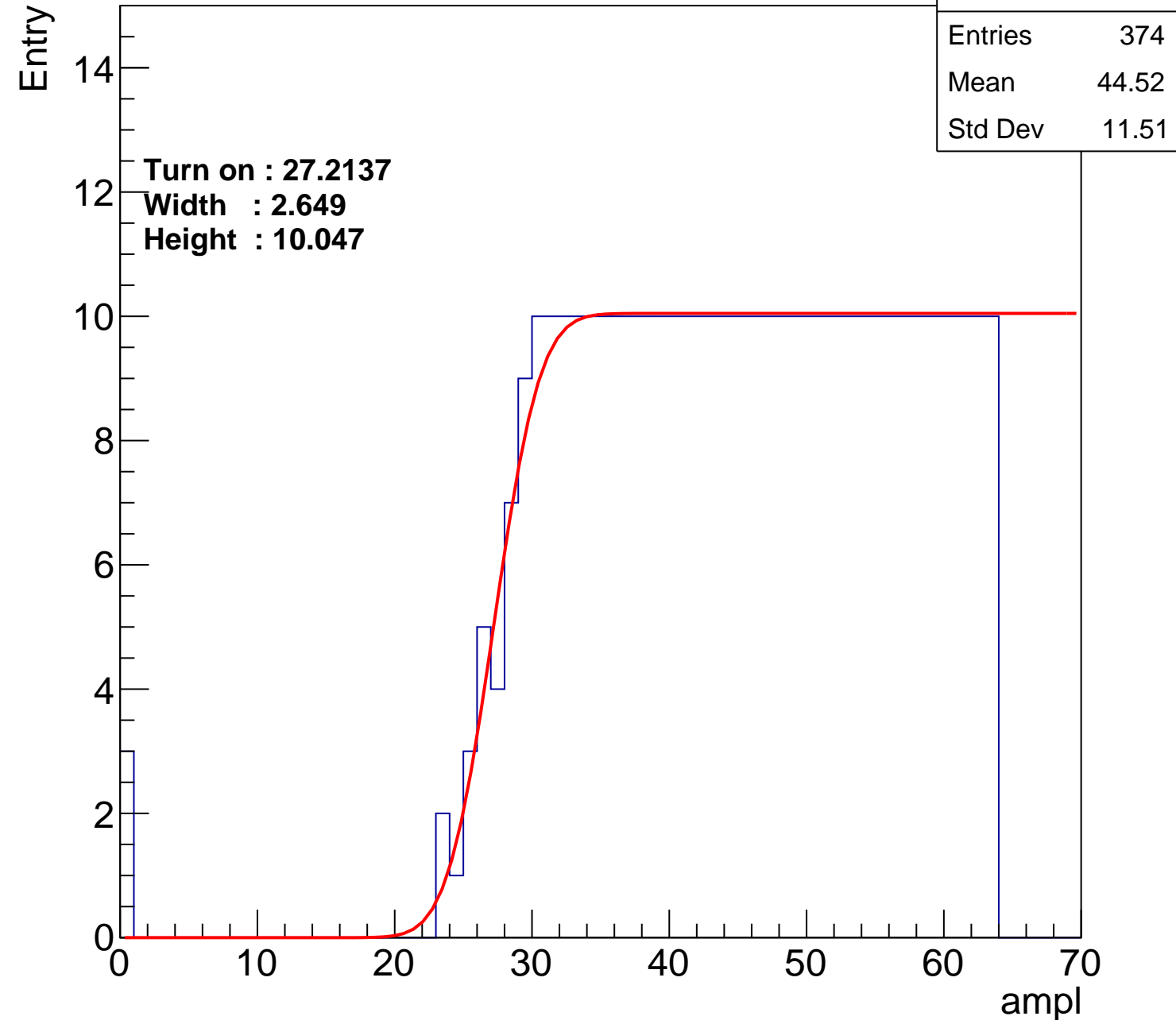
Width : 2.649

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch120

calib_packv5_042523_0143.root, FC#13, port D2

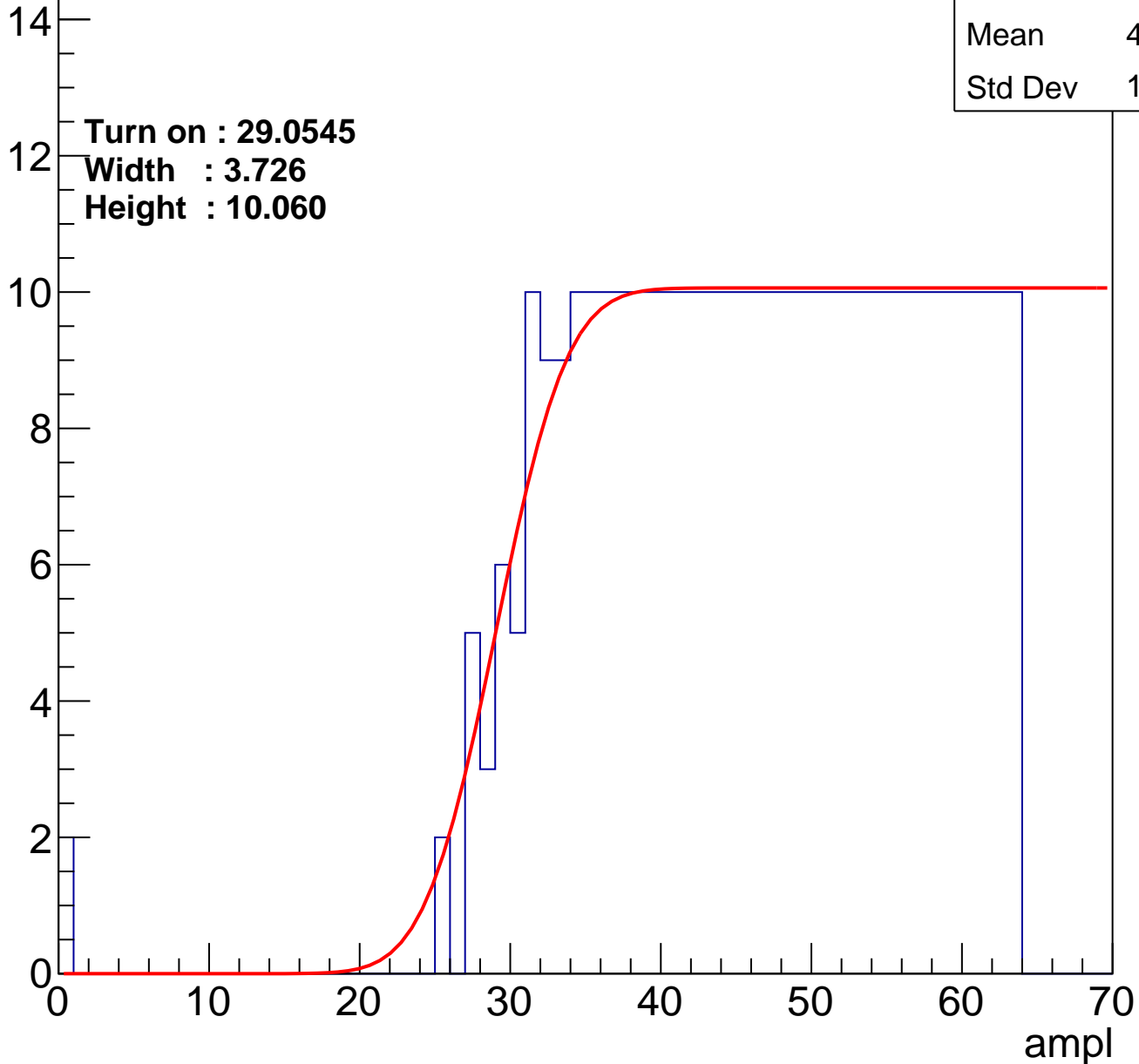
Entries	351
Mean	45.69
Std Dev	10.78

Turn on : 29.0545

Width : 3.726

Height : 10.060

Entry



B1L003S, U11-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	396
Mean	43.53
Std Dev	11.77

Turn on : 25.0476

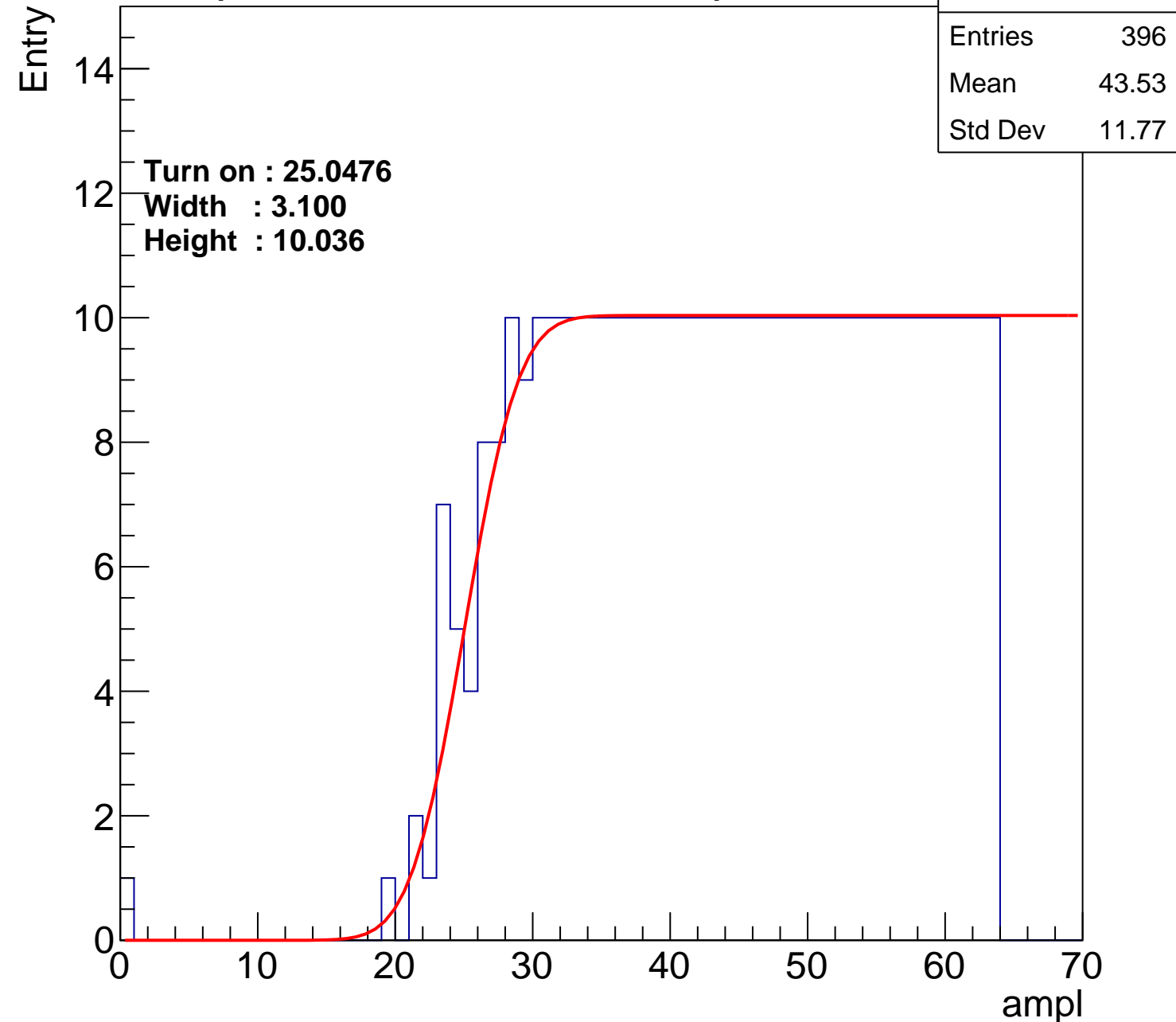
Width : 3.100

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch122

calib_packv5_042523_0143.root, FC#13, port D2

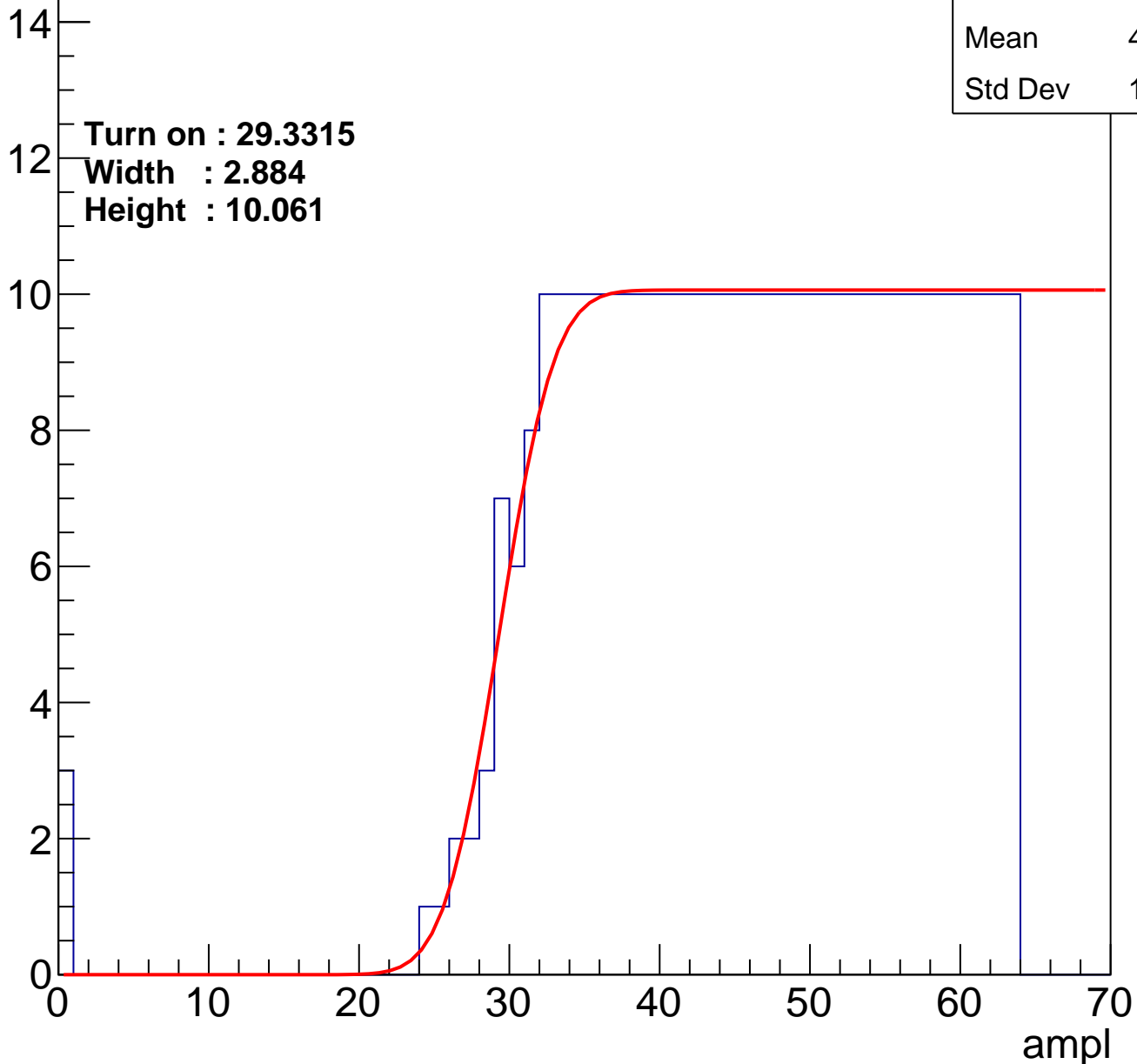
Entries	353
Mean	45.52
Std Dev	11.04

Turn on : 29.3315

Width : 2.884

Height : 10.061

Entry



B1L003S, U11-ch123

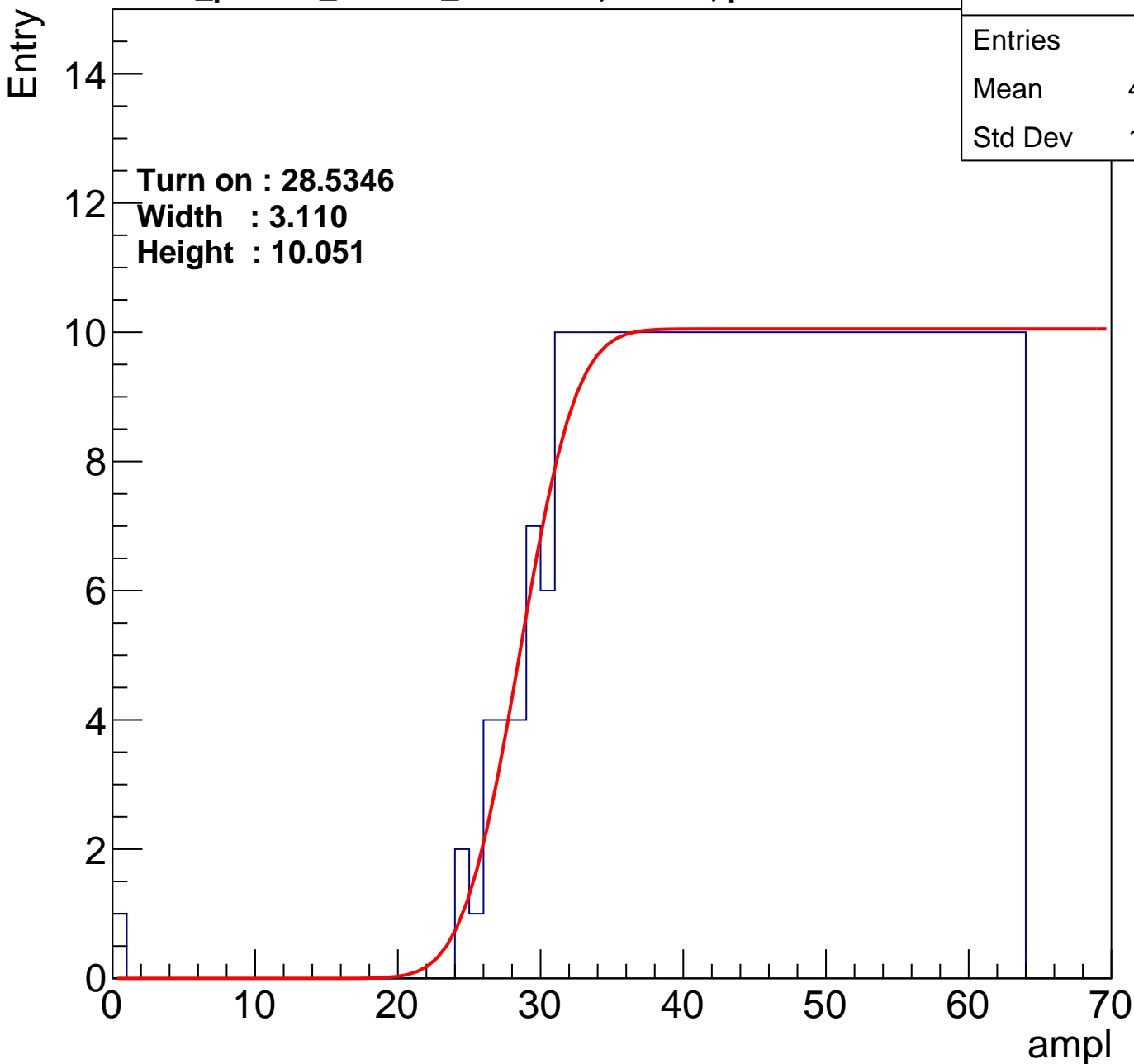
calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.38
Std Dev	10.76

Turn on : 28.5346

Width : 3.110

Height : 10.051



B1L003S, U11-ch124

calib_packv5_042523_0143.root, FC#13, port D2

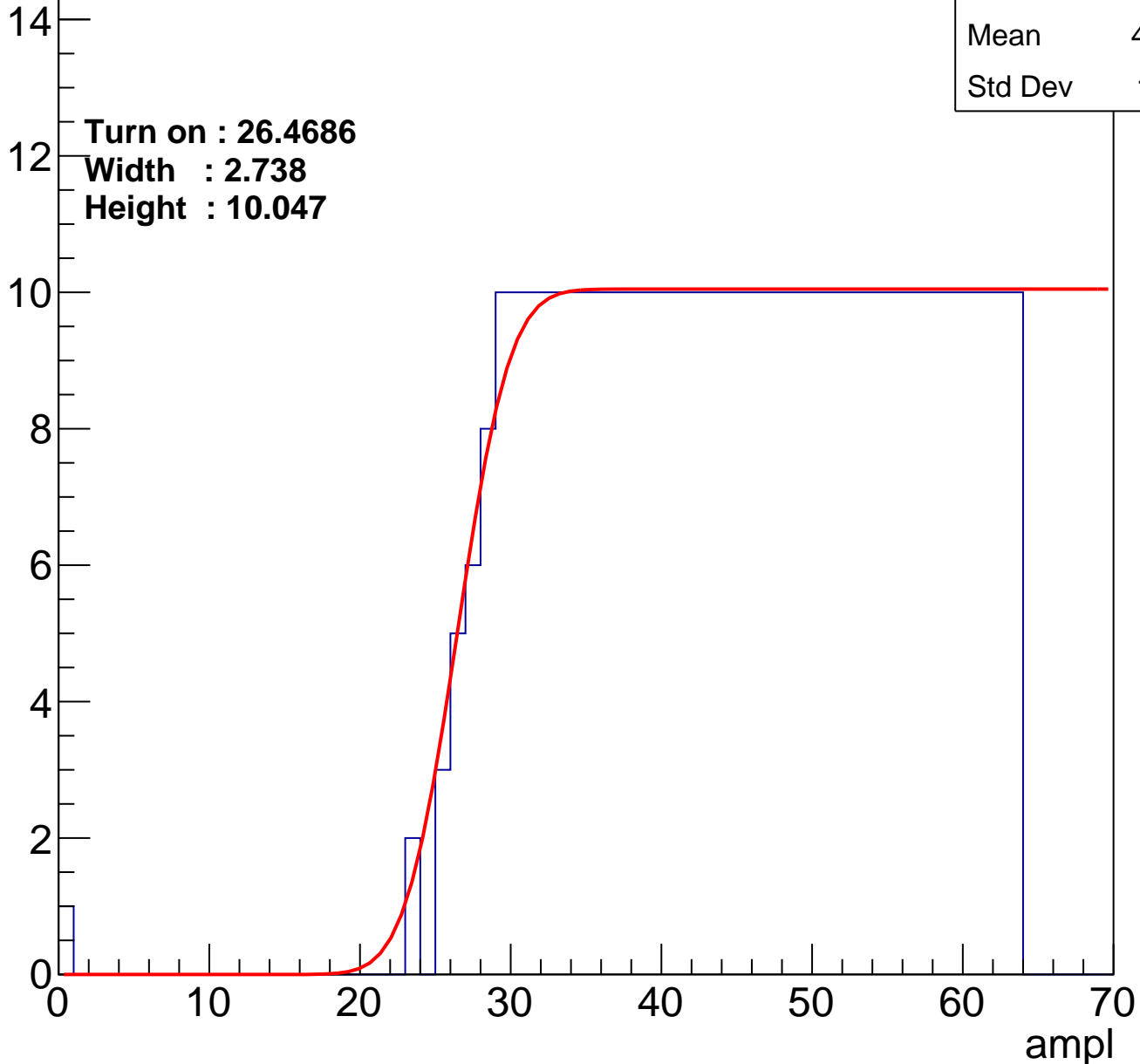
Entries	375
Mean	44.63
Std Dev	11.11

Turn on : 26.4686

Width : 2.738

Height : 10.047

Entry



B1L003S, U11-ch125

calib_packv5_042523_0143.root, FC#13, port D2

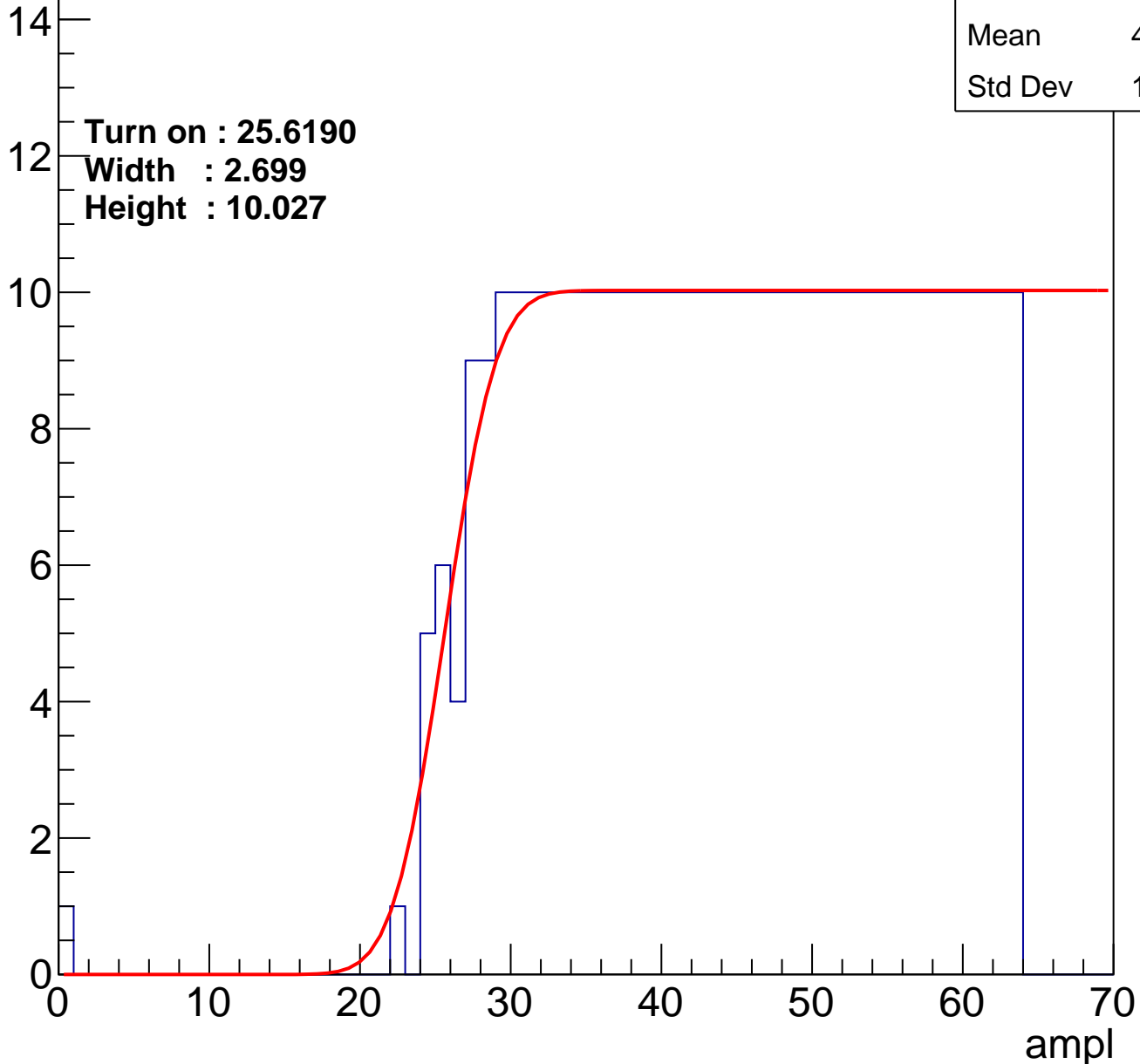
Entries	385
Mean	44.13
Std Dev	11.38

Turn on : 25.6190

Width : 2.699

Height : 10.027

Entry



B1L003S, U11-ch126

calib_packv5_042523_0143.root, FC#13, port D2

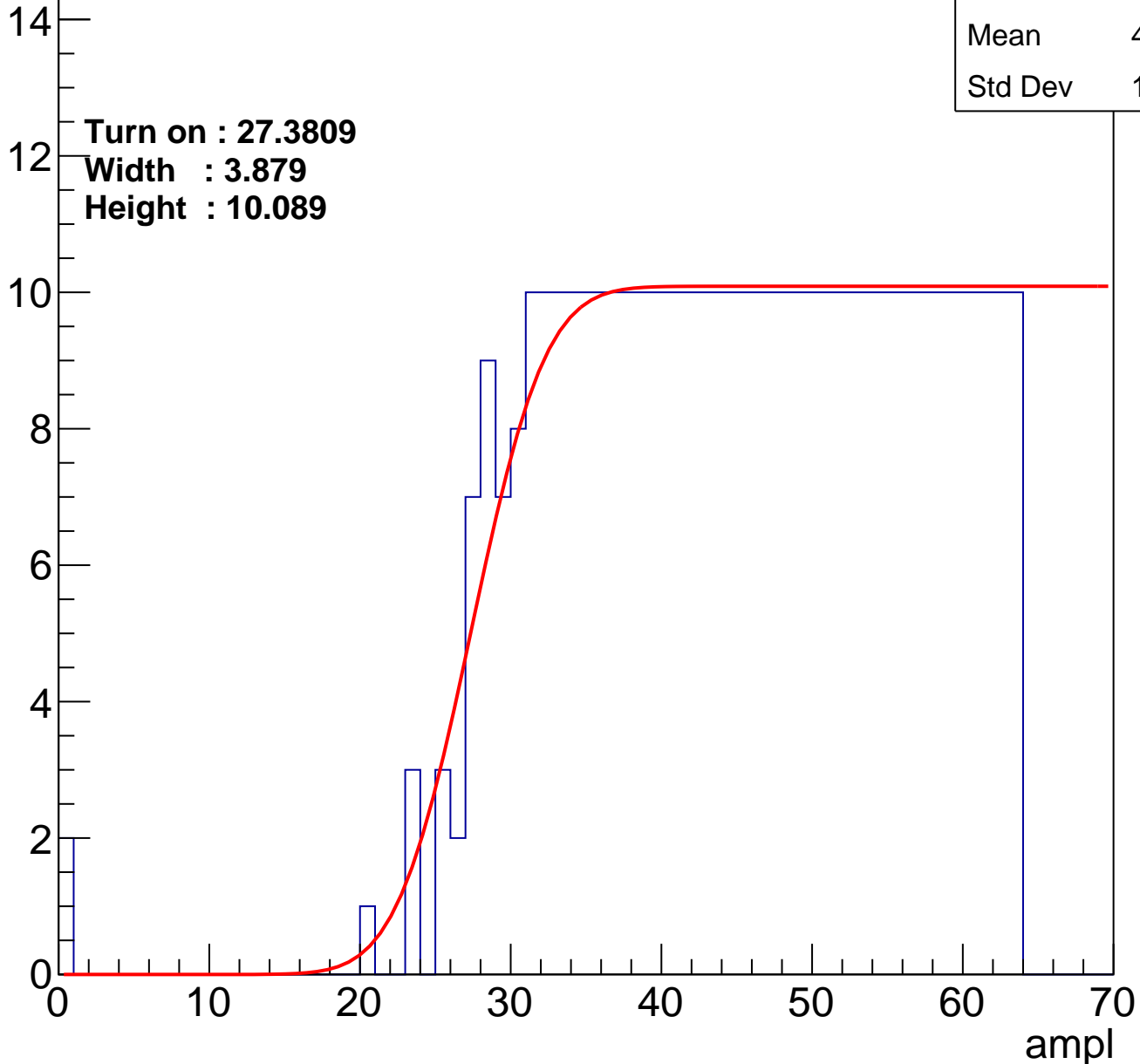
Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.3809

Width : 3.879

Height : 10.089

Entry



B1L003S, U11-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	335
Mean	46.47
Std Dev	10.4

Turn on : 30.7822

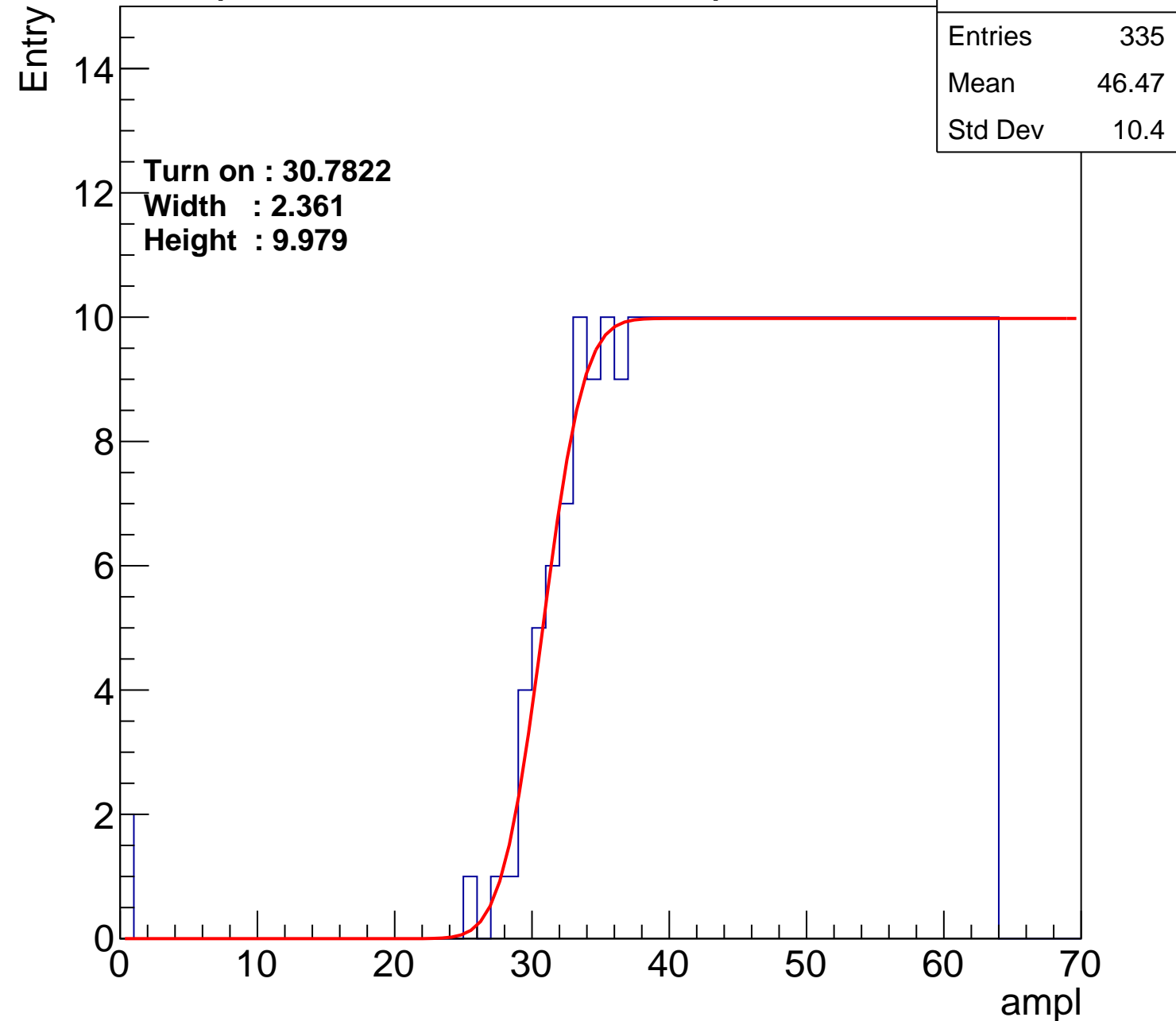
Width : 2.361

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U11-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	335
Mean	46.47
Std Dev	10.4

Turn on : 30.7822

Width : 2.361

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl

