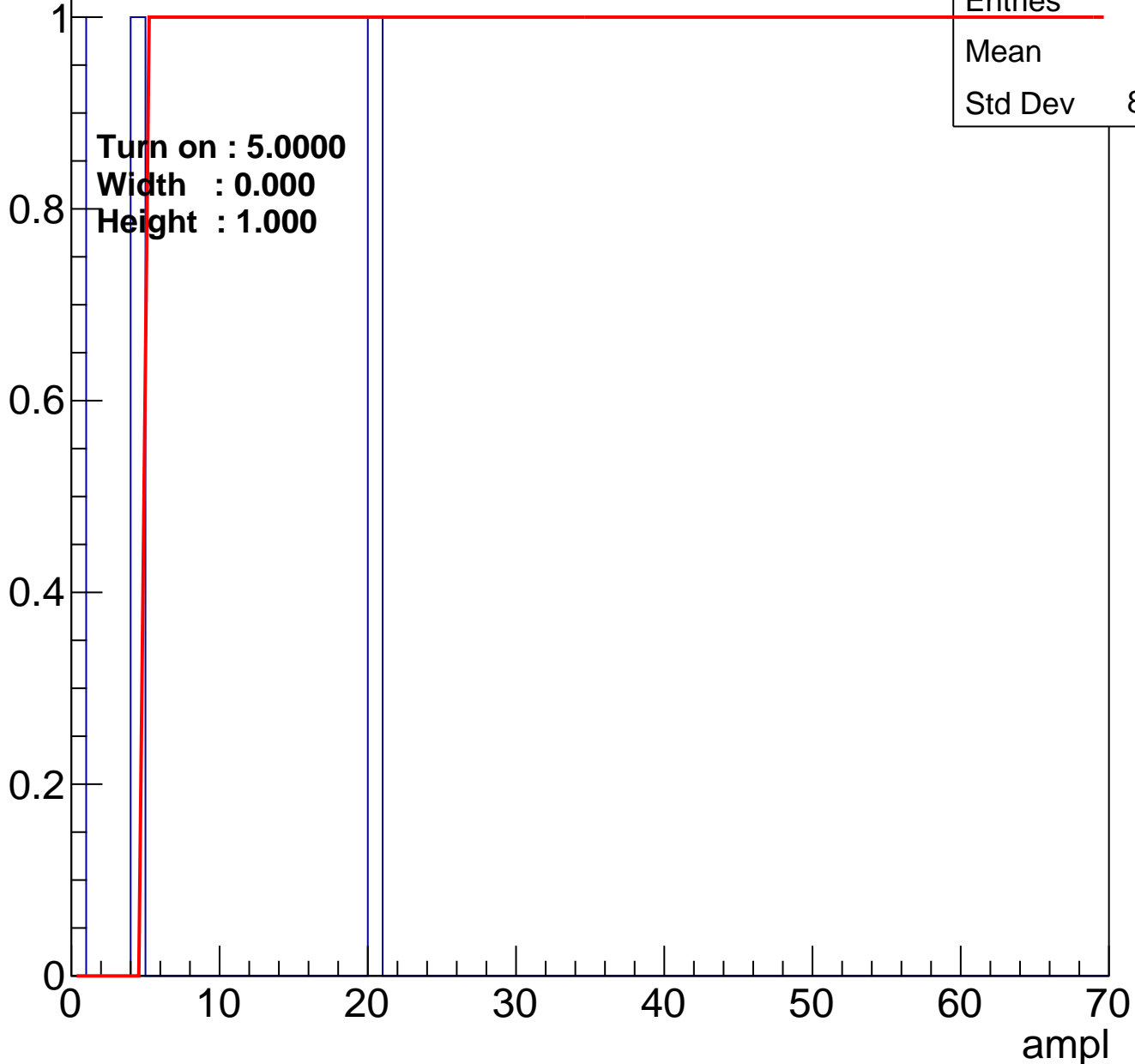


B0L101S, U13-ch0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 8 |
| Std Dev | 8.641 |

B0L101S, U13-ch1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

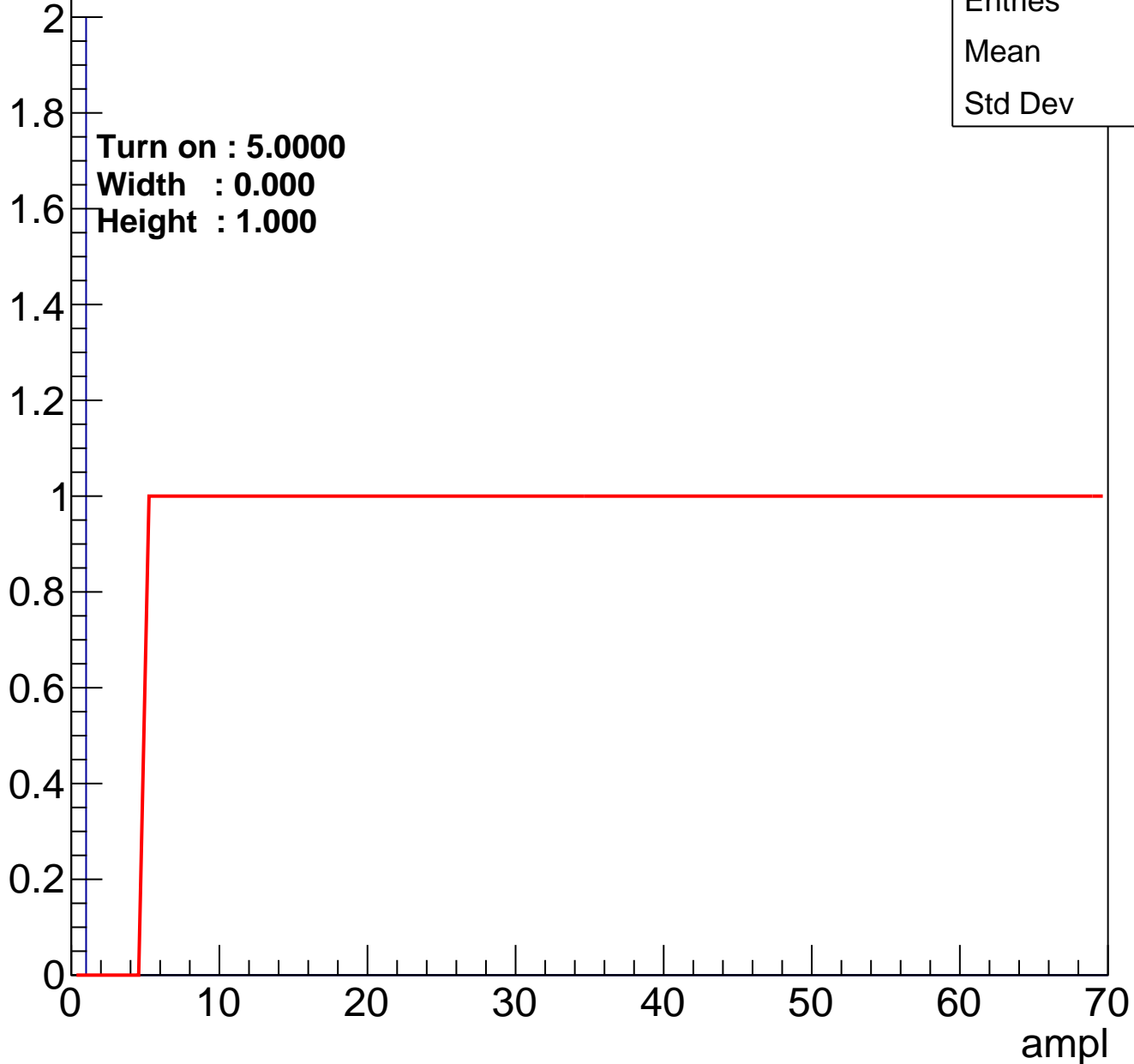


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

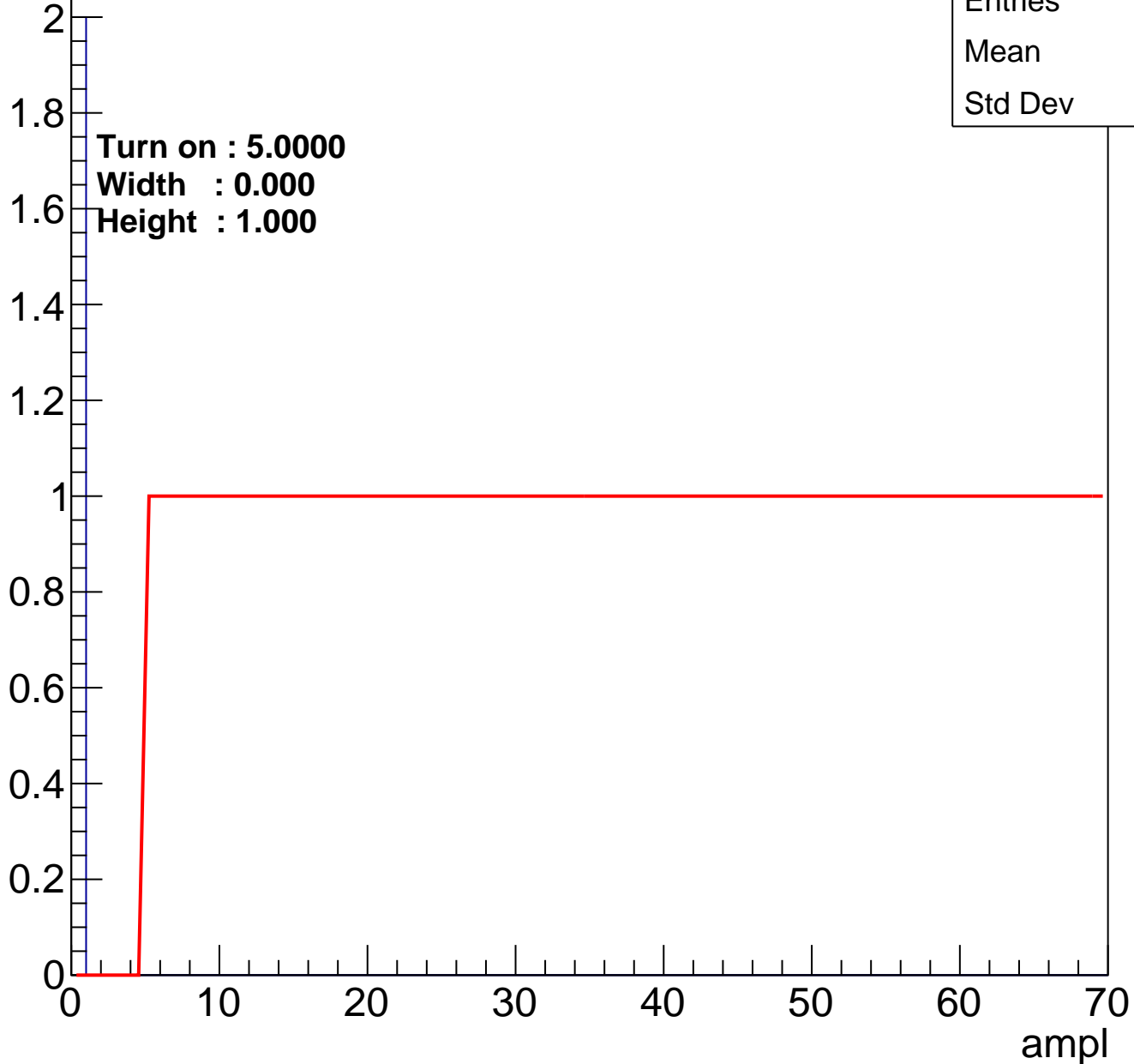


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch8

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch9

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch10

calib_packv5_042523_0143.root, FC#1, port C1

Entry

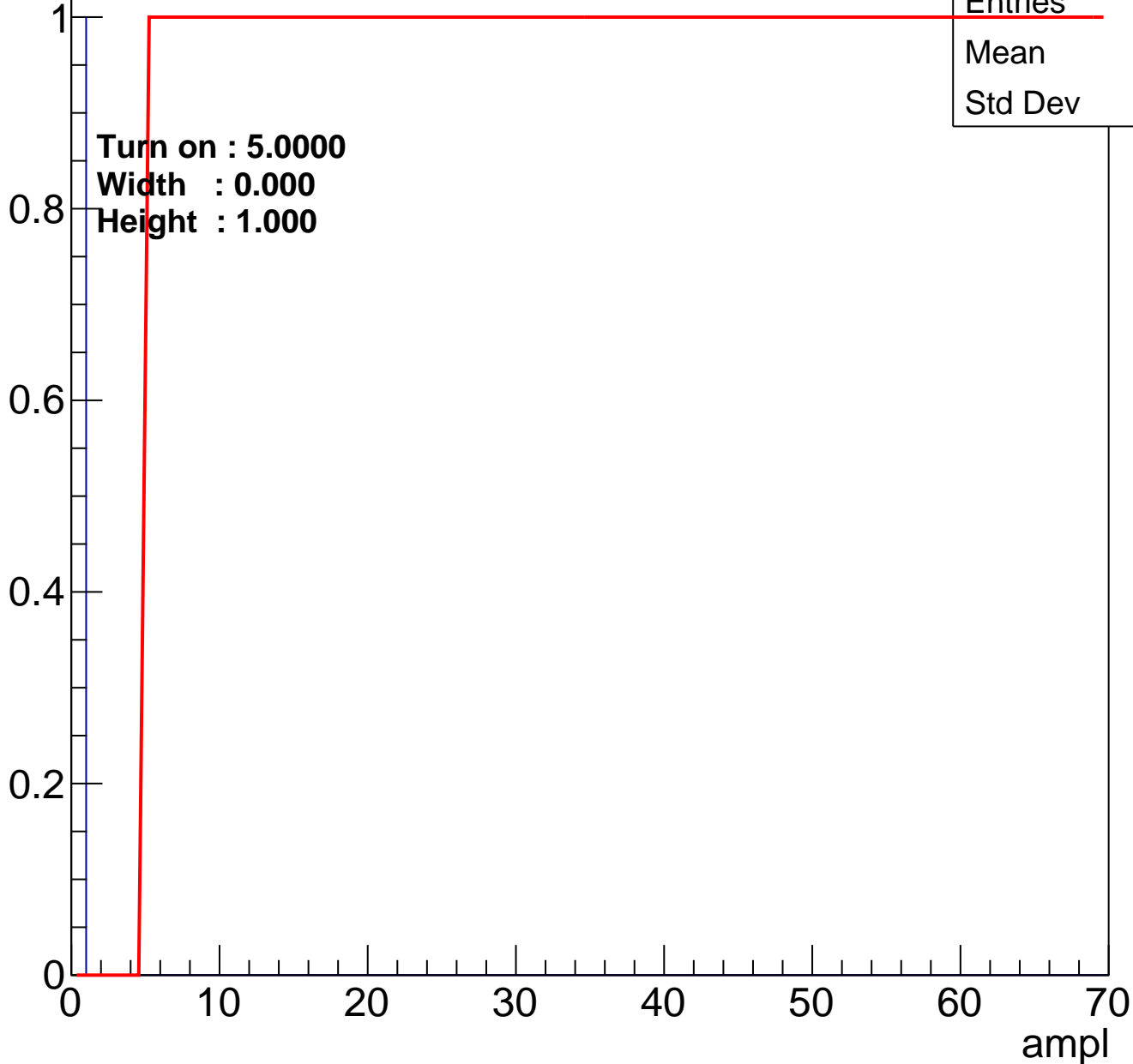


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch11

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch12

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch13

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch14

calib_packv5_042523_0143.root, FC#1, port C1

Entry

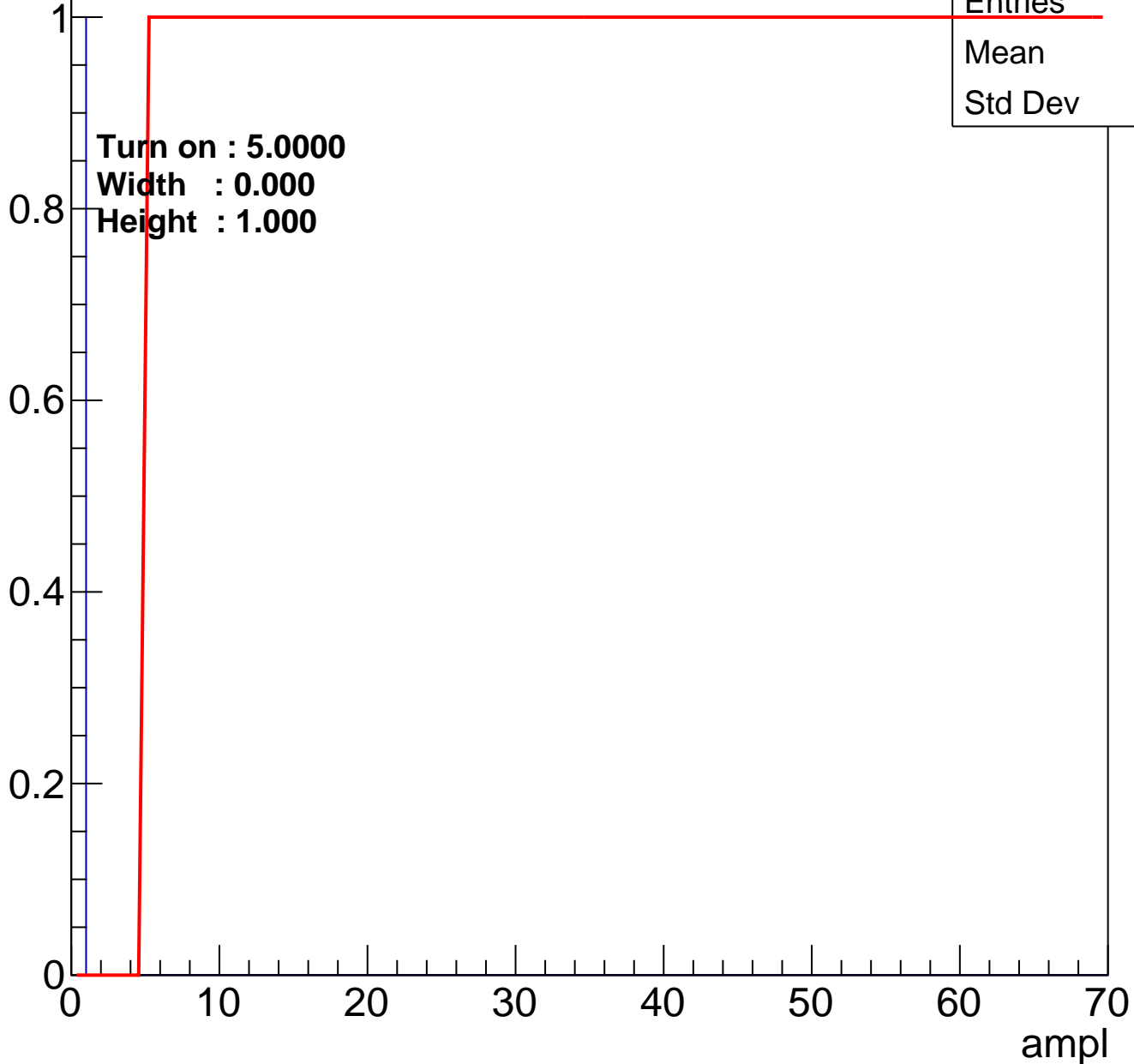


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch15

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch16

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch17

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch18

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch19

calib_packv5_042523_0143.root, FC#1, port C1

Entry

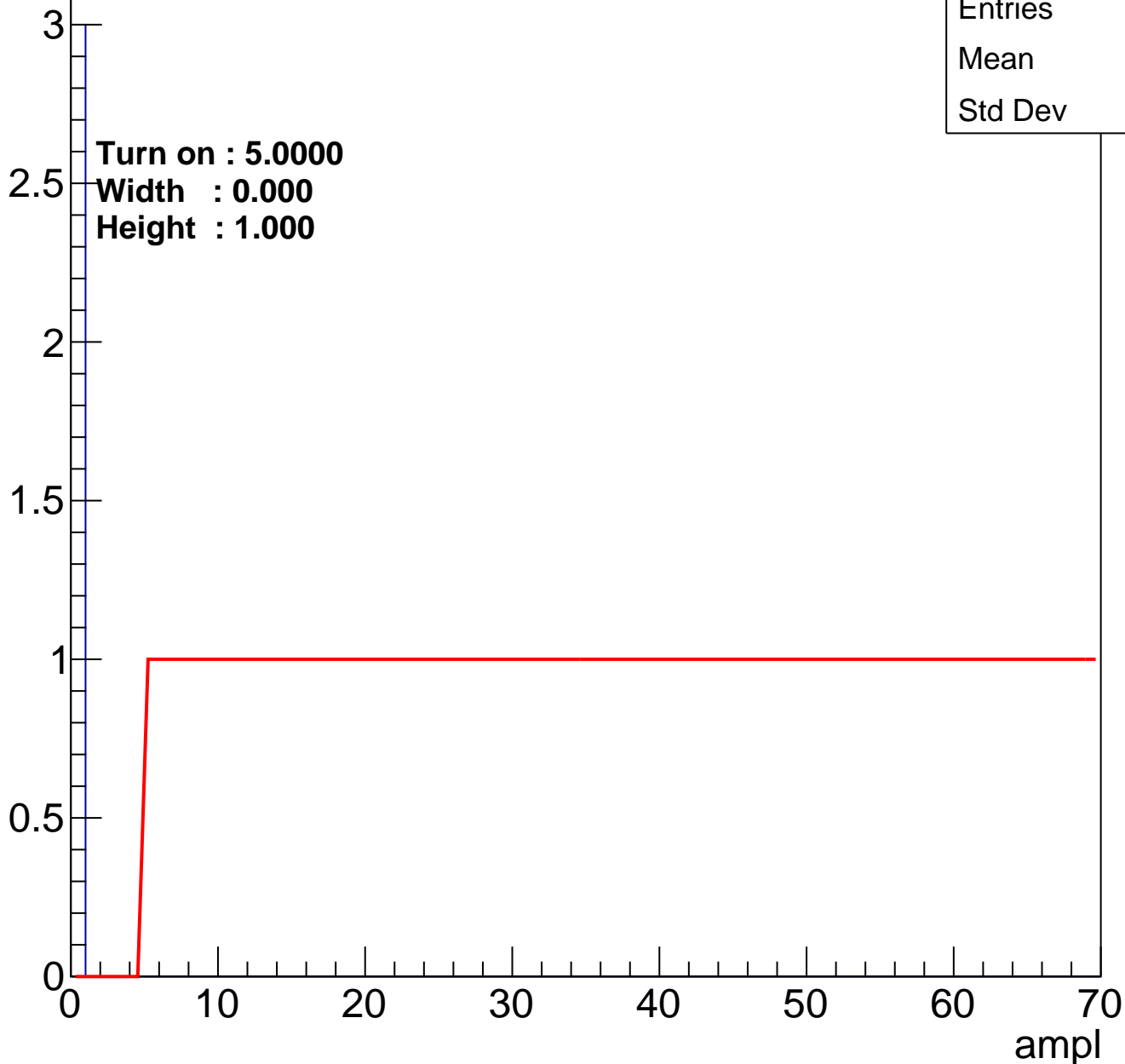


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch20

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch21

calib_packv5_042523_0143.root, FC#1, port C1

Entry

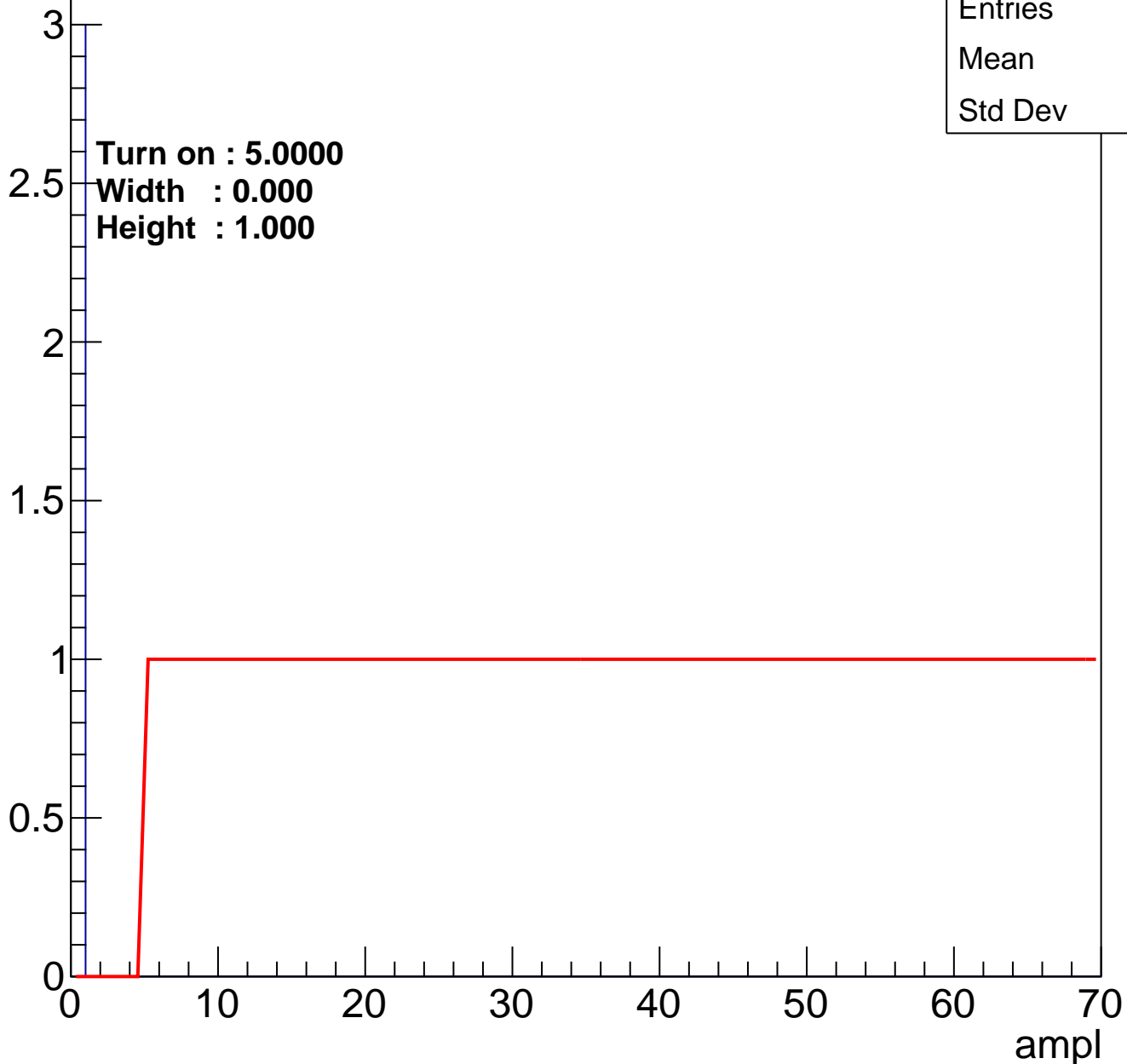


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch22

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch23

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch24

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch25

calib_packv5_042523_0143.root, FC#1, port C1

Entry

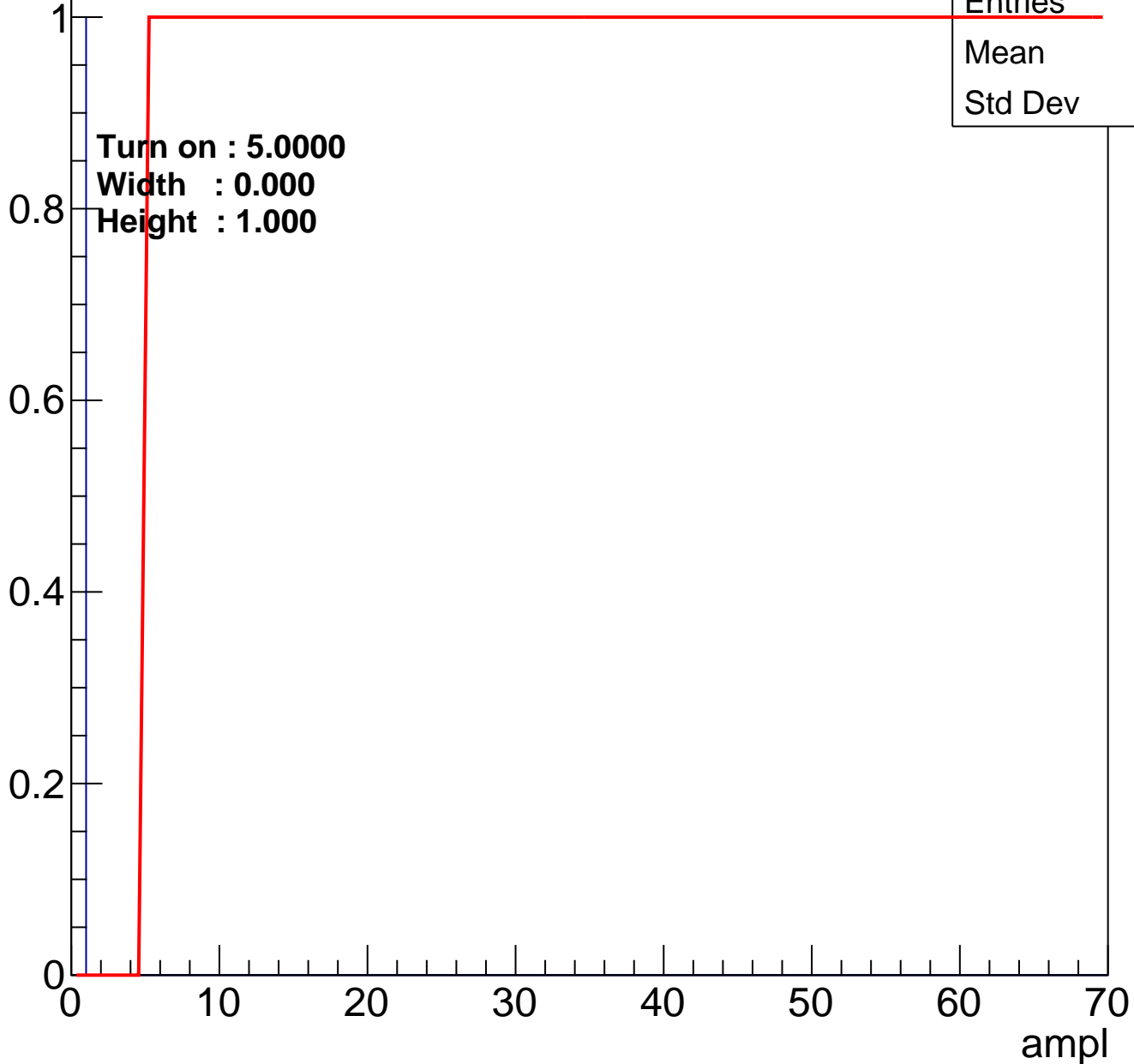


| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch26

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch27

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch28

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch29

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch30

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch31

calib_packv5_042523_0143.root, FC#1, port C1

Entry

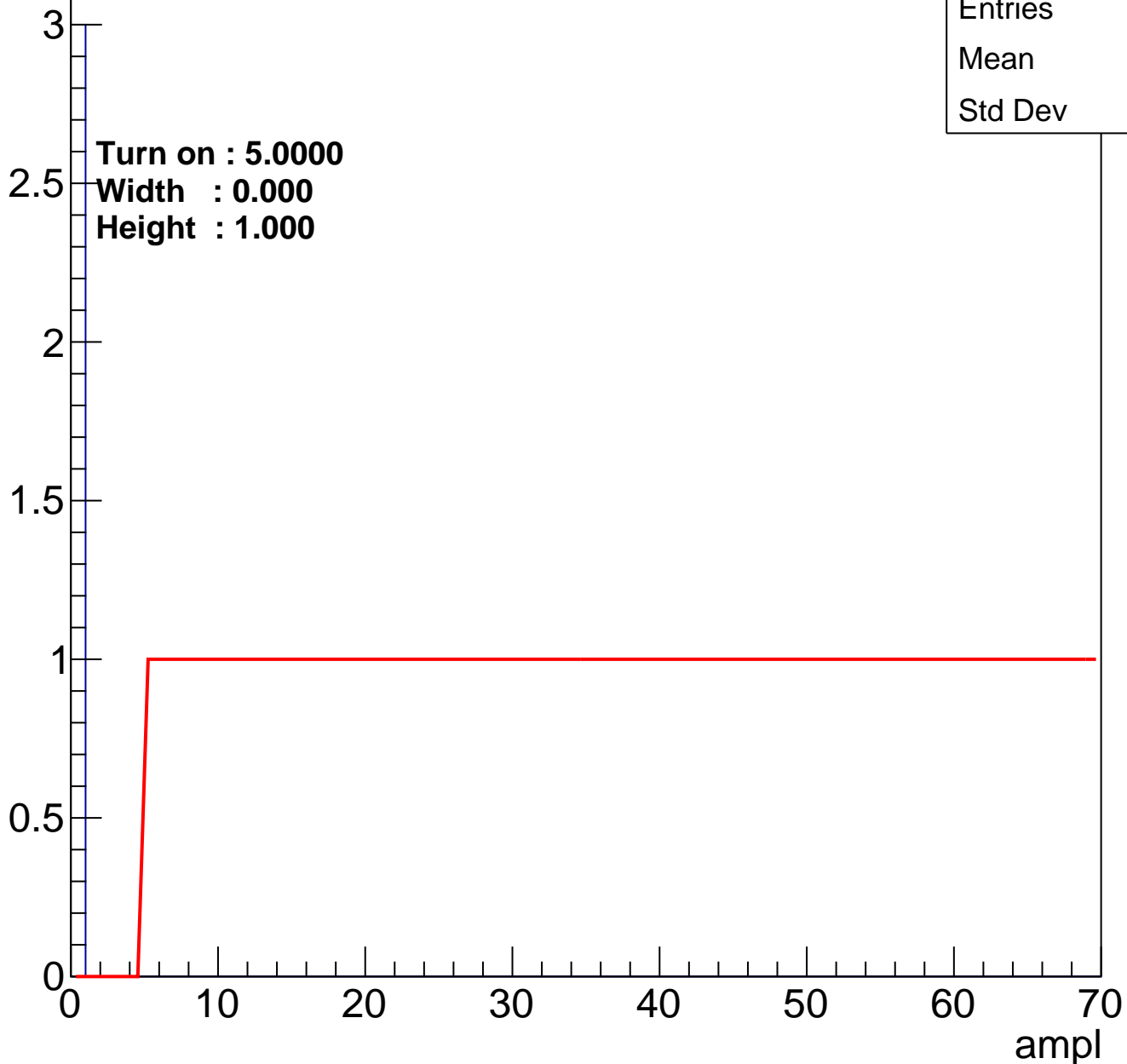


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch32

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch33

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch34

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch35

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch36

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch37

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch38

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch39

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch40

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch41

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch42

calib_packv5_042523_0143.root, FC#1, port C1

Entry

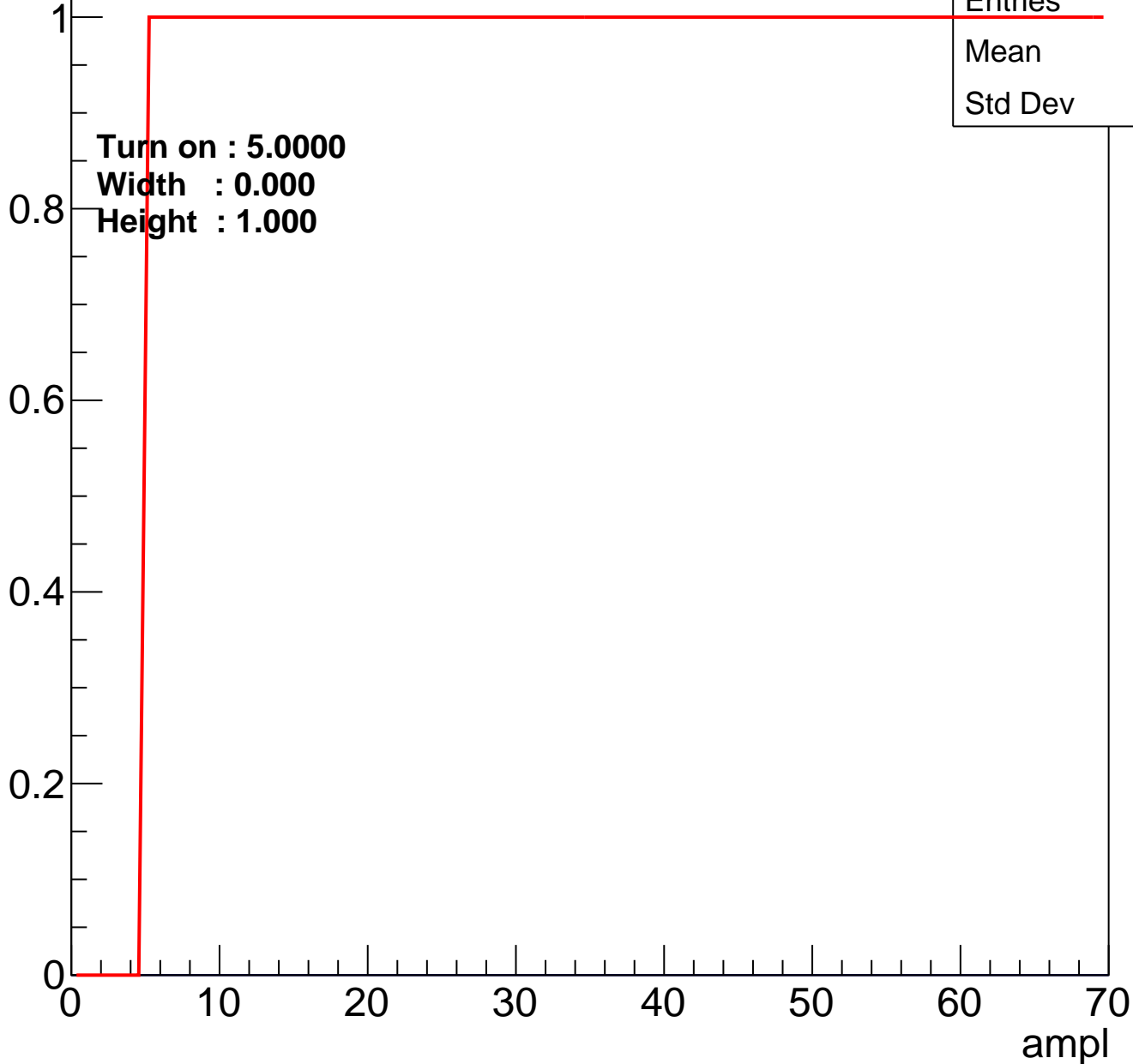


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch43

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch44

calib_packv5_042523_0143.root, FC#1, port C1

Entry

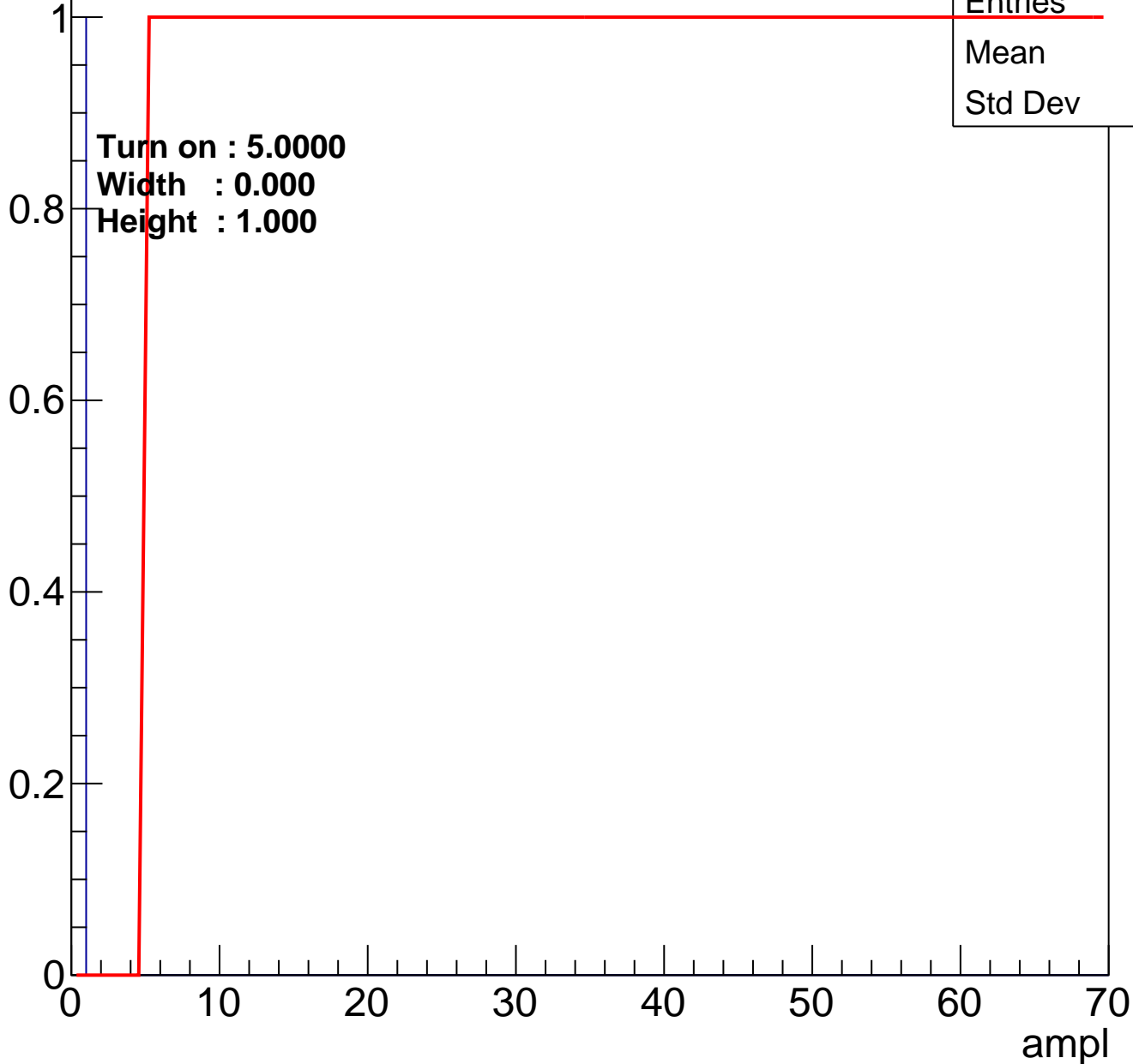


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch45

calib_packv5_042523_0143.root, FC#1, port C1

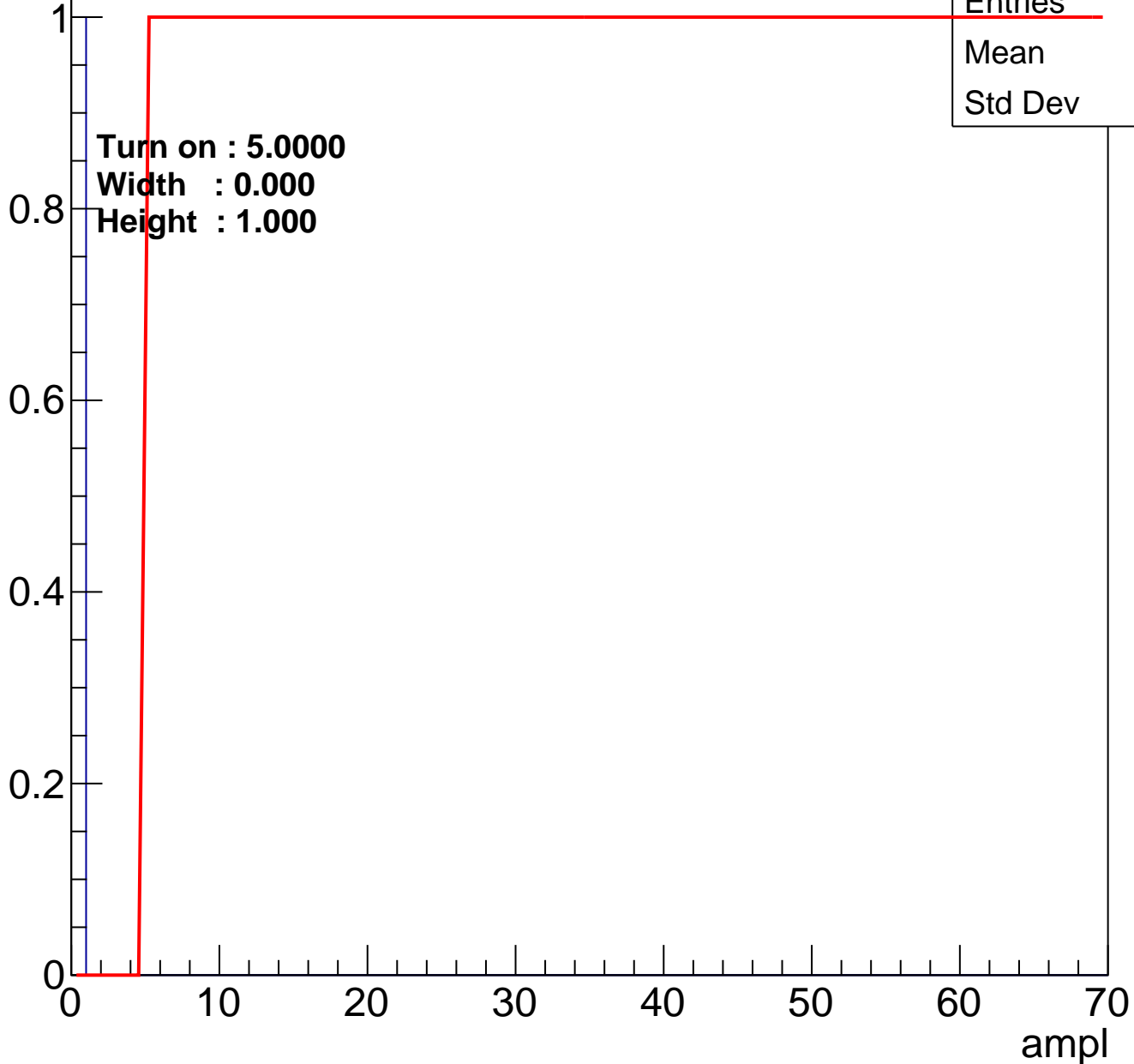
Entry



B0L101S, U13-ch46

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch47

calib_packv5_042523_0143.root, FC#1, port C1

Entry

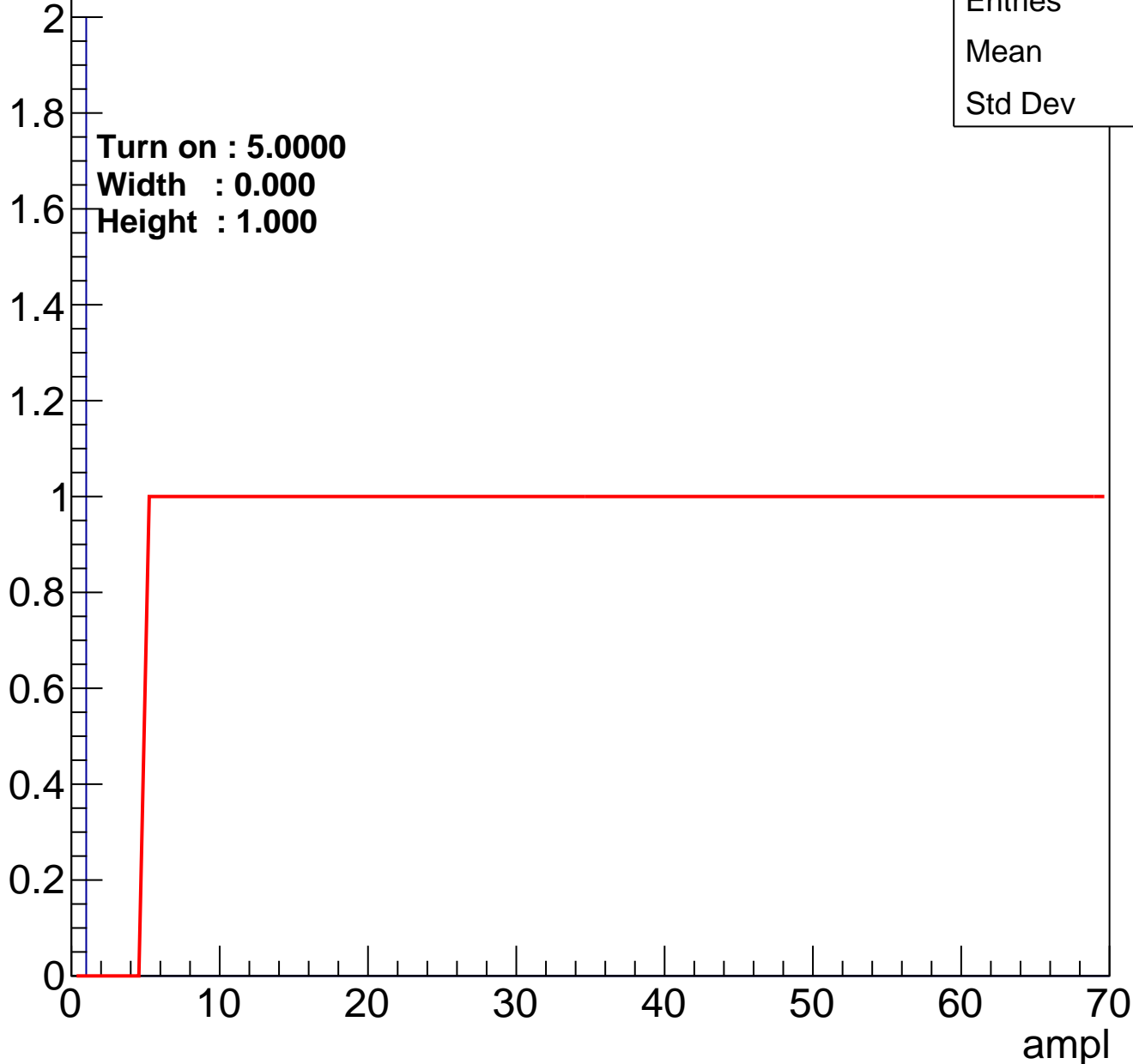


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch48

calib_packv5_042523_0143.root, FC#1, port C1

Entry

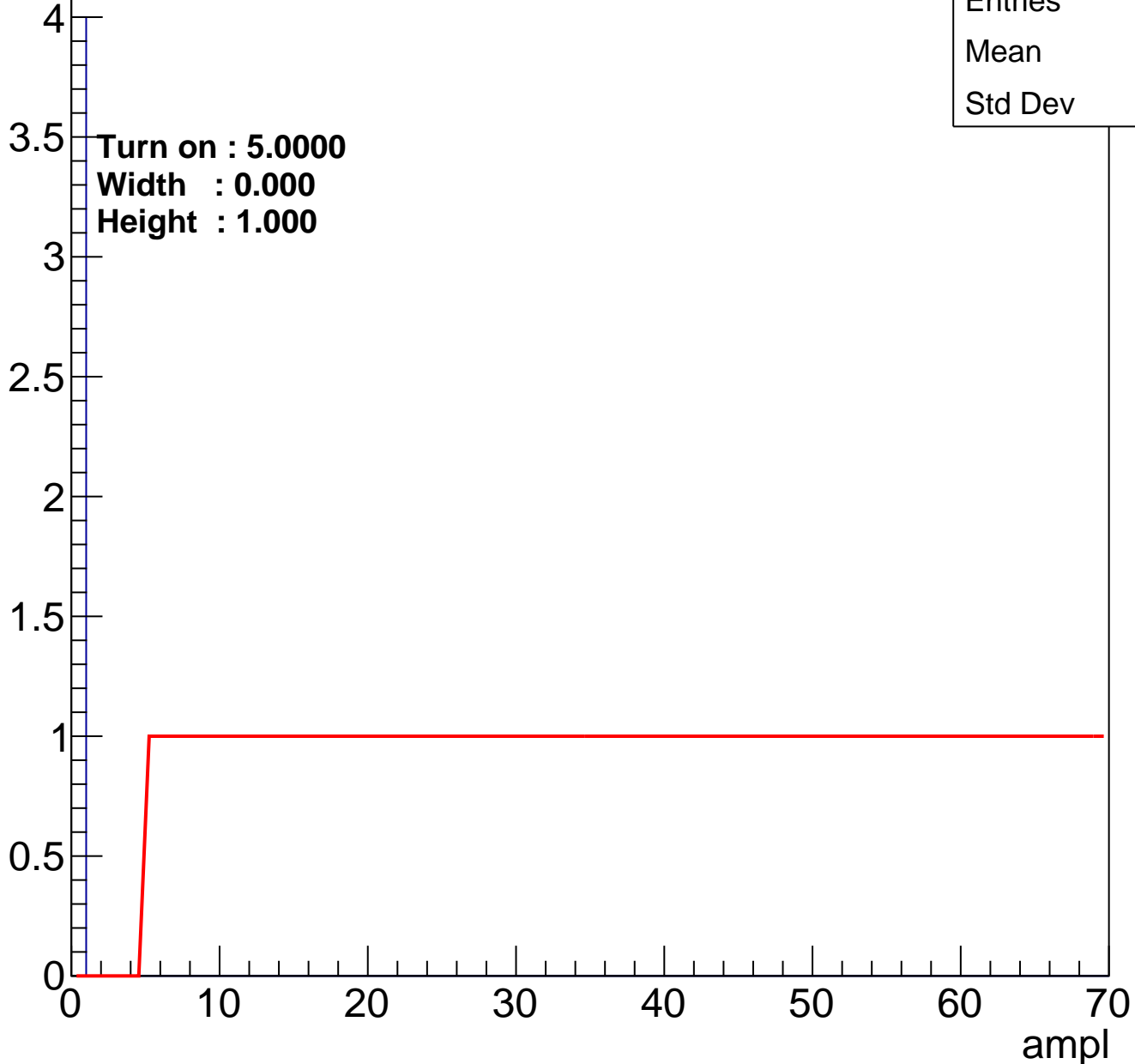


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch49

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch50

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch51

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch52

calib_packv5_042523_0143.root, FC#1, port C1

Entry

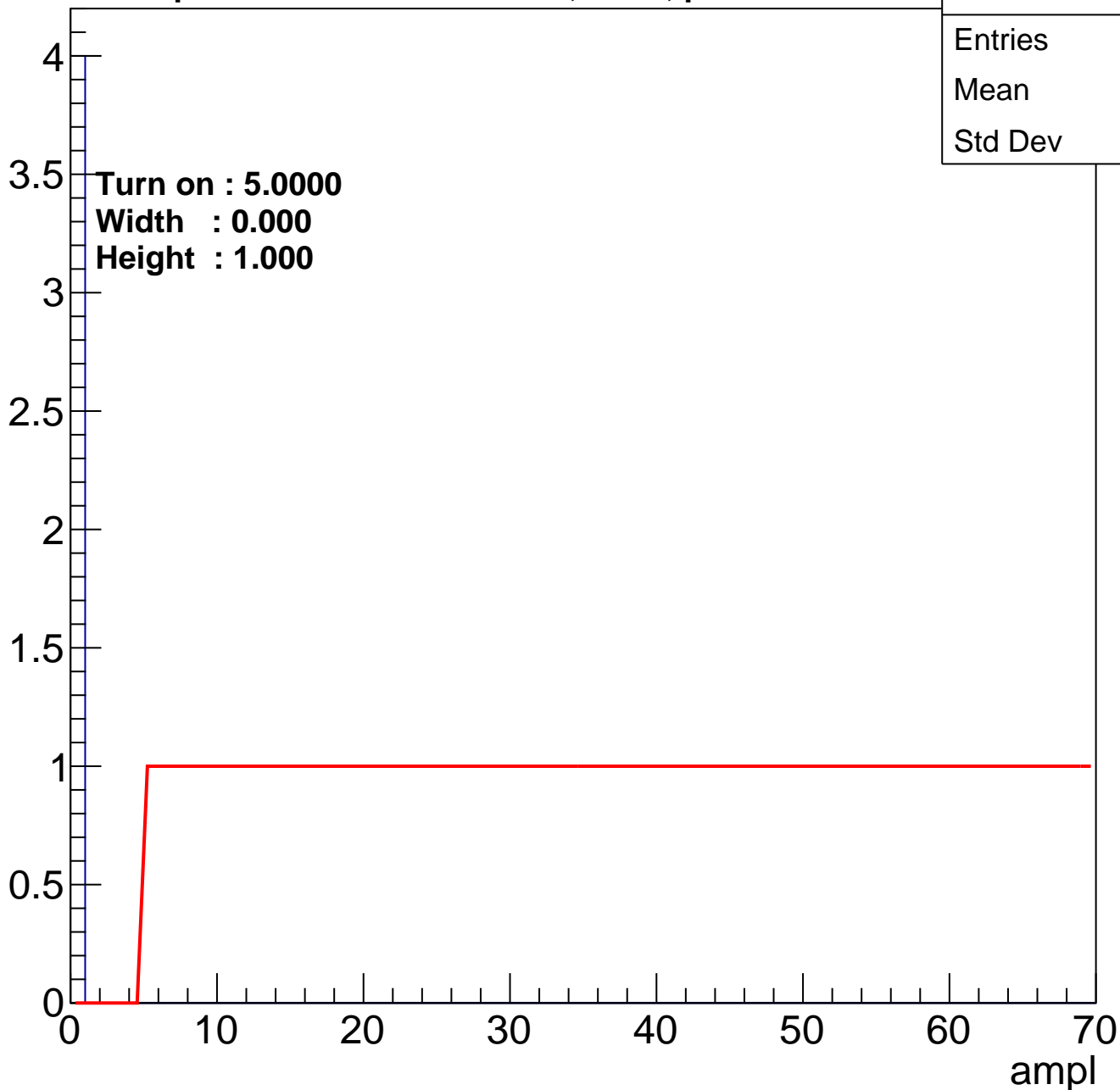


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch53

calib_packv5_042523_0143.root, FC#1, port C1

Entry

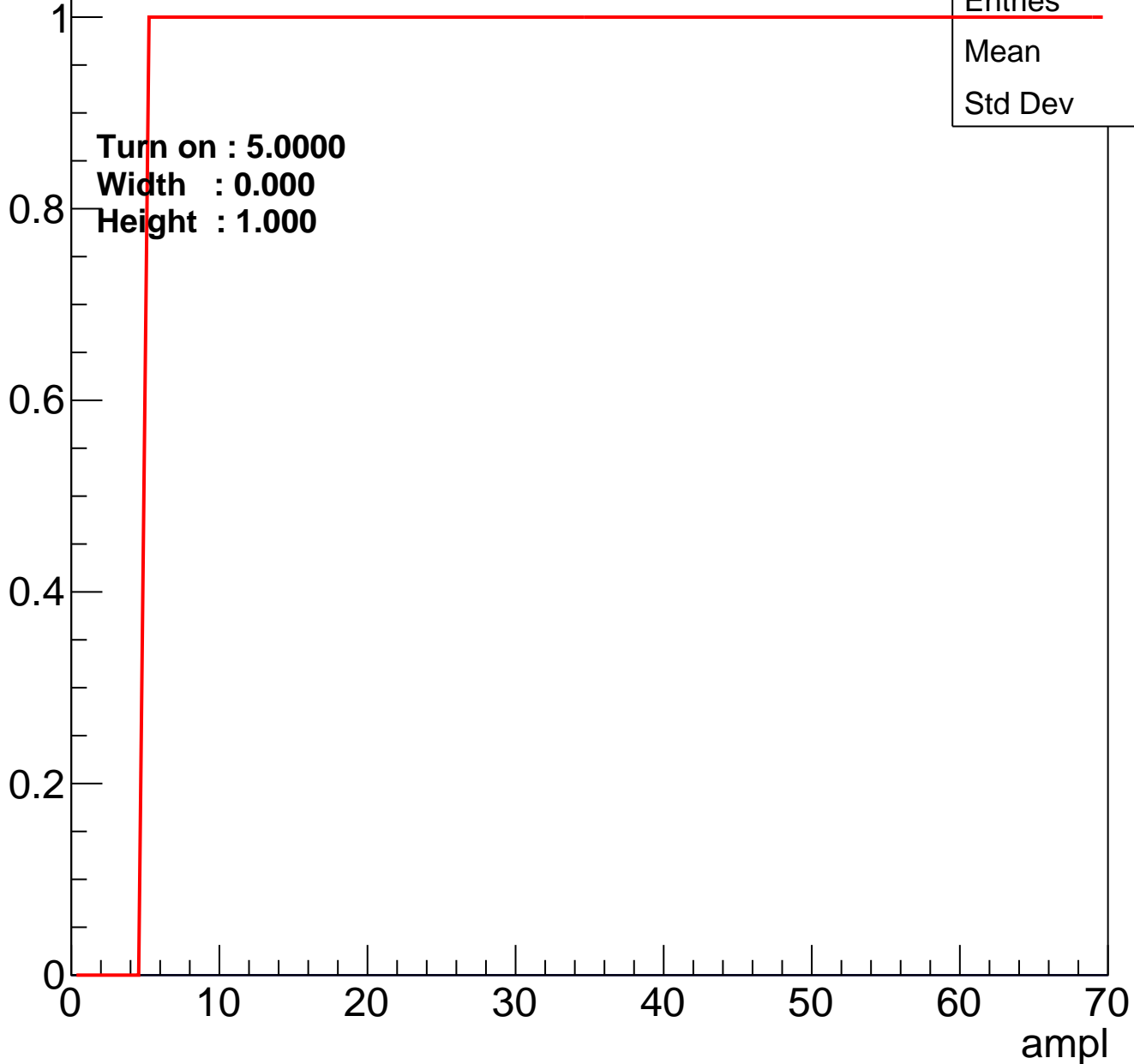


| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch54

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch55

calib_packv5_042523_0143.root, FC#1, port C1

Entry

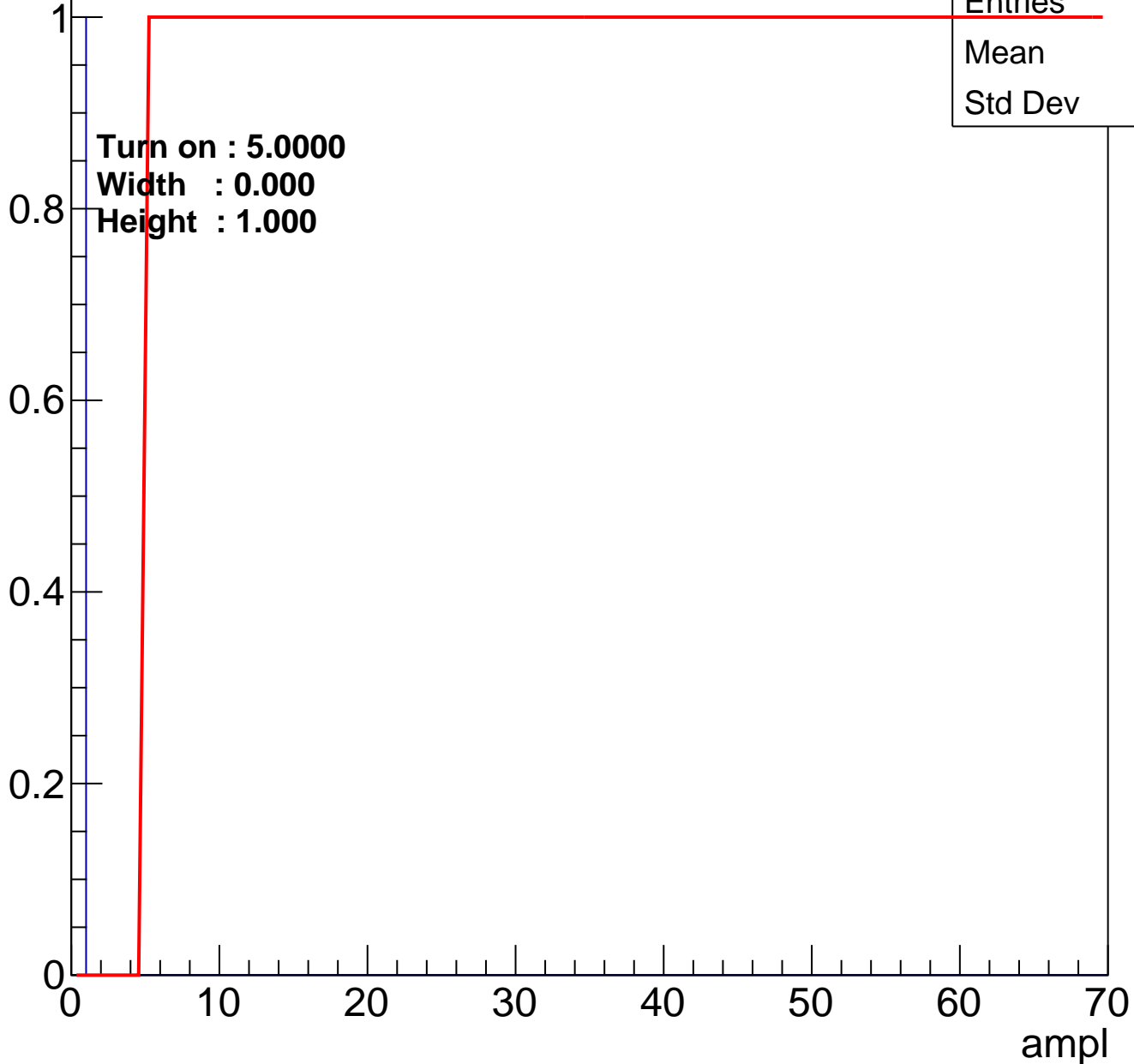


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch56

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch57

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch58

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch59

calib_packv5_042523_0143.root, FC#1, port C1

Entry

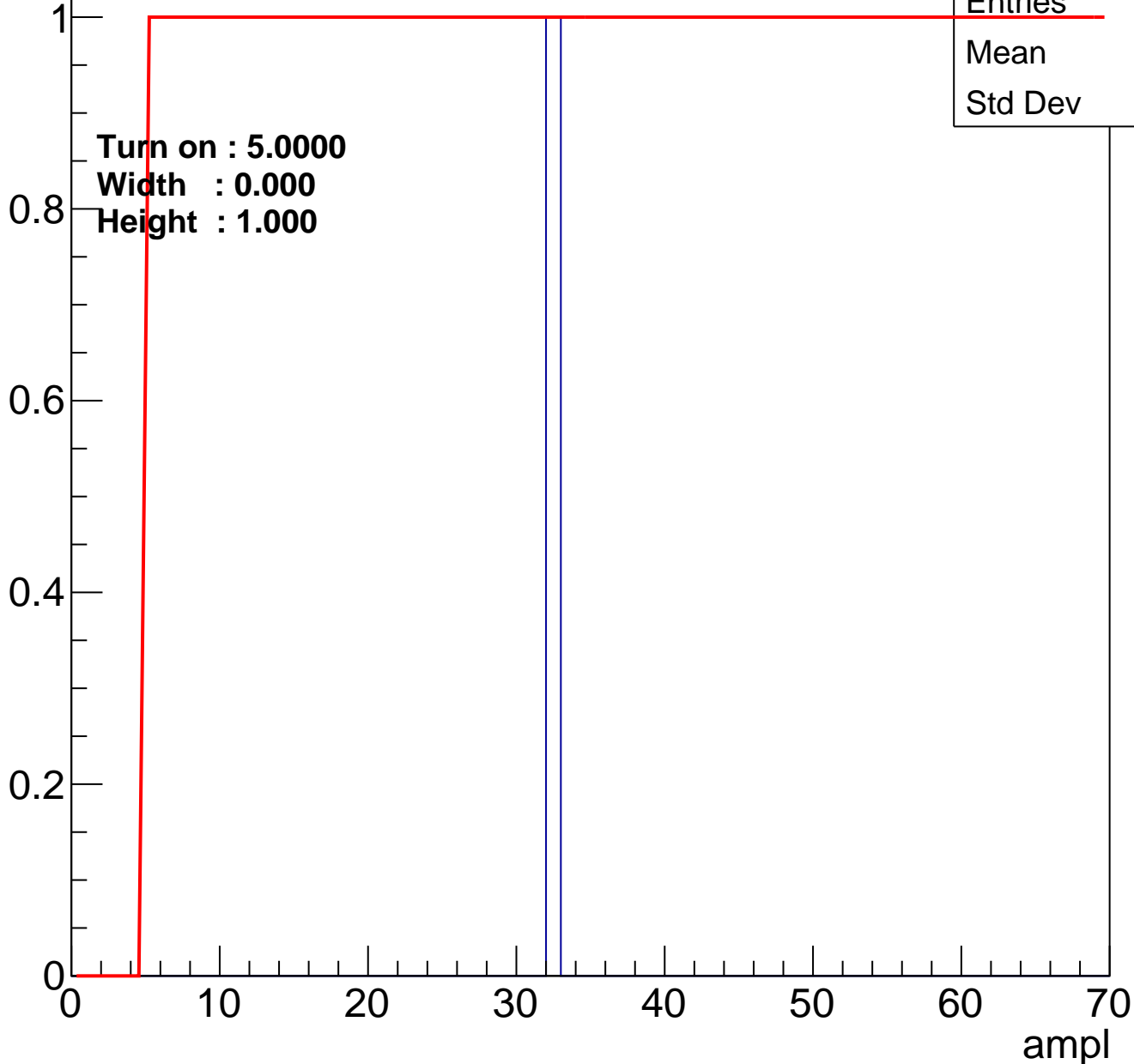


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch60

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch61

calib_packv5_042523_0143.root, FC#1, port C1

Entry

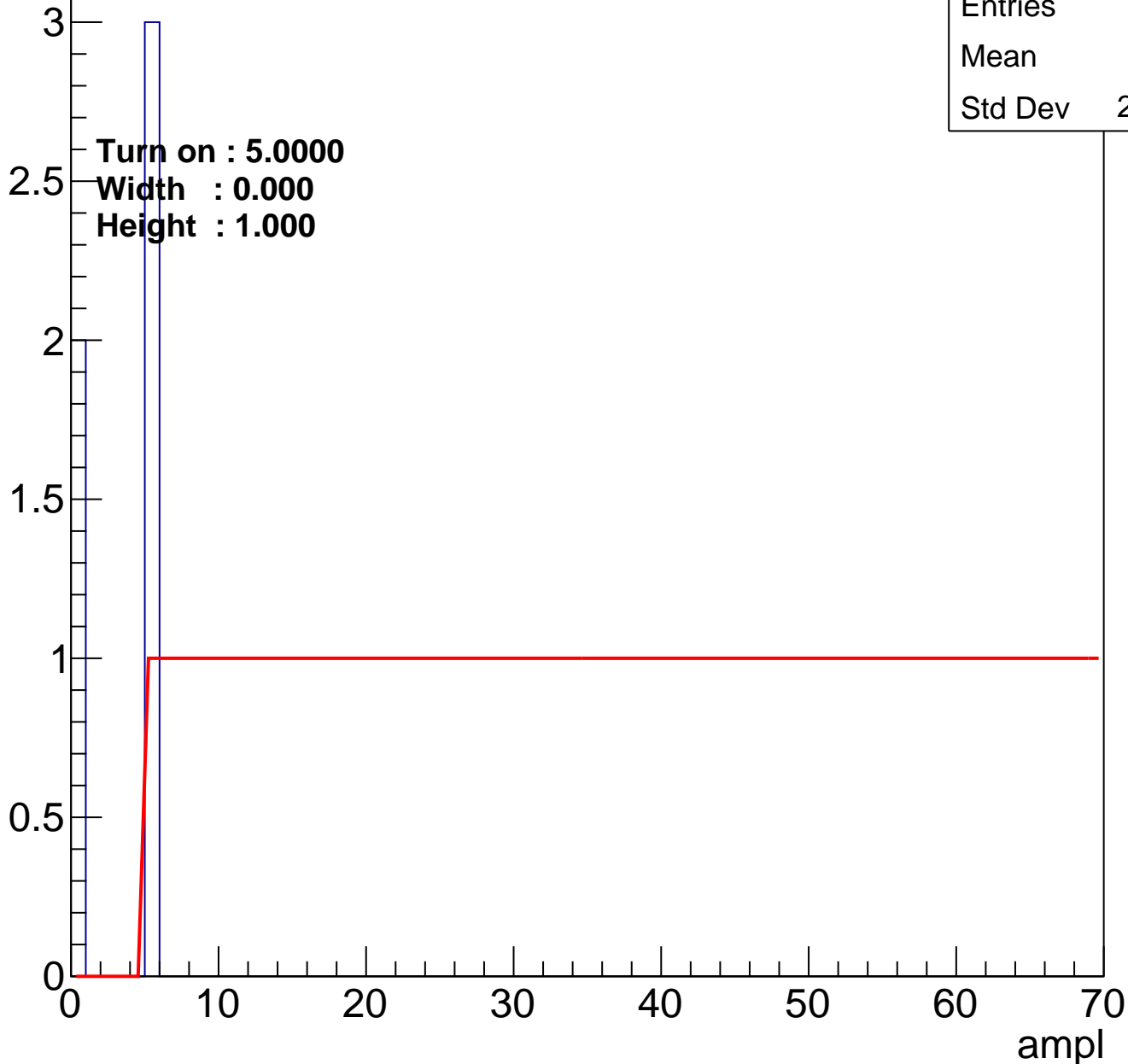


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch62

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch63

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch64

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch65

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch66

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch67

calib_packv5_042523_0143.root, FC#1, port C1

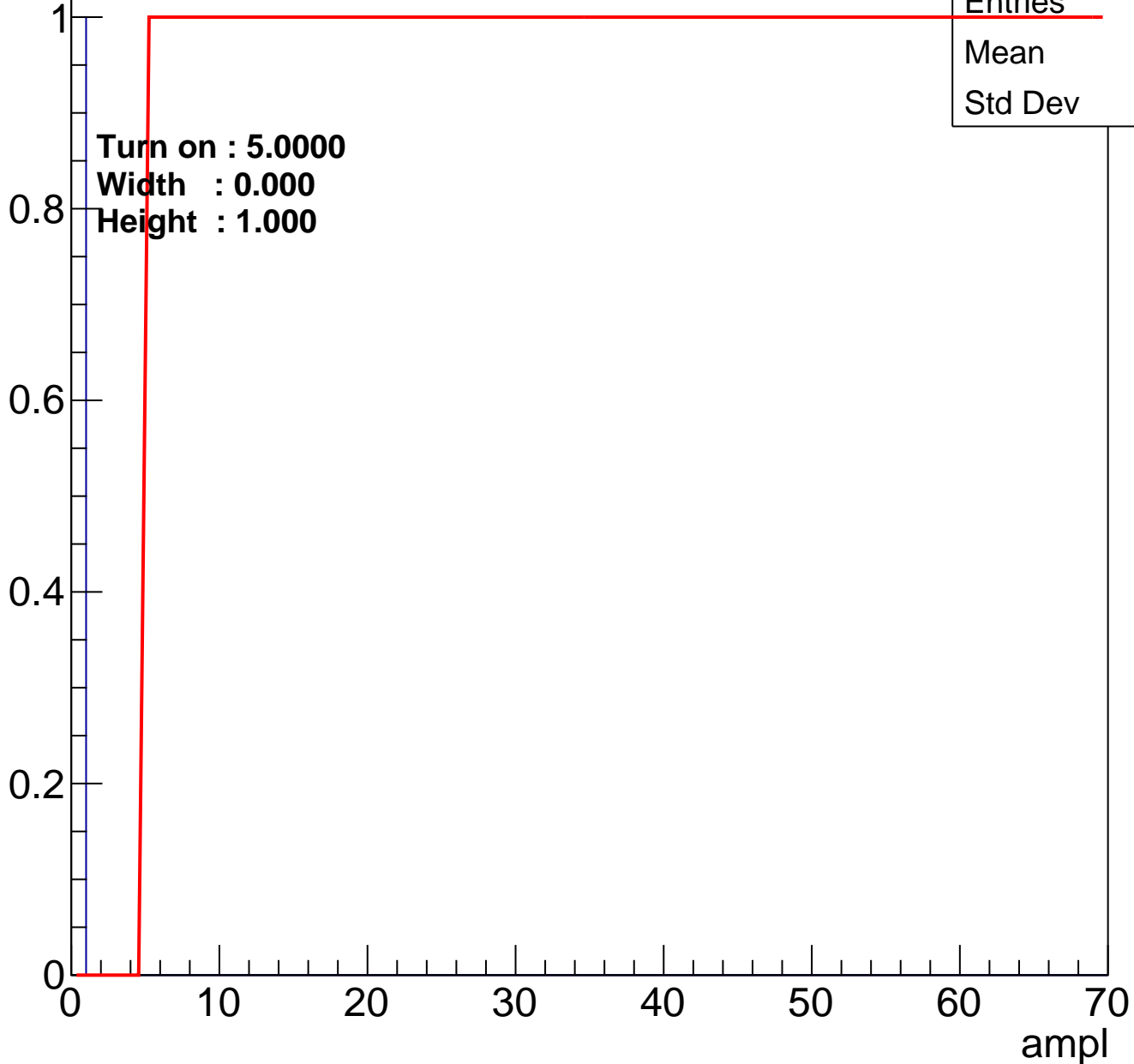
Entry



B0L101S, U13-ch68

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch69

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch70

calib_packv5_042523_0143.root, FC#1, port C1

Entry

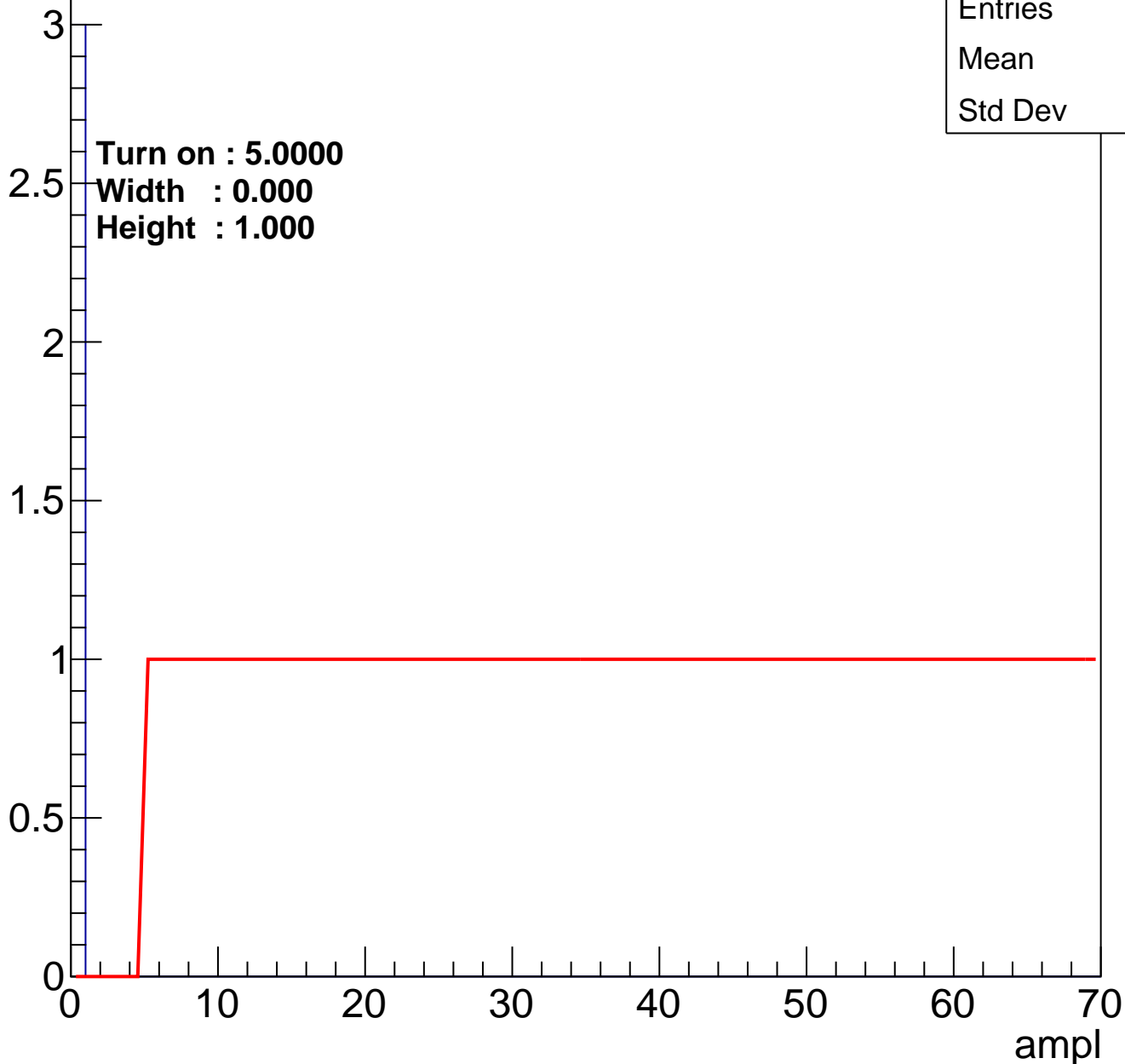


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch71

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch72

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Turn on : 5.0000

Width : 0.000

Height : 1.000

| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch73

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch74

calib_packv5_042523_0143.root, FC#1, port C1

Entry

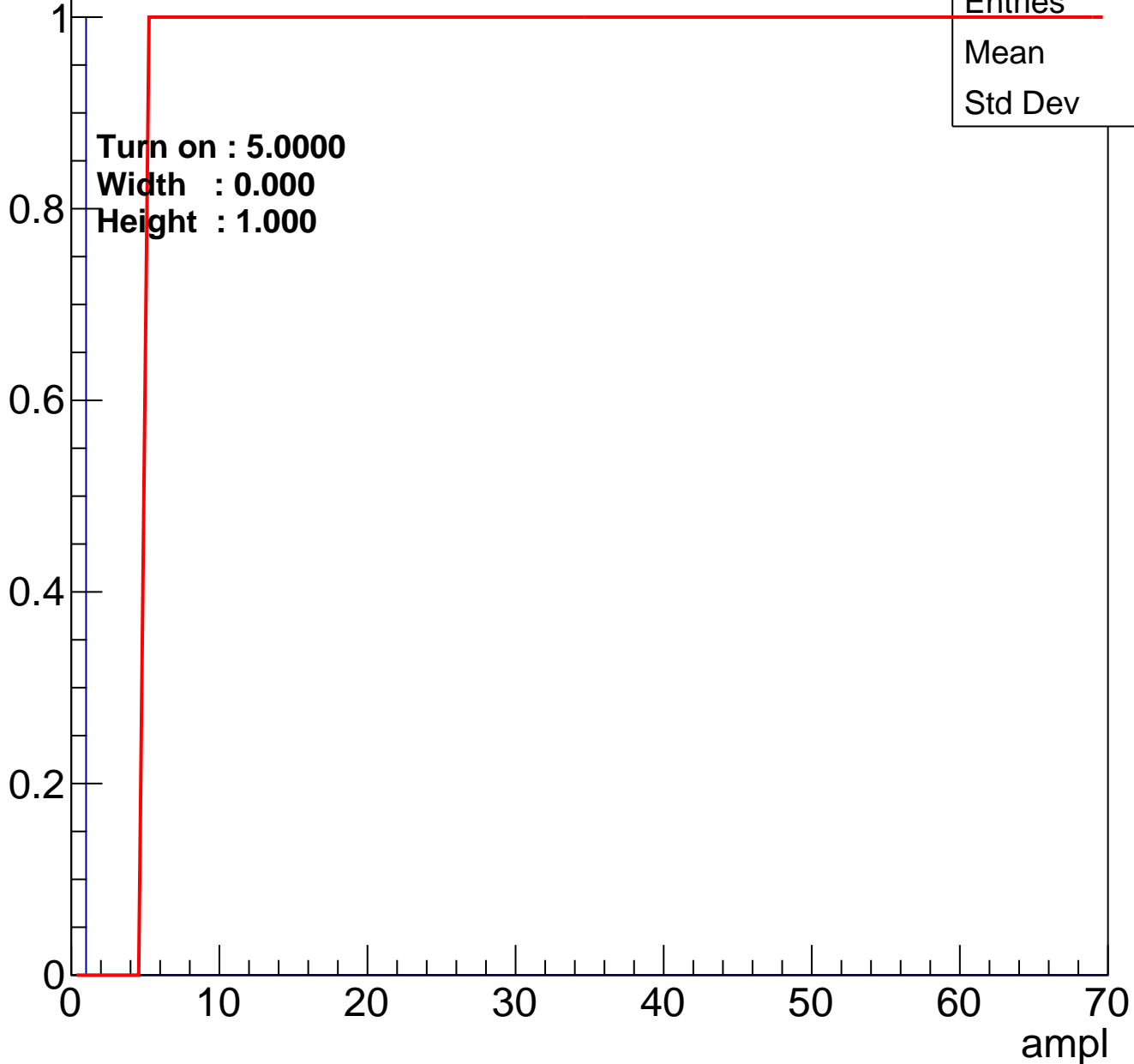


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch75

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch76

calib_packv5_042523_0143.root, FC#1, port C1

Entry

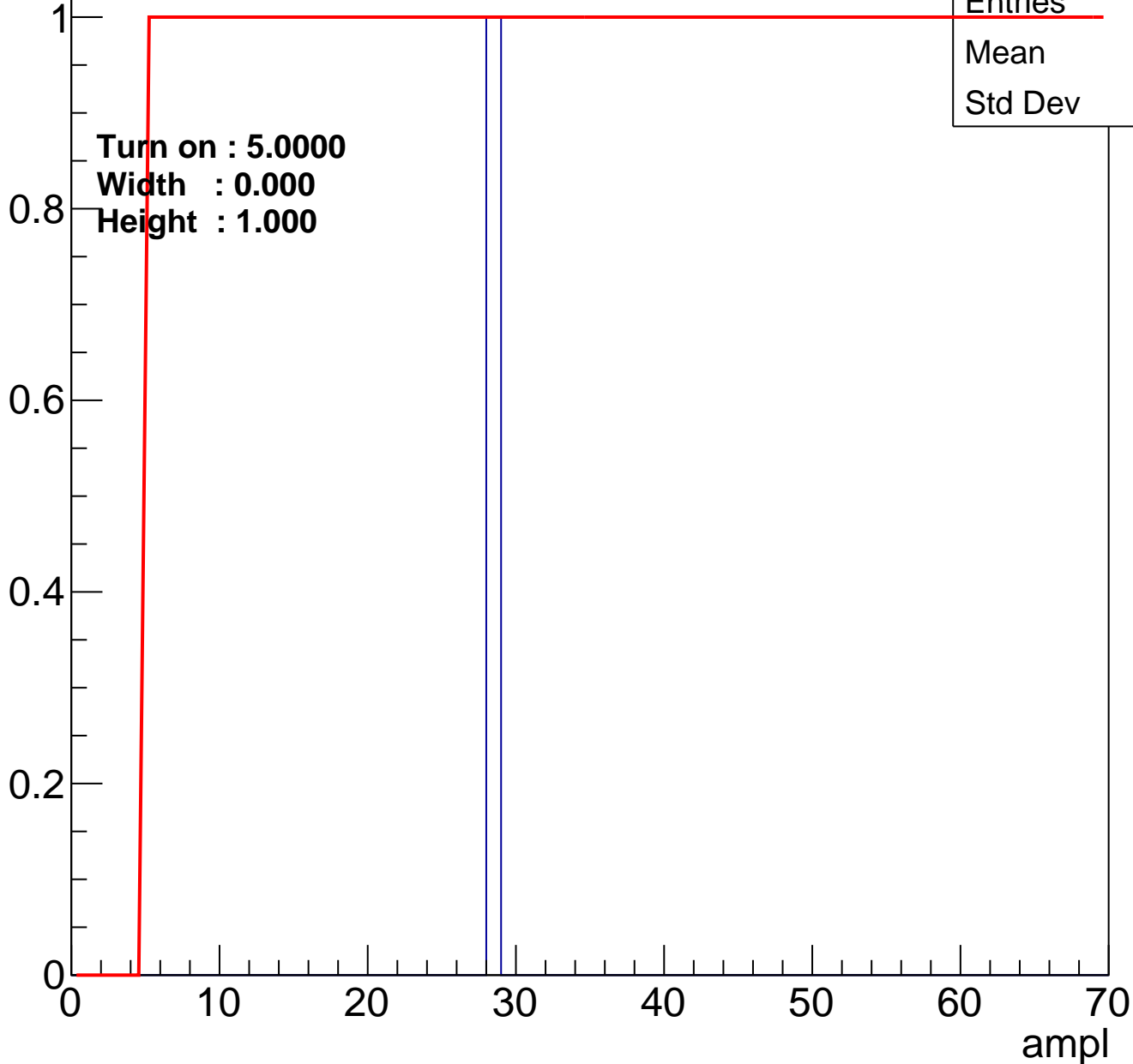


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch77

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch78

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch79

calib_packv5_042523_0143.root, FC#1, port C1

Entry

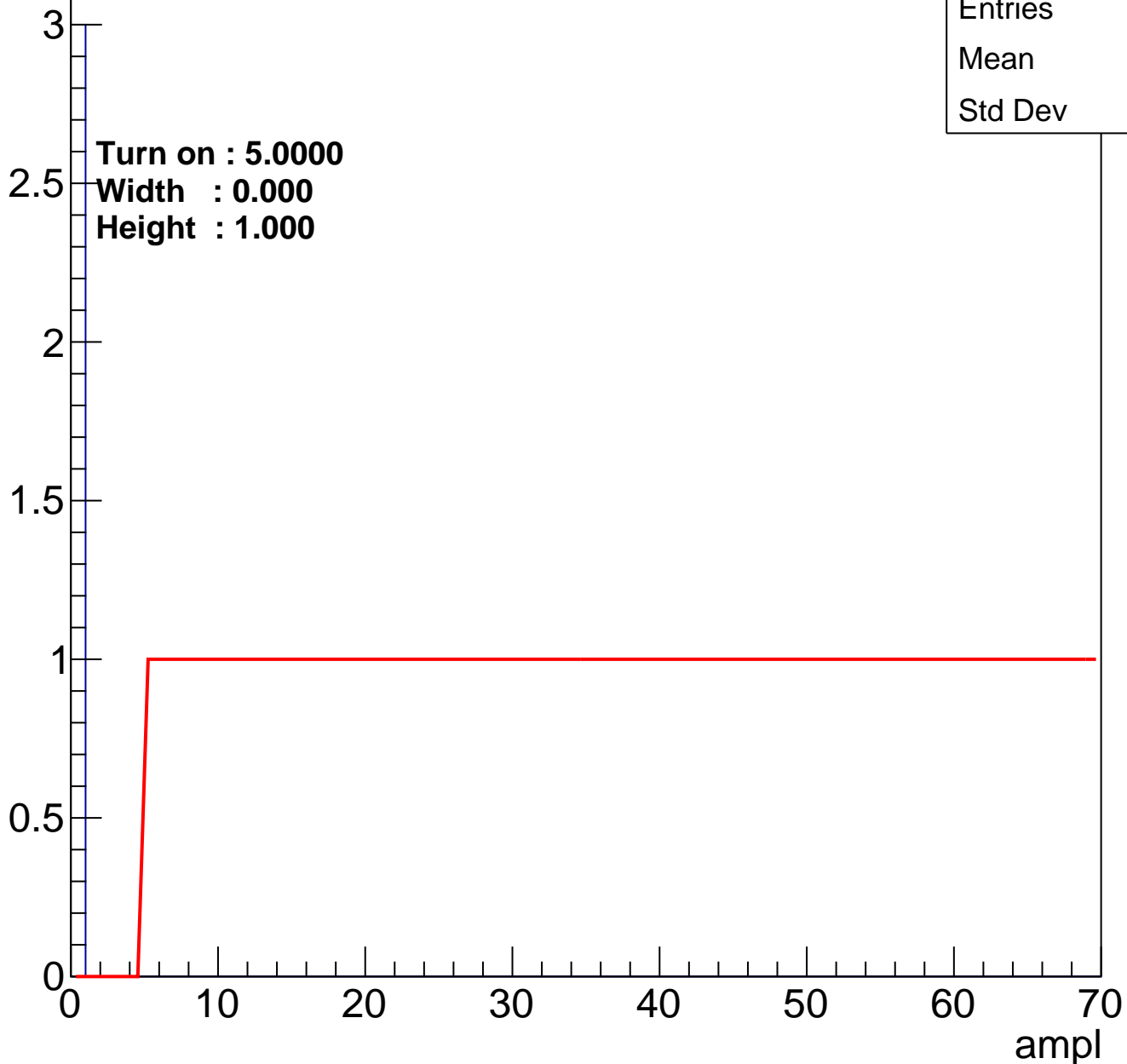


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch80

calib_packv5_042523_0143.root, FC#1, port C1

Entry

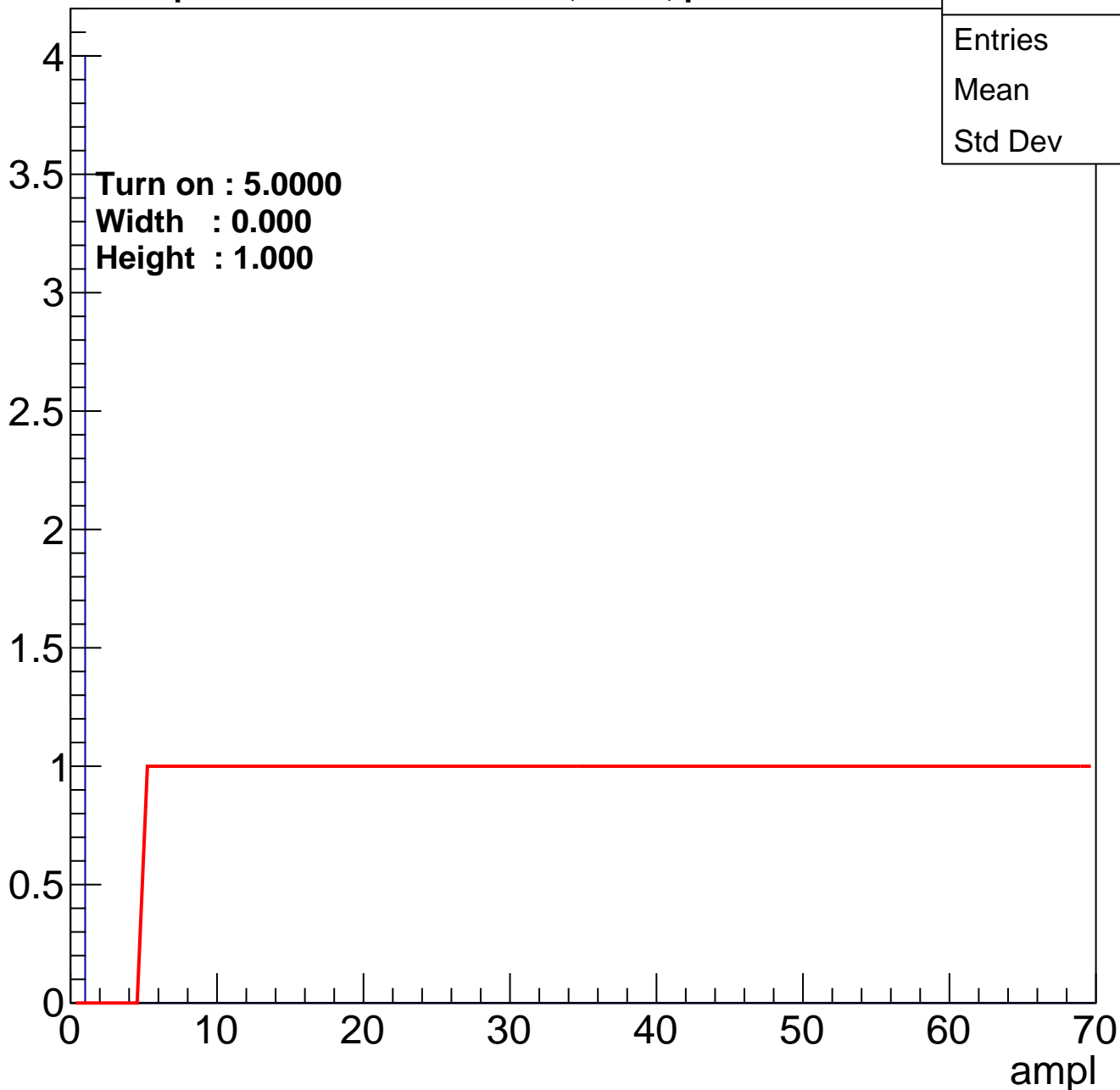


| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch81

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch82

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch83

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch84

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch85

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch86

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch87

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch88

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch89

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch90

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch91

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch92

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch93

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

ampl

B0L101S, U13-ch94

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch95

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch96

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch97

calib_packv5_042523_0143.root, FC#1, port C1

Entry

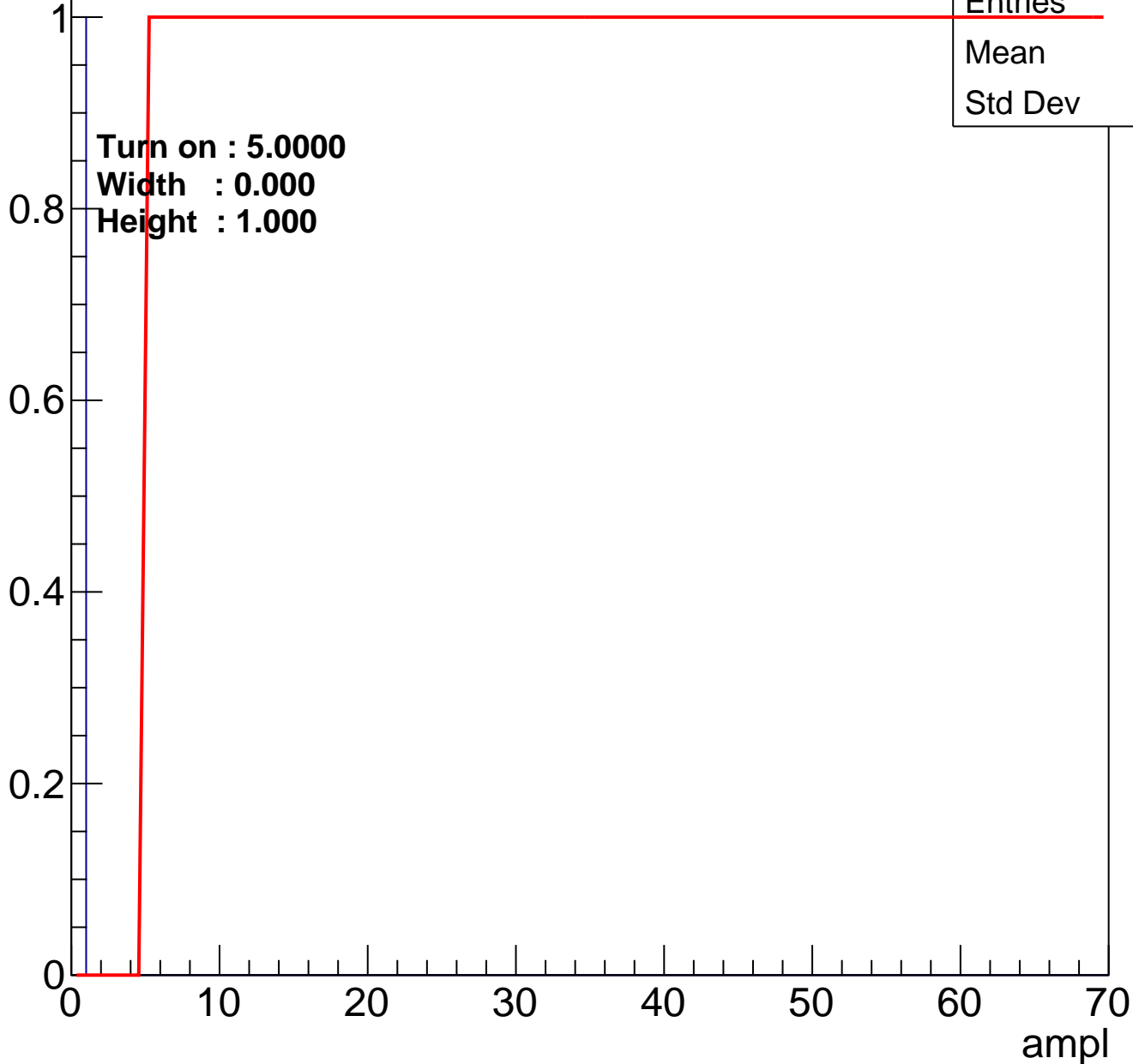


| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch98

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch99

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch100

calib_packv5_042523_0143.root, FC#1, port C1

Entry

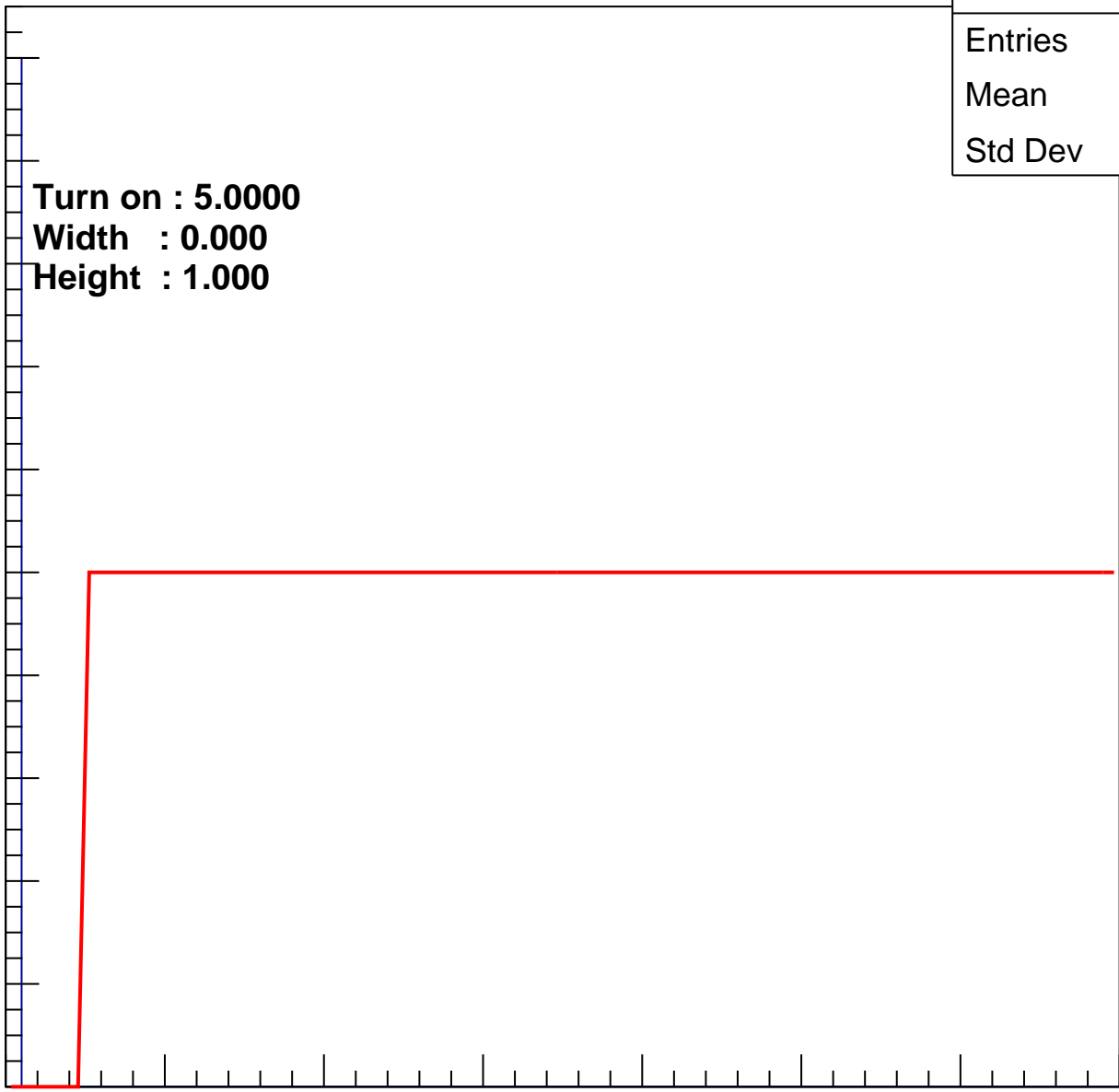
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl



B0L101S, U13-ch101

calib_packv5_042523_0143.root, FC#1, port C1

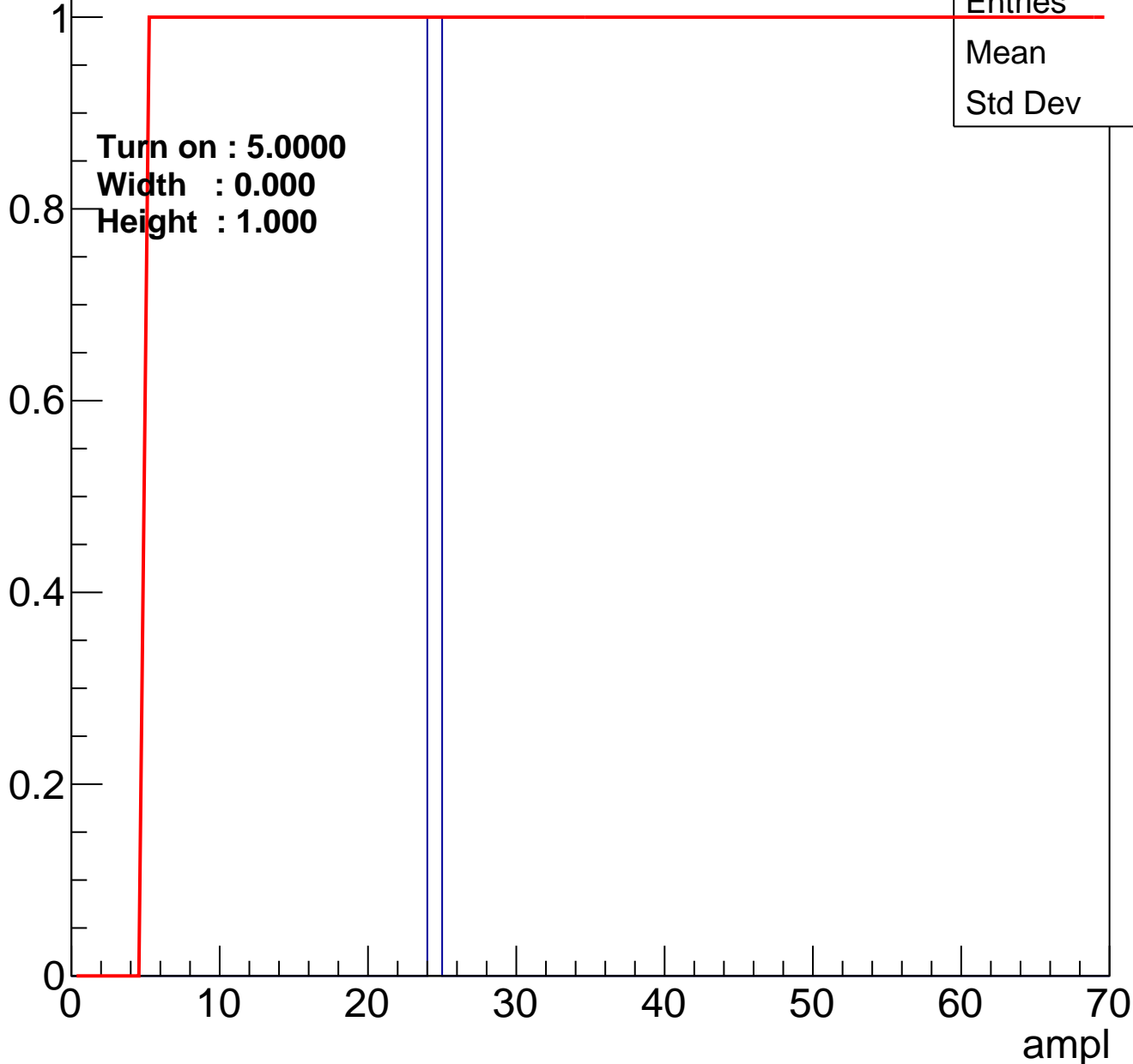
Entry



B0L101S, U13-ch102

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch103

calib_packv5_042523_0143.root, FC#1, port C1

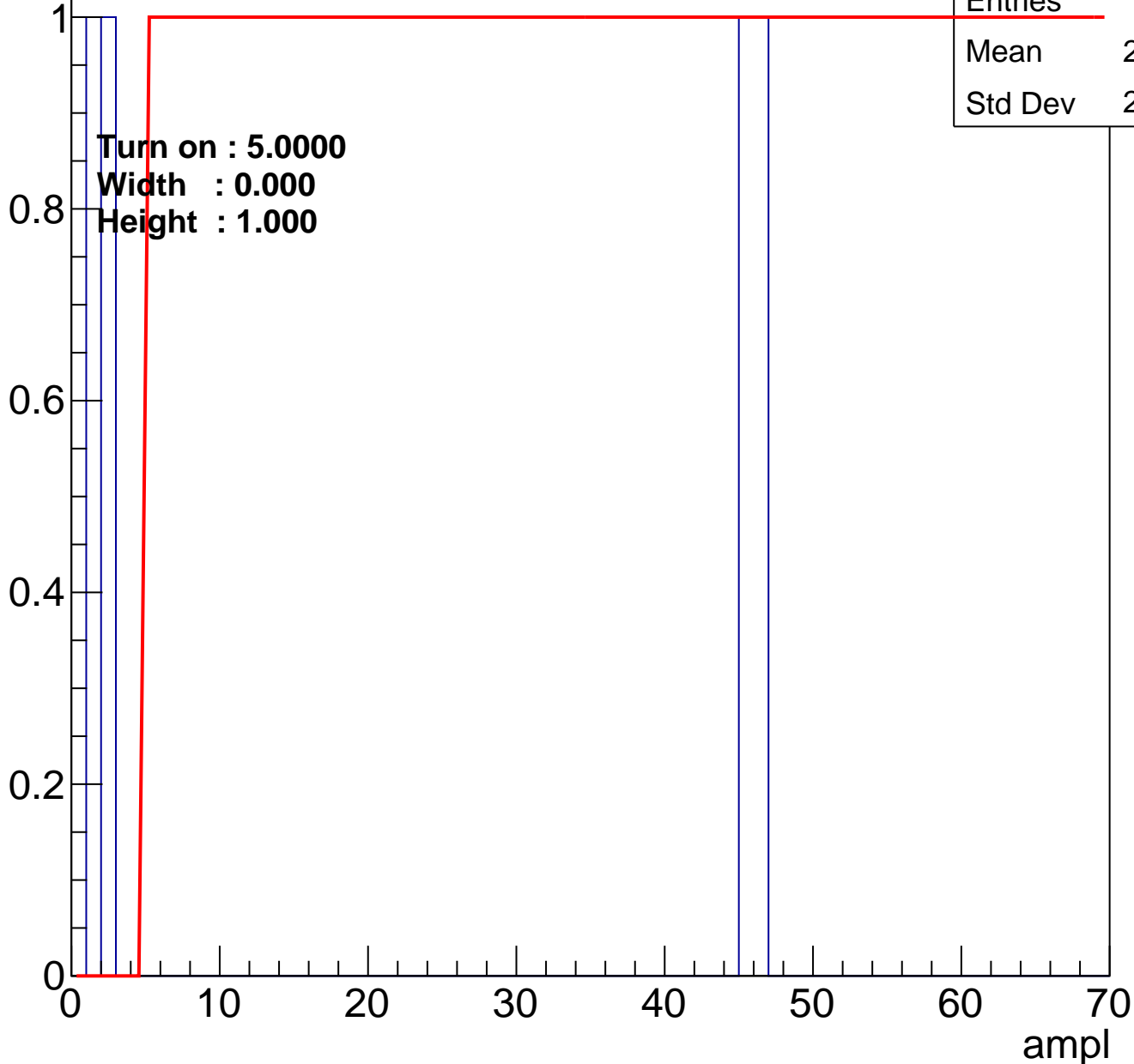
Entry



B0L101S, U13-ch104

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 23.25 |
| Std Dev | 22.26 |

B0L101S, U13-ch105

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch106

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch107

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch108

calib_packv5_042523_0143.root, FC#1, port C1

Entry

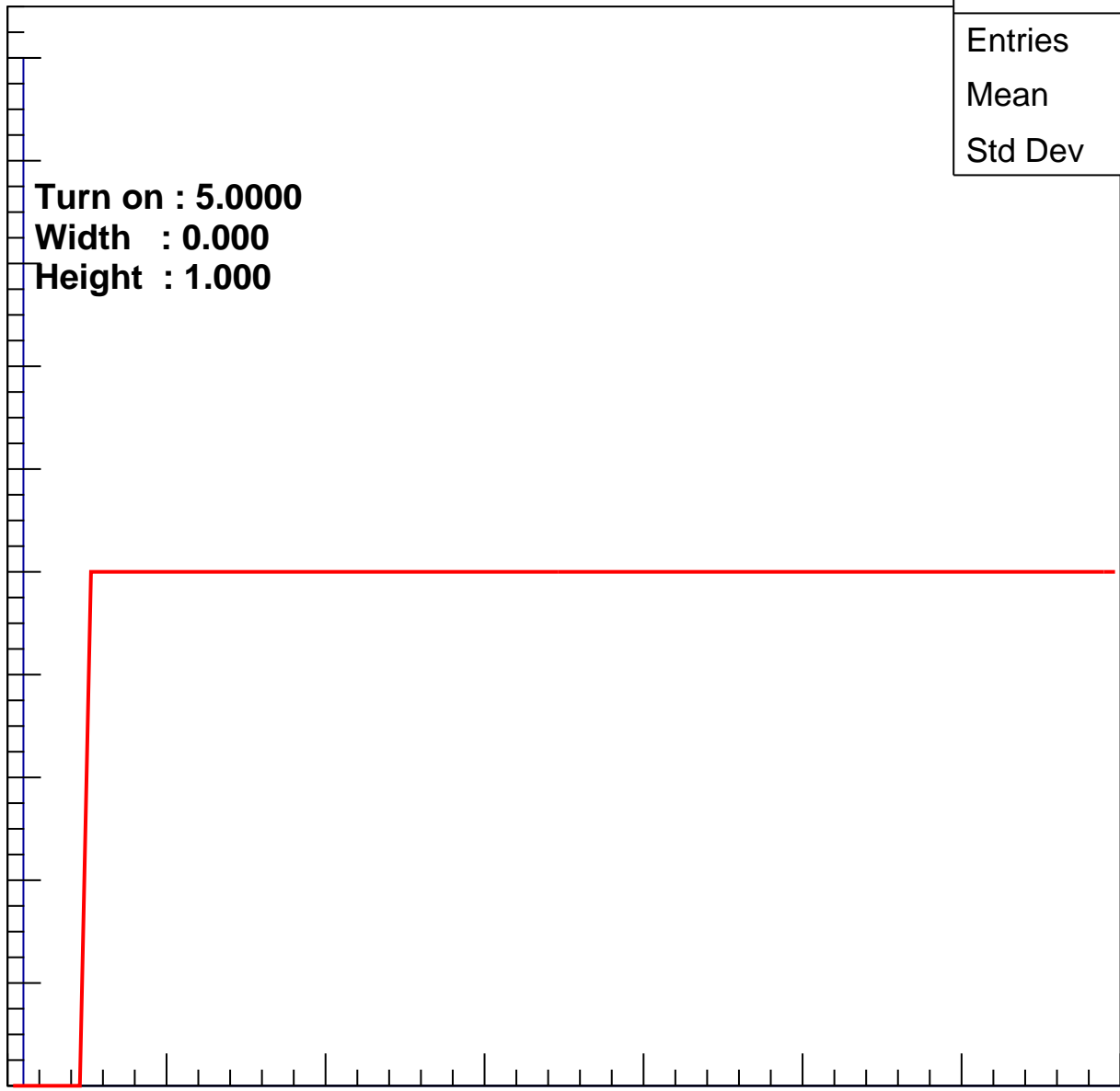
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

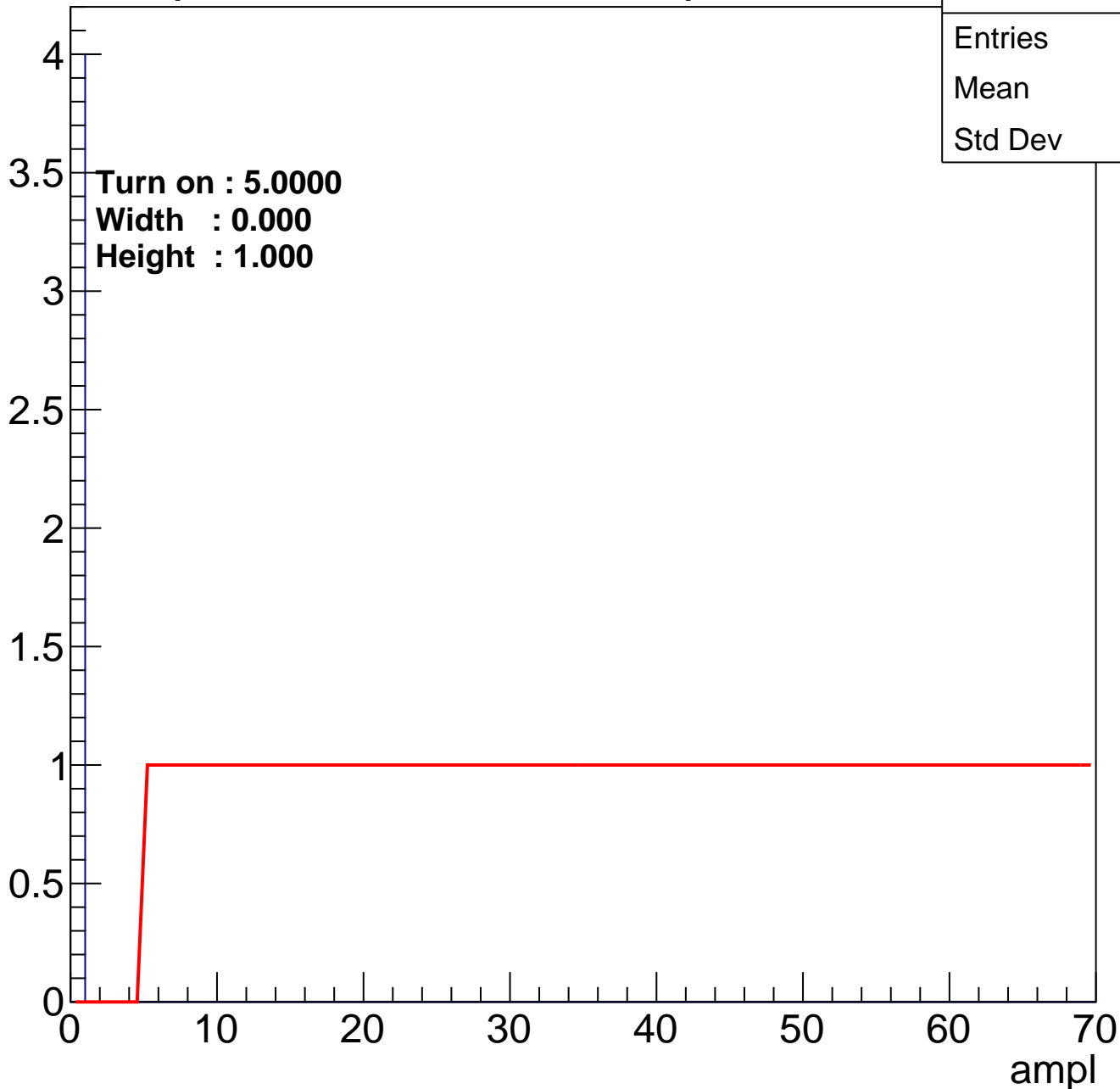
ampl



B0L101S, U13-ch109

calib_packv5_042523_0143.root, FC#1, port C1

Entry

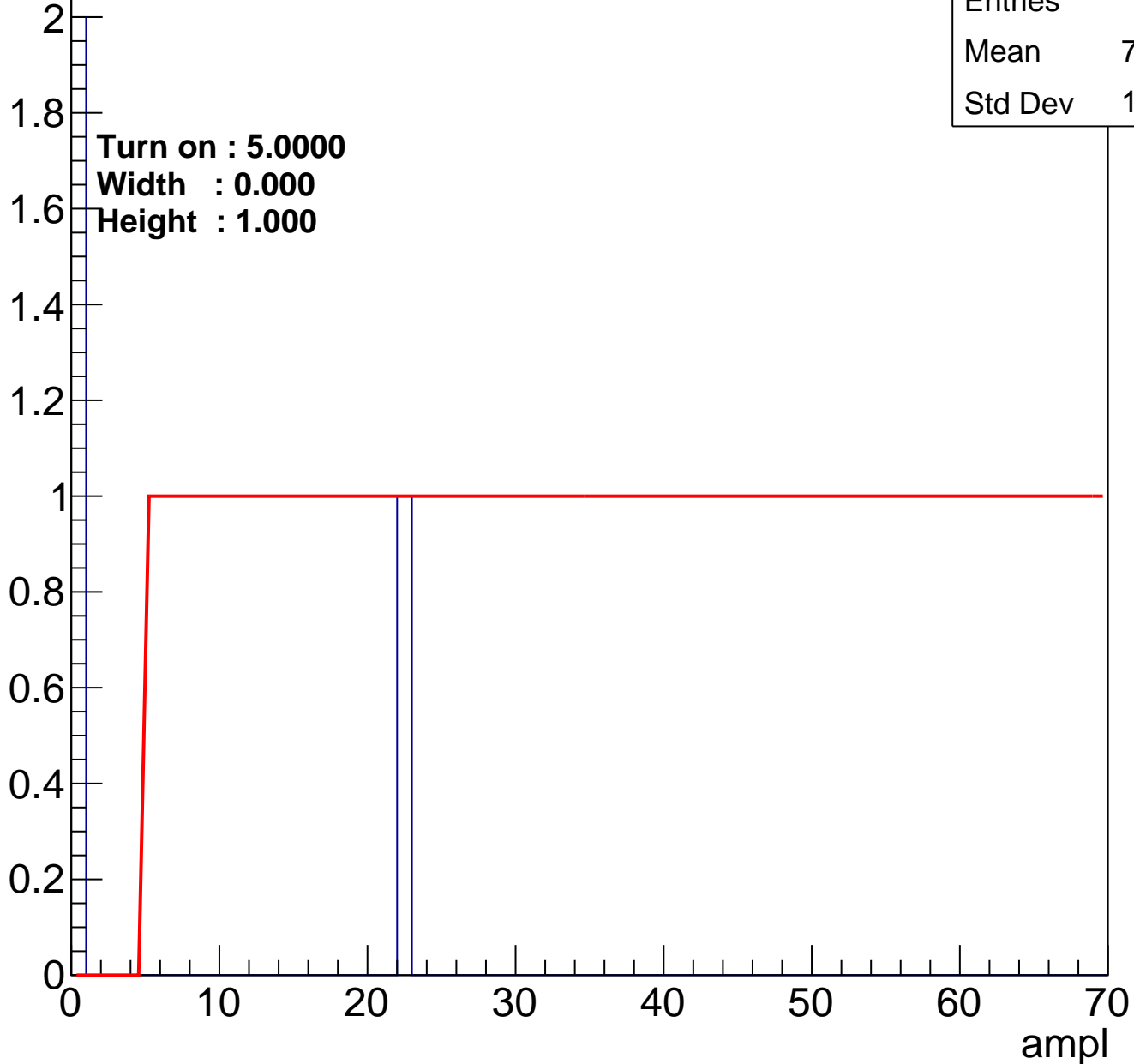


| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch111

calib_packv5_042523_0143.root, FC#1, port C1

Entry

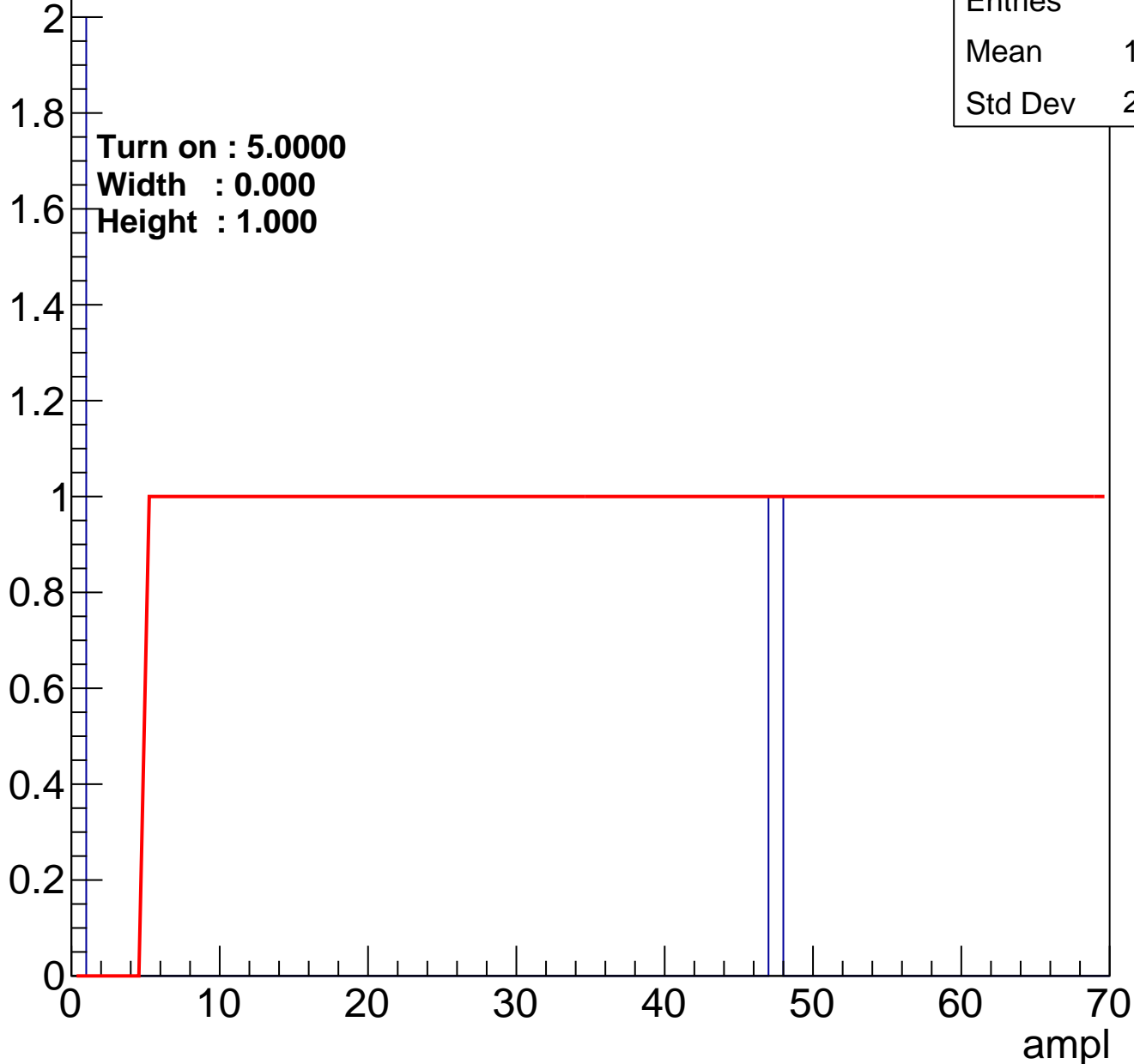


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch112

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch113

calib_packv5_042523_0143.root, FC#1, port C1

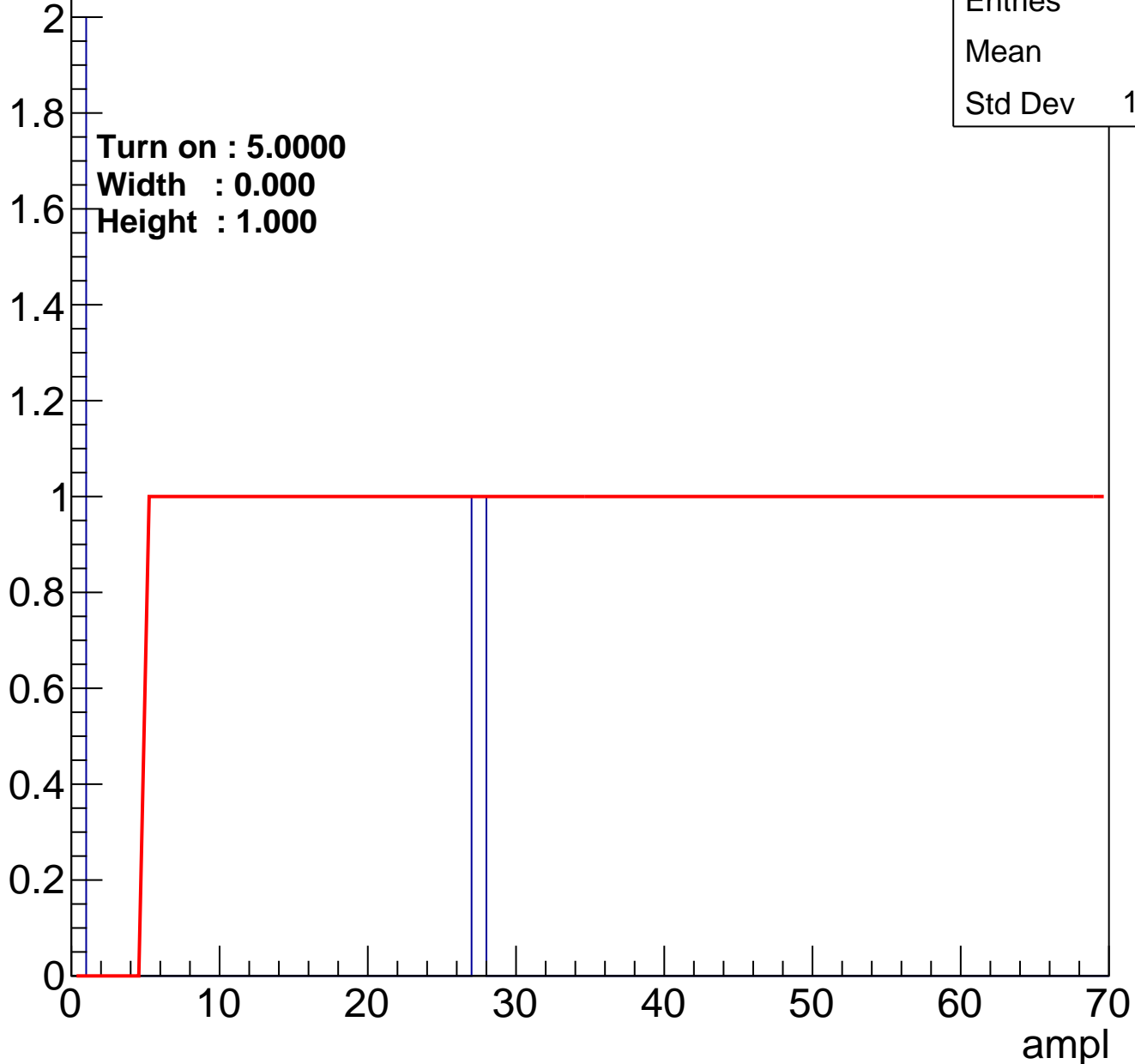
Entry



B0L101S, U13-ch114

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 9 |
| Std Dev | 12.73 |

B0L101S, U13-ch115

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch116

calib_packv5_042523_0143.root, FC#1, port C1

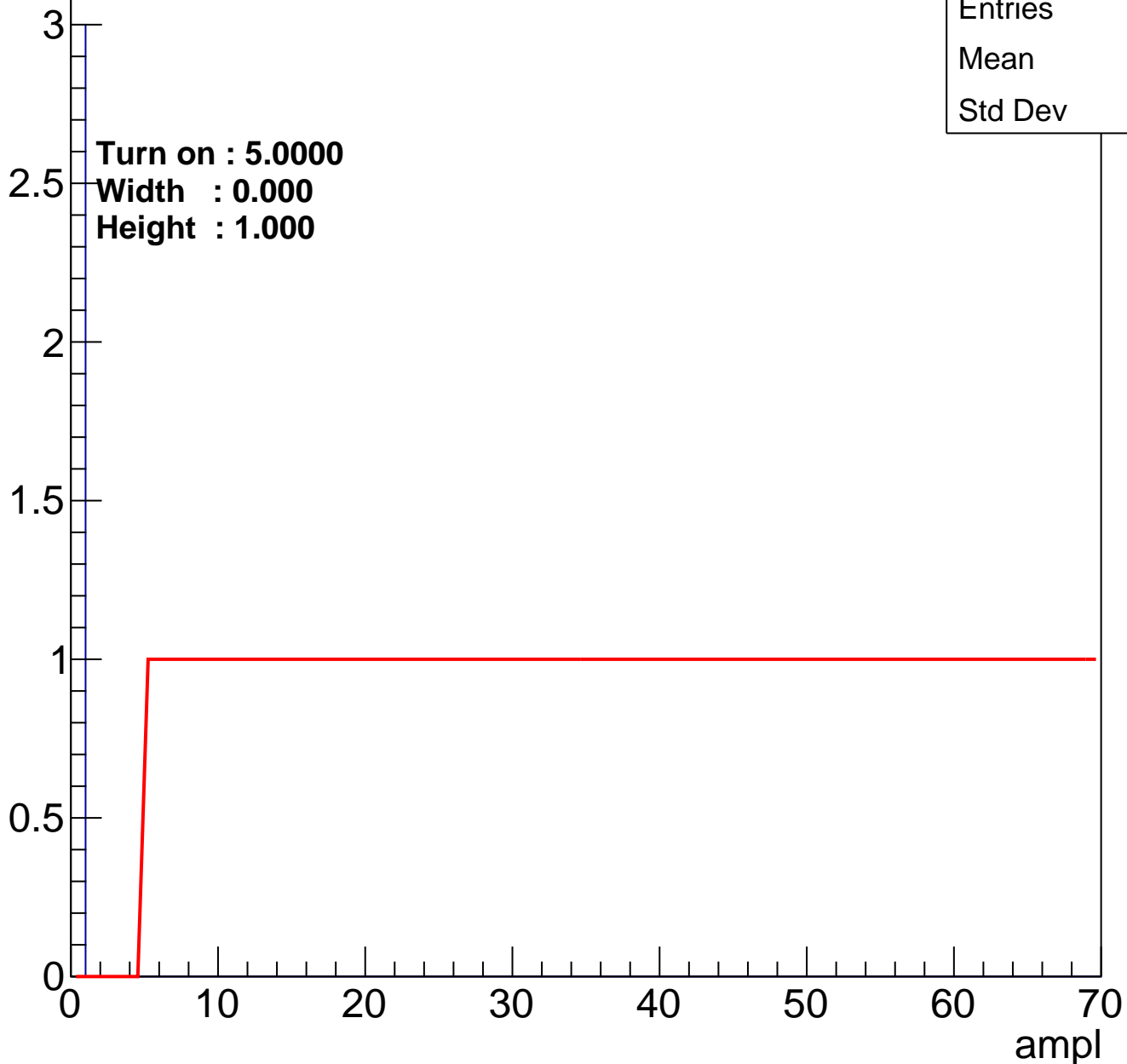
Entry



B0L101S, U13-ch117

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U13-ch118

calib_packv5_042523_0143.root, FC#1, port C1

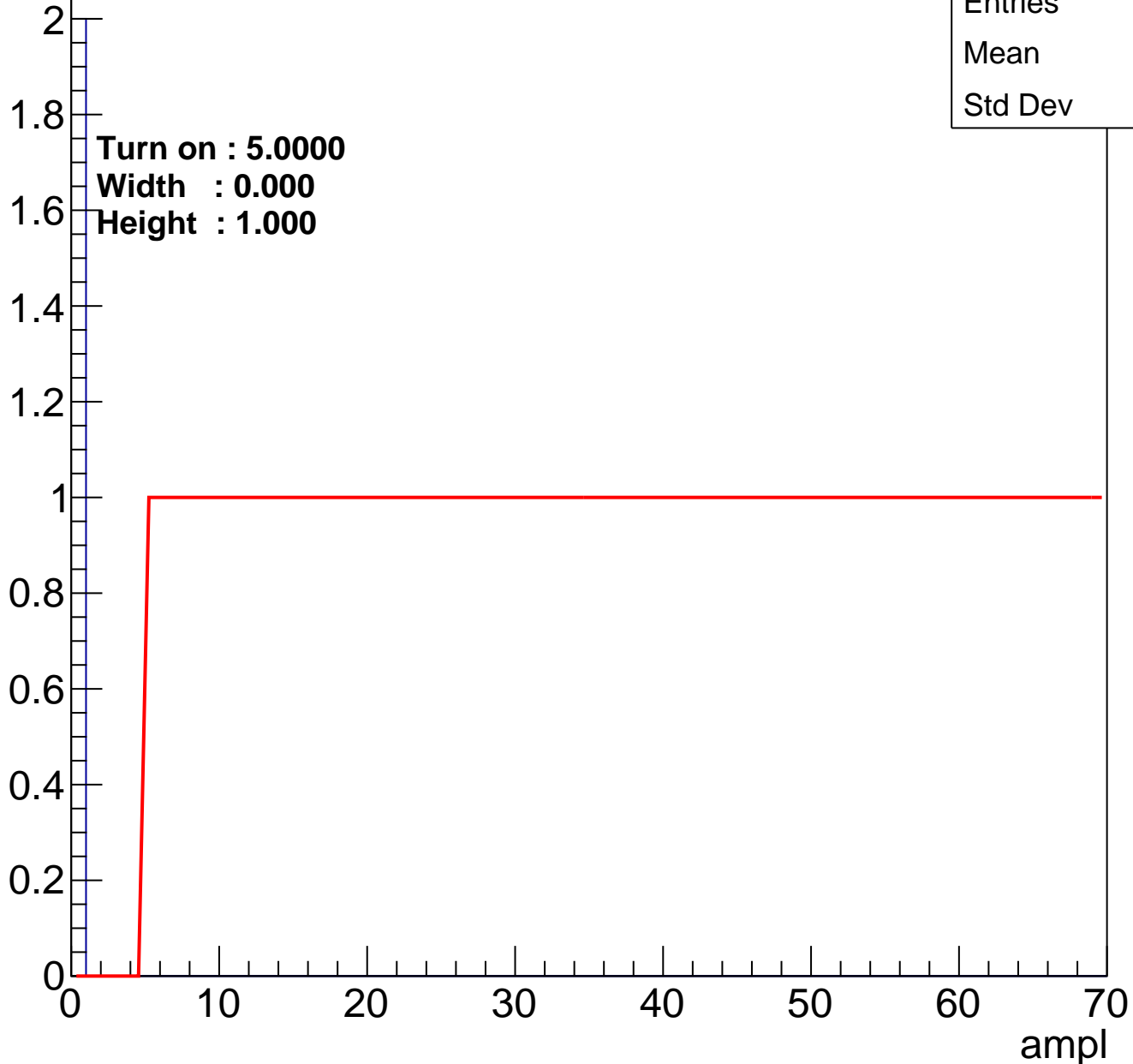
Entry



B0L101S, U13-ch119

calib_packv5_042523_0143.root, FC#1, port C1

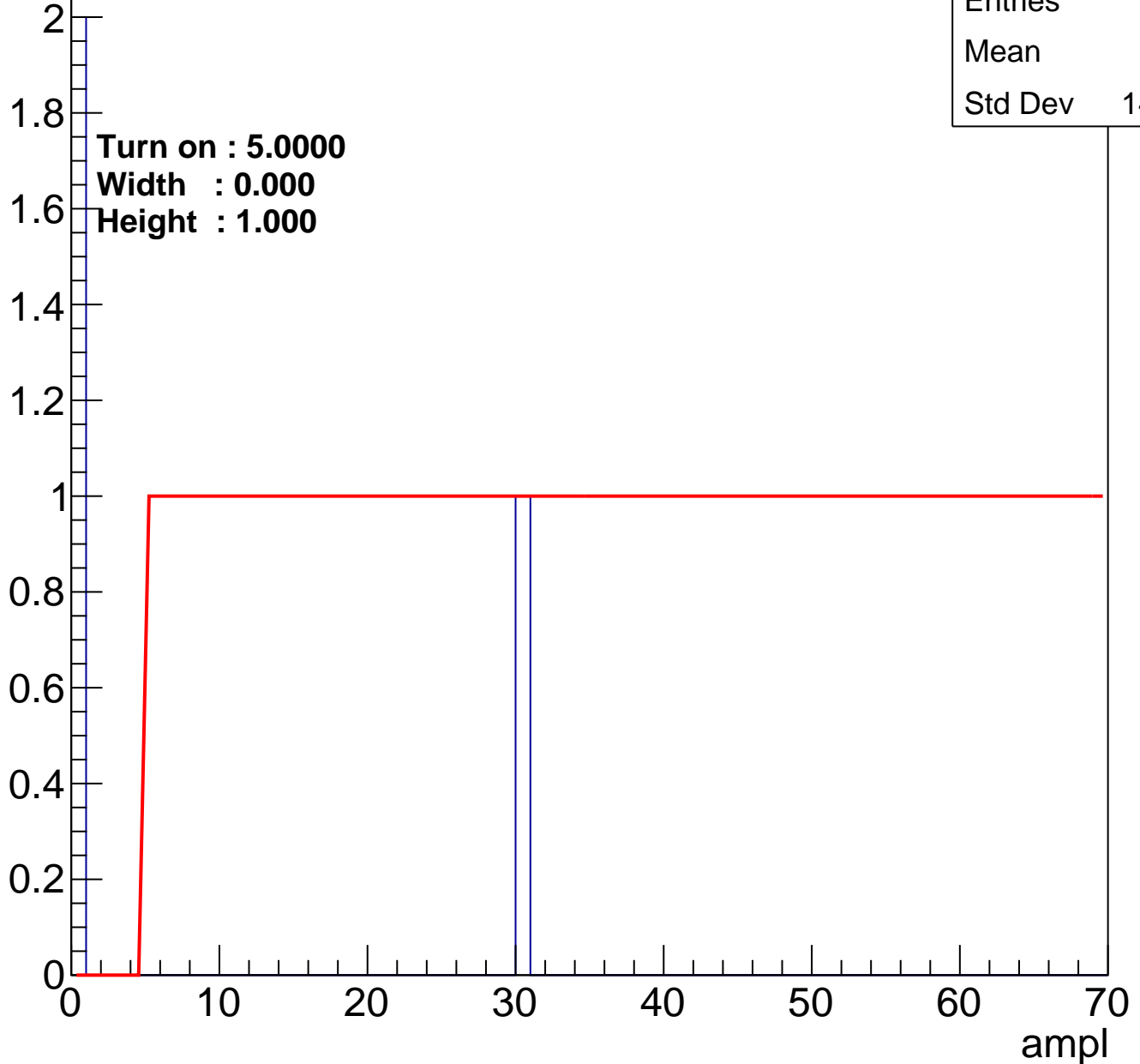
Entry



B0L101S, U13-ch120

calib_packv5_042523_0143.root, FC#1, port C1

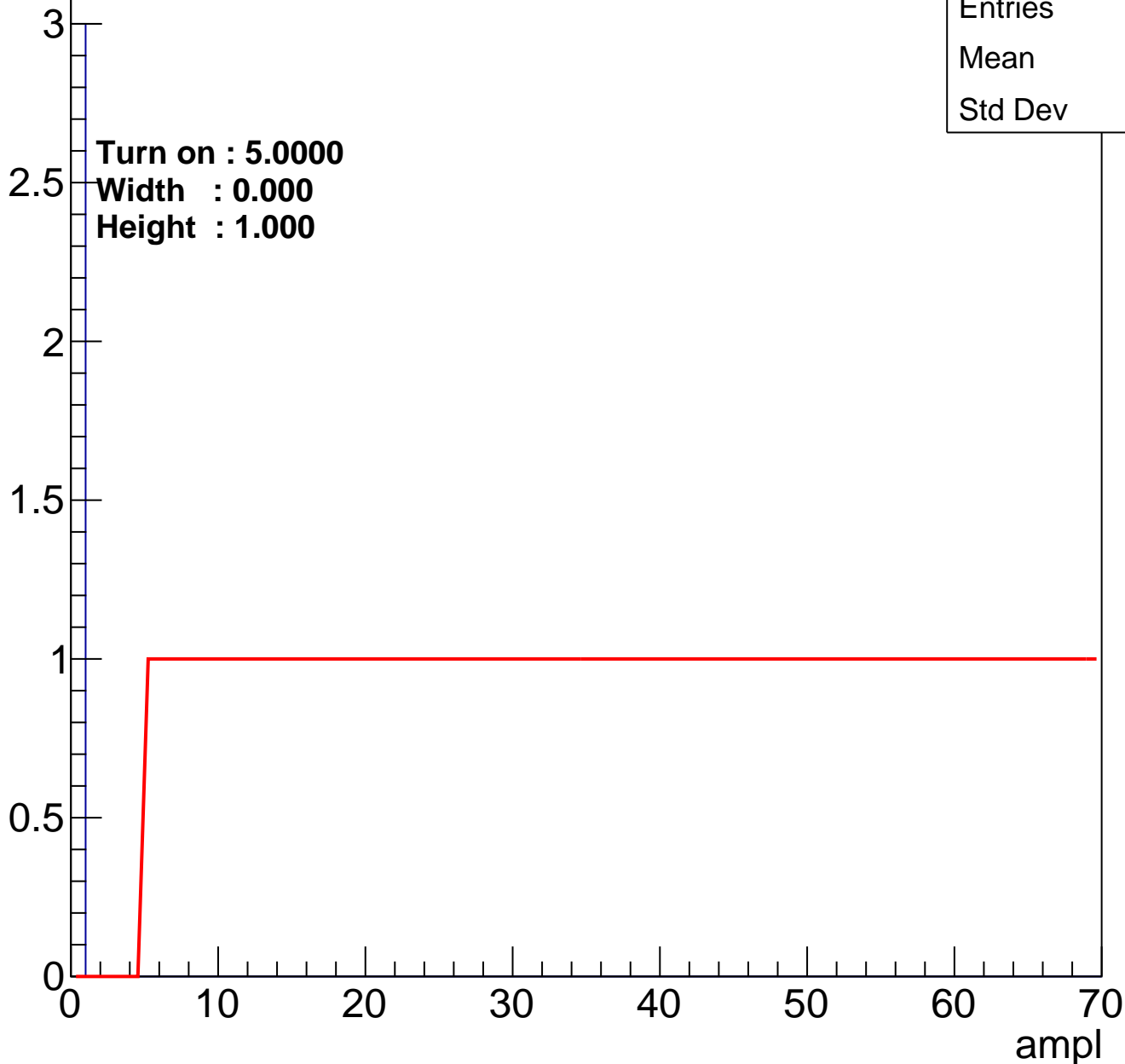
Entry



B0L101S, U13-ch121

calib_packv5_042523_0143.root, FC#1, port C1

Entry

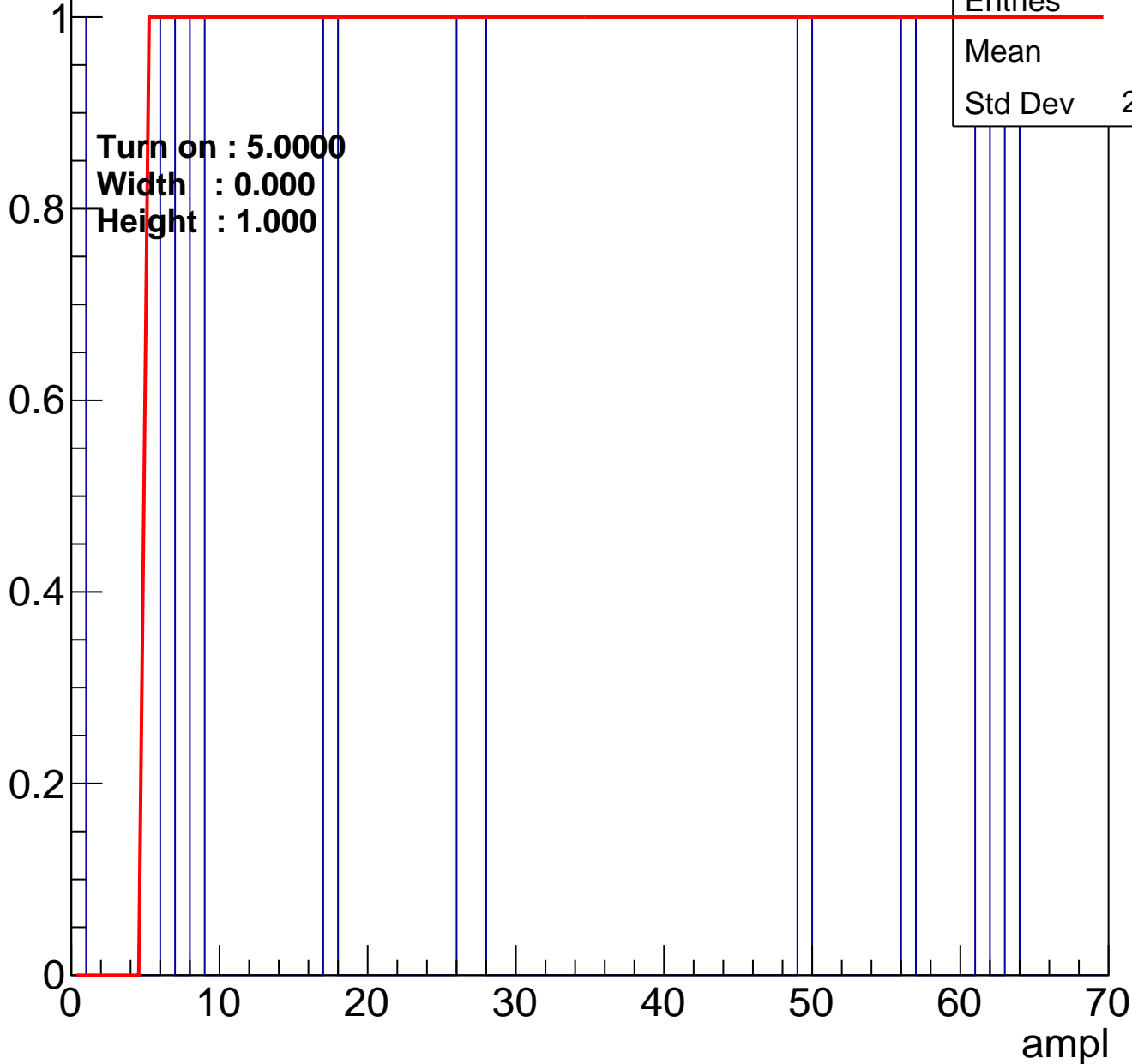


| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch122

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 10 |
| Mean | 31.3 |
| Std Dev | 22.86 |

B0L101S, U13-ch123

calib_packv5_042523_0143.root, FC#1, port C1

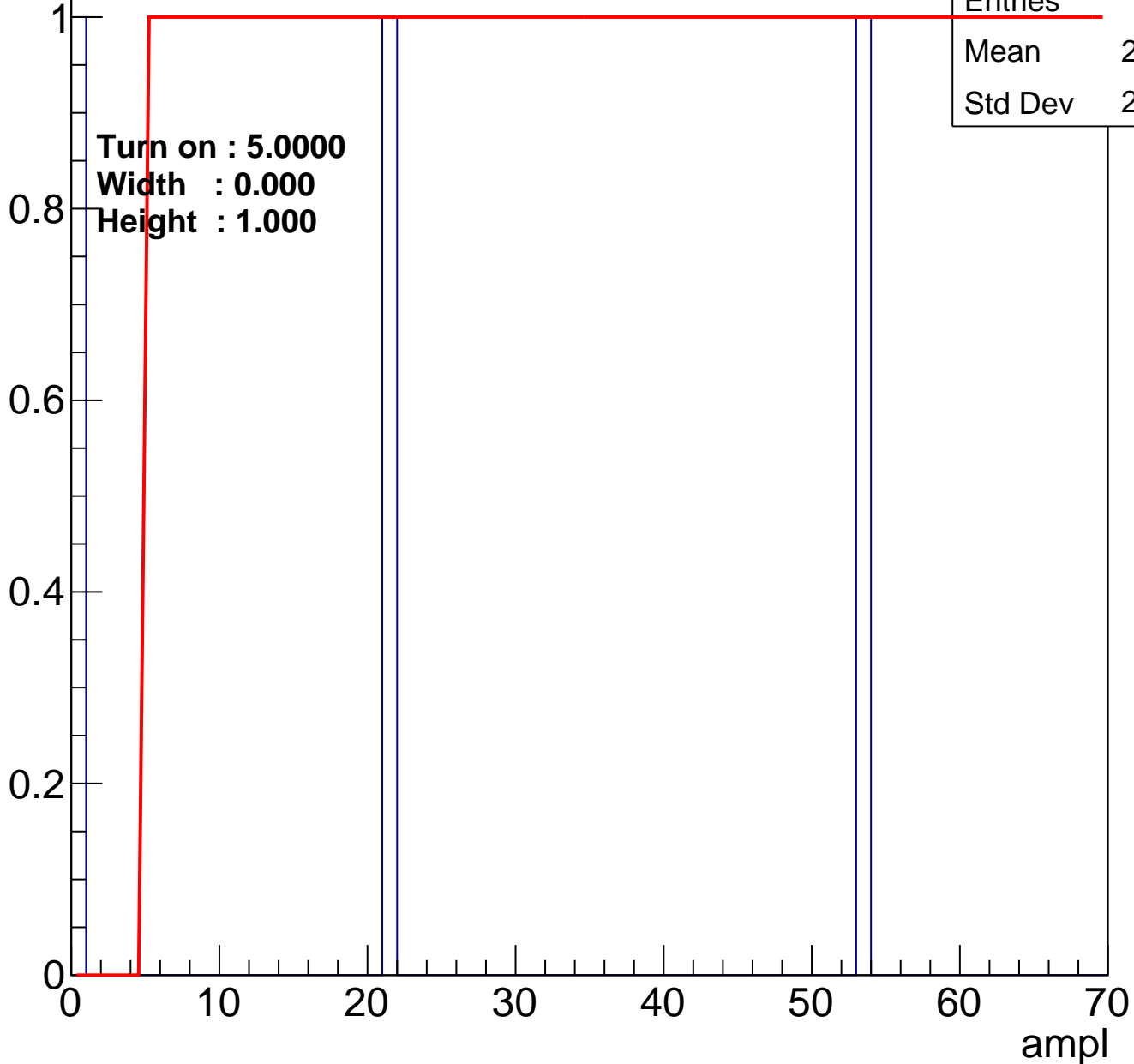
Entry



B0L101S, U13-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 24.67 |
| Std Dev | 21.79 |

B0L101S, U13-ch125

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L101S, U13-ch126

calib_packv5_042523_0143.root, FC#1, port C1

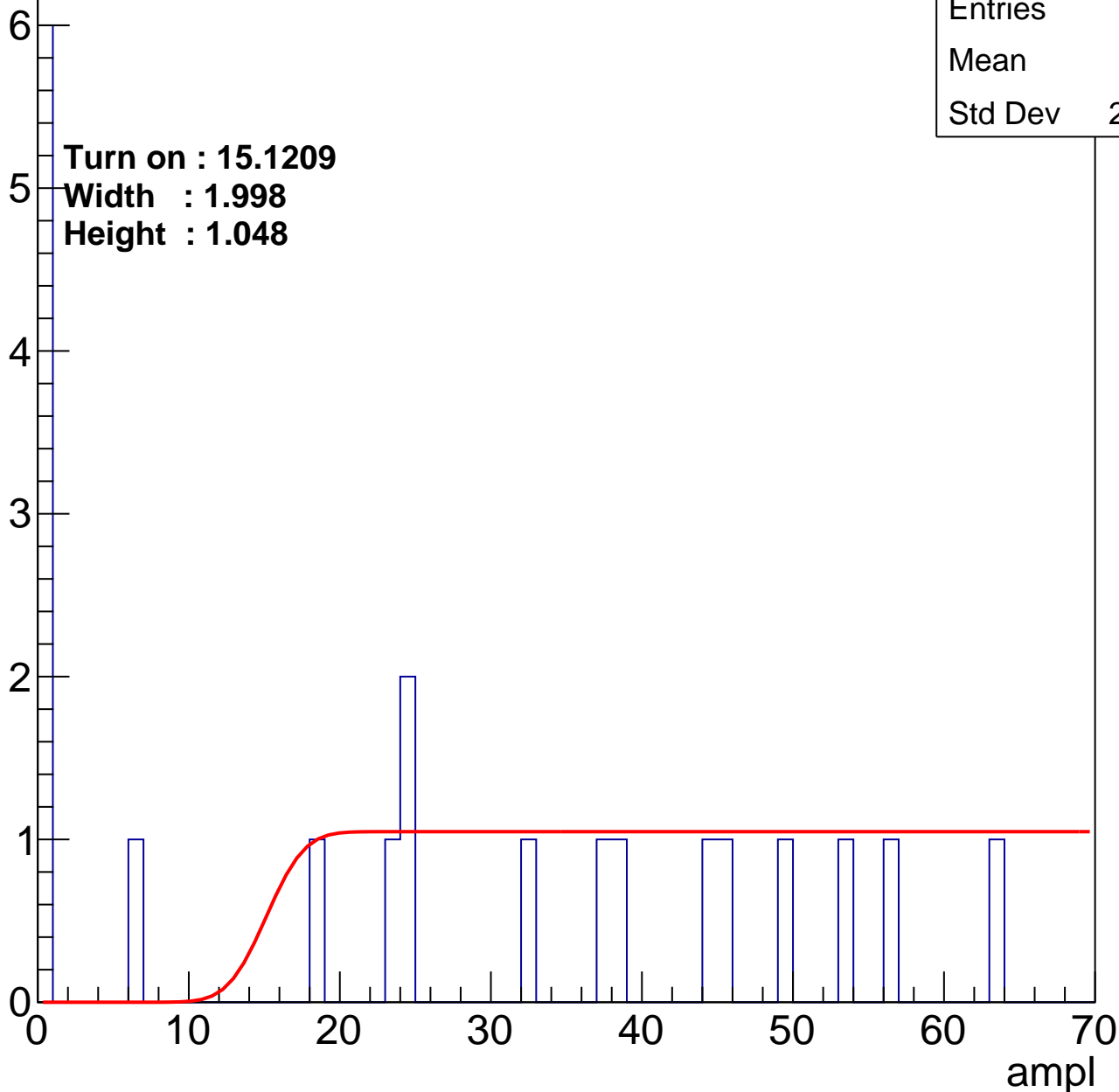
Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 25.6 |
| Std Dev | 21.27 |

Turn on : 15.1209

Width : 1.998

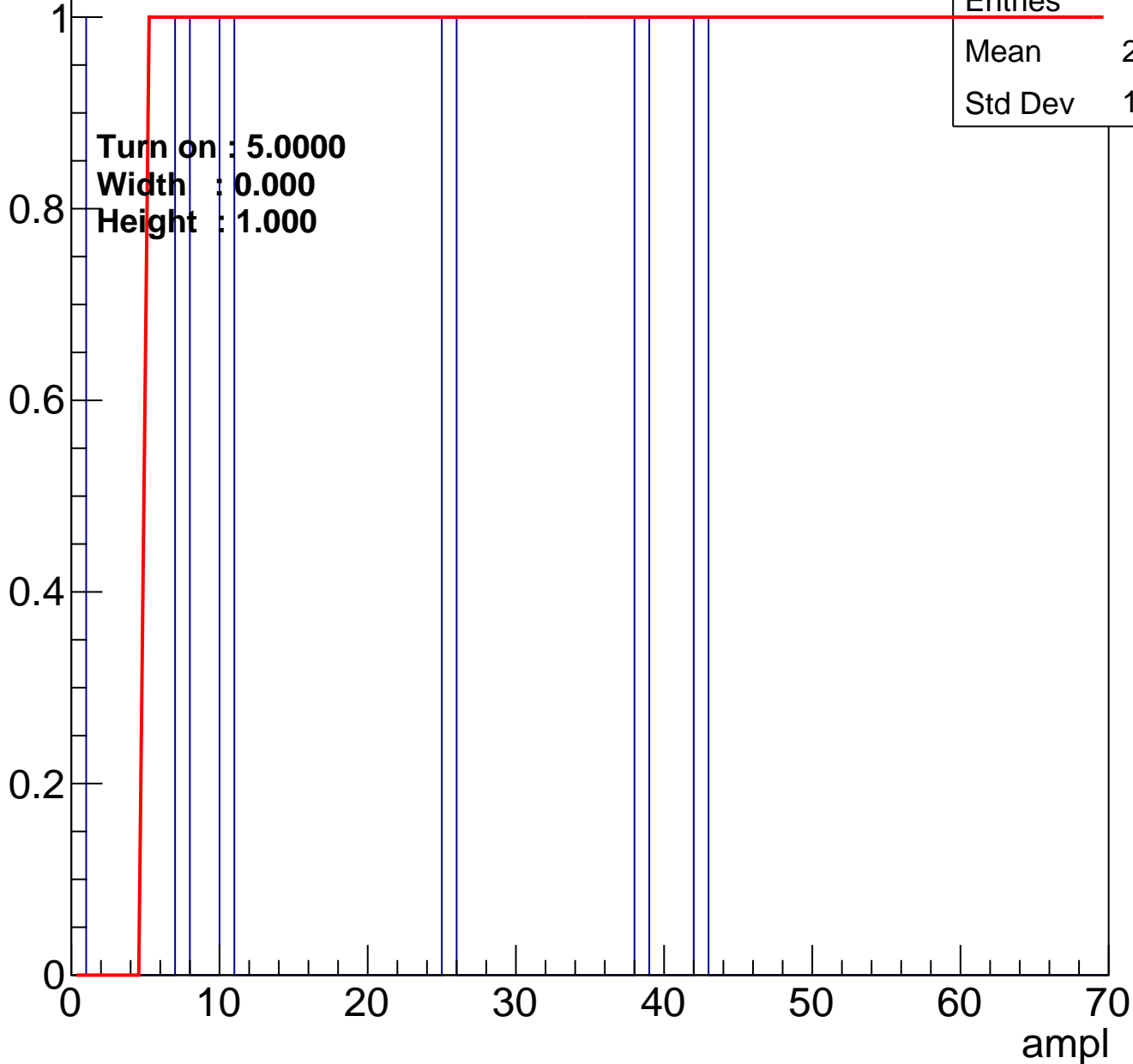
Height : 1.048



B0L101S, U13-ch127

calib_packv5_042523_0143.root, FC#1, port C1

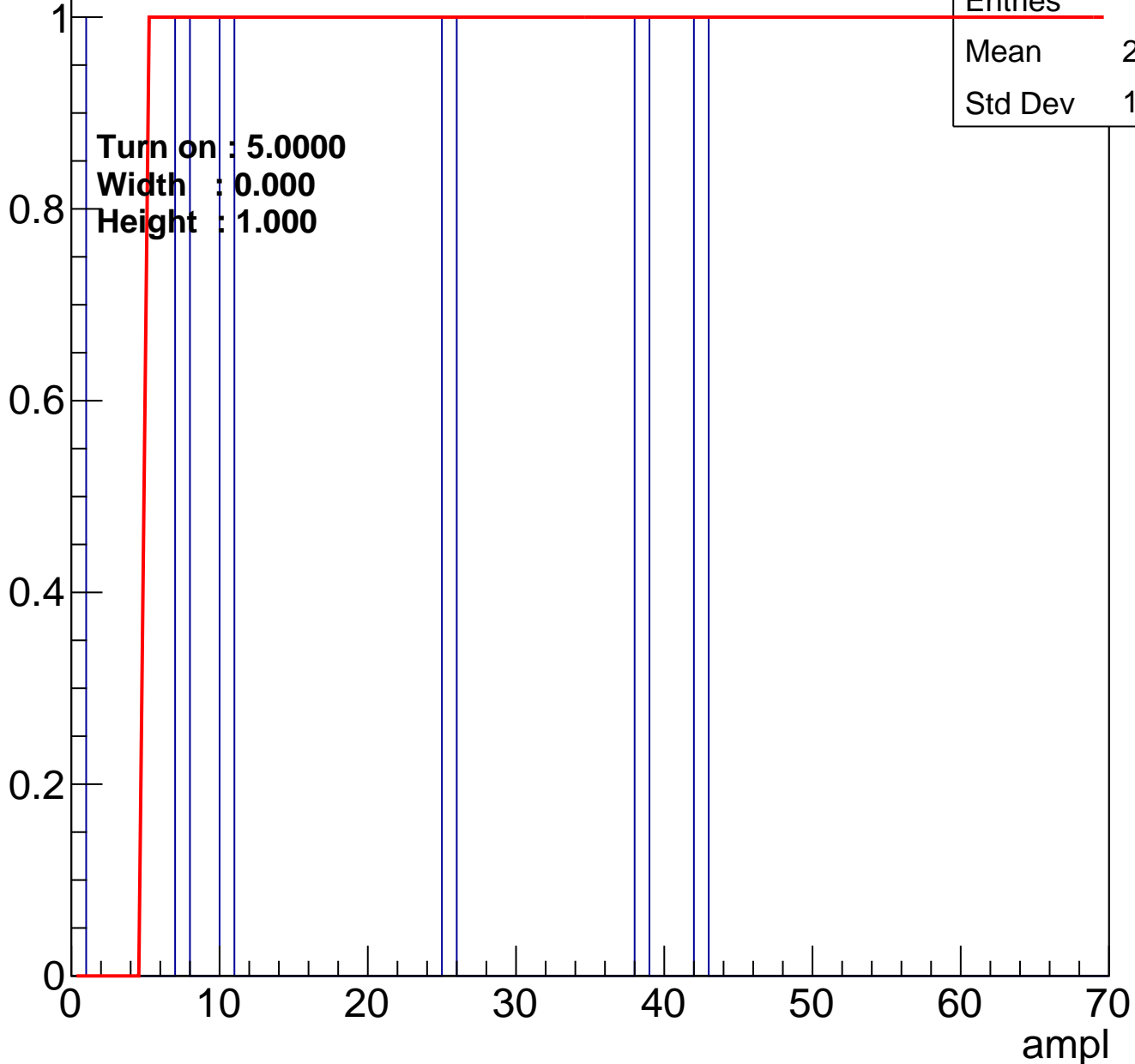
Entry



B0L101S, U13-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry



| | |
|---------|-------|
| Entries | 6 |
| Mean | 20.33 |
| Std Dev | 15.82 |