

B0L102S, U6-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	413
Mean	42.44
Std Dev	12.79

Turn on : 23.2427

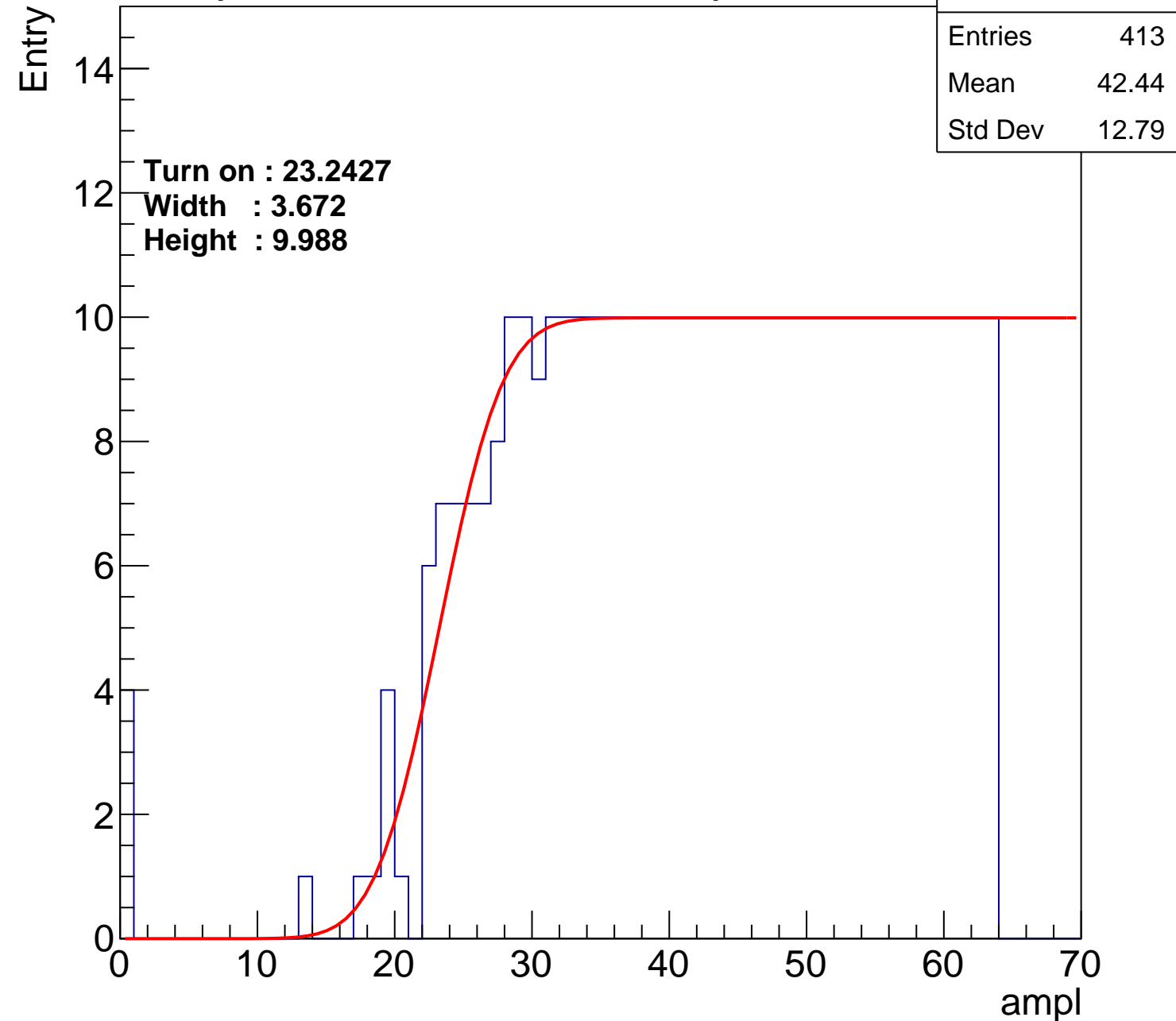
Width : 3.672

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.8386

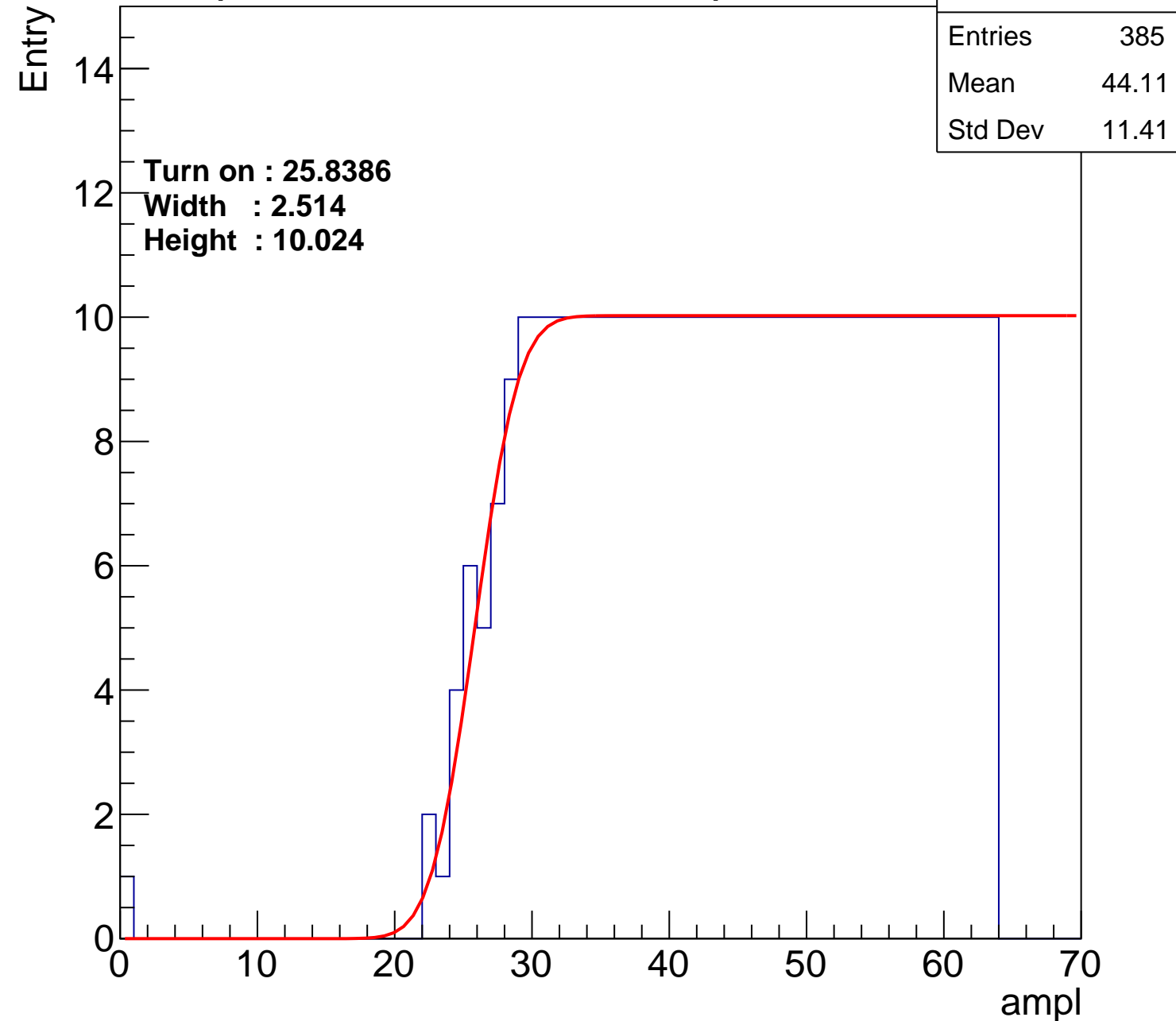
Width : 2.514

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch2

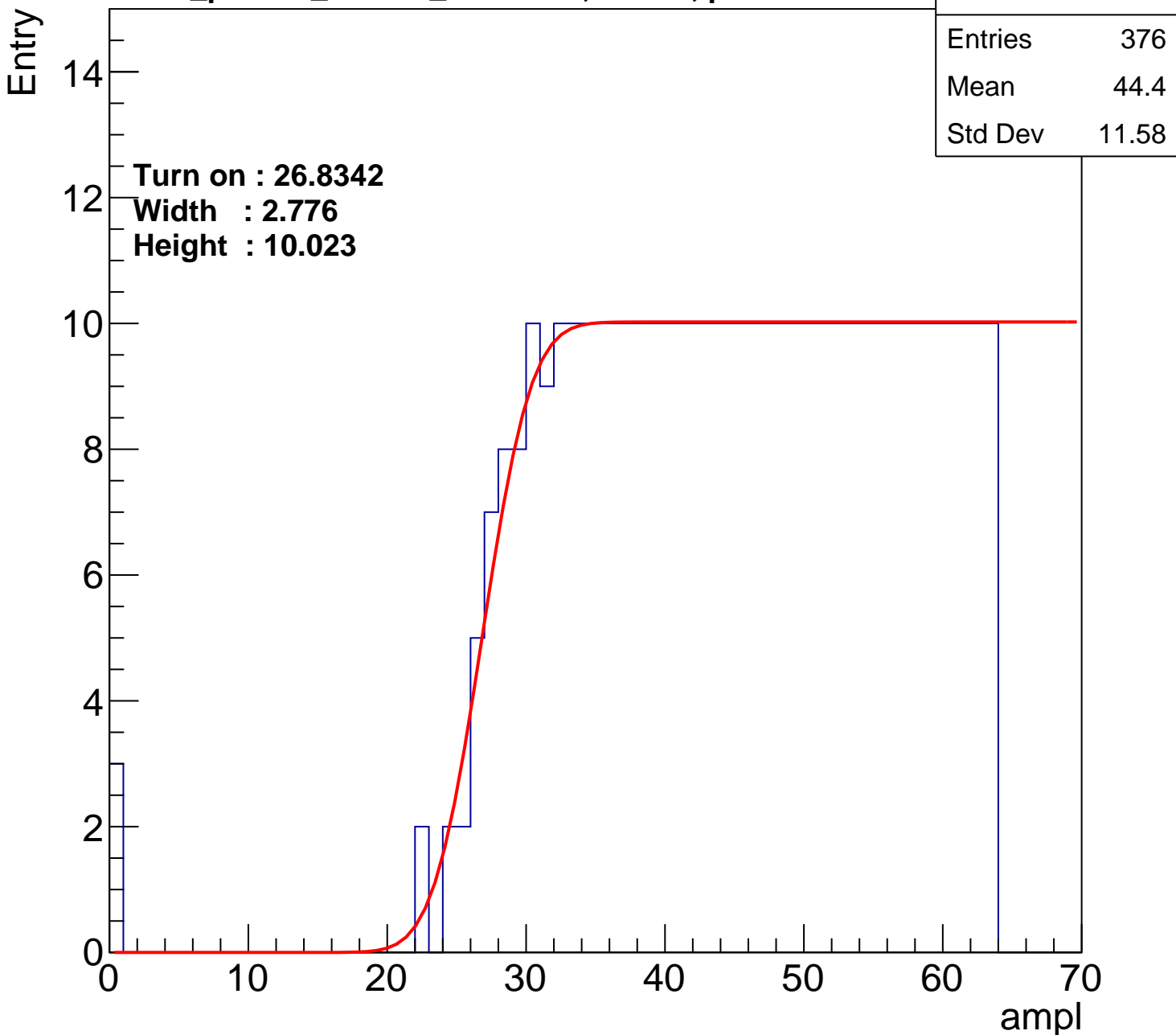
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.8342

Width : 2.776

Height : 10.023

Entries	376
Mean	44.4
Std Dev	11.58



B0L102S, U6-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44
Std Dev	12.03

Turn on : 26.6049

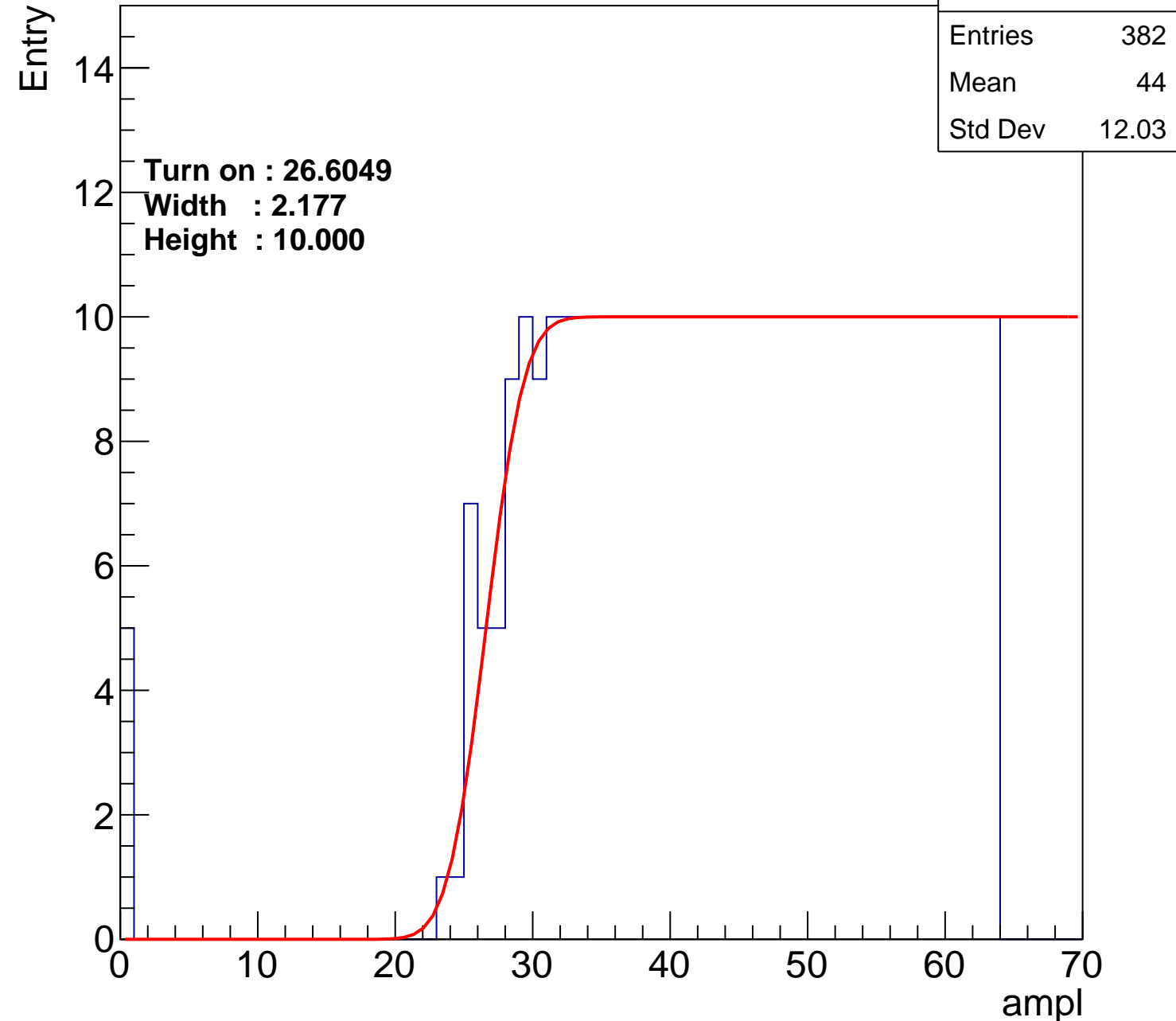
Width : 2.177

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch4

calib_packv5_042523_0143.root, FC#12, port B1

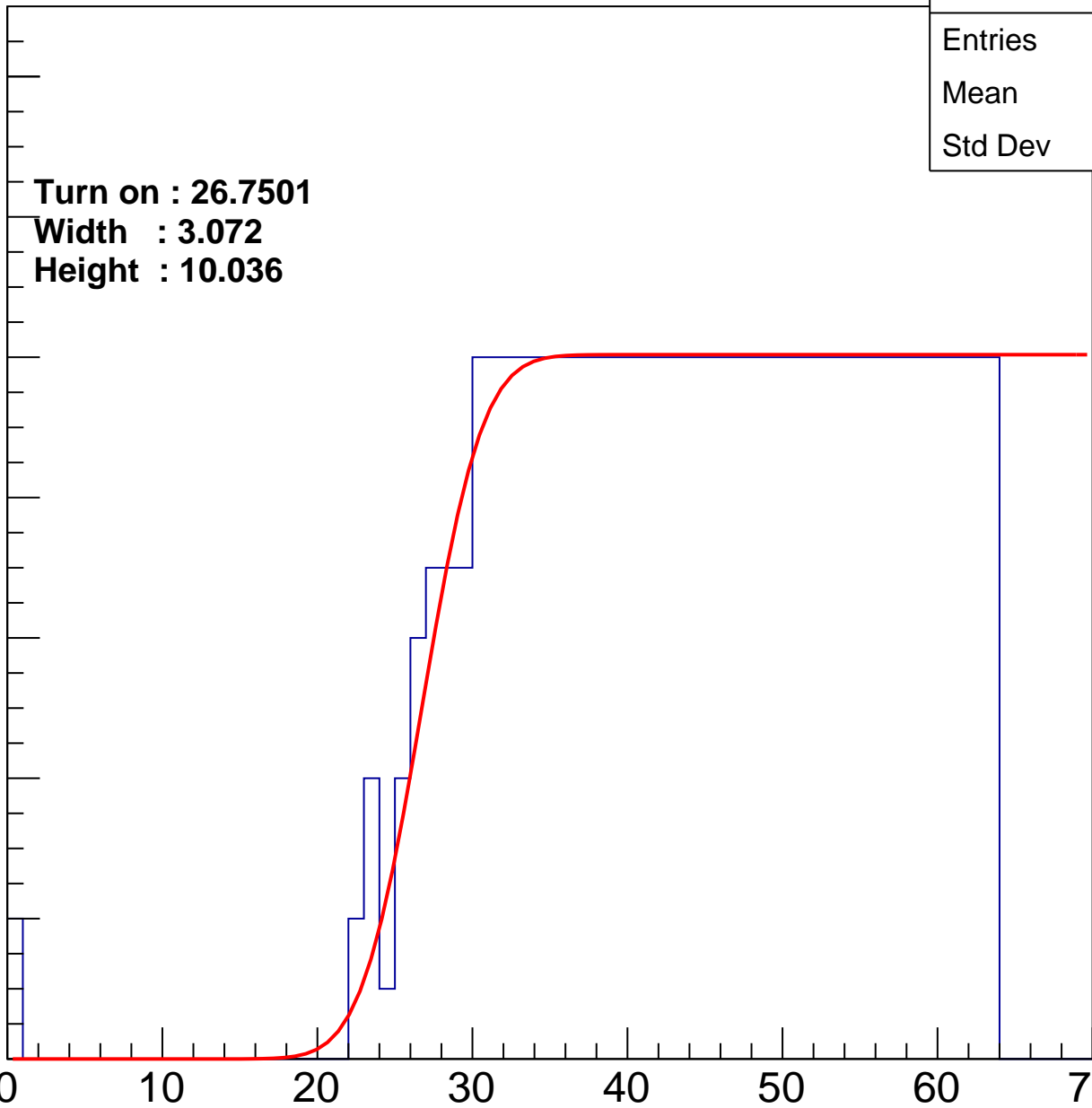
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7501
Width : 3.072
Height : 10.036

Entries	380
Mean	44.25
Std Dev	11.54

ampl



B0L102S, U6-ch5

calib_packv5_042523_0143.root, FC#12, port B1

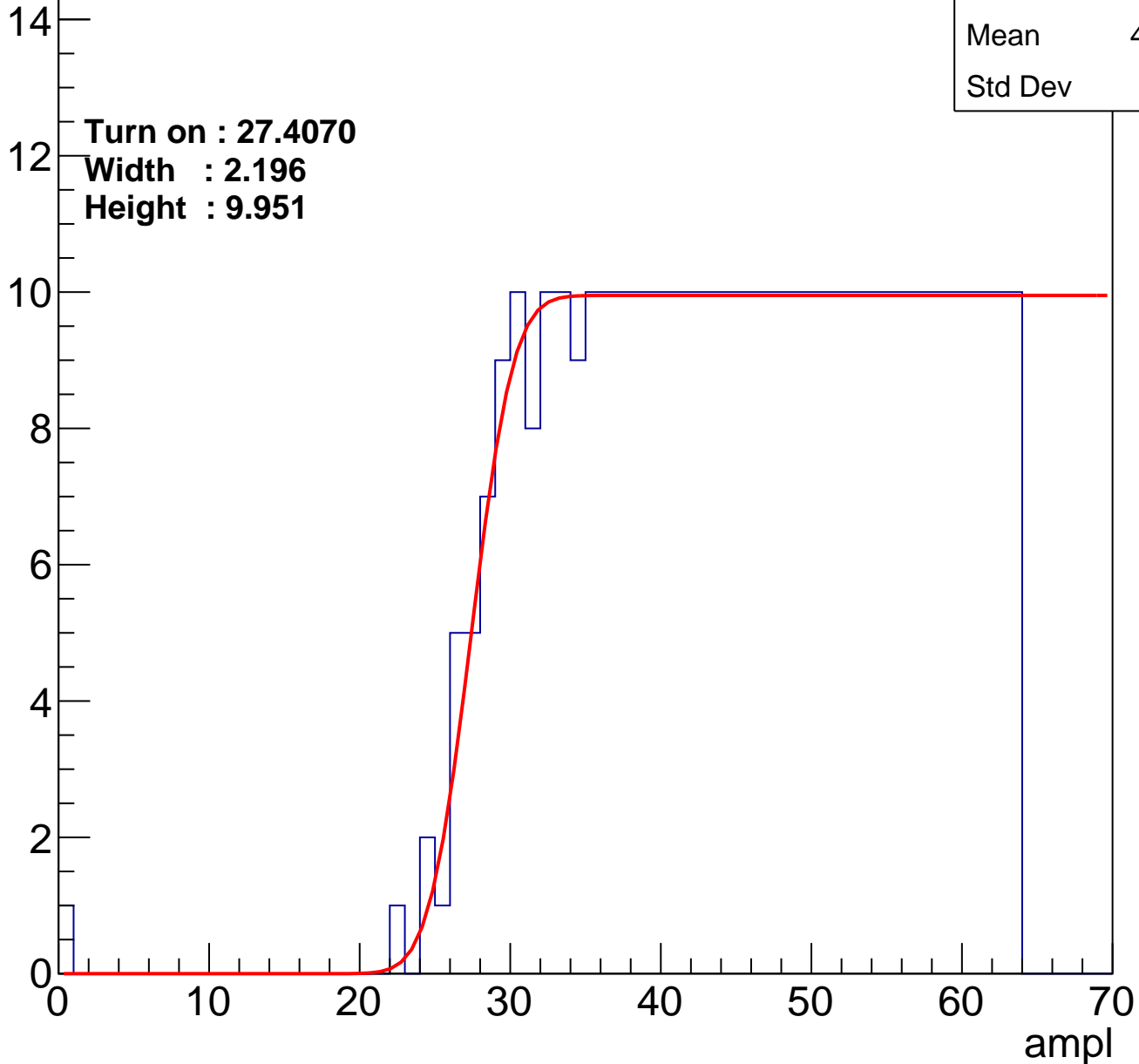
Entries	368
Mean	44.92
Std Dev	11

Turn on : 27.4070

Width : 2.196

Height : 9.951

Entry



B0L102S, U6-ch6

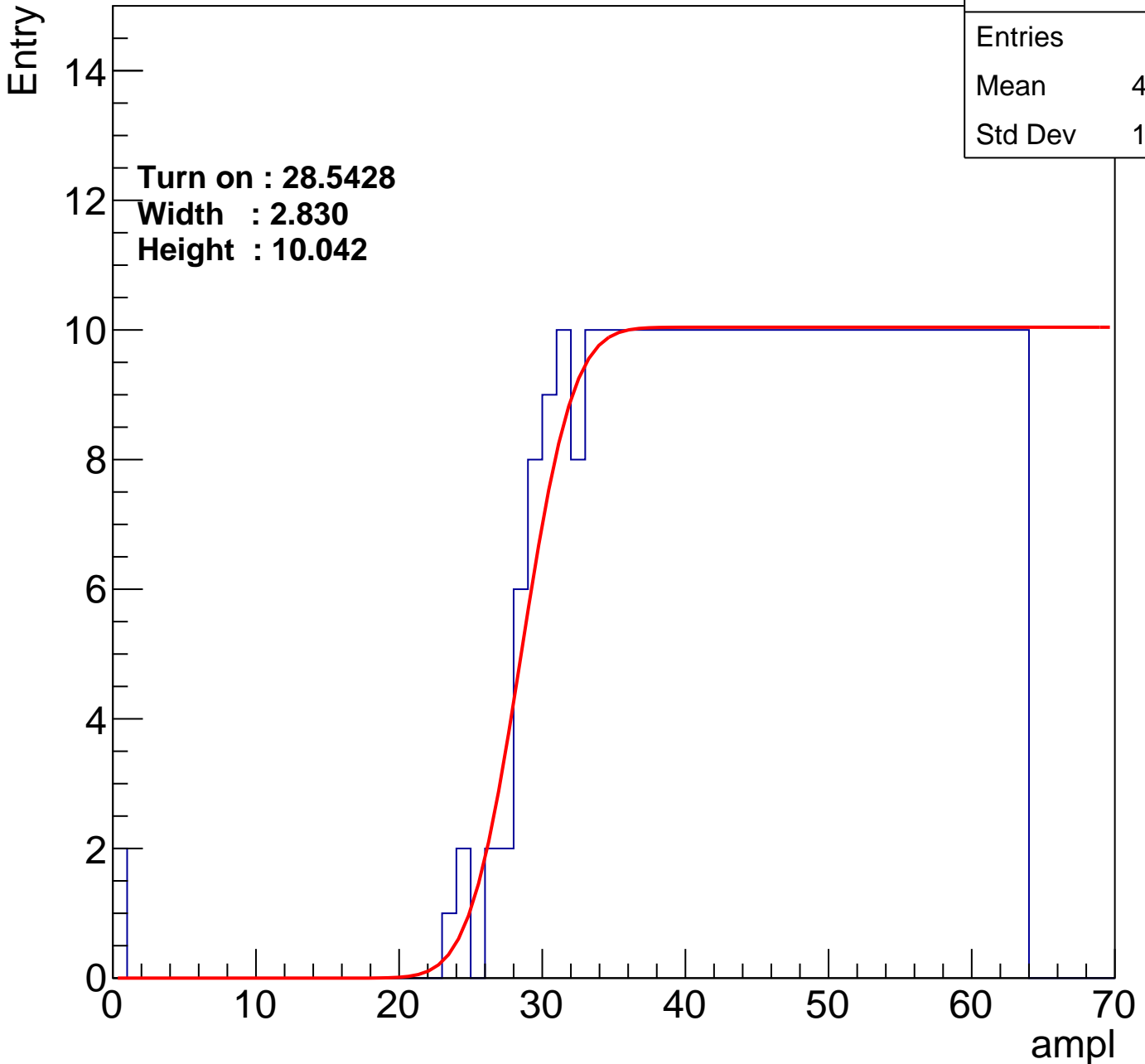
calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.26
Std Dev	10.99

Turn on : 28.5428

Width : 2.830

Height : 10.042



B0L102S, U6-ch7

calib_packv5_042523_0143.root, FC#12, port B1

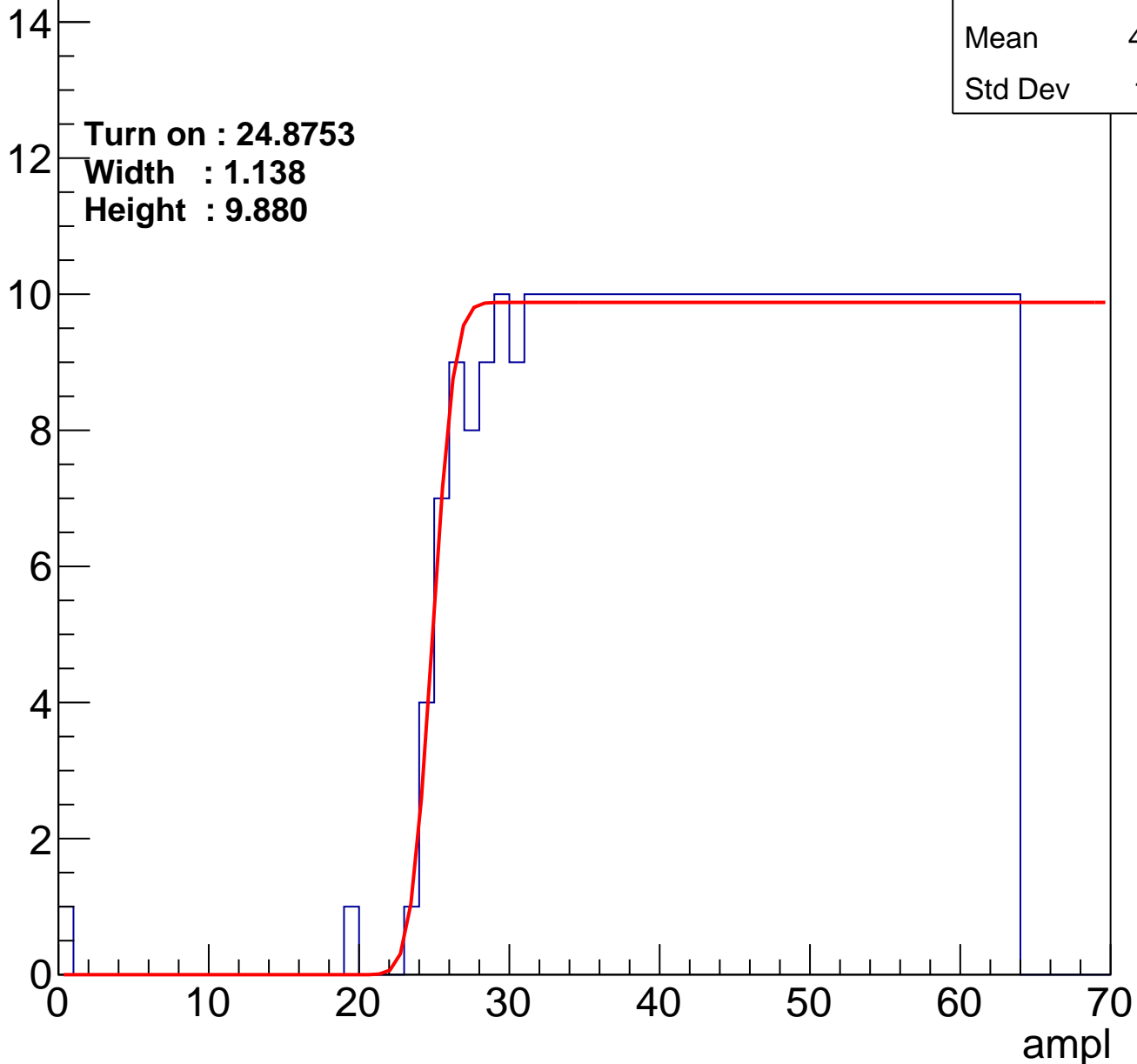
Entry

Entries	389
Mean	43.92
Std Dev	11.51

Turn on : 24.8753

Width : 1.138

Height : 9.880



B0L102S, U6-ch8

calib_packv5_042523_0143.root, FC#12, port B1

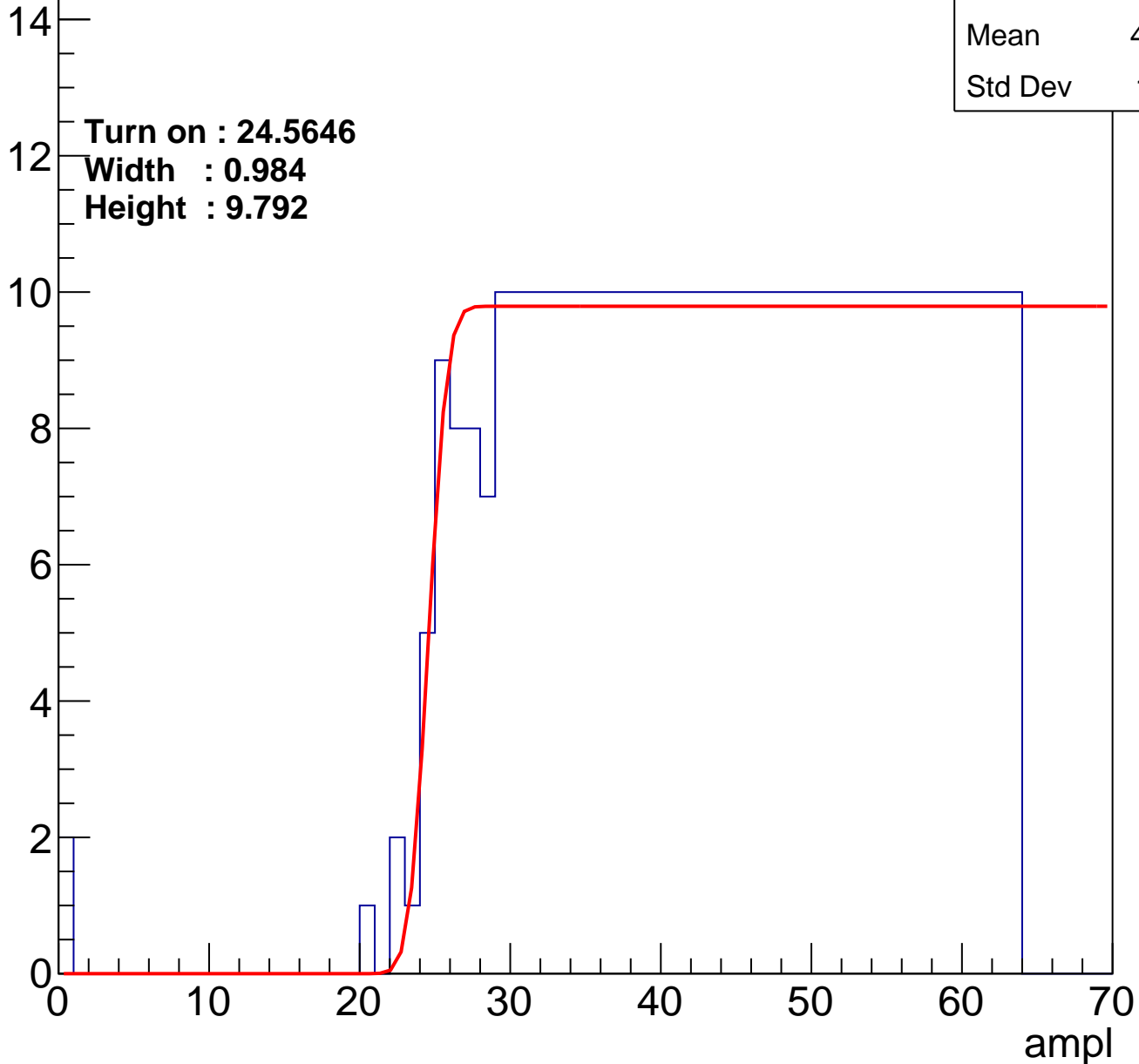
Entries	393
Mean	43.64
Std Dev	11.81

Turn on : 24.5646

Width : 0.984

Height : 9.792

Entry



B0L102S, U6-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.4386

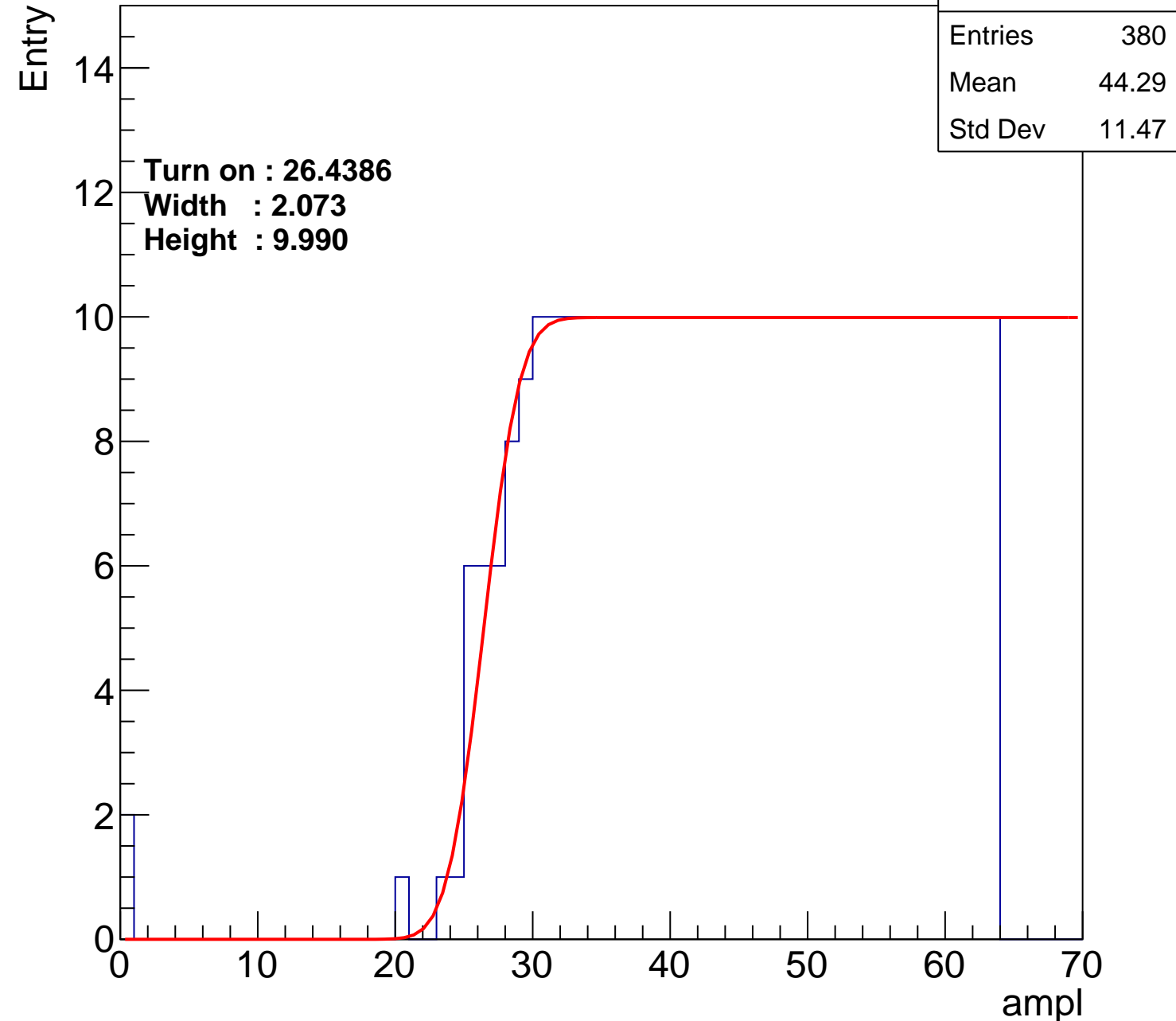
Width : 2.073

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.73
Std Dev	11.77

Turn on : 24.6535

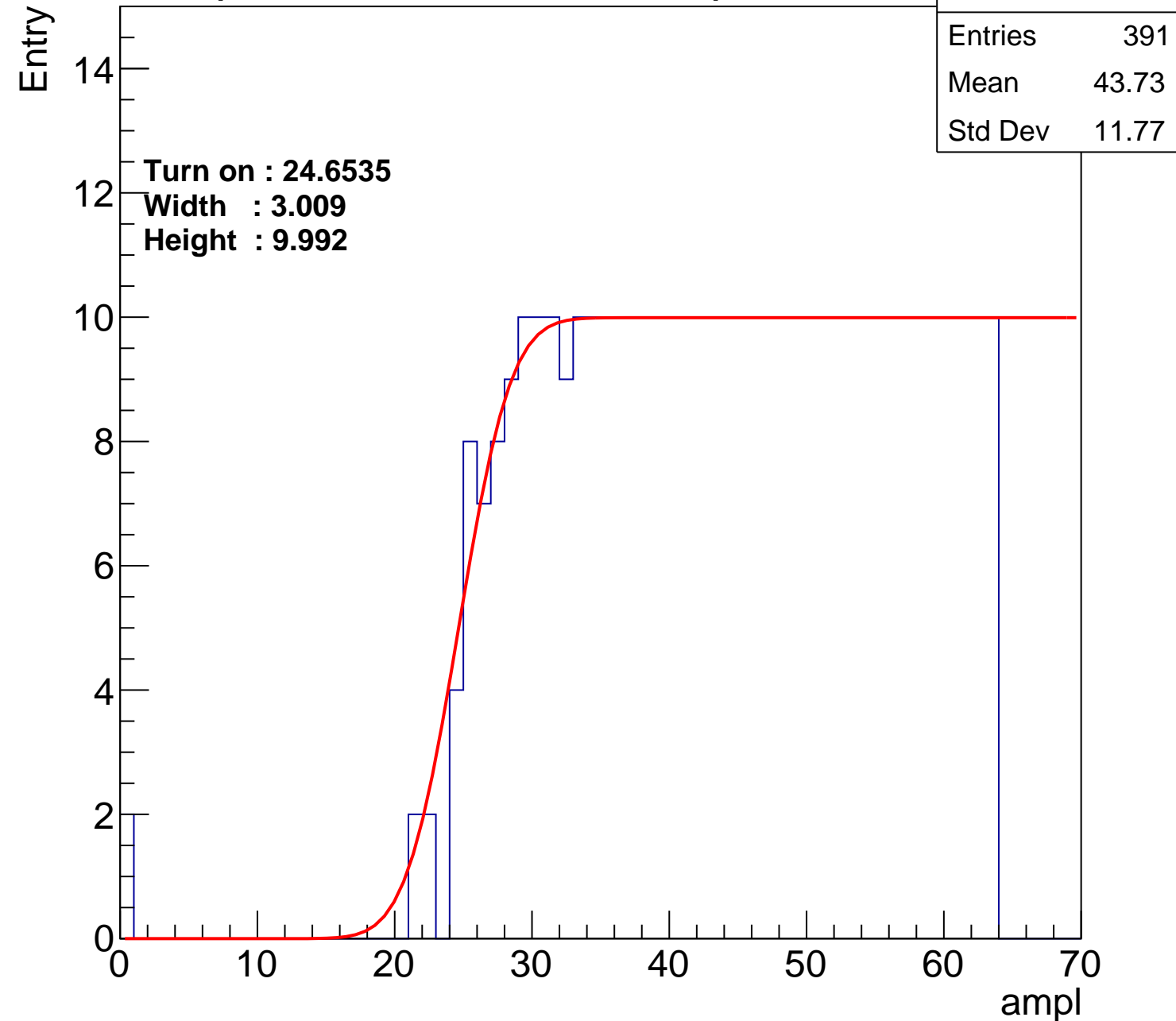
Width : 3.009

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch11

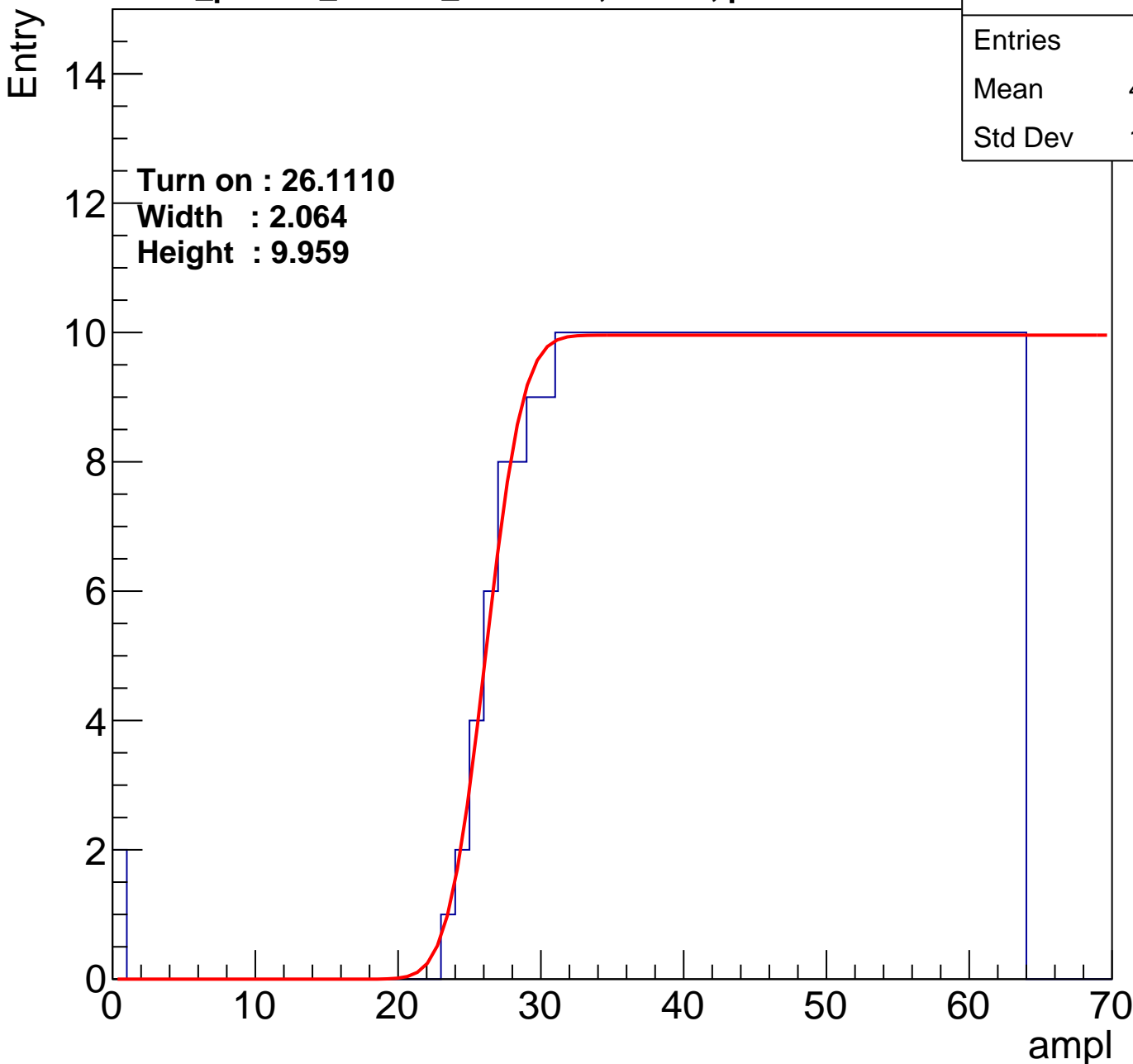
calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.35
Std Dev	11.43

Turn on : 26.1110

Width : 2.064

Height : 9.959



B0L102S, U6-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.2
Std Dev	11.83

Turn on : 26.7605

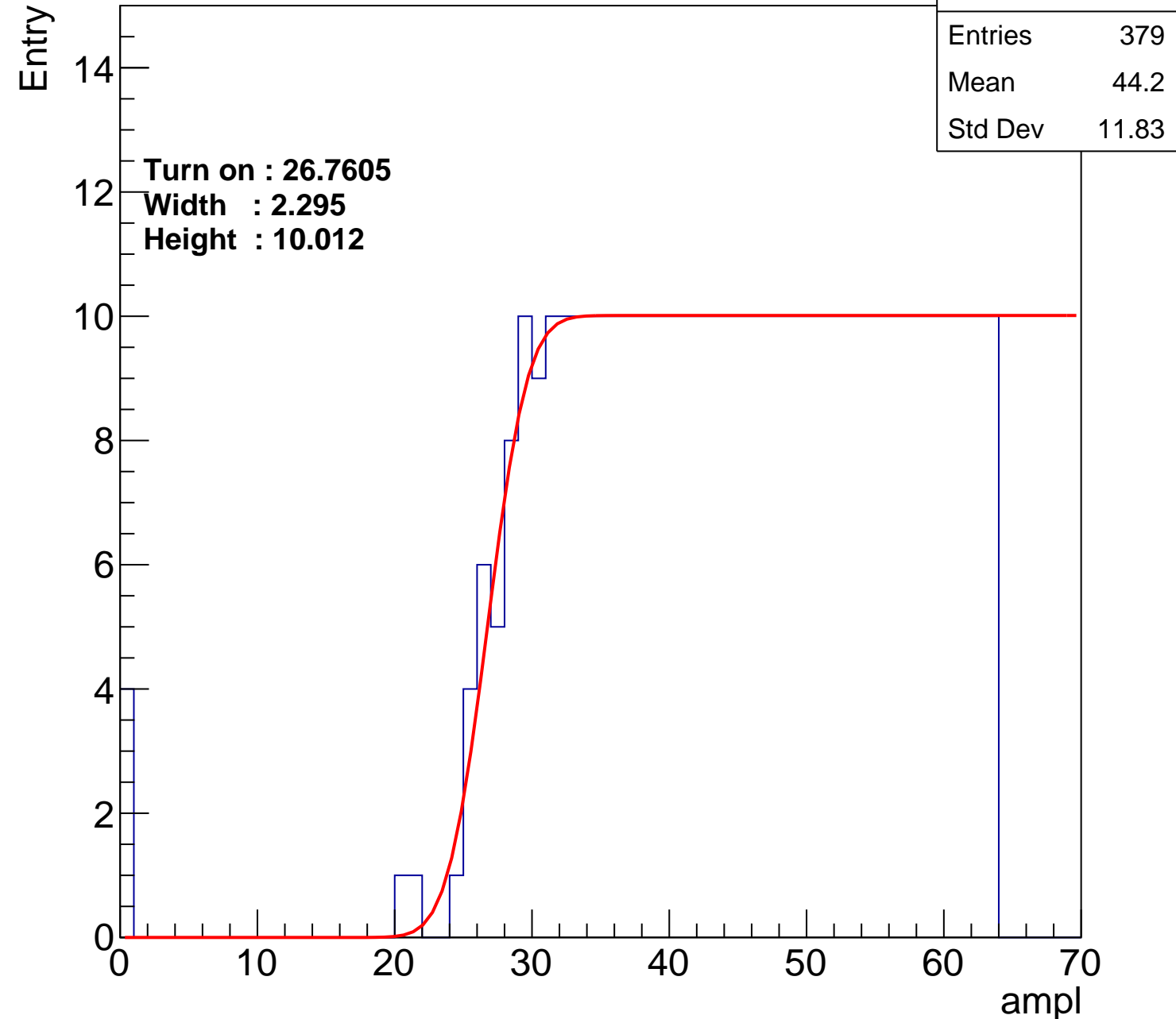
Width : 2.295

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.4055

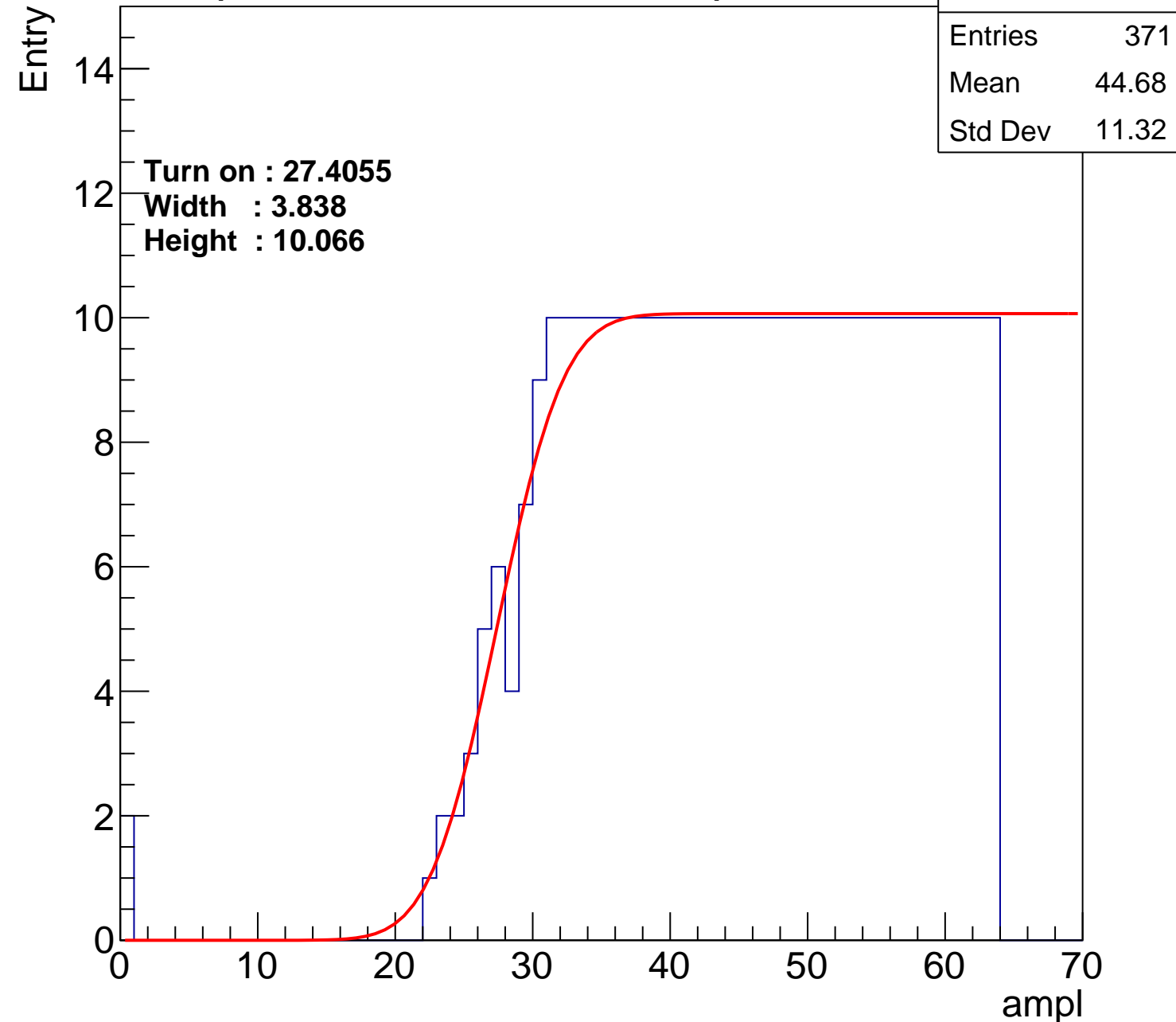
Width : 3.838

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.58
Std Dev	11.98

Turn on : 24.9470

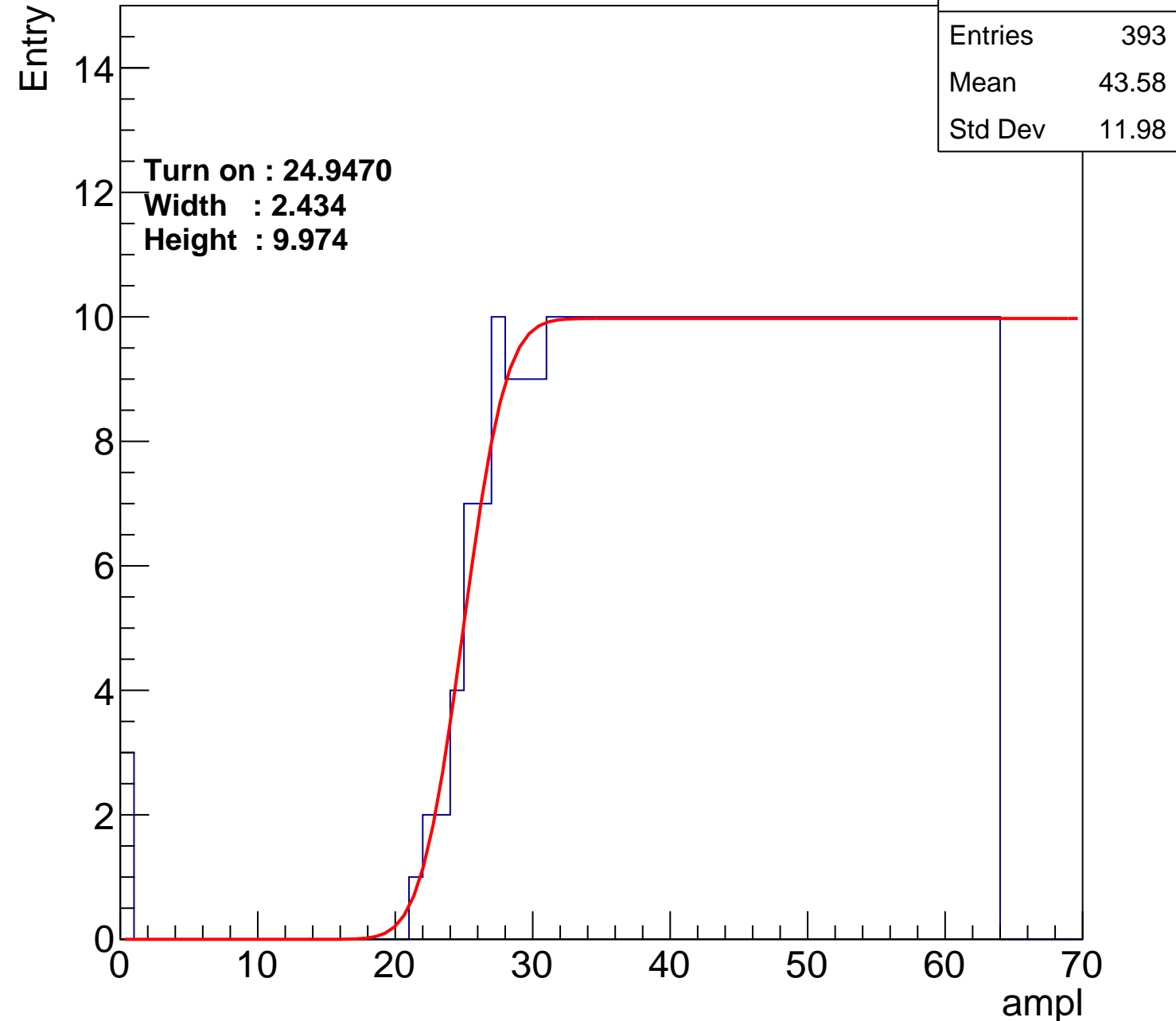
Width : 2.434

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.58
Std Dev	11.65

Turn on : 27.6840

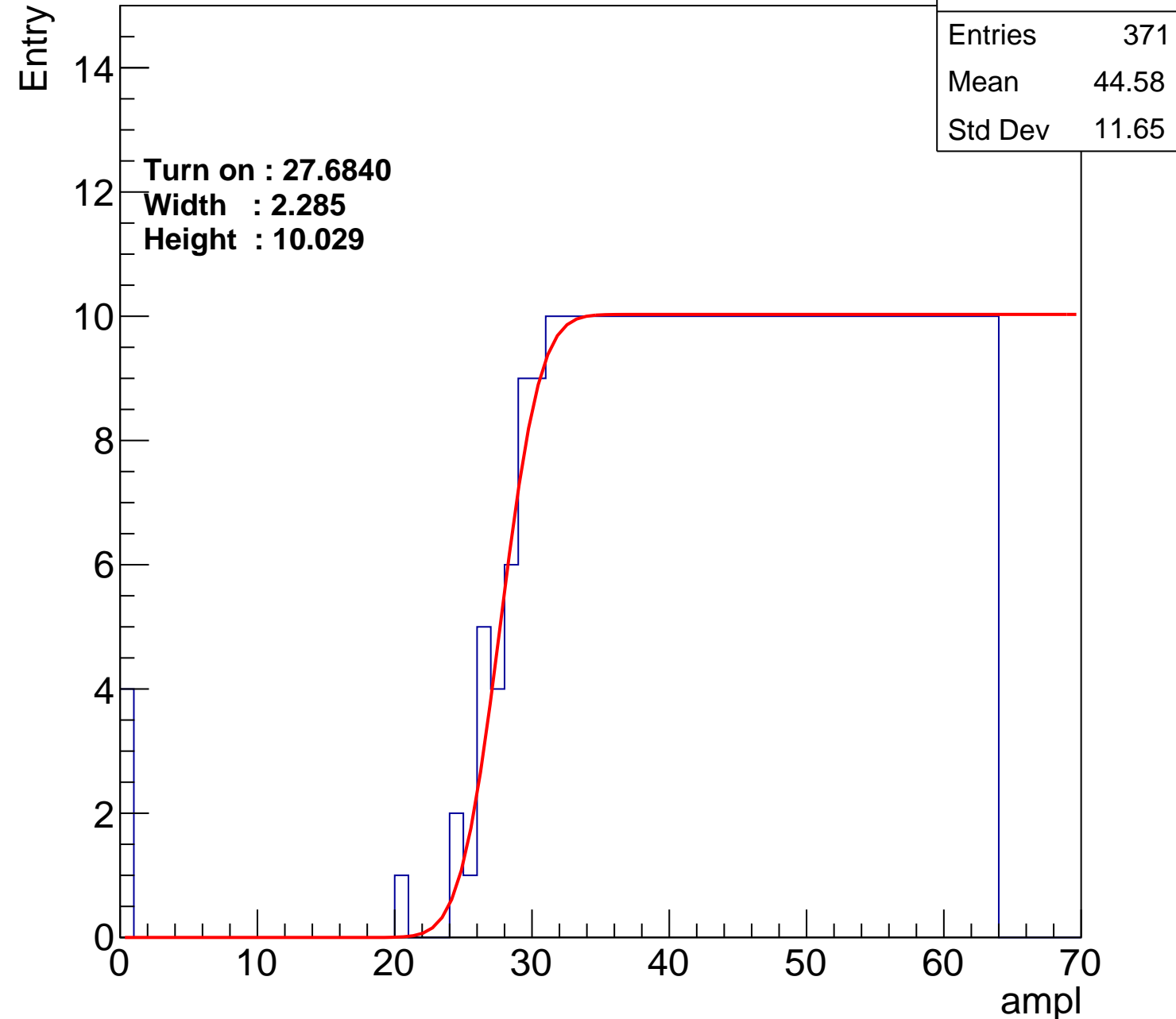
Width : 2.285

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch16

calib_packv5_042523_0143.root, FC#12, port B1

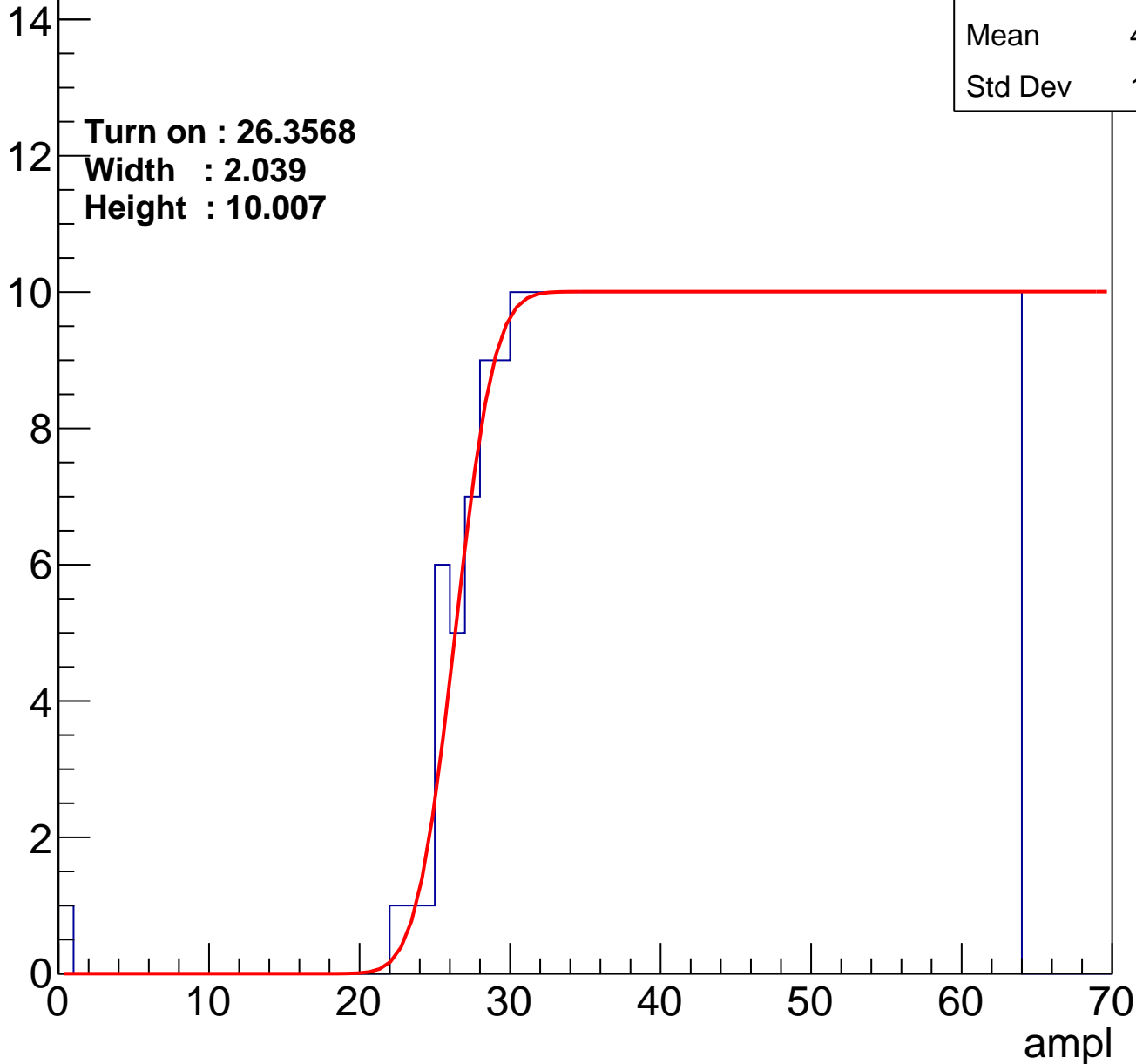
Entry

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.3568

Width : 2.039

Height : 10.007



B0L102S, U6-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.29
Std Dev	12.18

Turn on : 24.3931

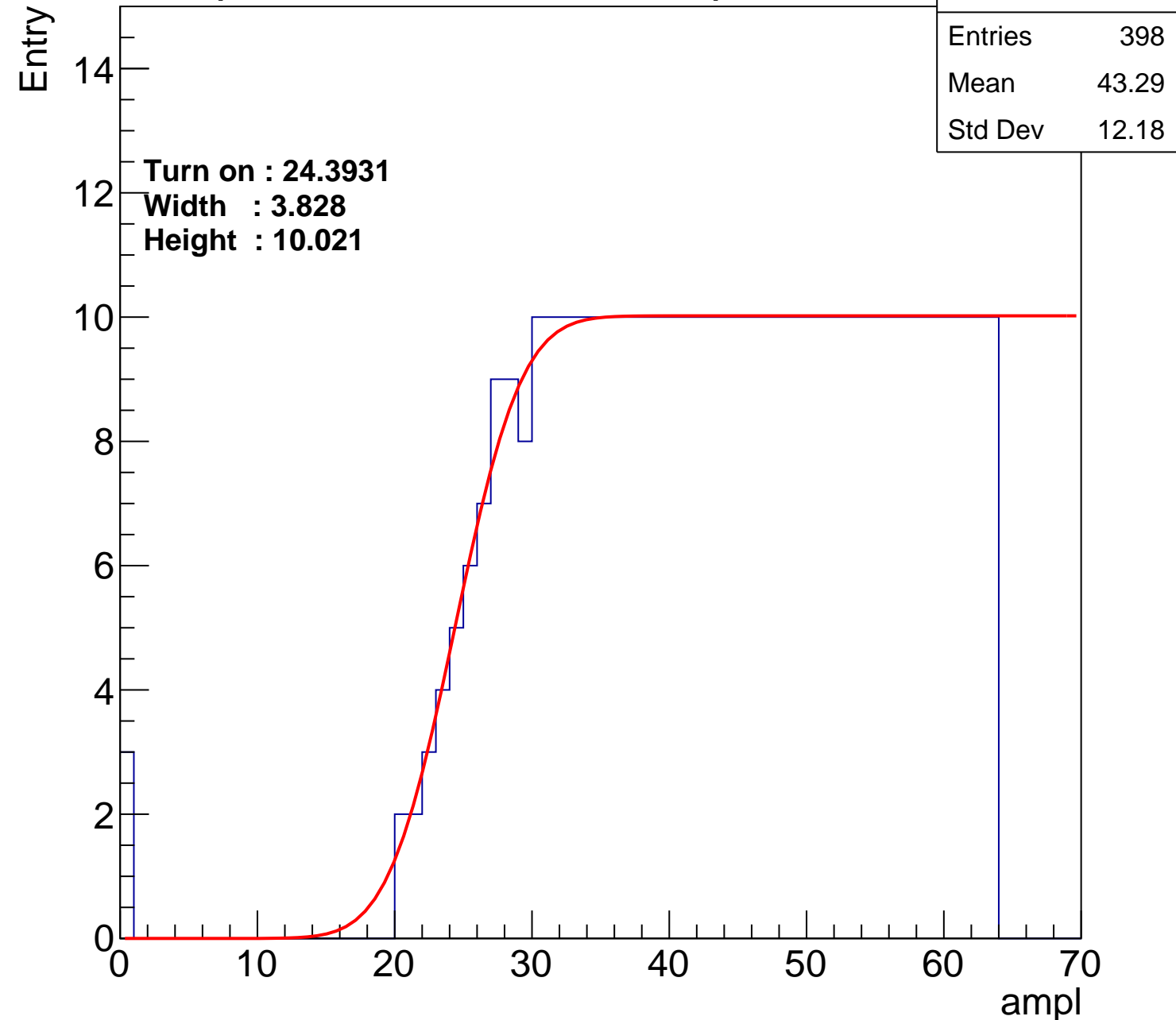
Width : 3.828

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 26.1569

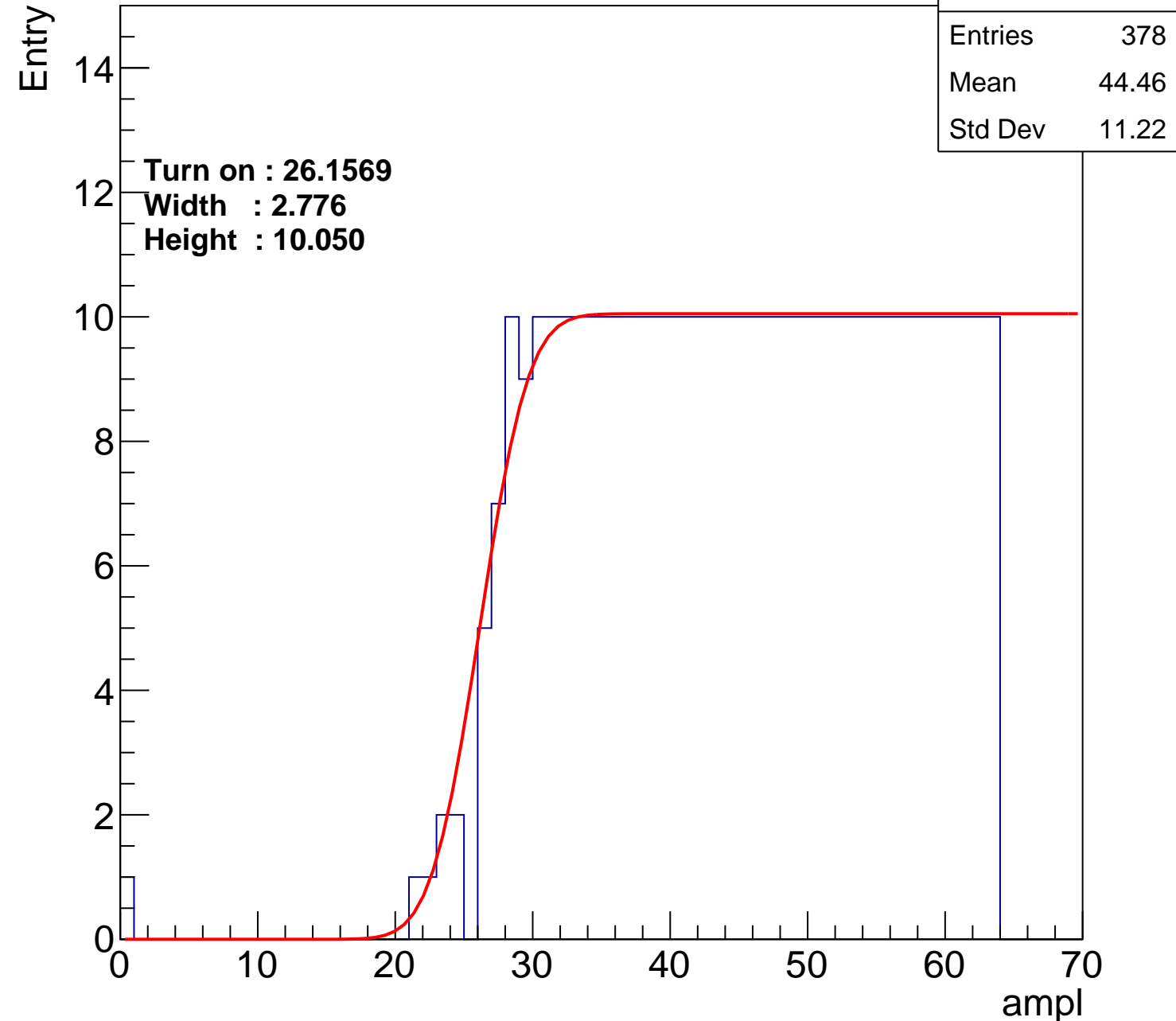
Width : 2.776

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.9452

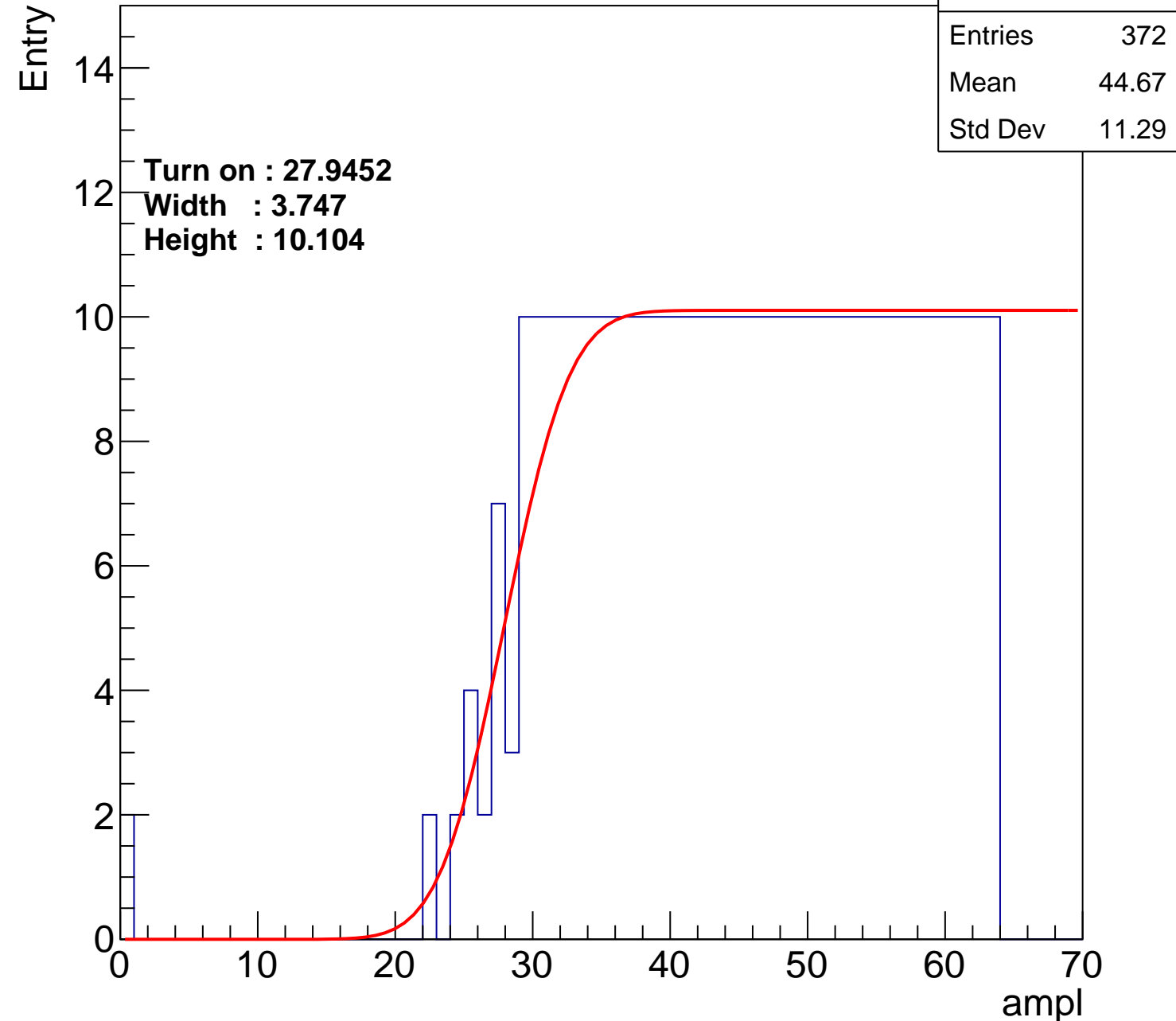
Width : 3.747

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.25
Std Dev	12.07

Turn on : 24.6266

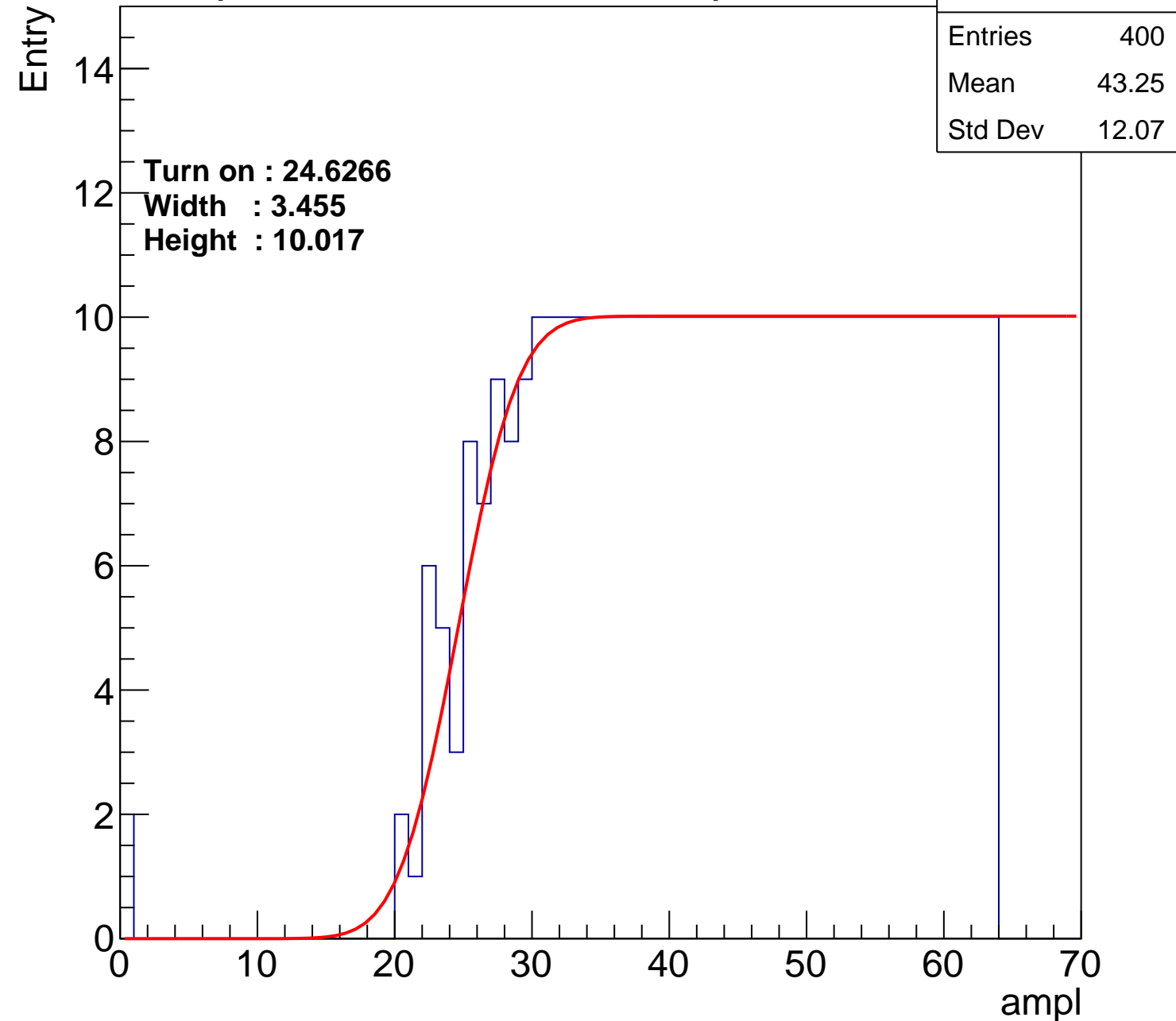
Width : 3.455

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.7410

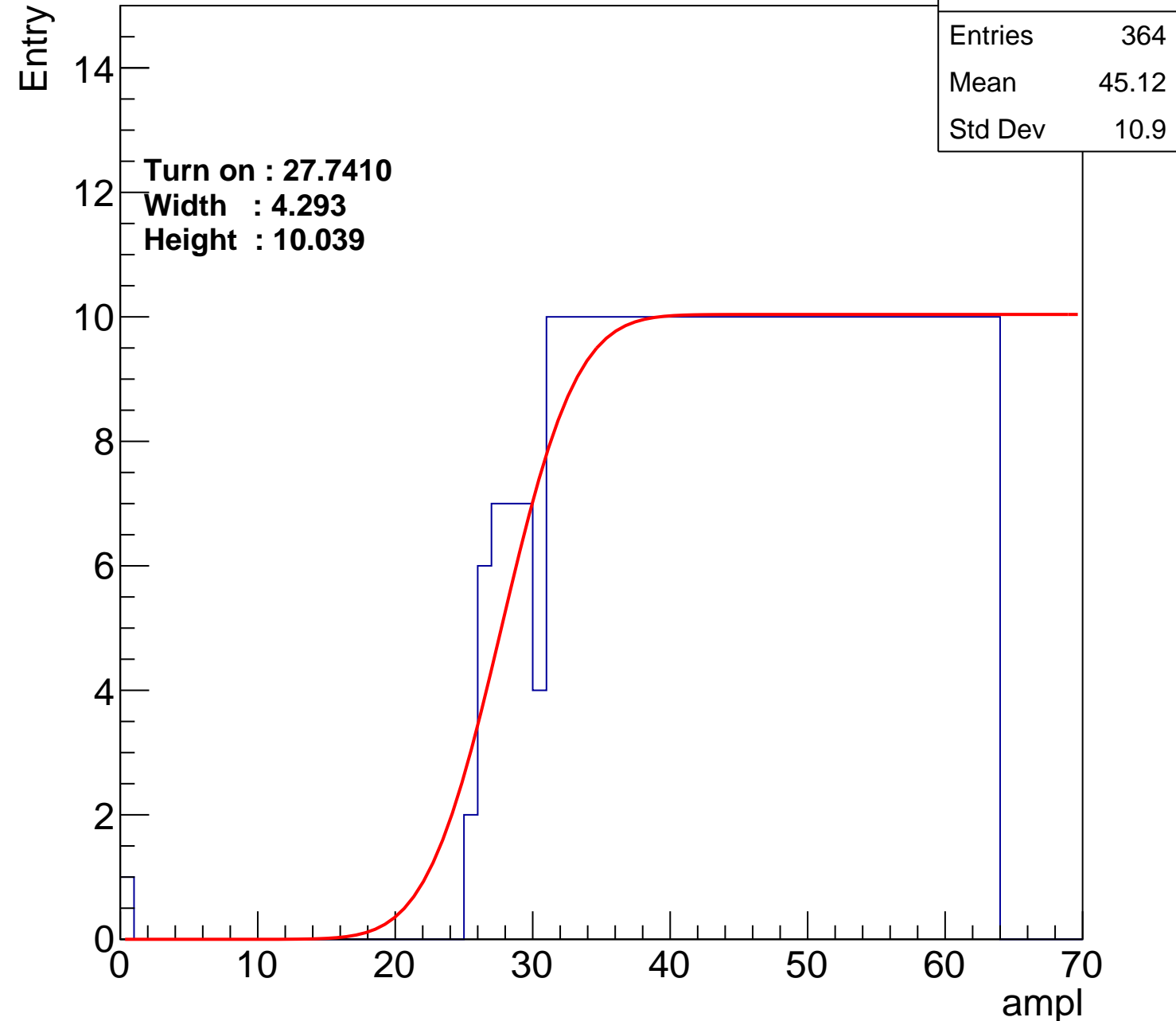
Width : 4.293

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.81
Std Dev	12.01

Turn on : 25.8232

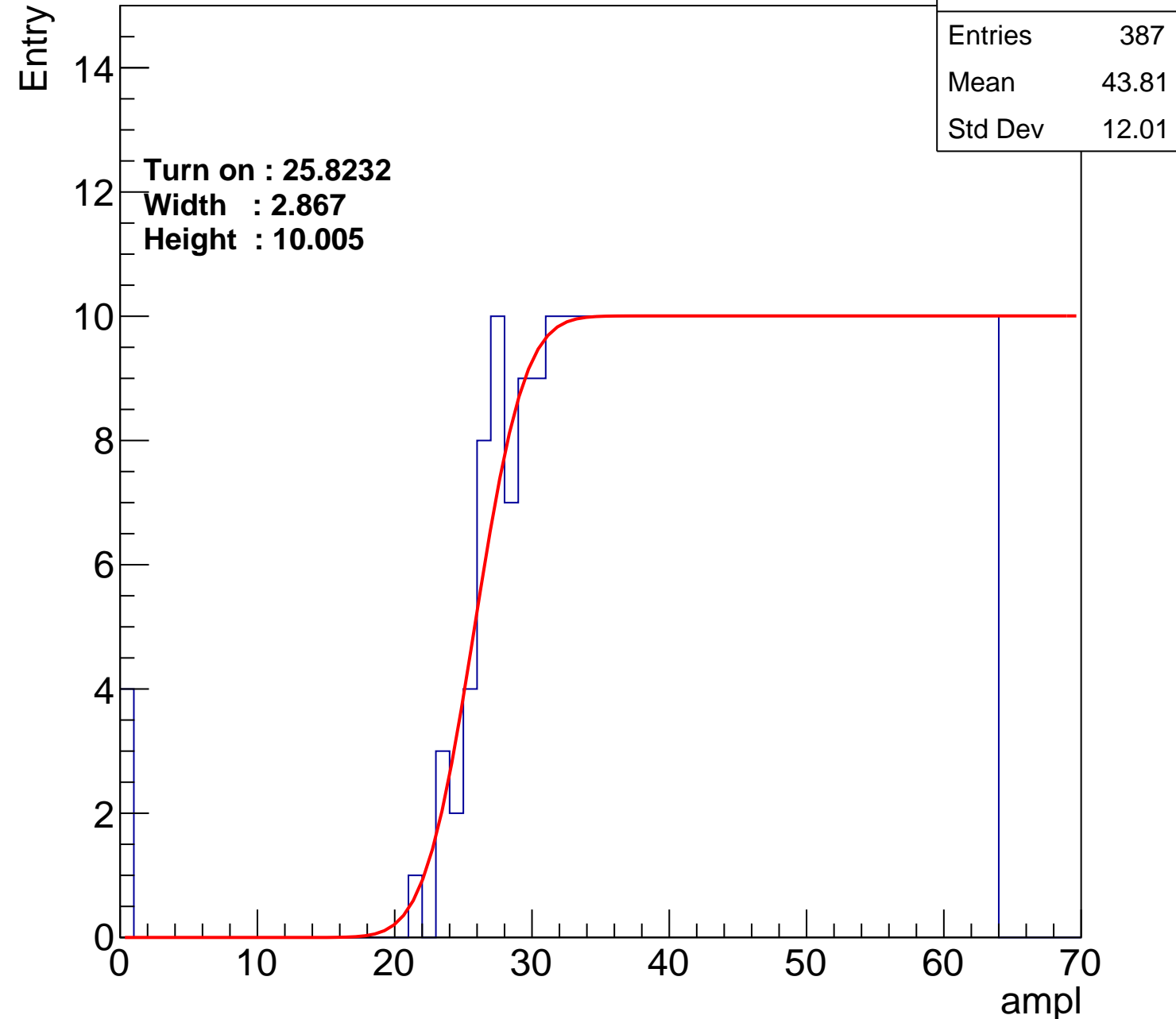
Width : 2.867

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.34
Std Dev	10.76

Turn on : 28.7055

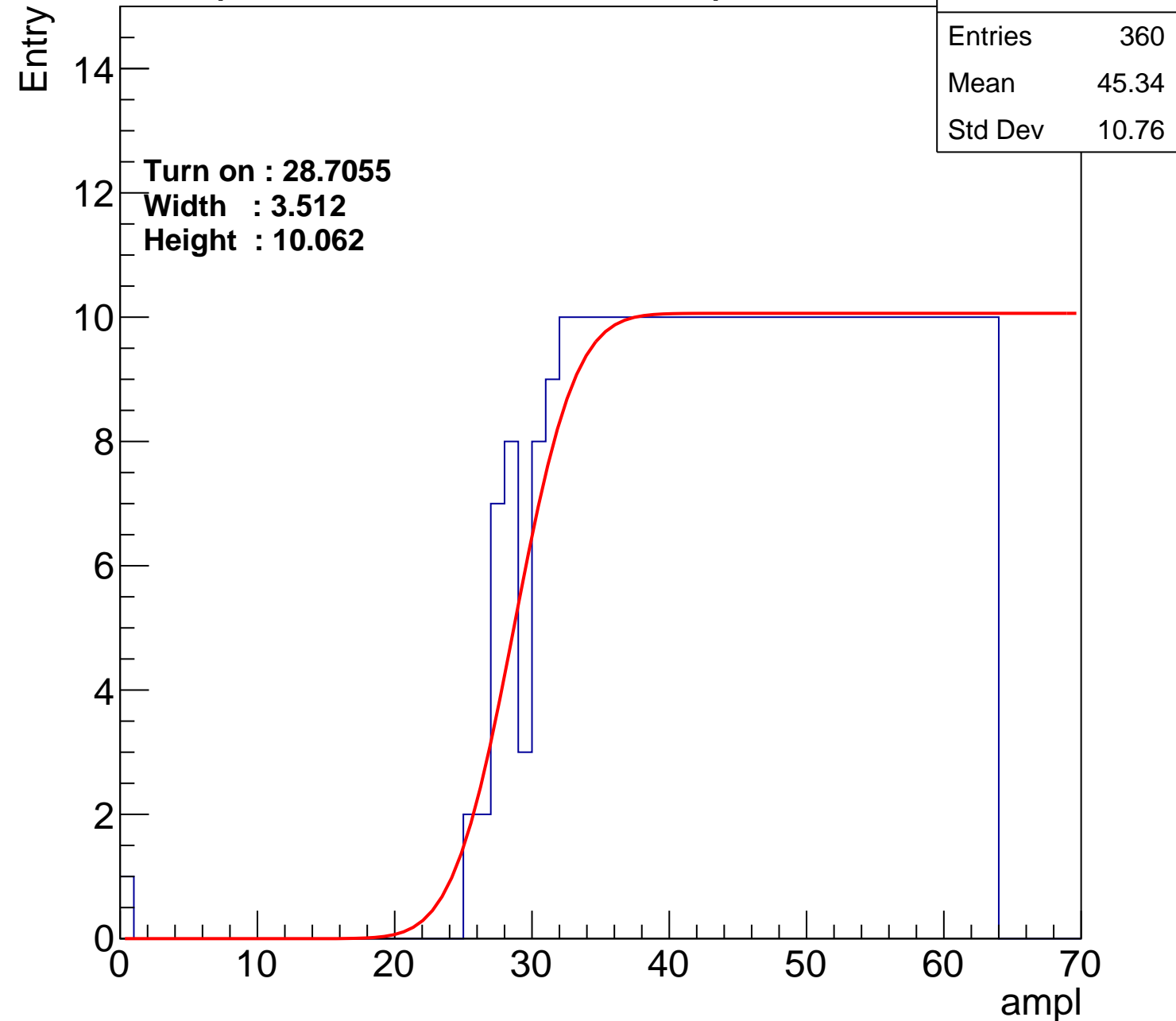
Width : 3.512

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.73
Std Dev	12.54

Turn on : 23.3848

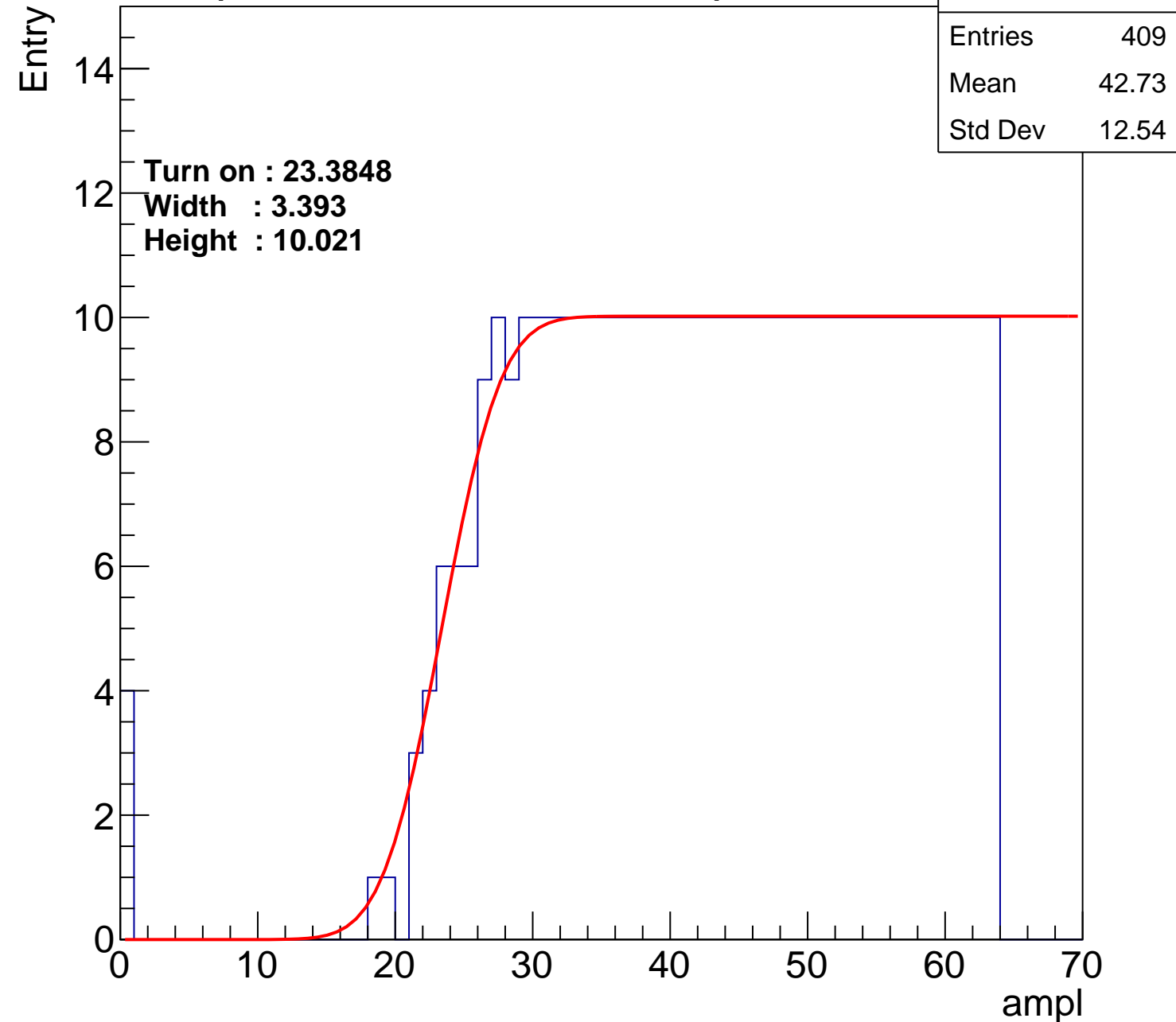
Width : 3.393

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.9347

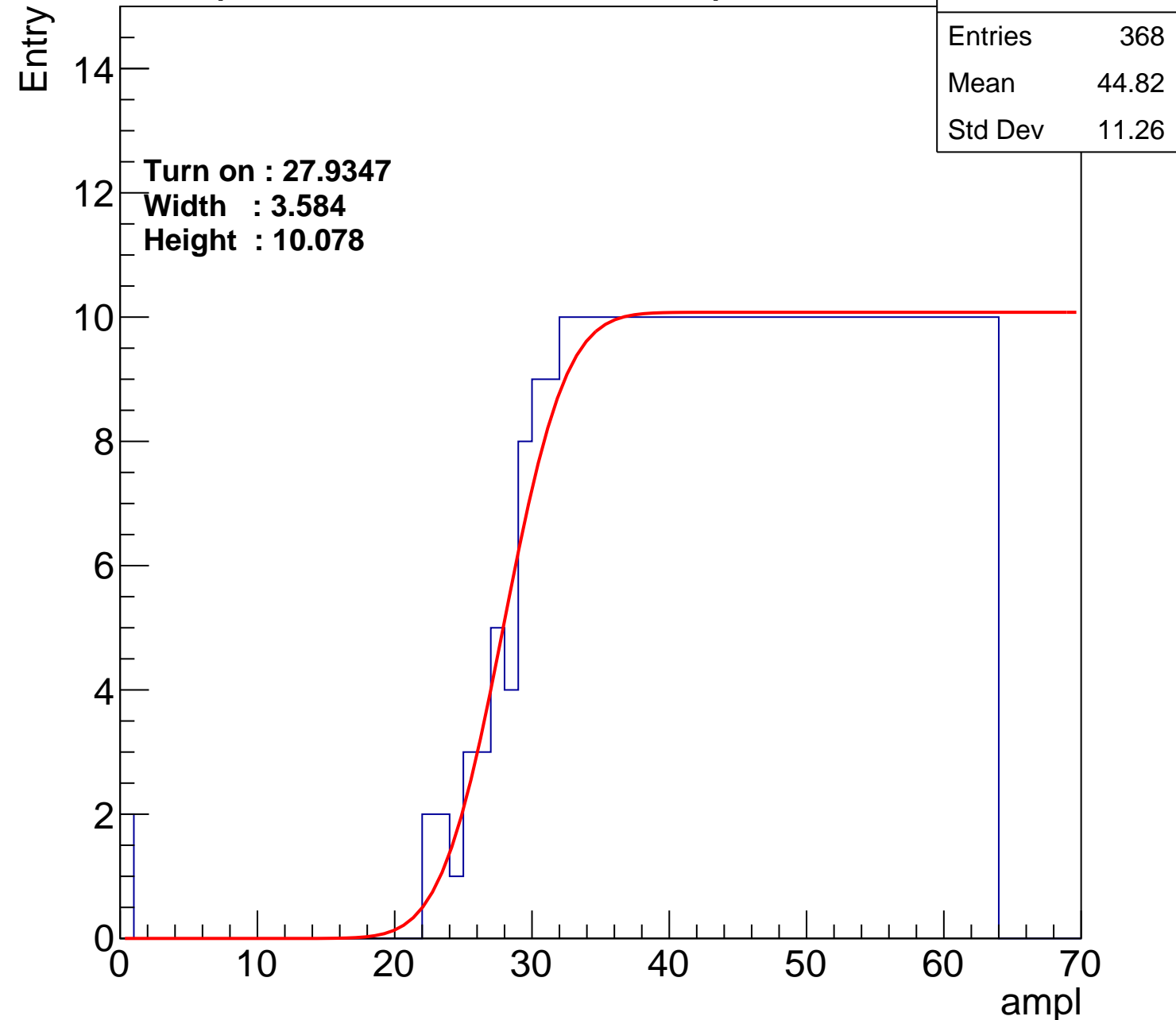
Width : 3.584

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.77
Std Dev	11.9

Turn on : 25.2906

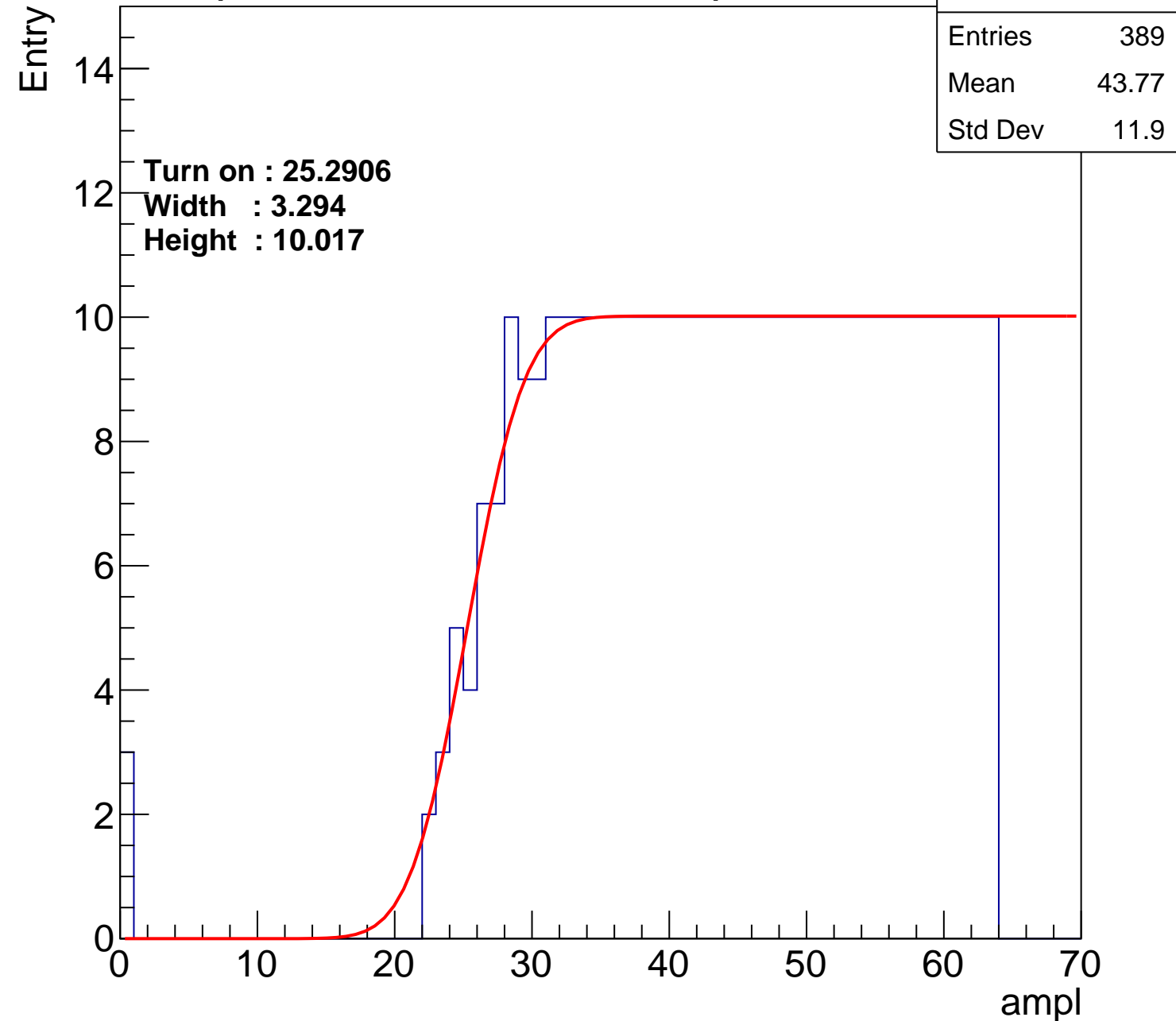
Width : 3.294

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.83
Std Dev	11.18

Turn on : 27.3227

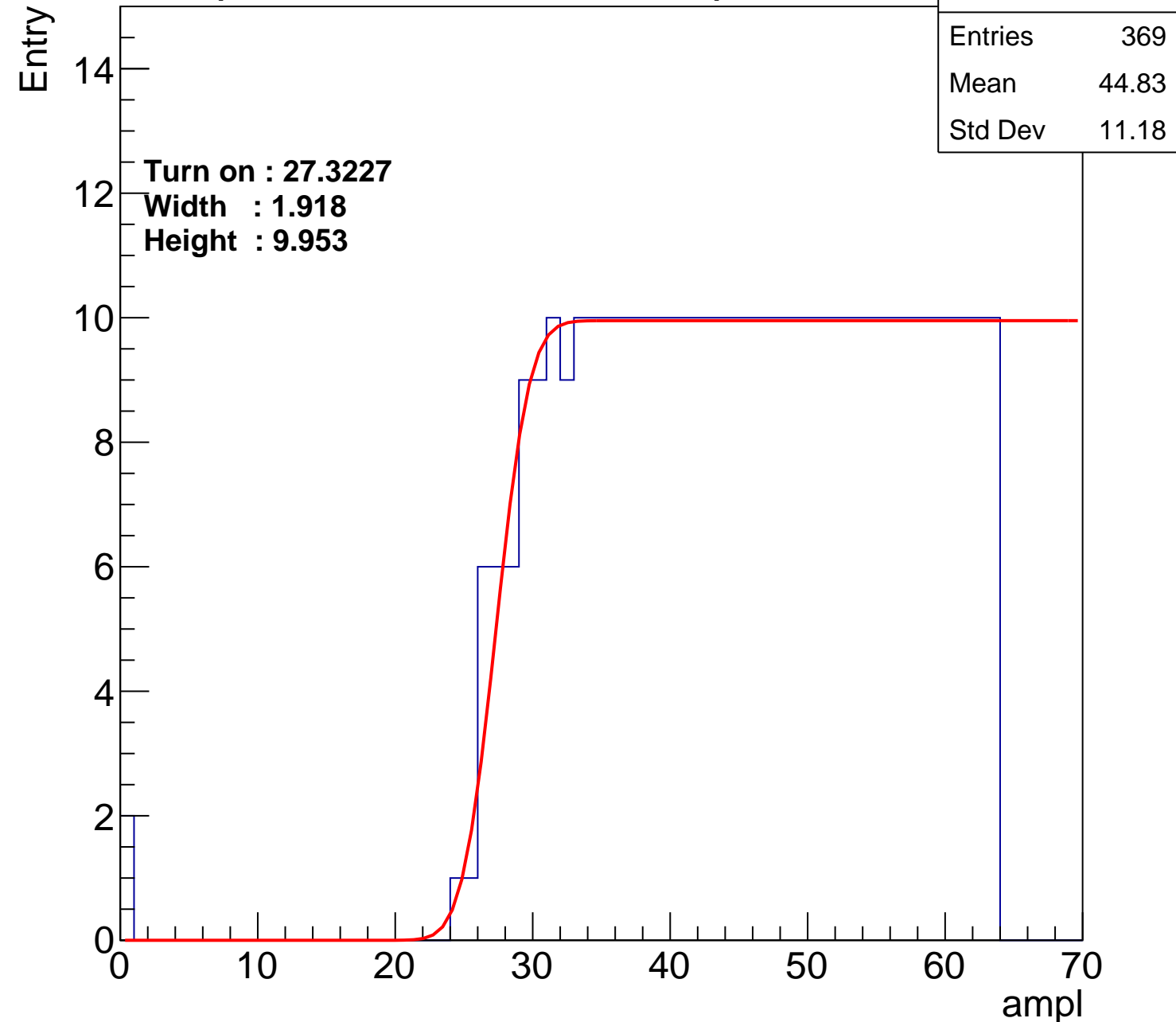
Width : 1.918

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
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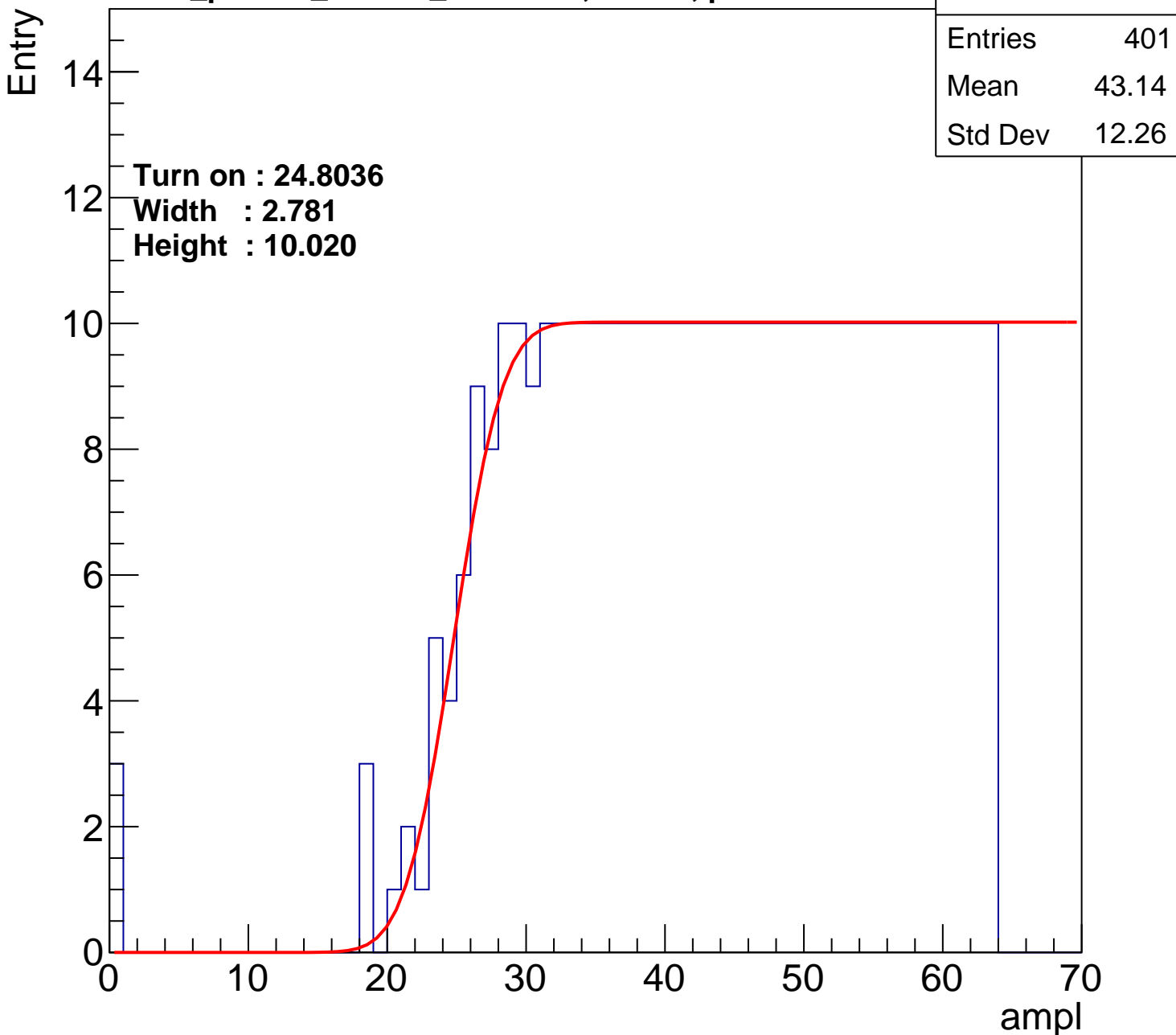
Mean	43.14
------	-------

Std Dev	12.26
---------	-------

Turn on : 24.8036

Width : 2.781

Height : 10.020



B0L102S, U6-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.78
Std Dev	11.09

Turn on : 27.7778

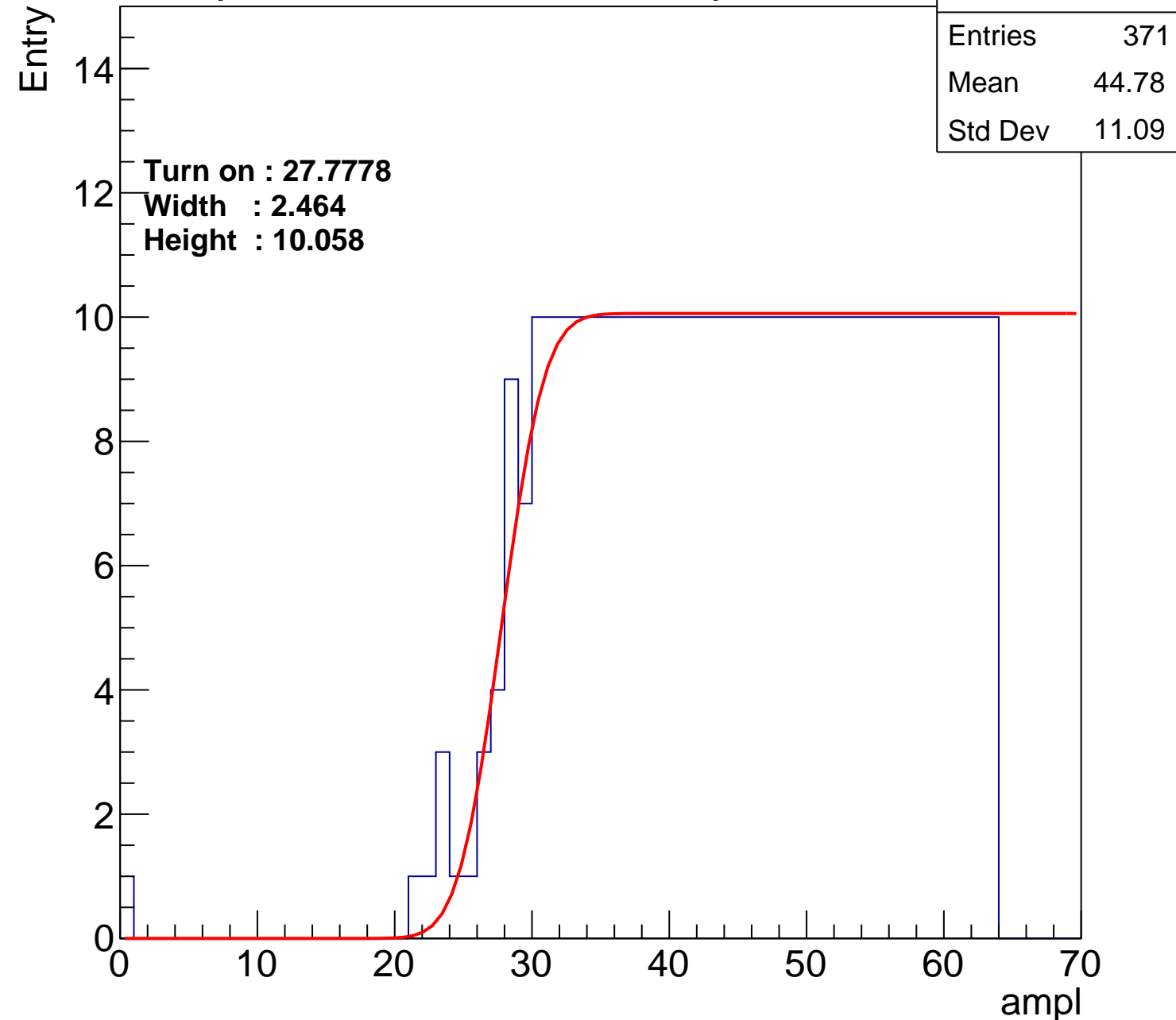
Width : 2.464

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch30

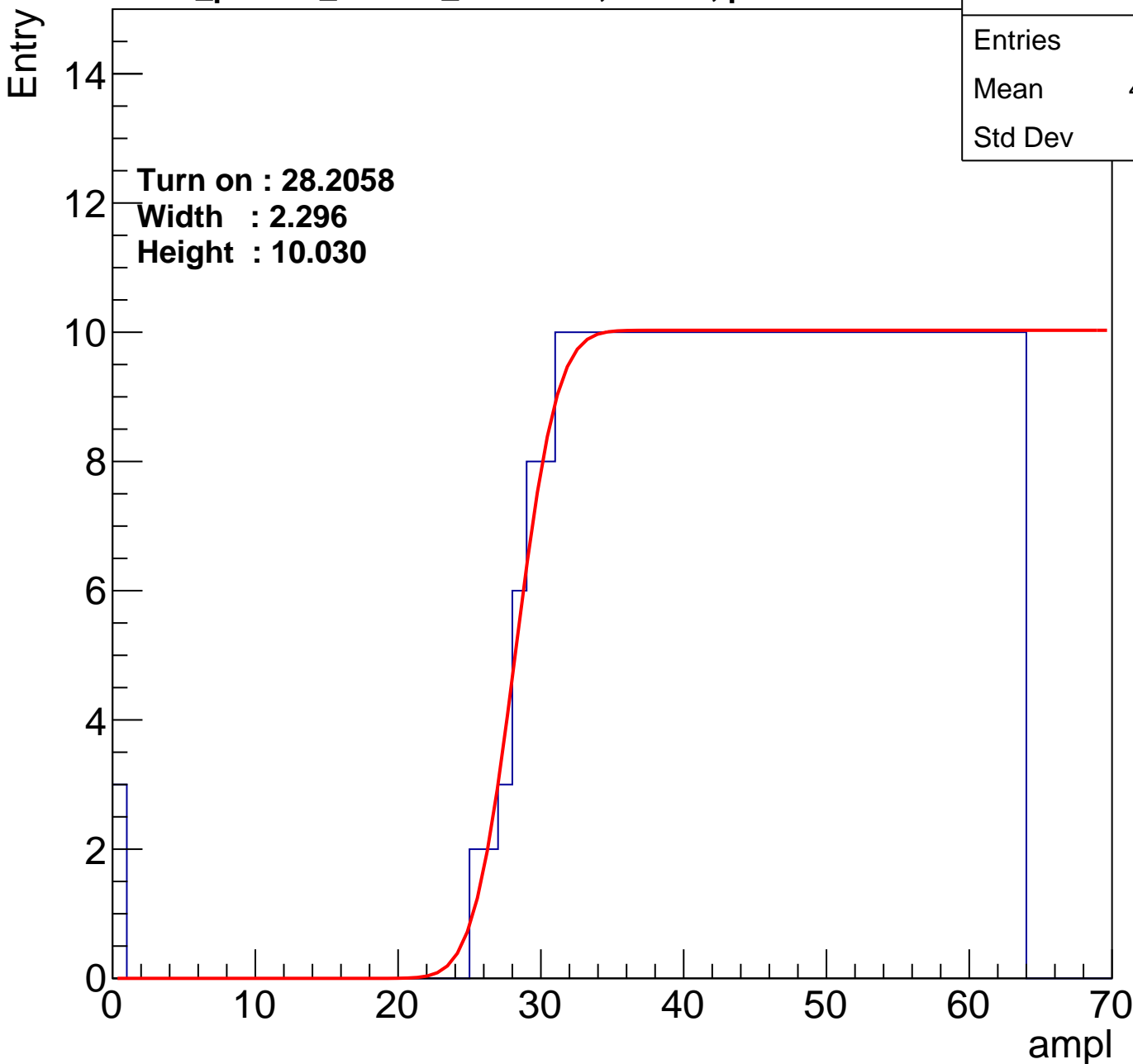
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.2058

Width : 2.296

Height : 10.030

Entries	362
Mean	45.12
Std Dev	11.2



B0L102S, U6-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 27.3484

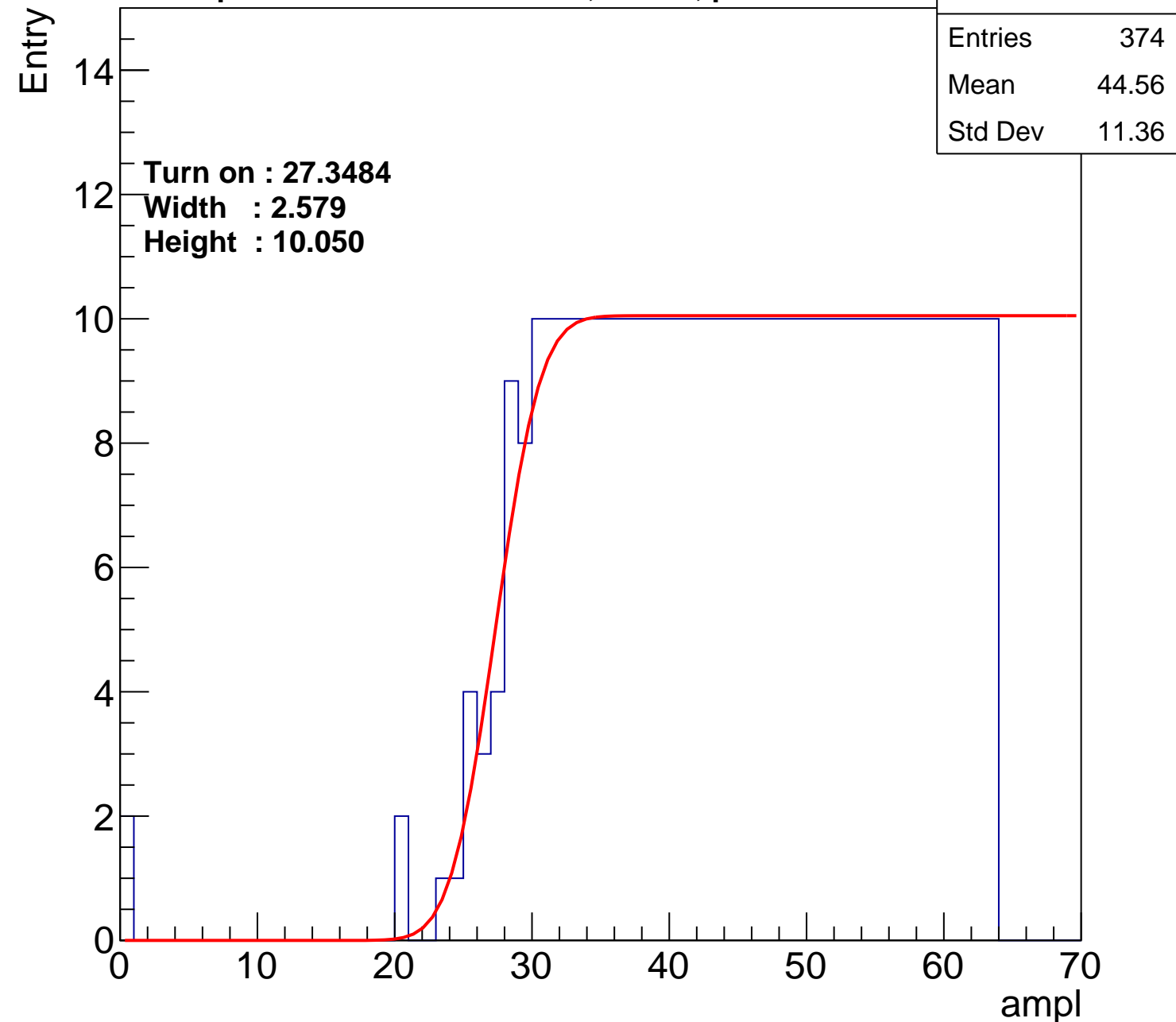
Width : 2.579

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	43.09
Std Dev	12.13

Turn on : 24.5767

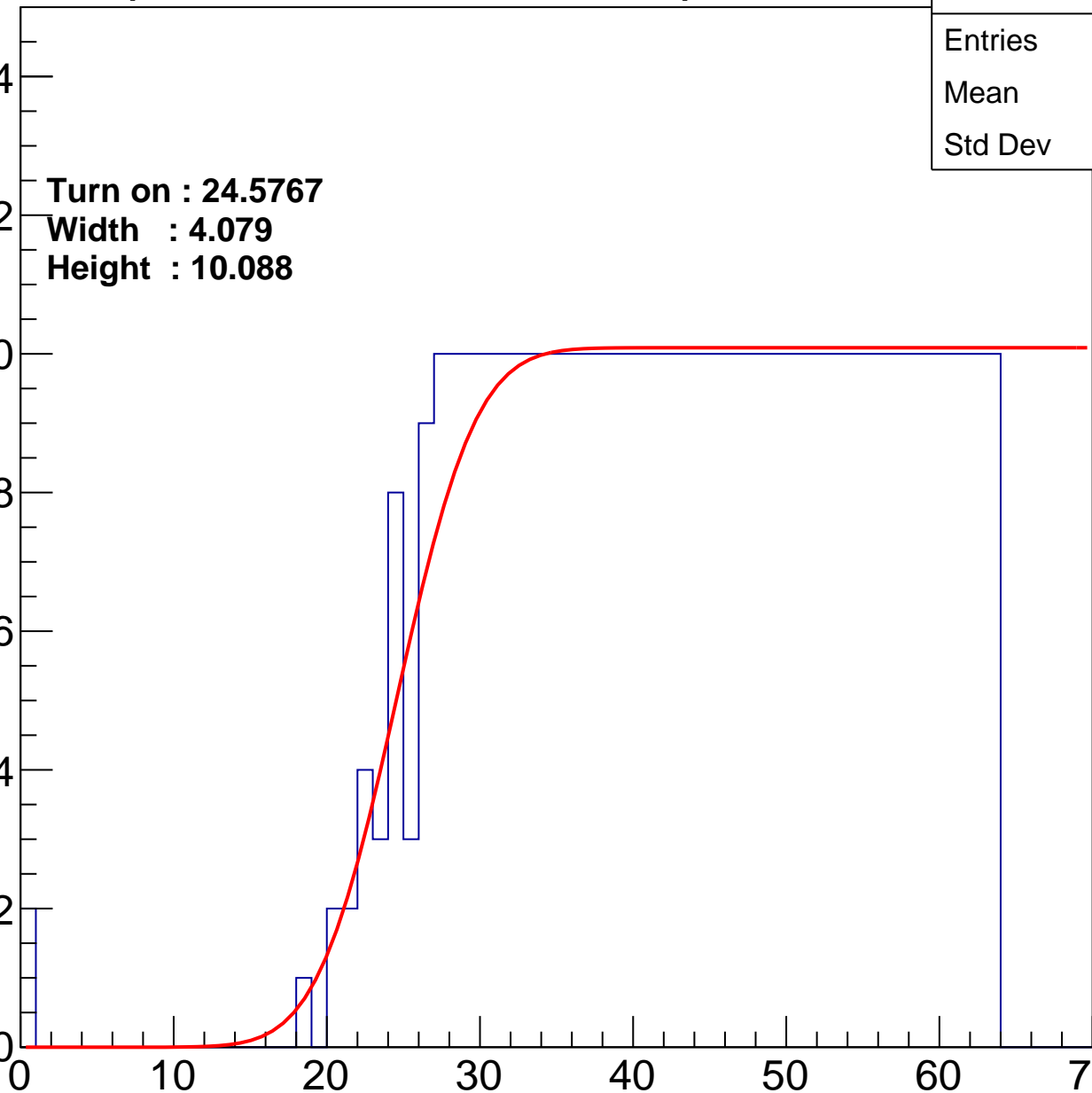
Width : 4.079

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.5620

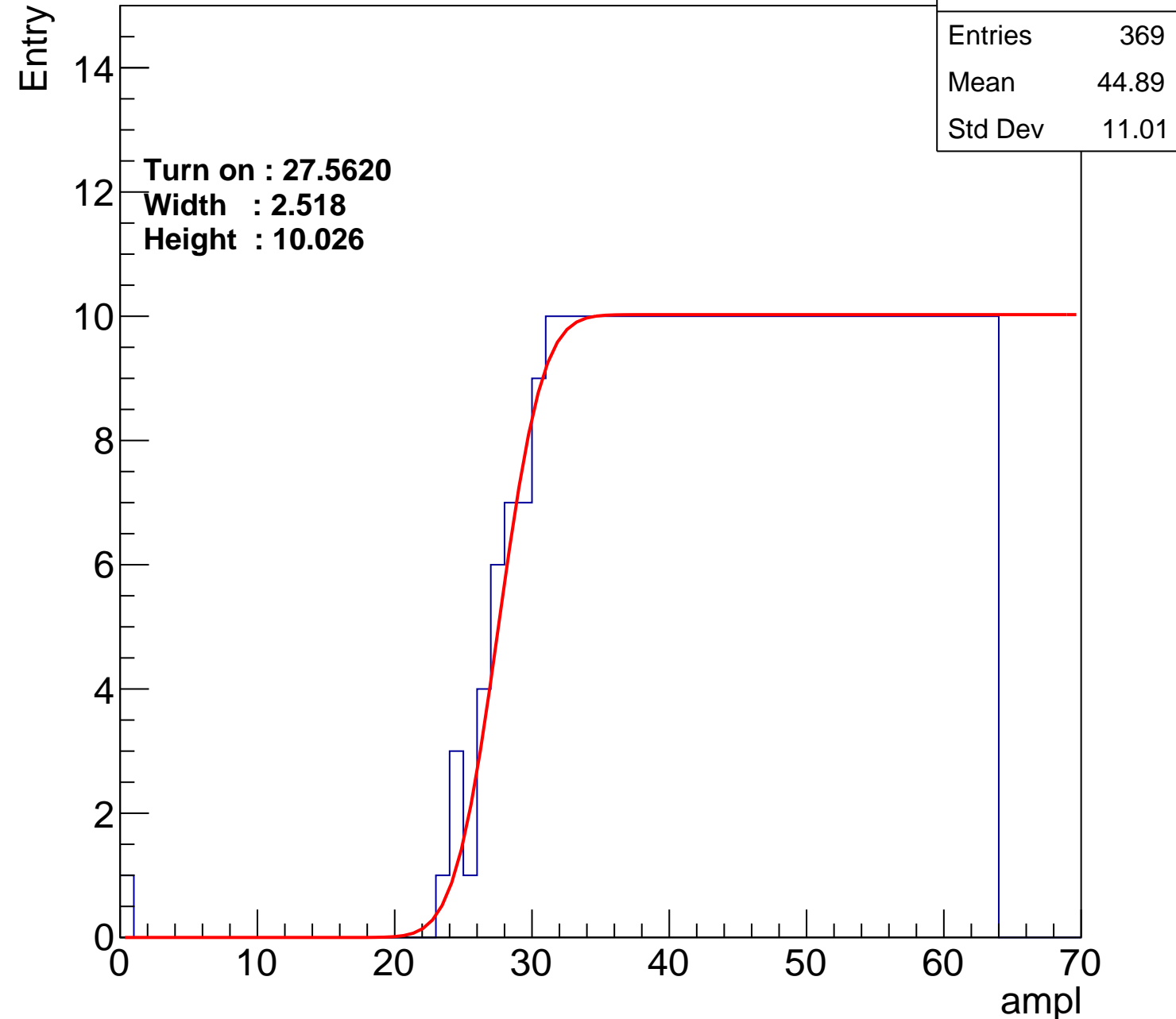
Width : 2.518

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.47
Std Dev	11.88

Turn on : 24.2308

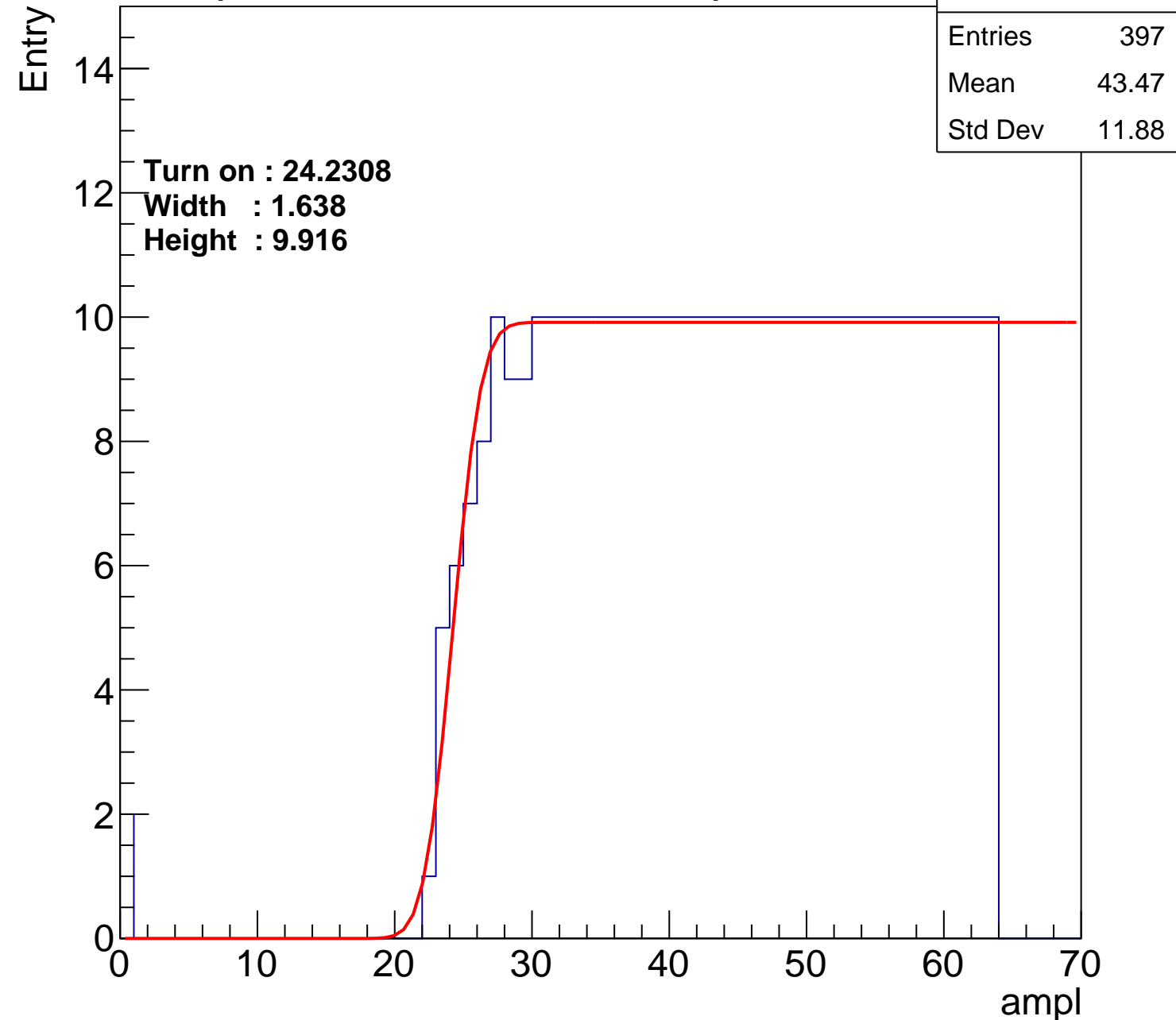
Width : 1.638

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 25.8883

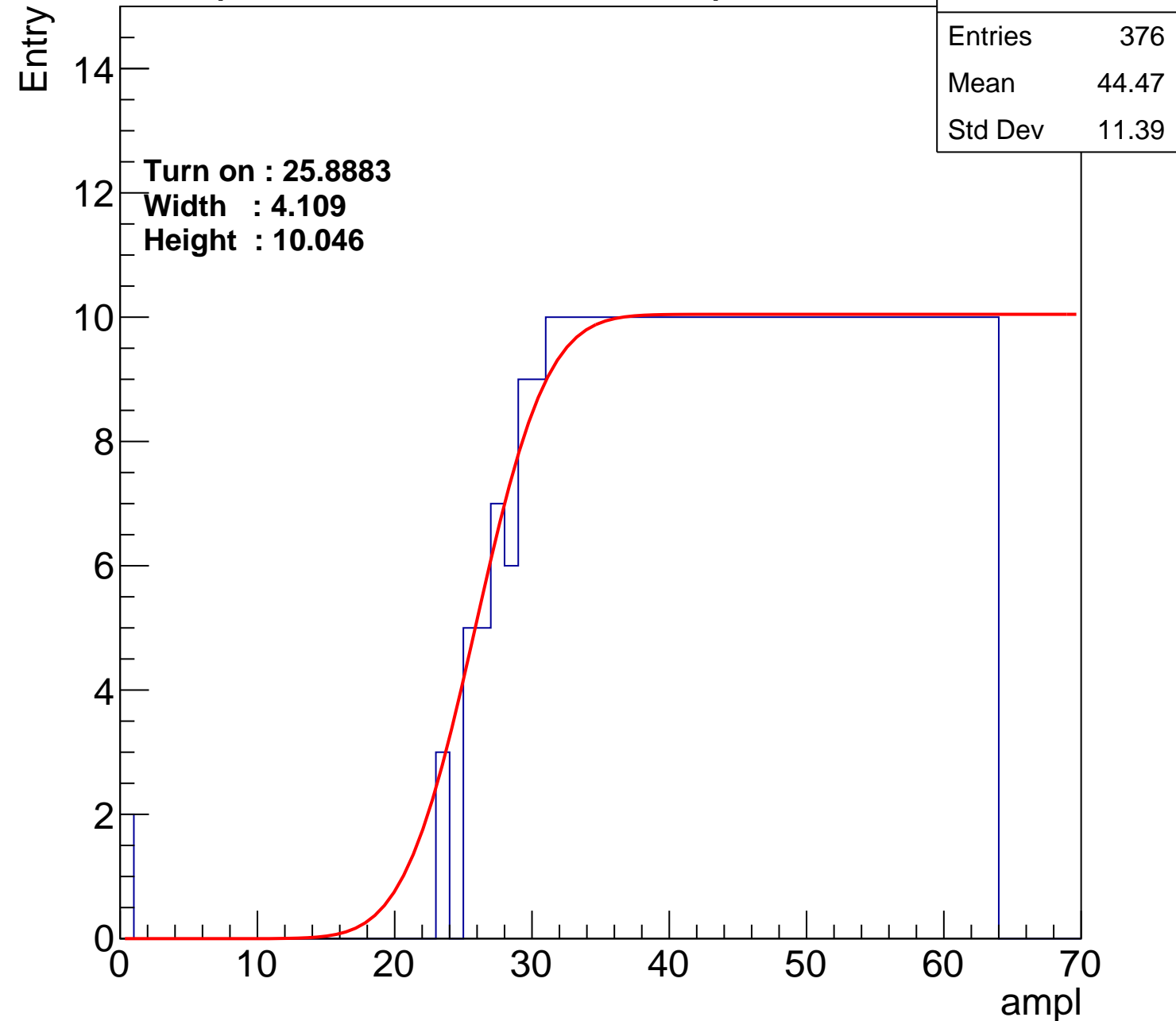
Width : 4.109

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.15
Std Dev	11.6

Turn on : 26.6647

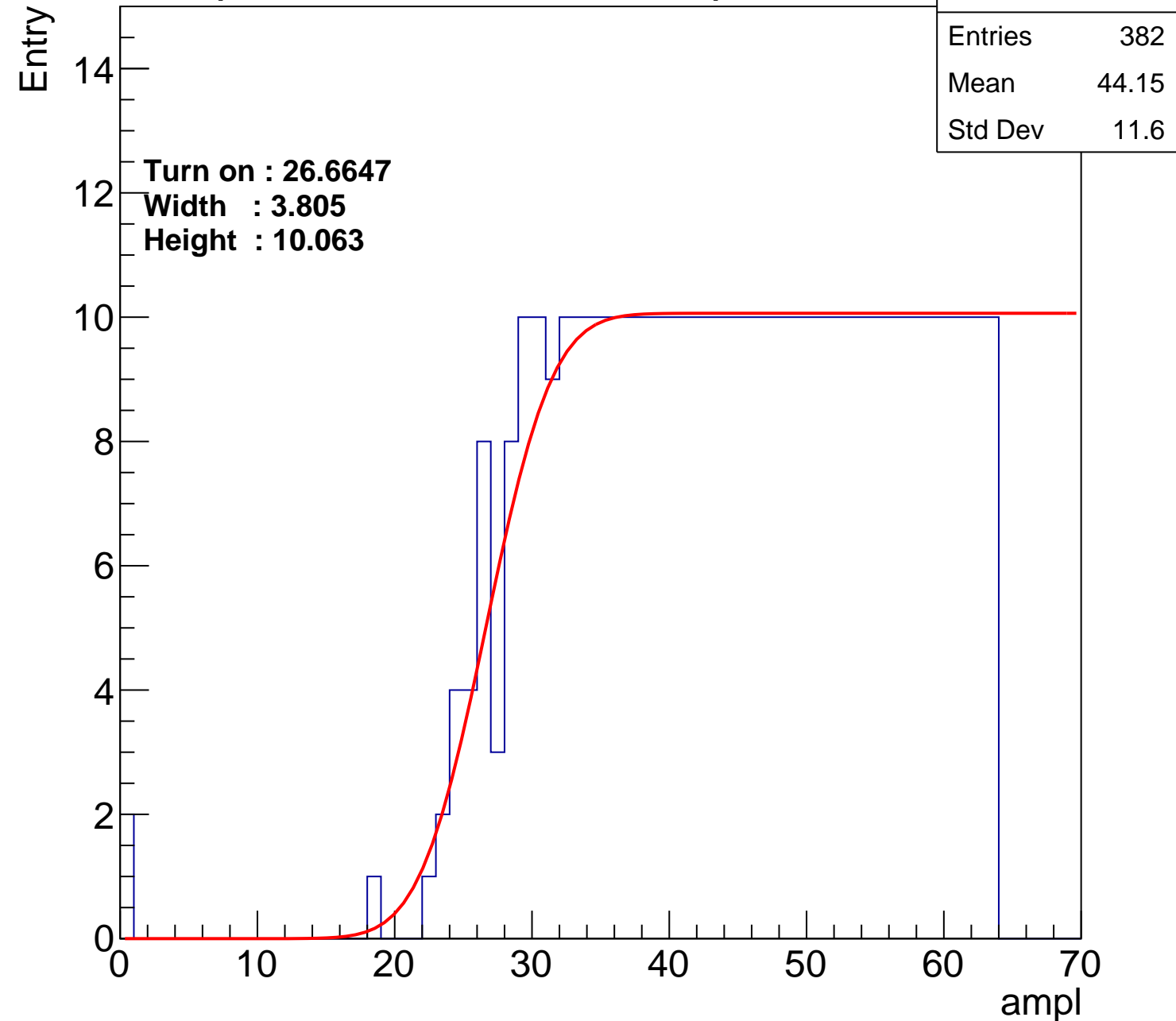
Width : 3.805

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.6849

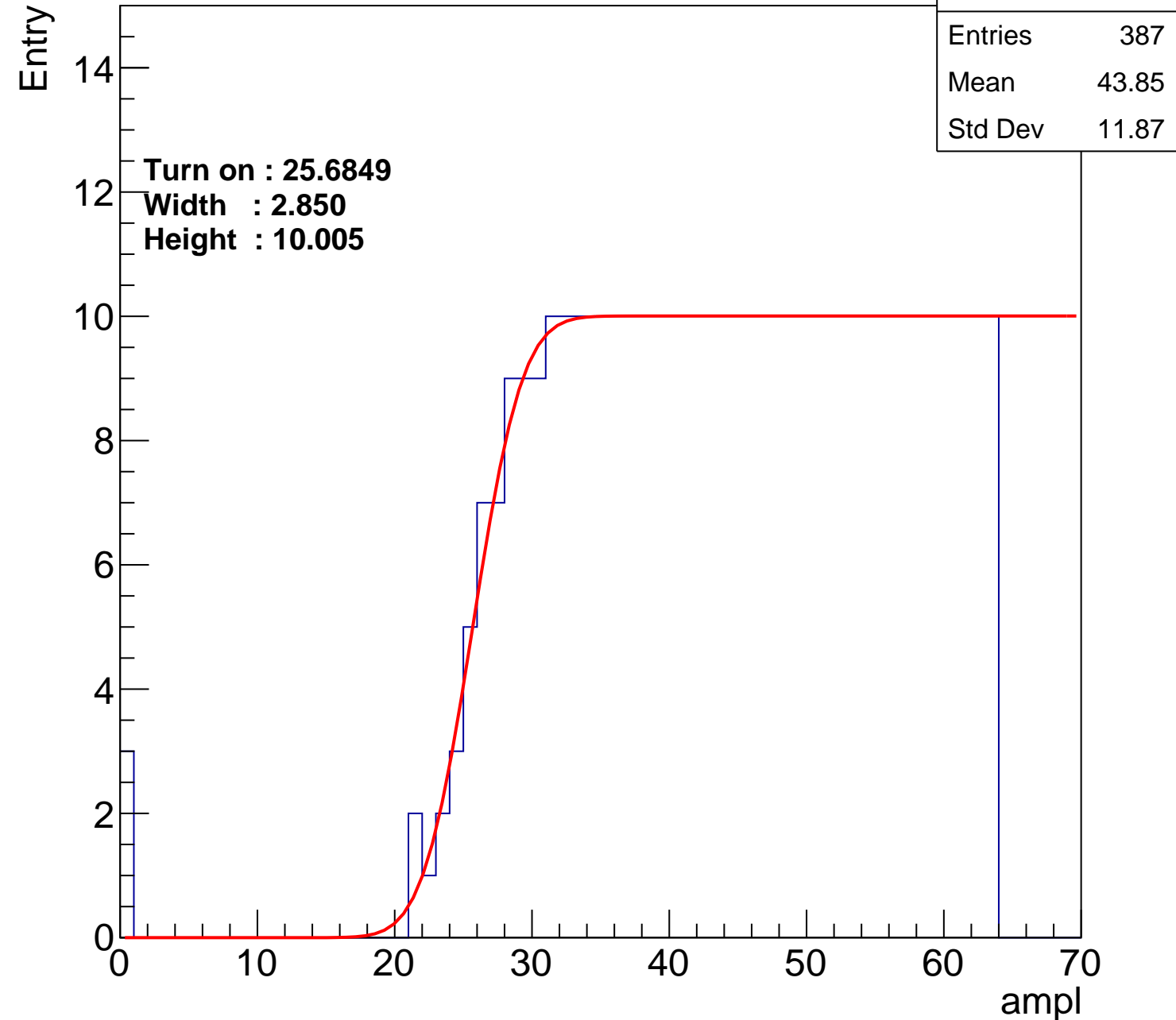
Width : 2.850

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch38

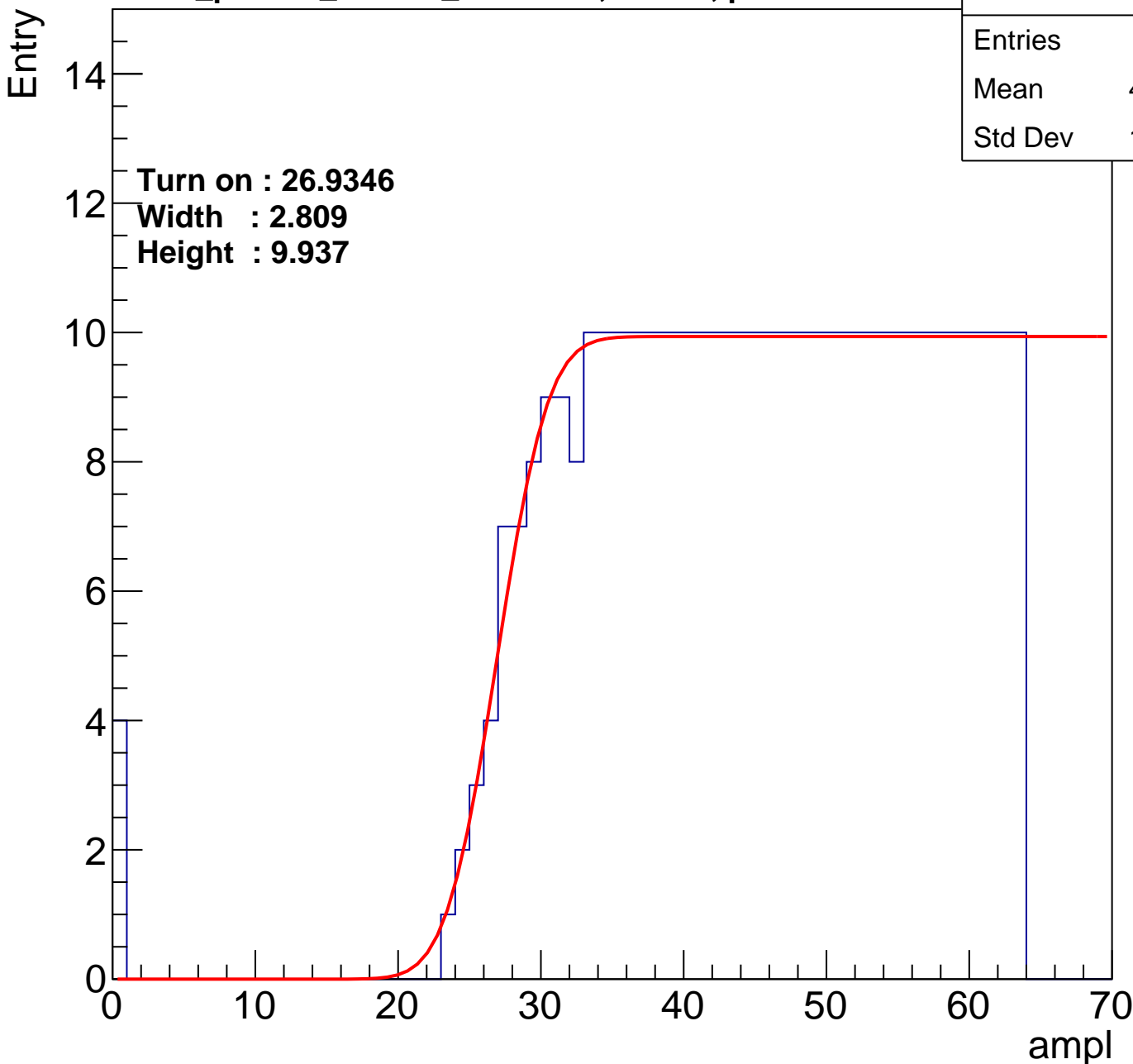
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.9346

Width : 2.809

Height : 9.937

Entries	372
Mean	44.49
Std Dev	11.72



B0L102S, U6-ch39

calib_packv5_042523_0143.root, FC#12, port B1

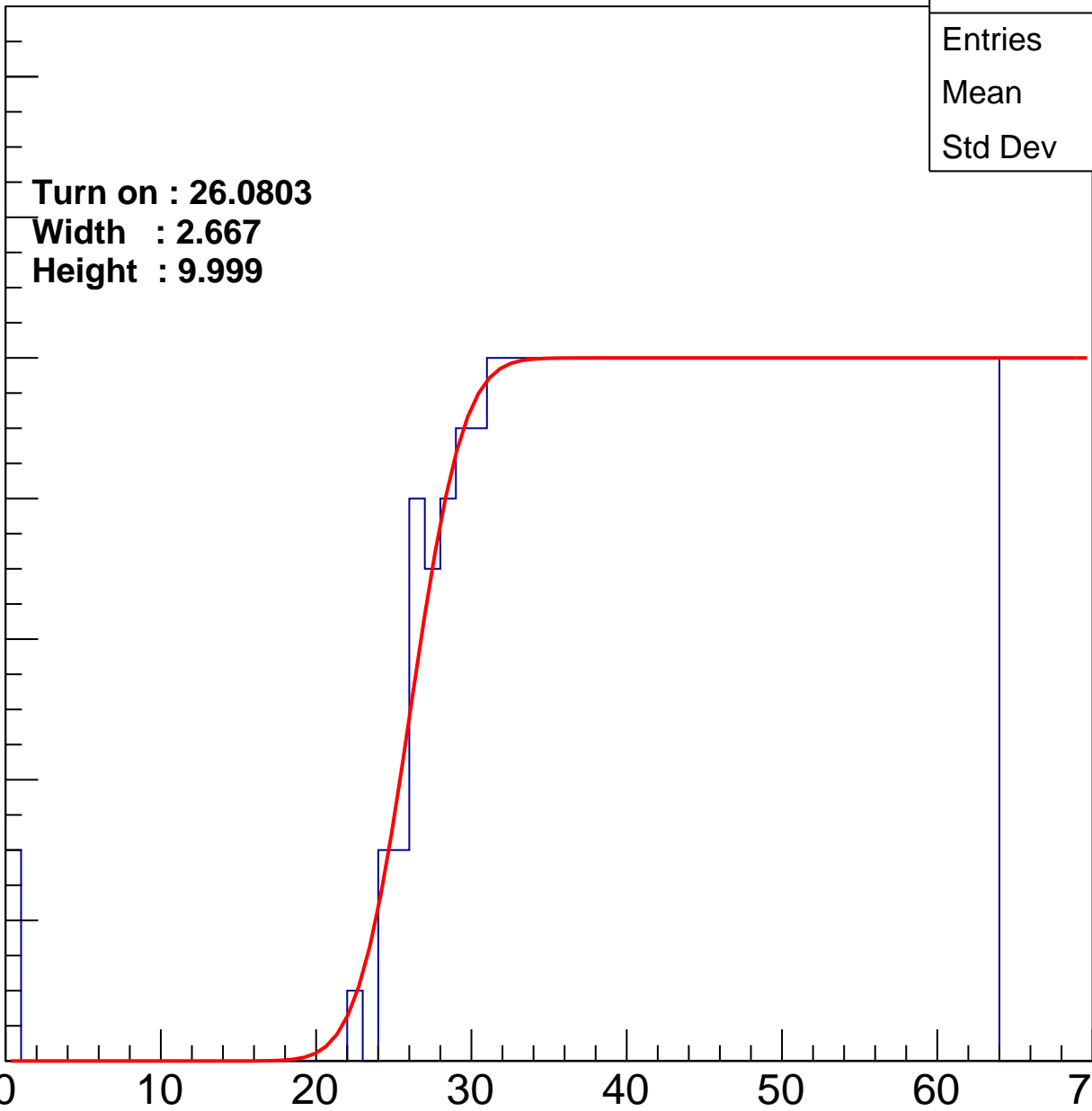
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0803
Width : 2.667
Height : 9.999

Entries	381
Mean	44.18
Std Dev	11.67

ampl



B0L102S, U6-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 25.7353

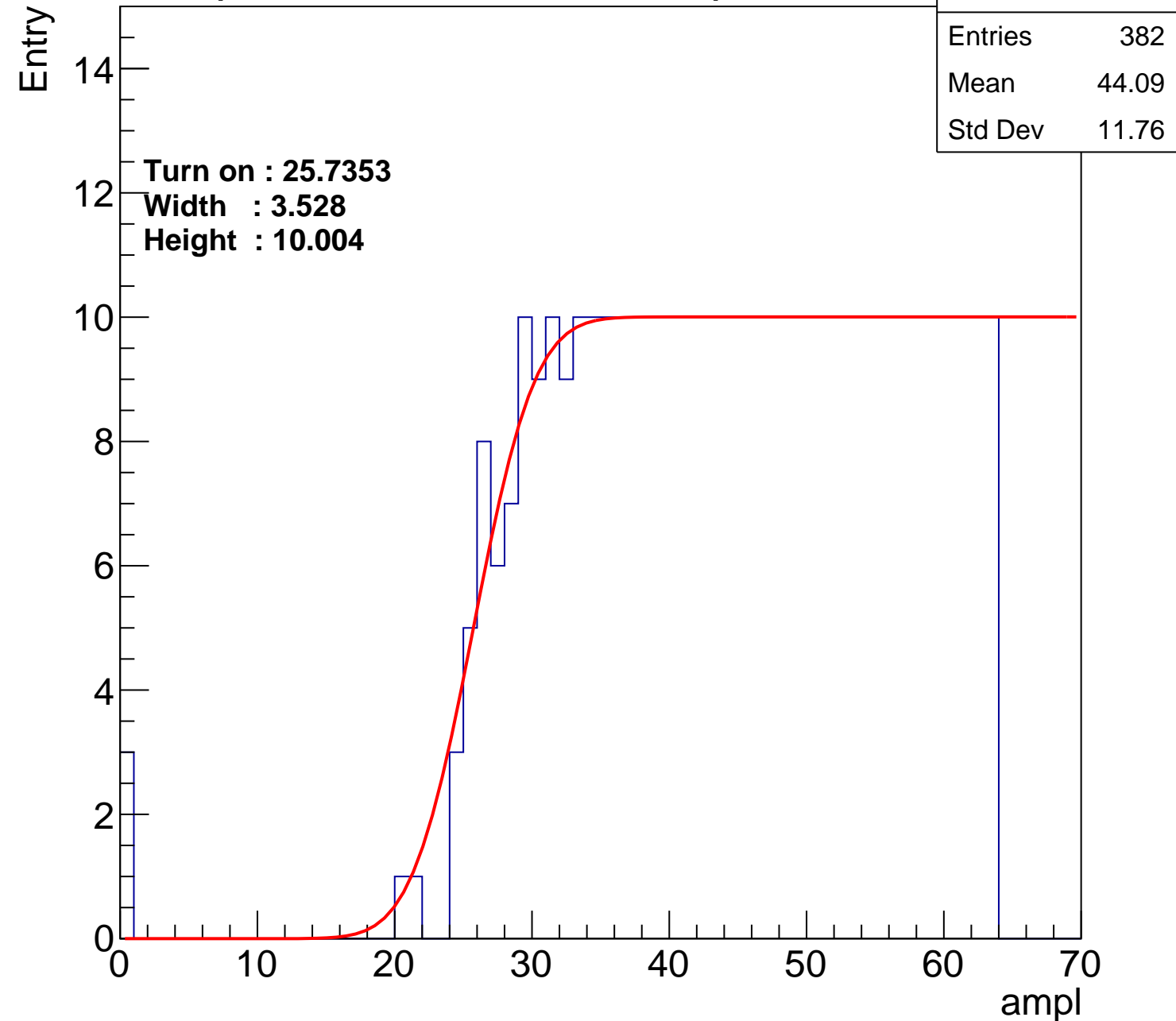
Width : 3.528

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 28.5033

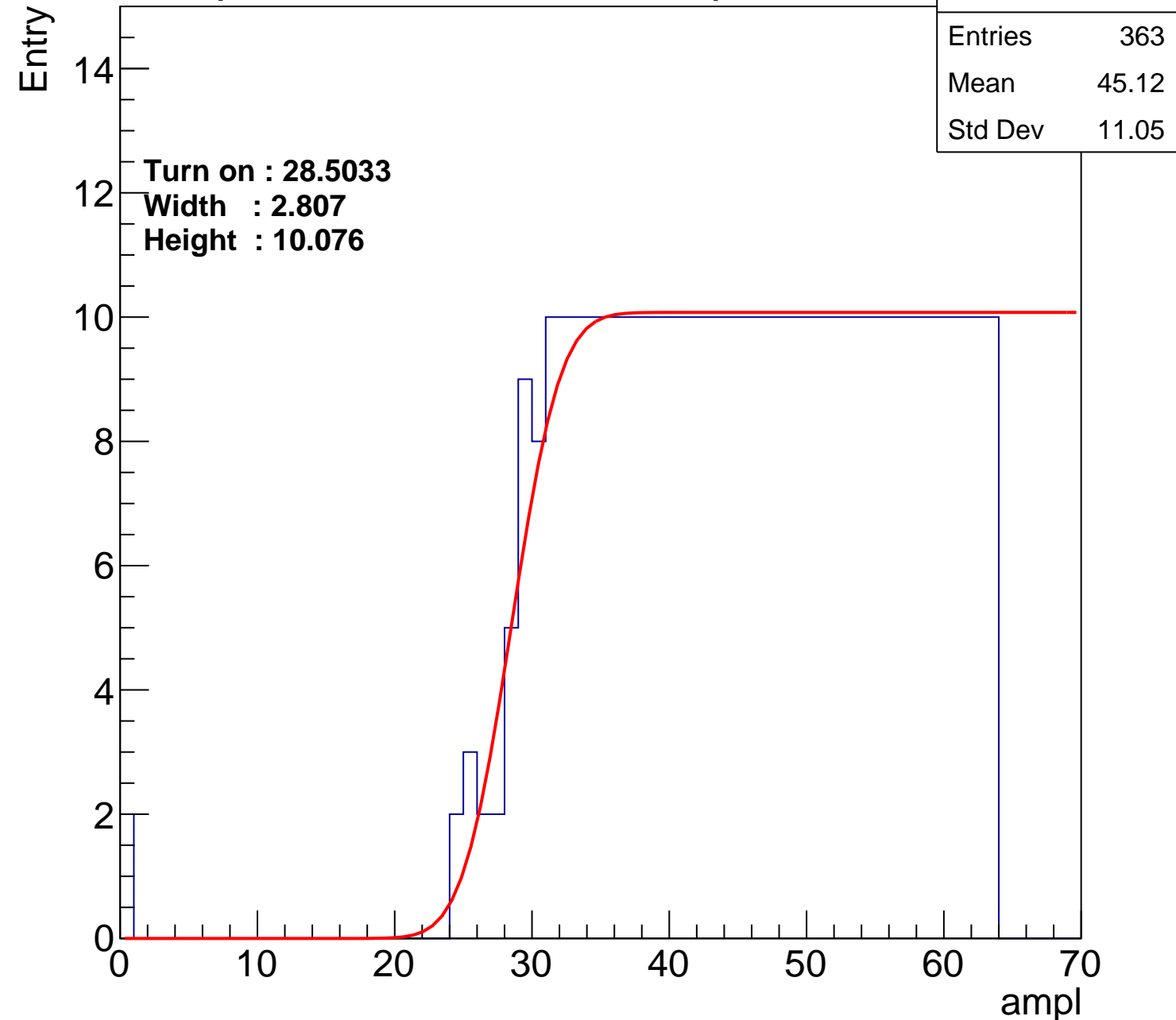
Width : 2.807

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.87

Turn on : 24.5586

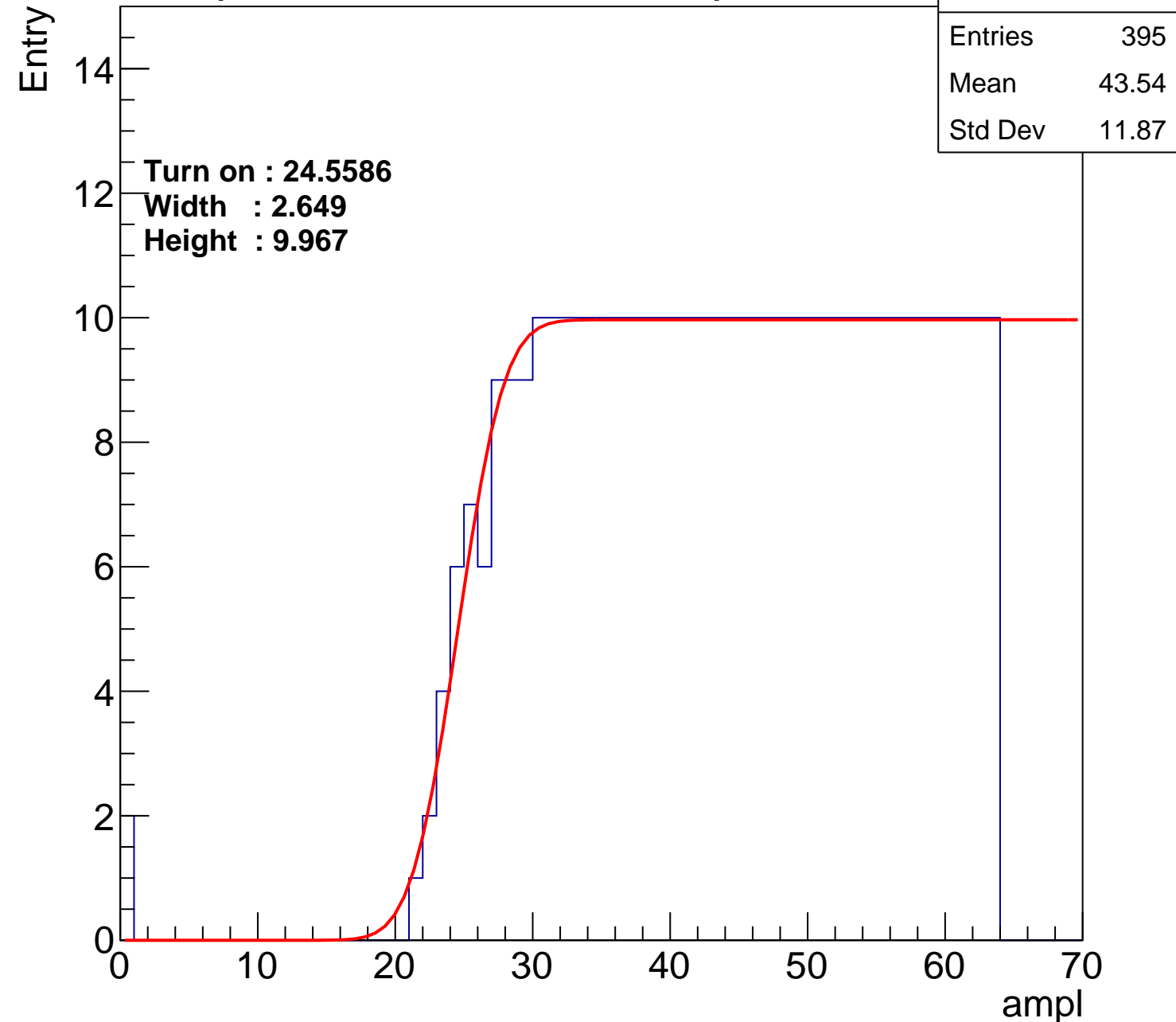
Width : 2.649

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.57
Std Dev	11.94

Turn on : 25.4454

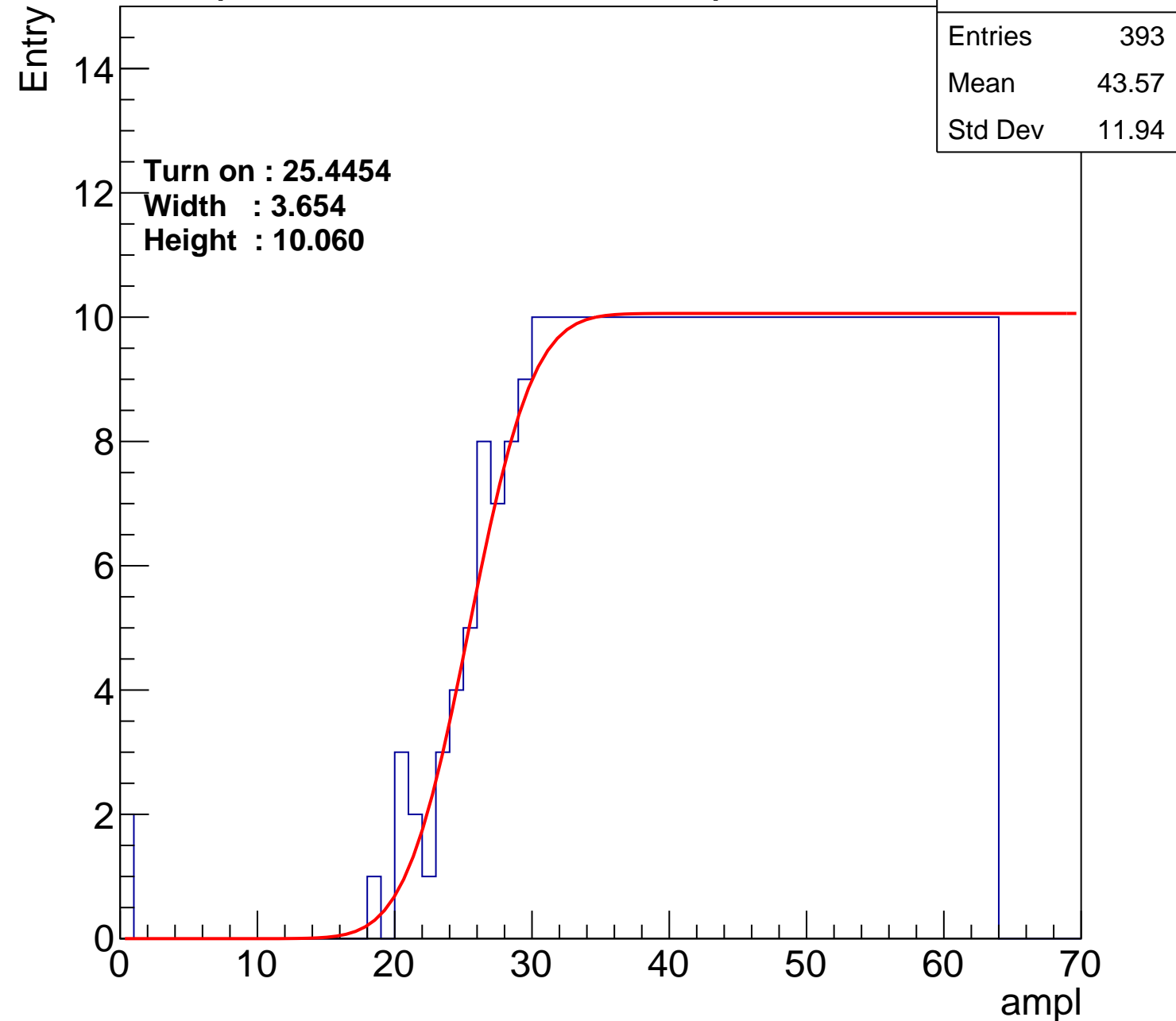
Width : 3.654

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.9
Std Dev	12.03

Turn on : 27.1678

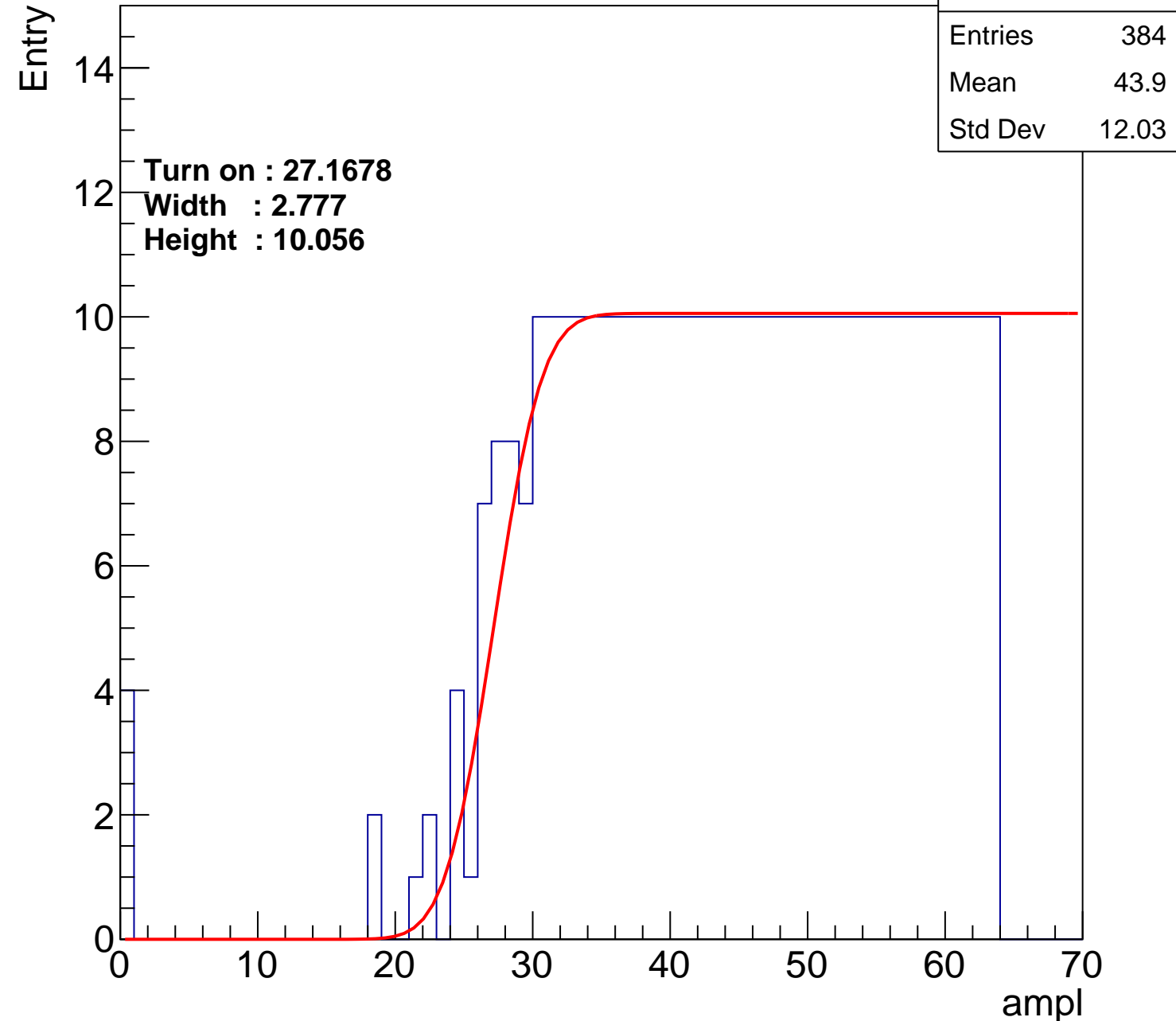
Width : 2.777

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 26.3103

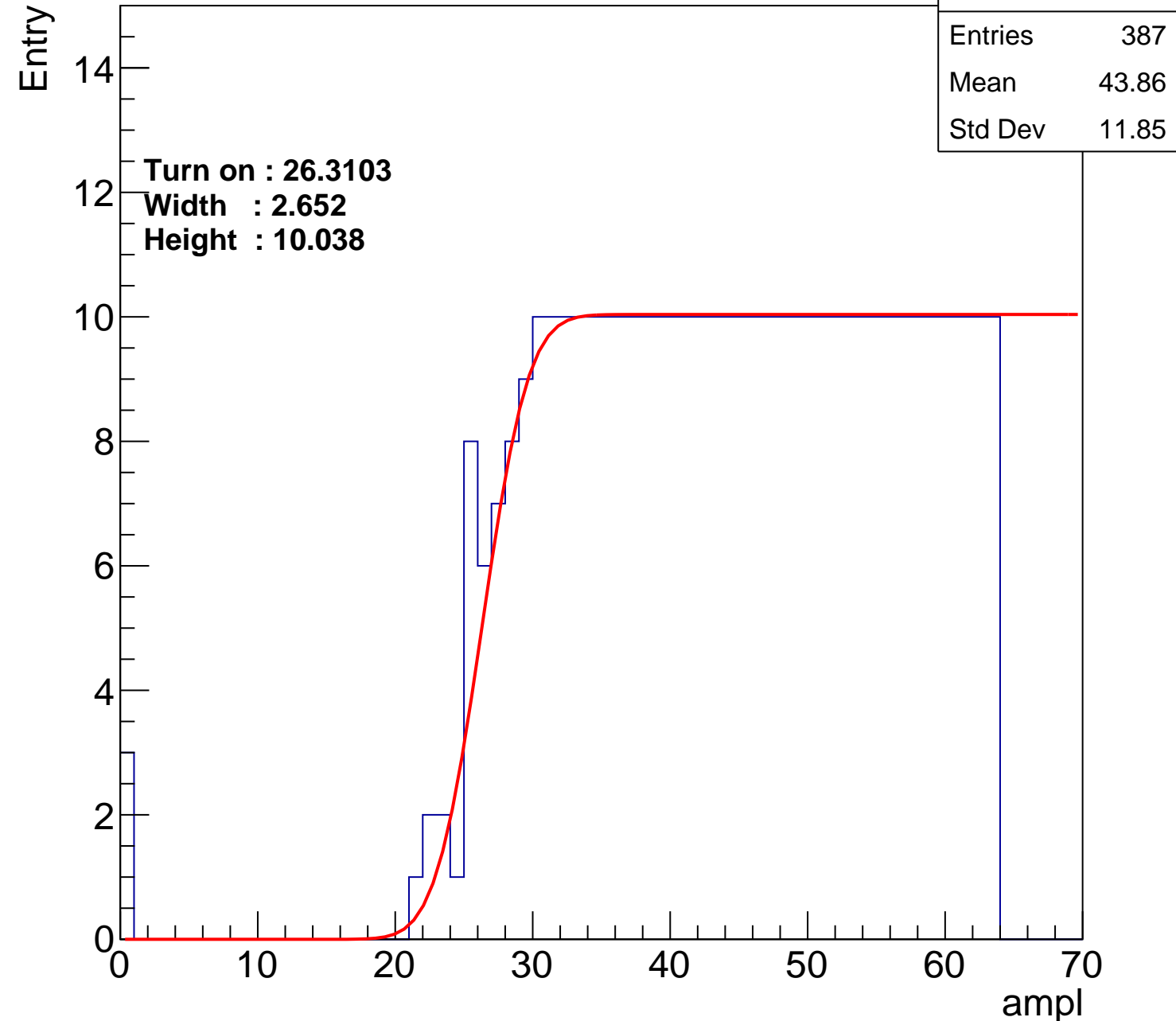
Width : 2.652

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.4050

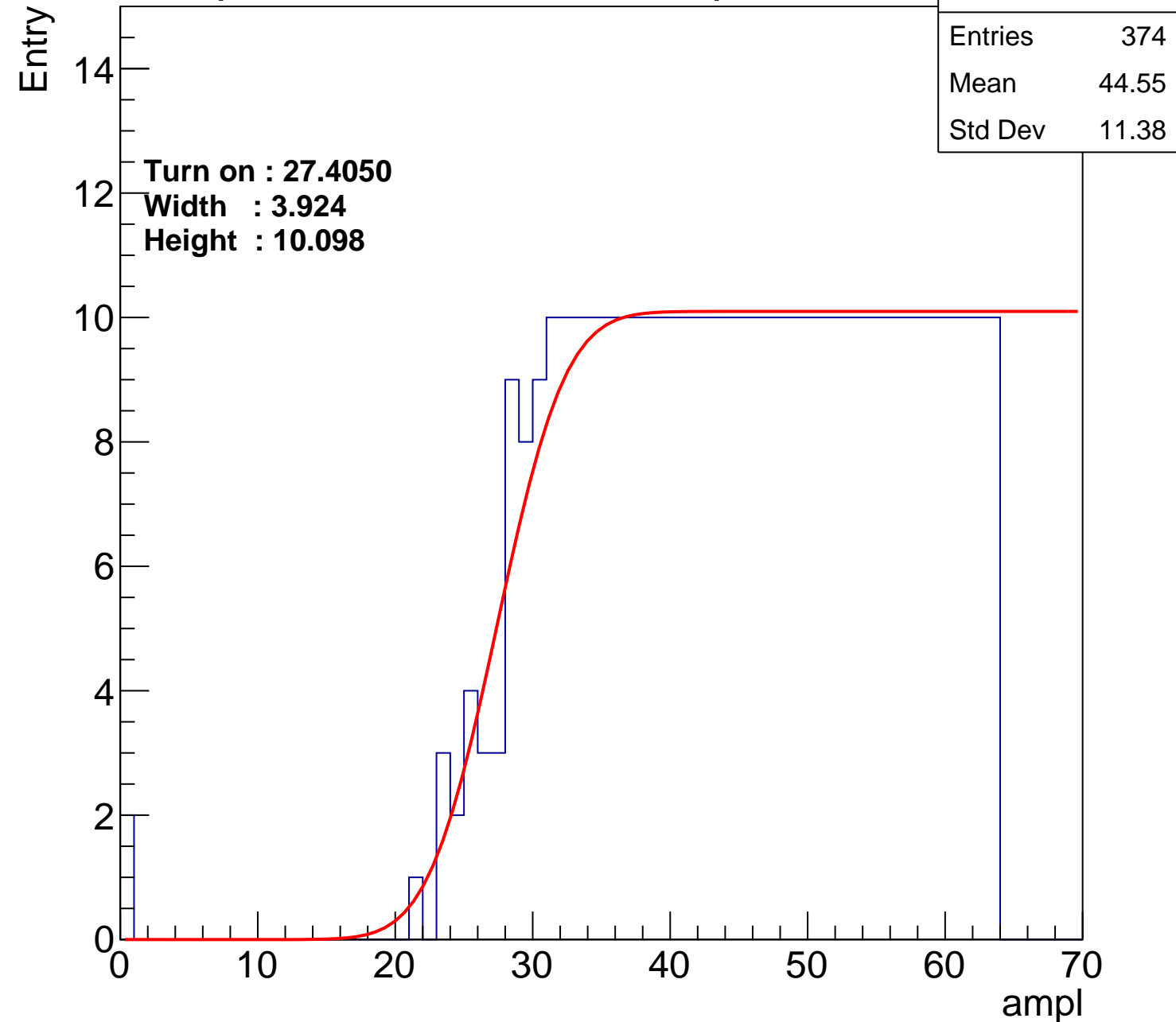
Width : 3.924

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.1542

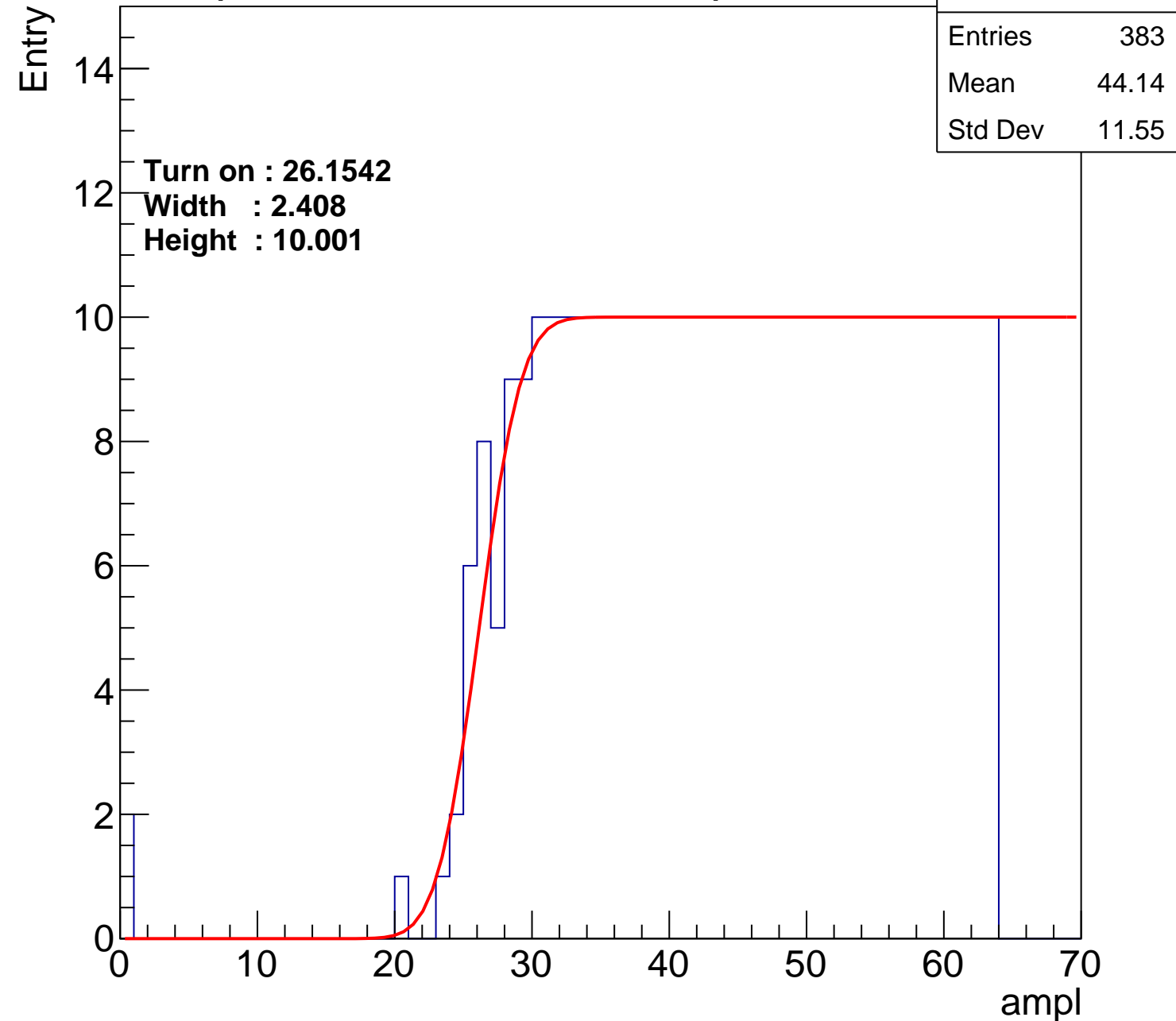
Width : 2.408

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 26.5042

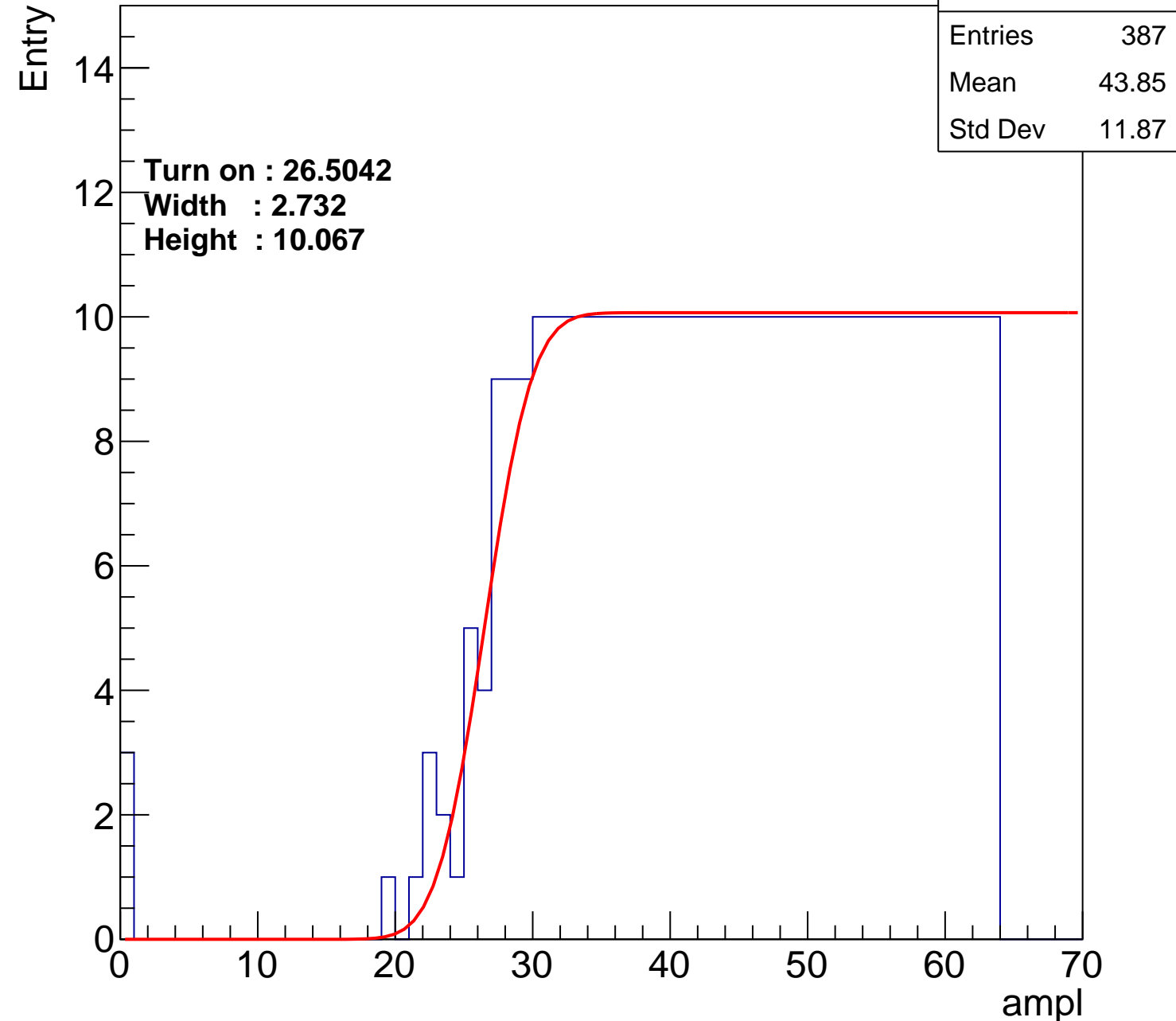
Width : 2.732

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch49

calib_packv5_042523_0143.root, FC#12, port B1

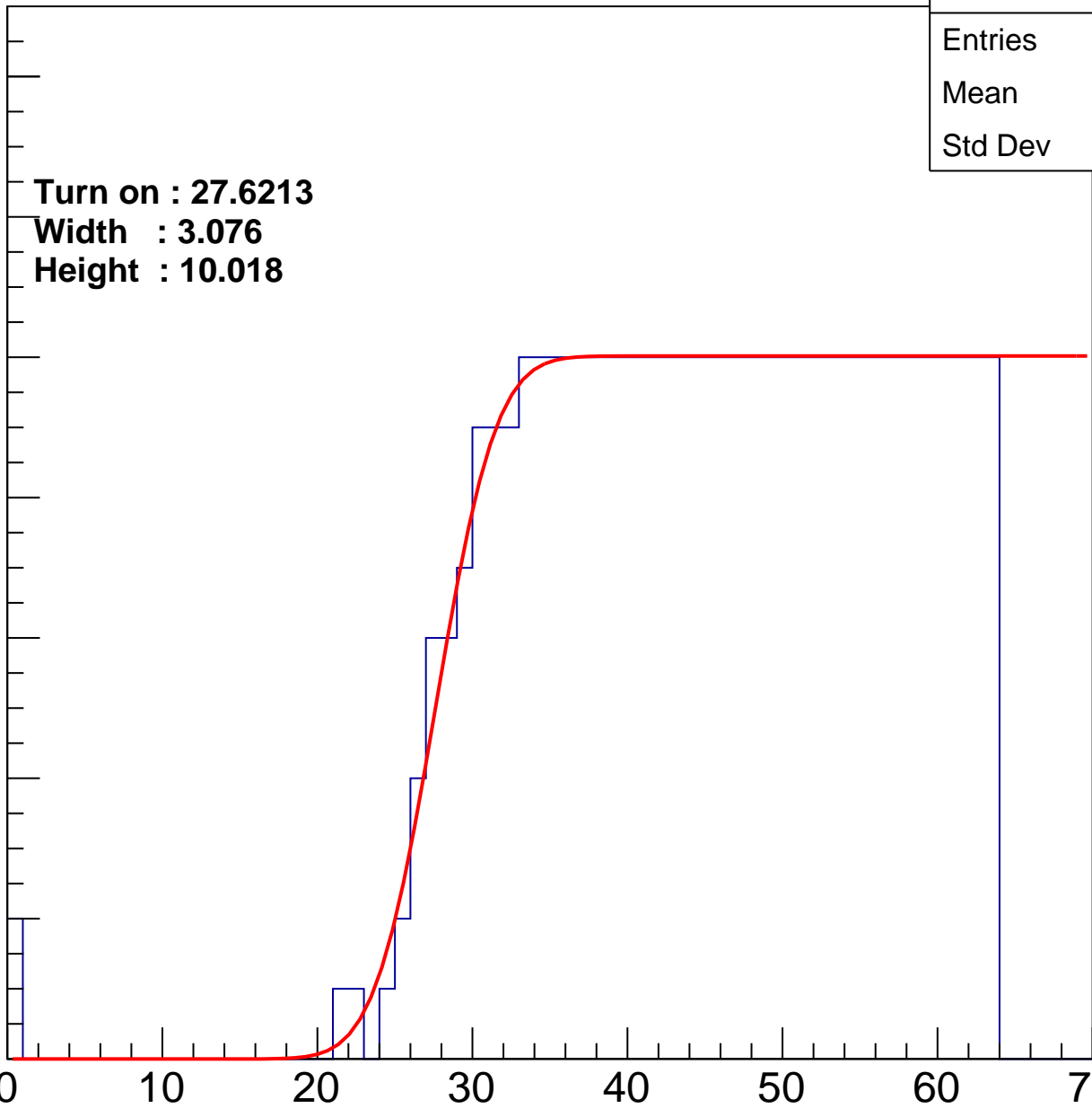
Entry

14
12
10
8
6
4
2
0

Turn on : 27.6213
Width : 3.076
Height : 10.018

Entries	367
Mean	44.88
Std Dev	11.22

ampl



B0L102S, U6-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.19
Std Dev	11.59

Turn on : 26.5978

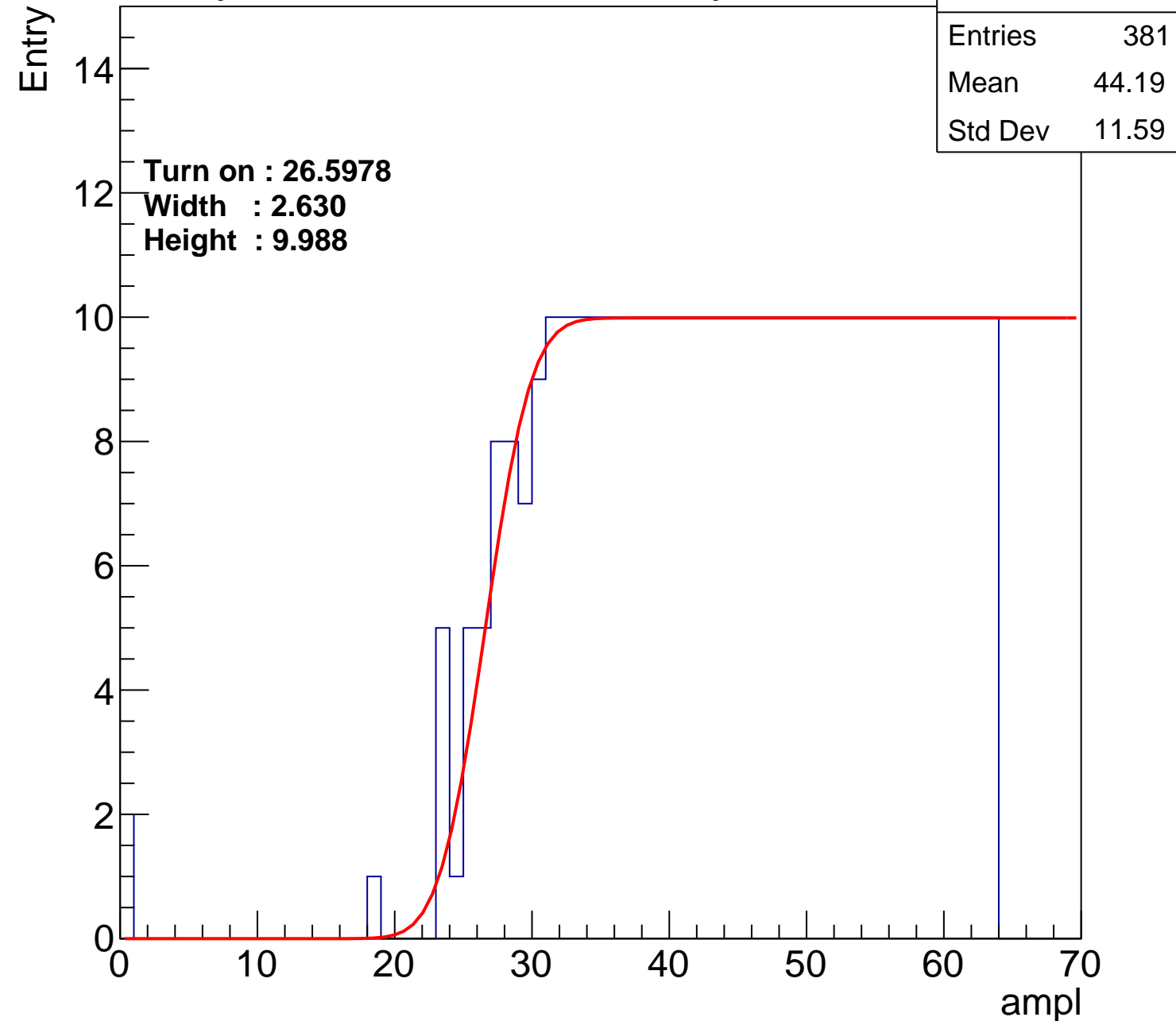
Width : 2.630

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.99
Std Dev	11.88

Turn on : 26.6510

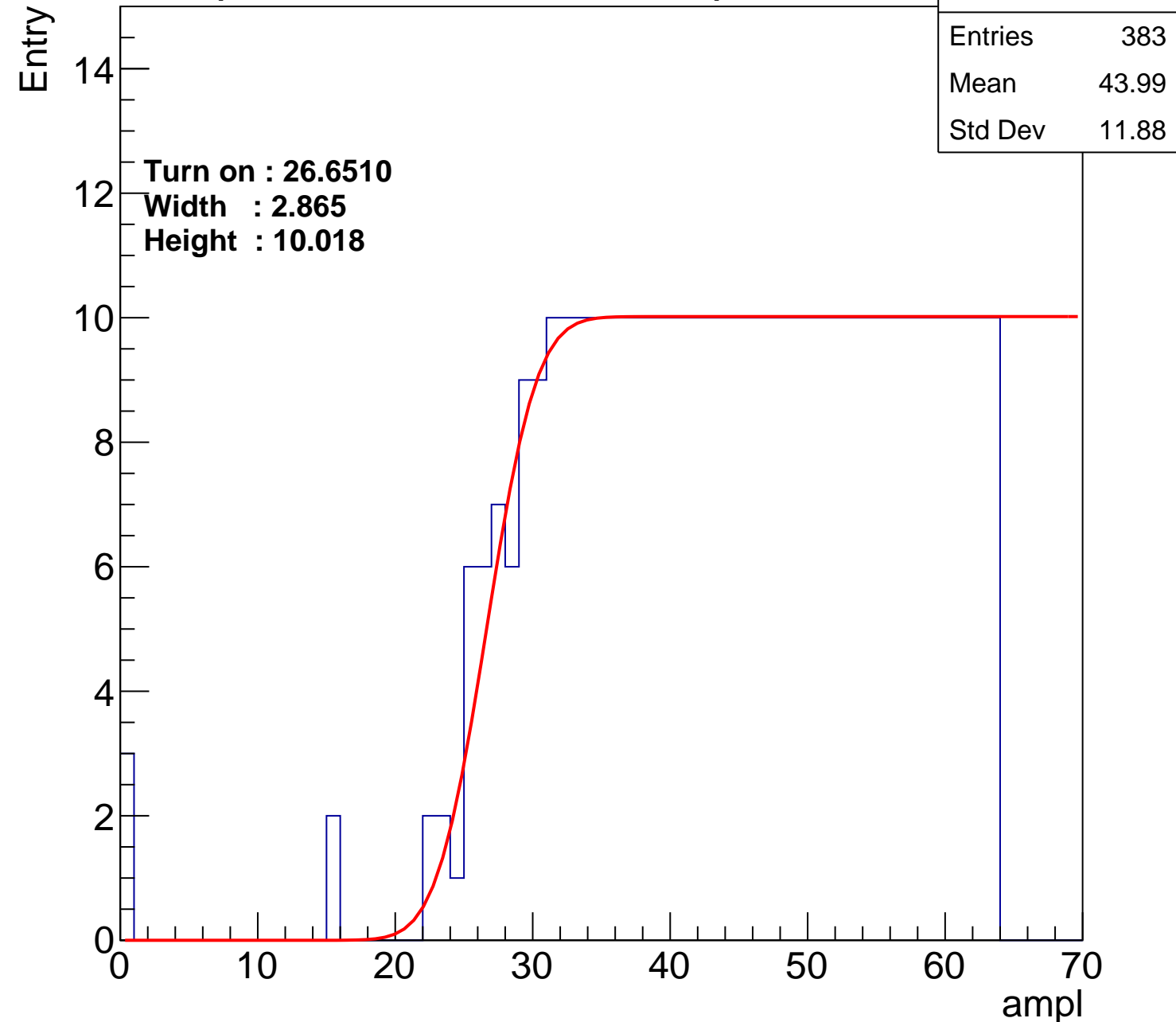
Width : 2.865

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.16
Std Dev	12.12

Turn on : 24.2535

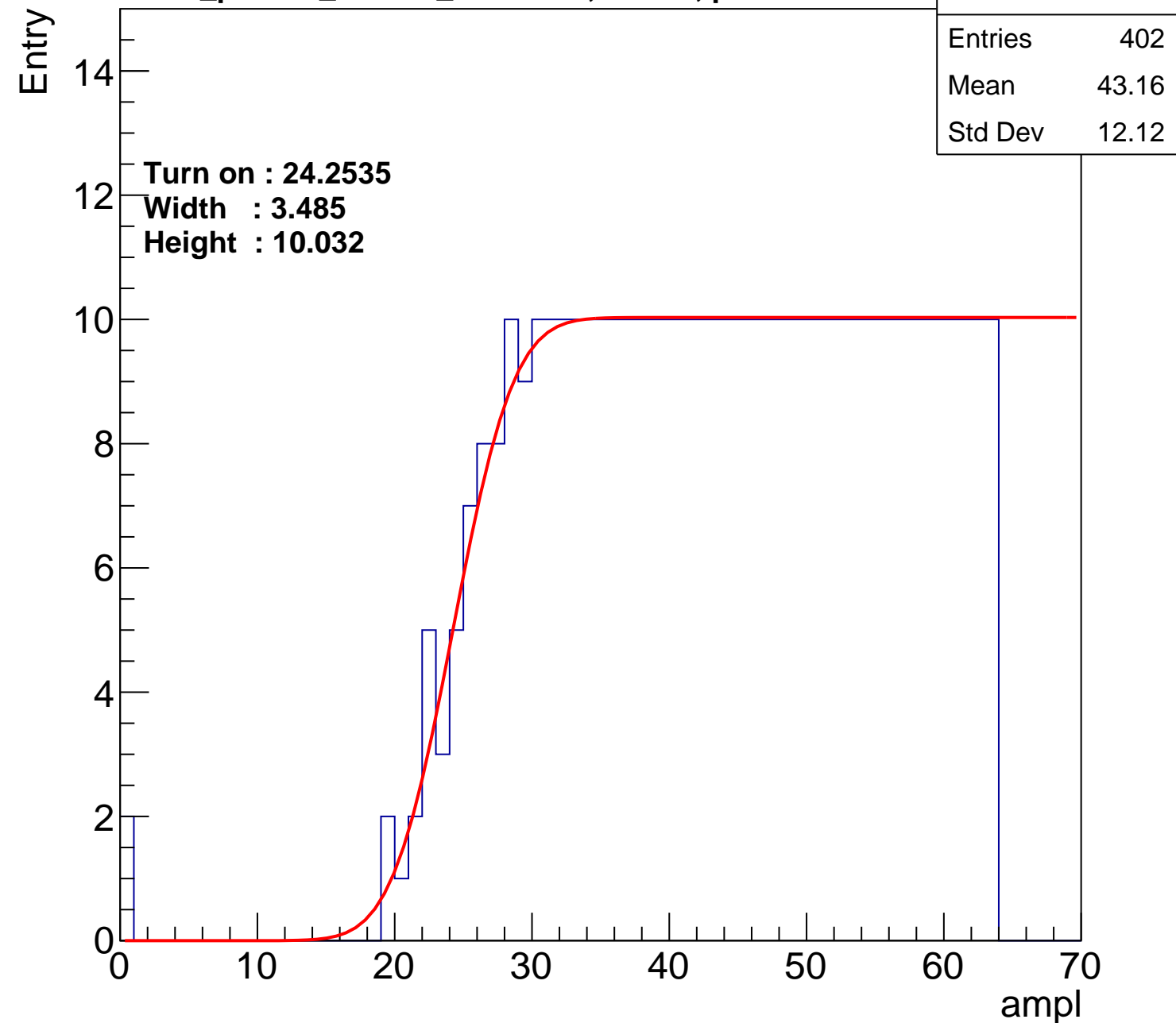
Width : 3.485

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch53

calib_packv5_042523_0143.root, FC#12, port B1

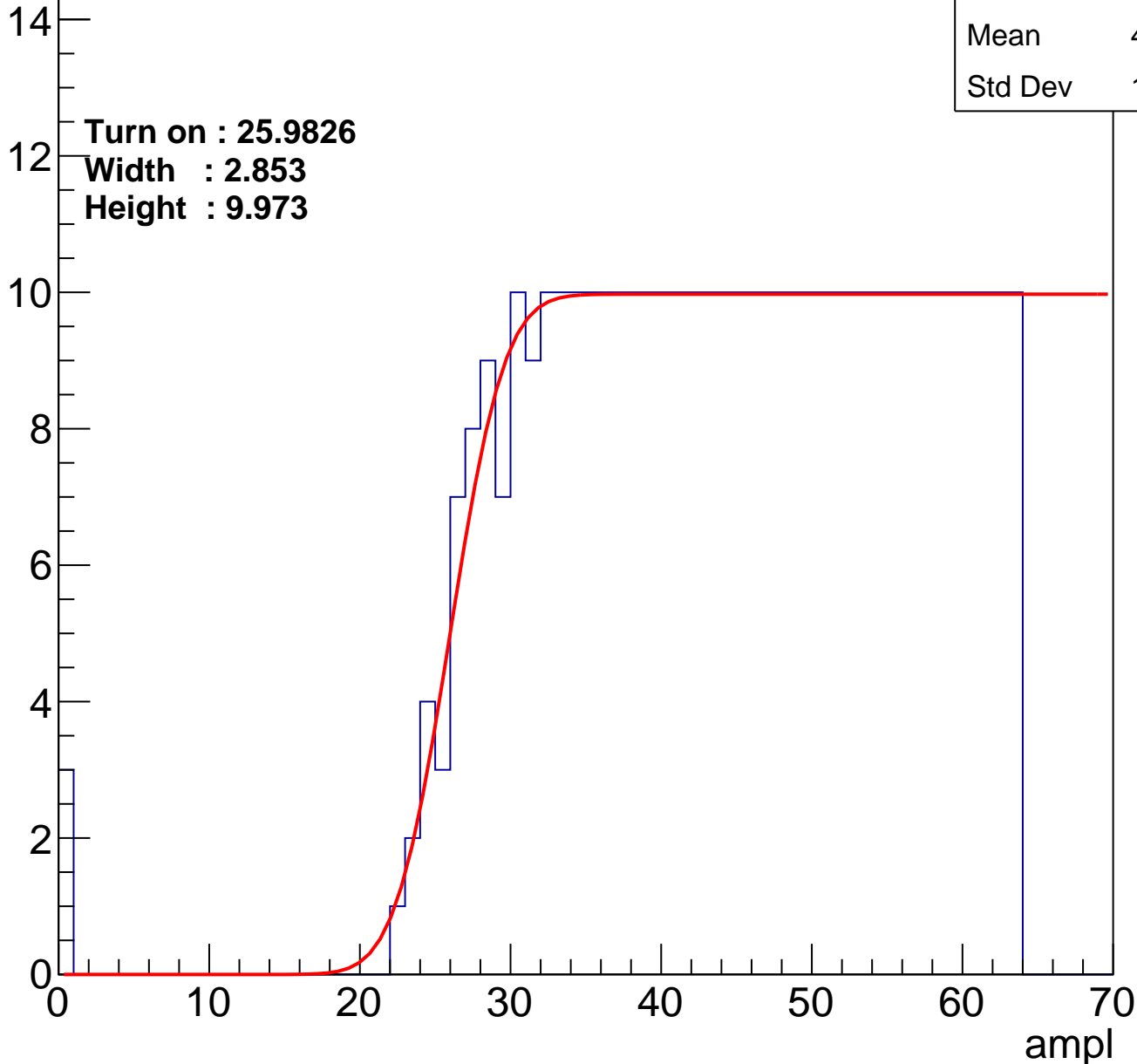
Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 25.9826

Width : 2.853

Height : 9.973

Entry



B0L102S, U6-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.77
Std Dev	12.63

Turn on : 23.9678

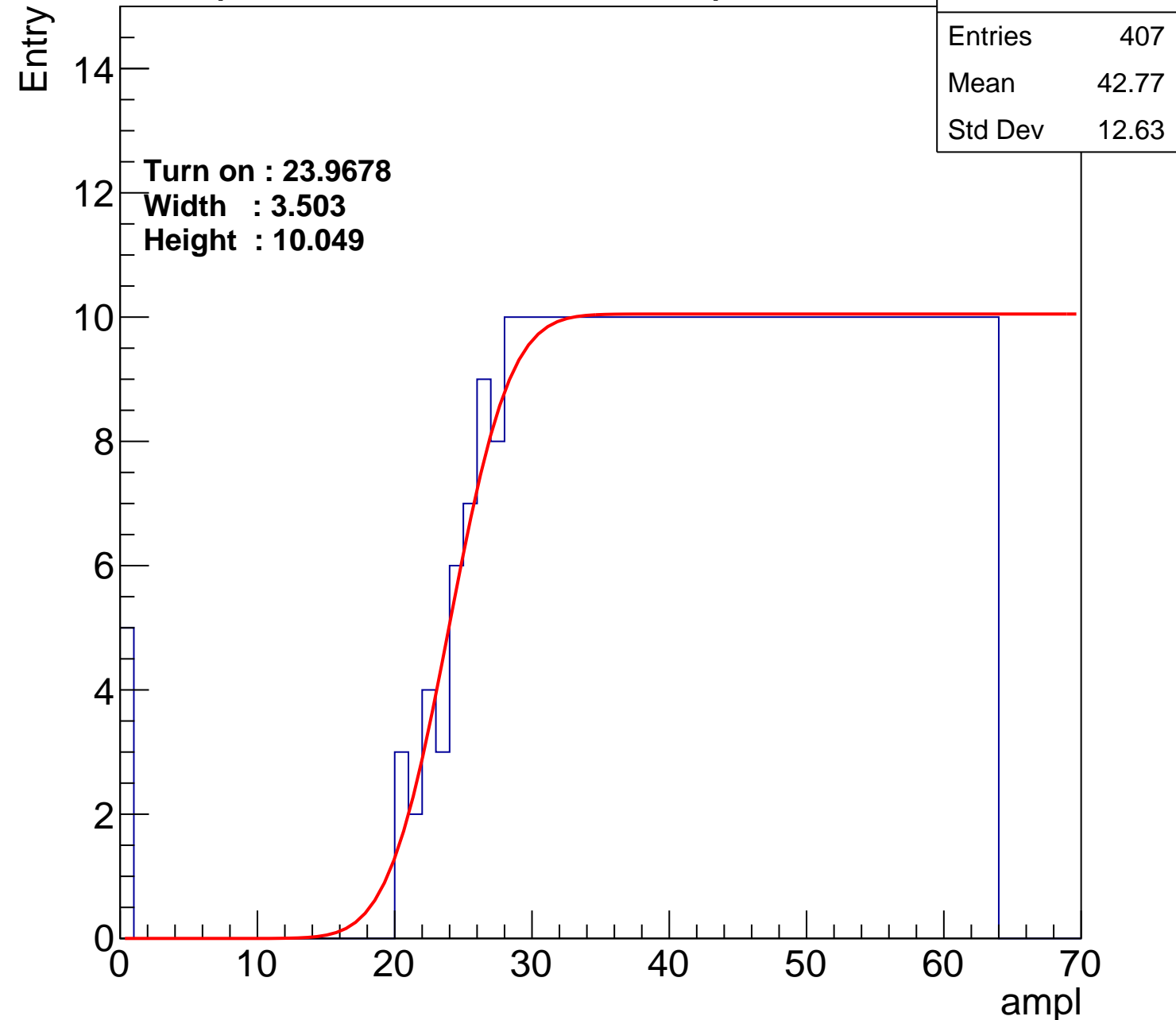
Width : 3.503

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.03
Std Dev	12.04

Turn on : 26.7745

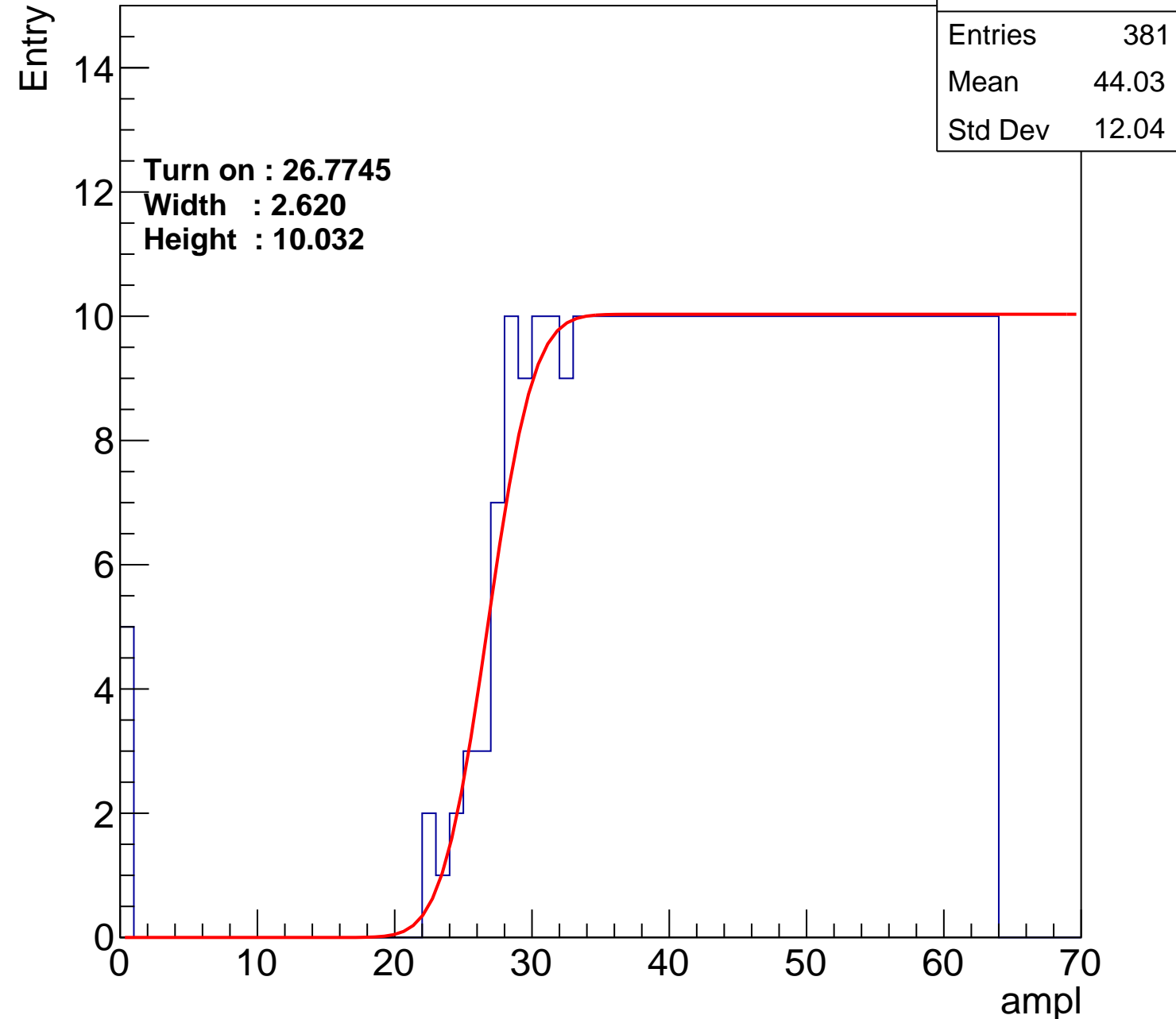
Width : 2.620

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 27.3876

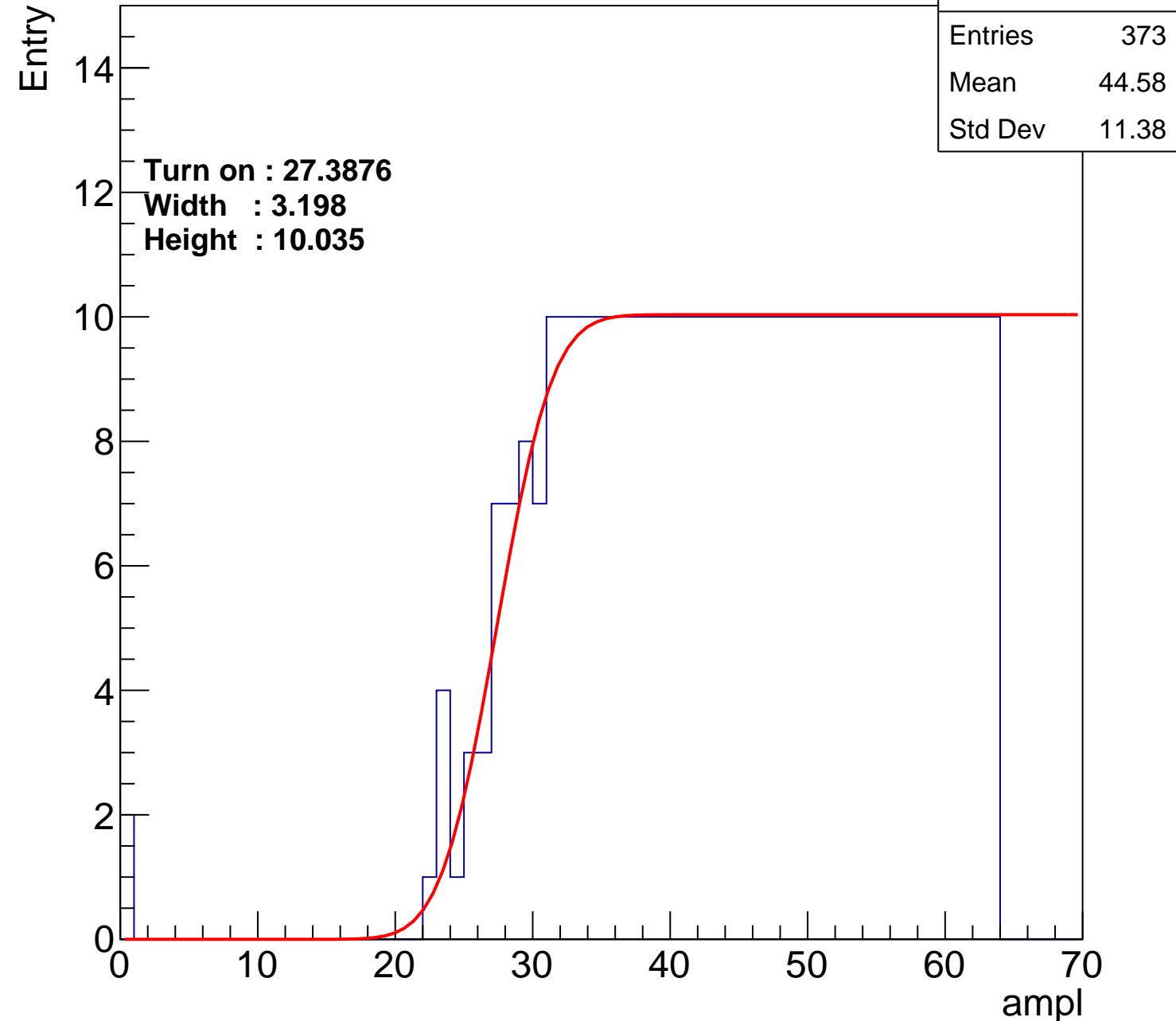
Width : 3.198

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.61
Std Dev	11.89

Turn on : 26.2197

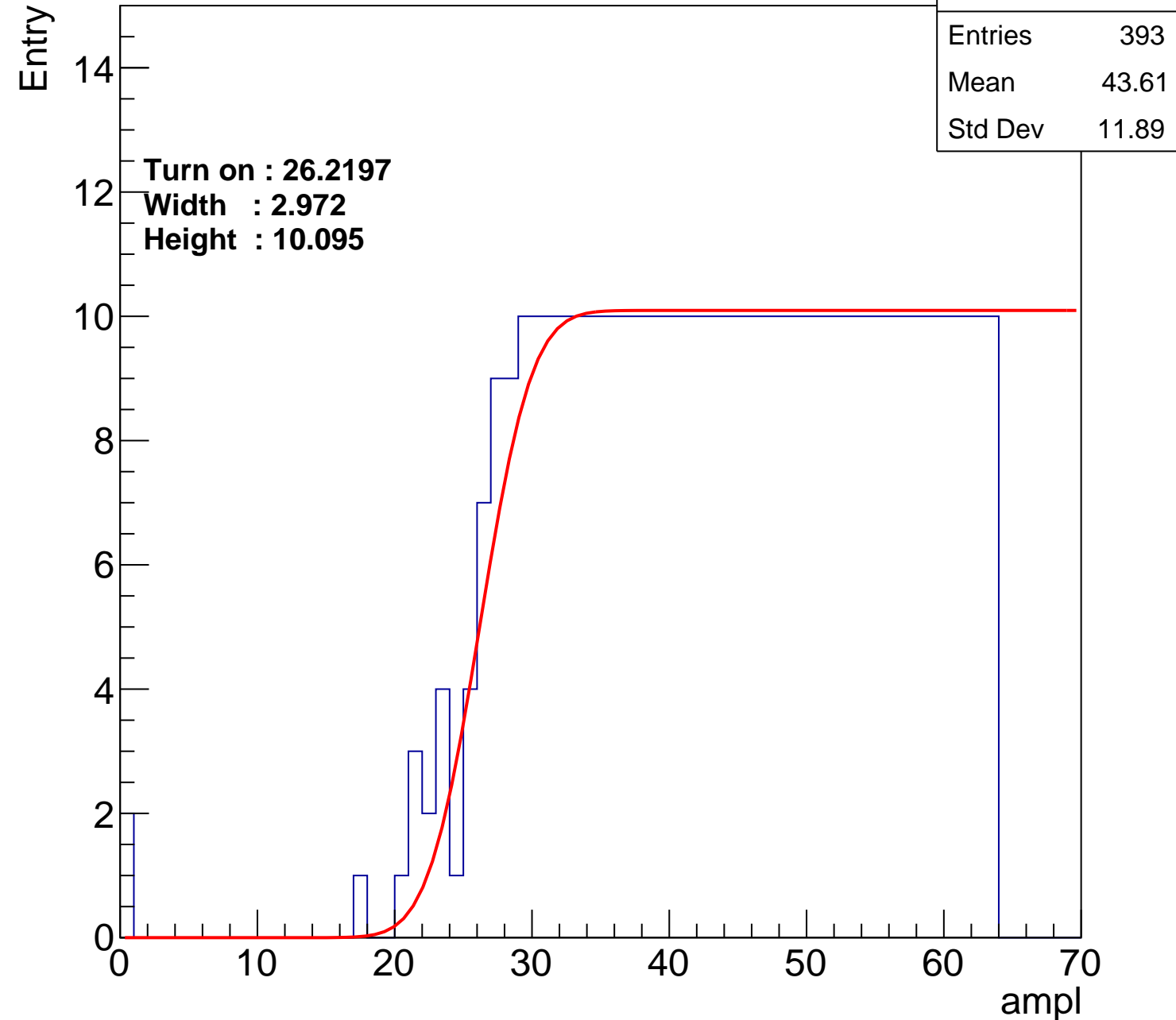
Width : 2.972

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.73
Std Dev	11.91

Turn on : 25.4180

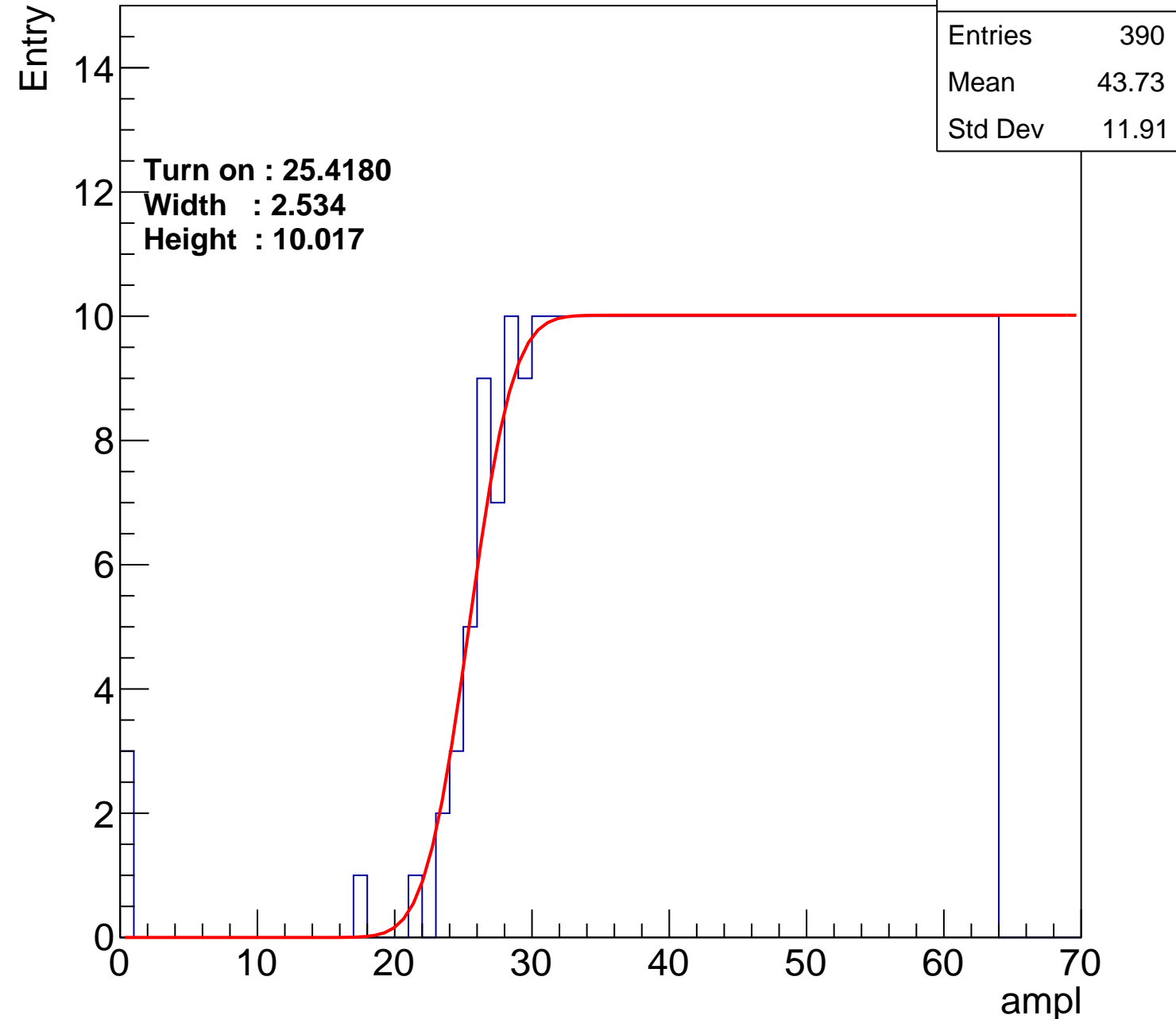
Width : 2.534

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch59

calib_packv5_042523_0143.root, FC#12, port B1

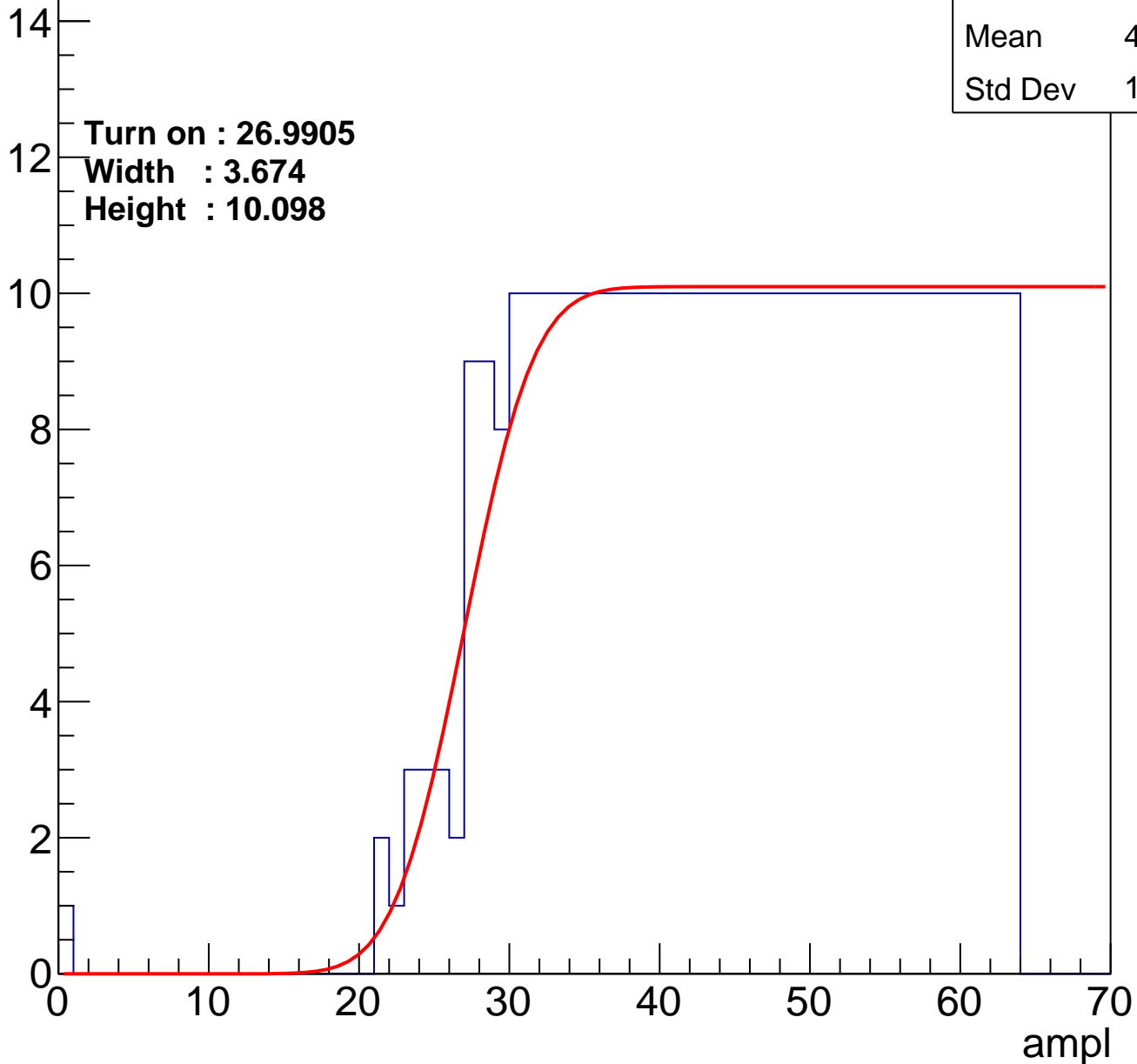
Entries	381
Mean	44.28
Std Dev	11.37

Turn on : 26.9905

Width : 3.674

Height : 10.098

Entry



B0L102S, U6-ch60

calib_packv5_042523_0143.root, FC#12, port B1

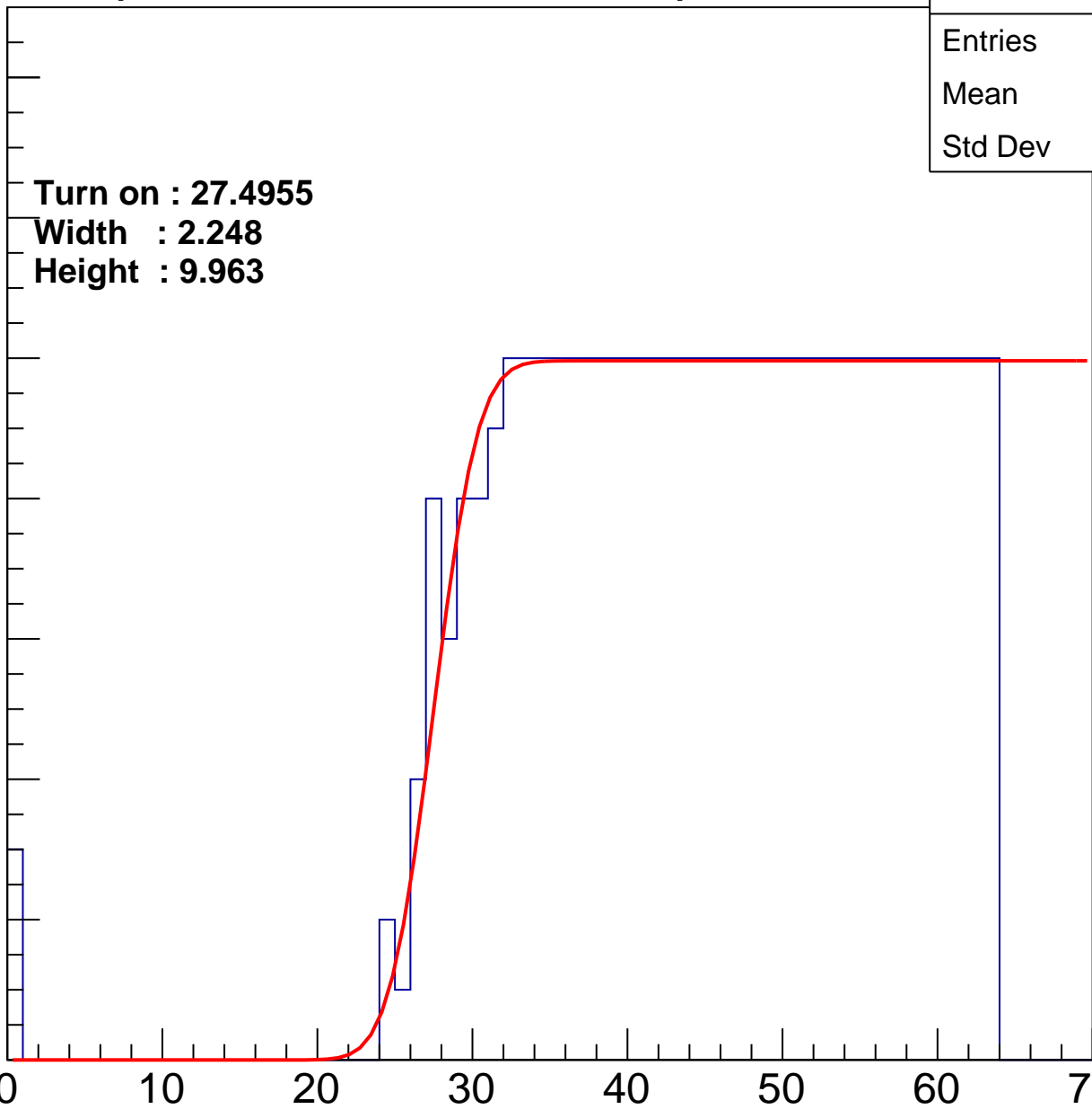
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4955
Width : 2.248
Height : 9.963

Entries	369
Mean	44.75
Std Dev	11.4

ampl



B0L102S, U6-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	357
Mean	45.39
Std Dev	10.94

Turn on : 28.5258

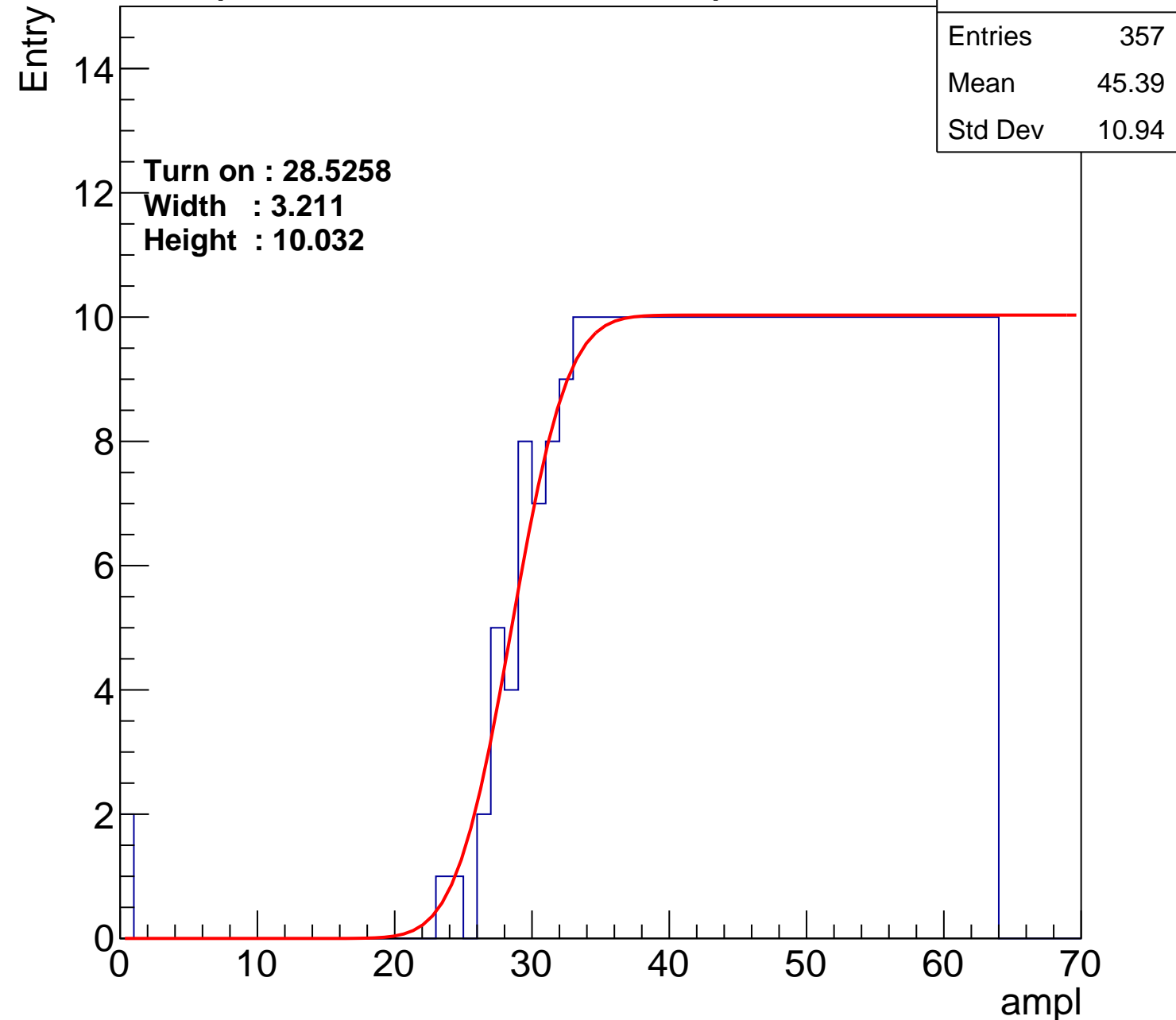
Width : 3.211

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.07
Std Dev	11.91

Turn on : 26.2282

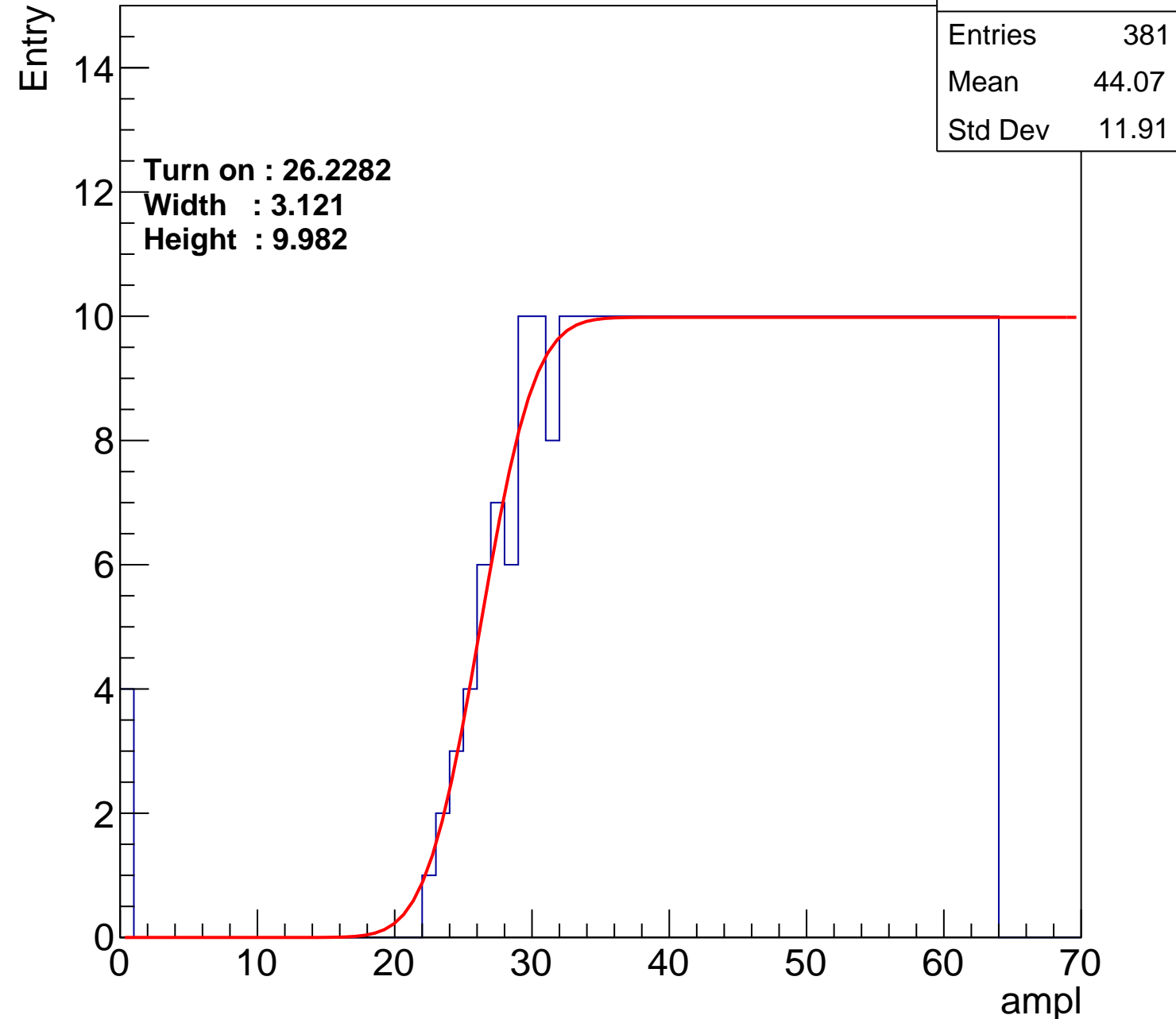
Width : 3.121

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.43
Std Dev	11.72

Turn on : 26.9173

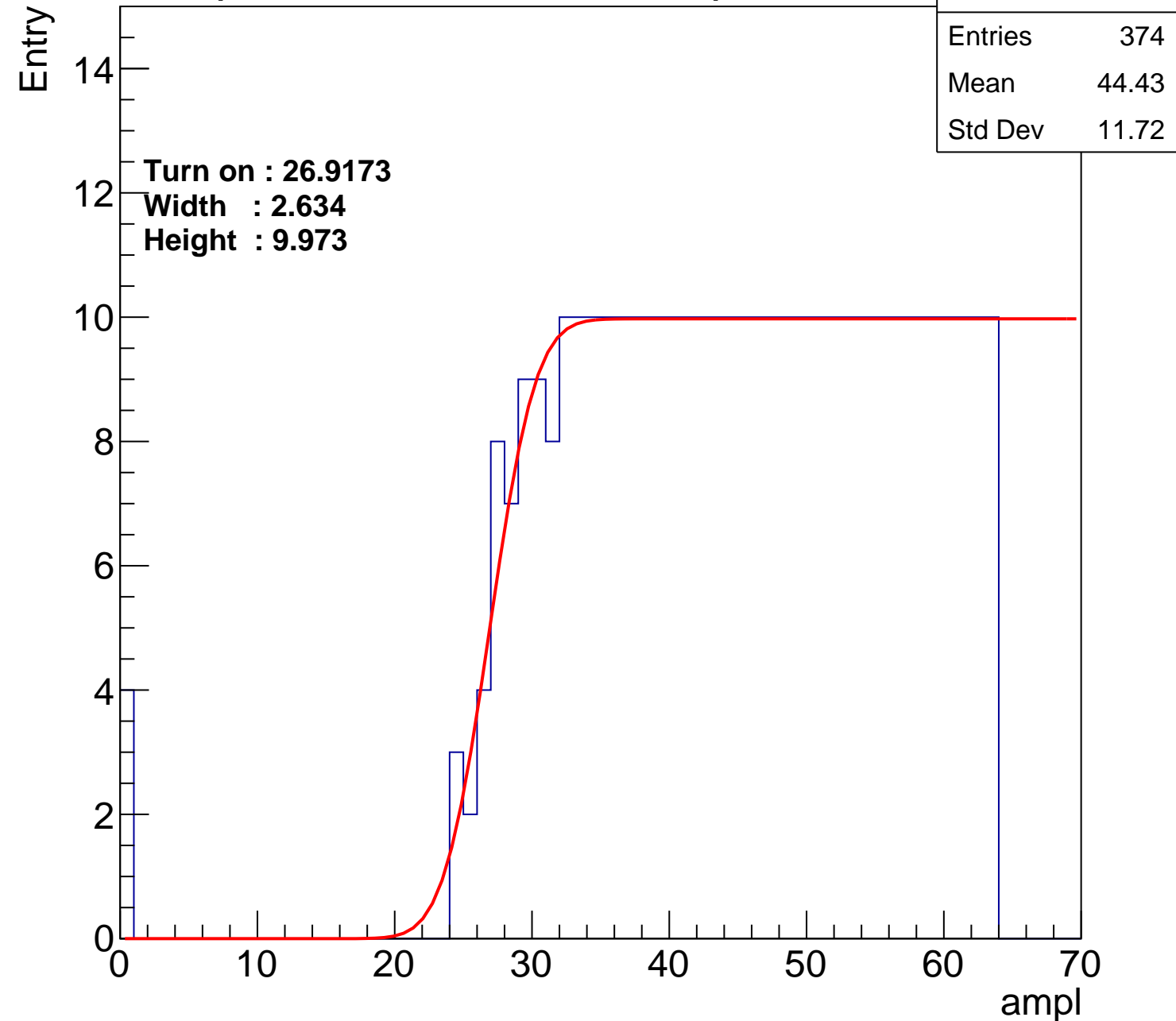
Width : 2.634

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.22
Std Dev	11.45

Turn on : 25.9274

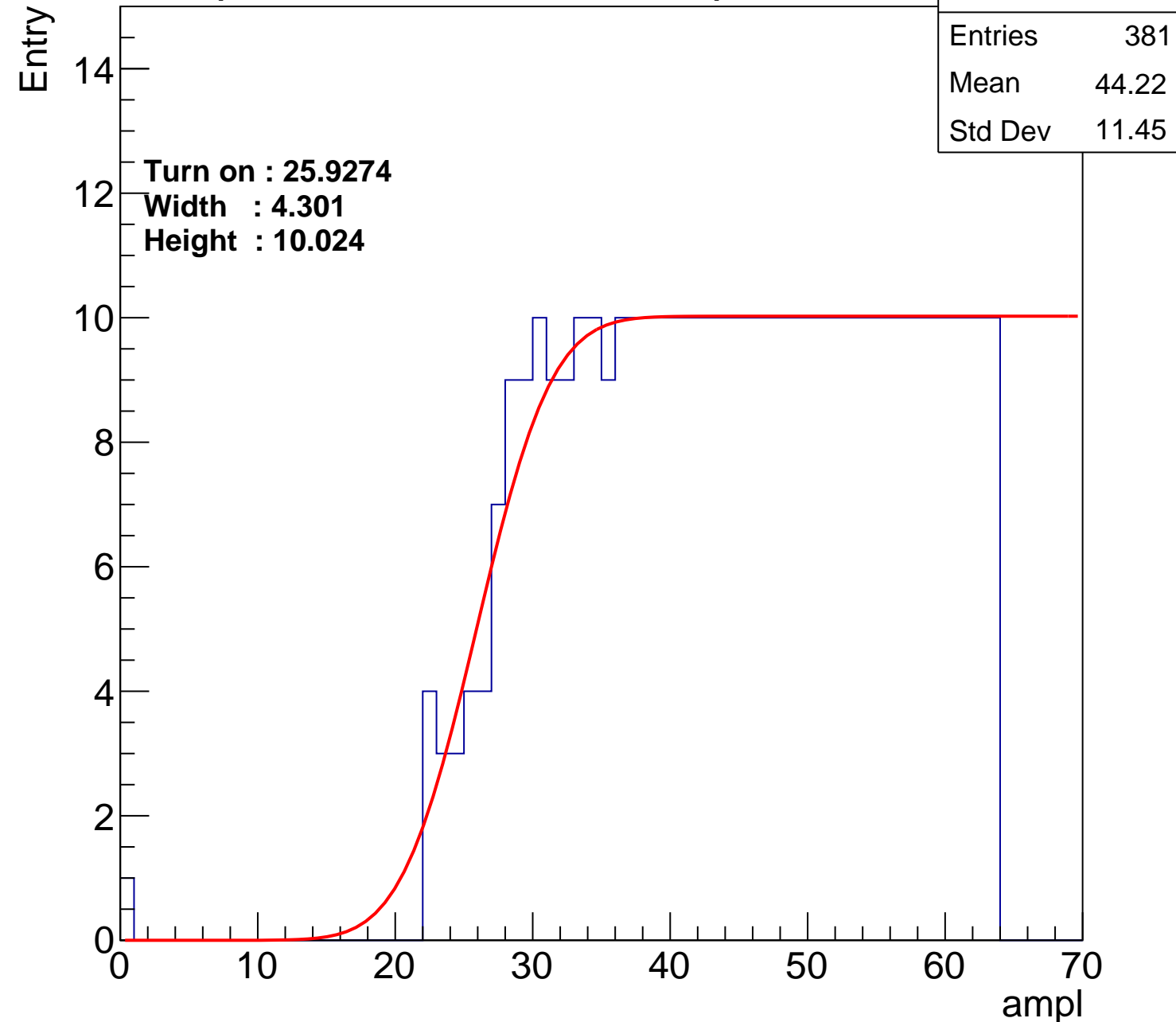
Width : 4.301

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 26.8755

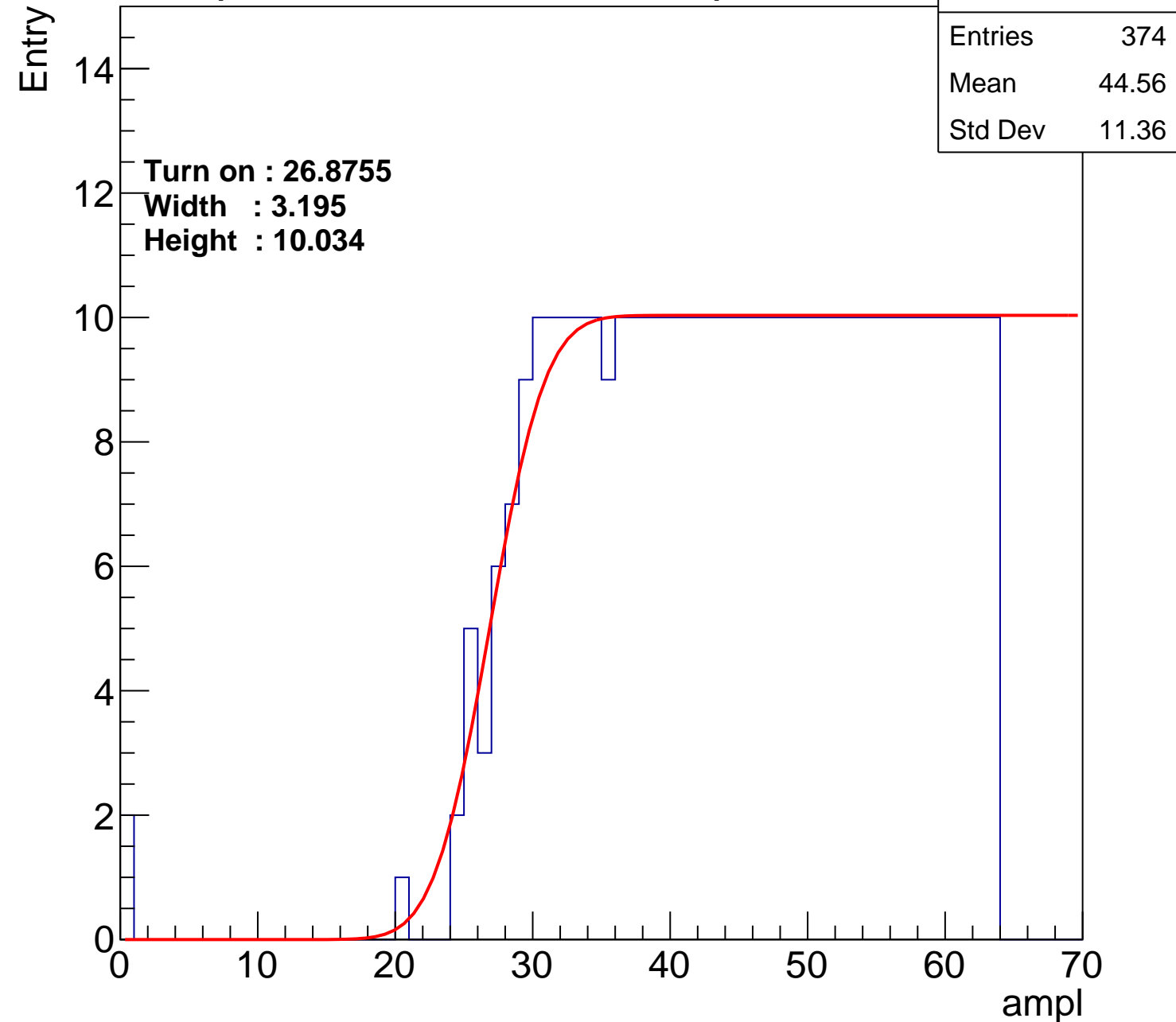
Width : 3.195

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch66

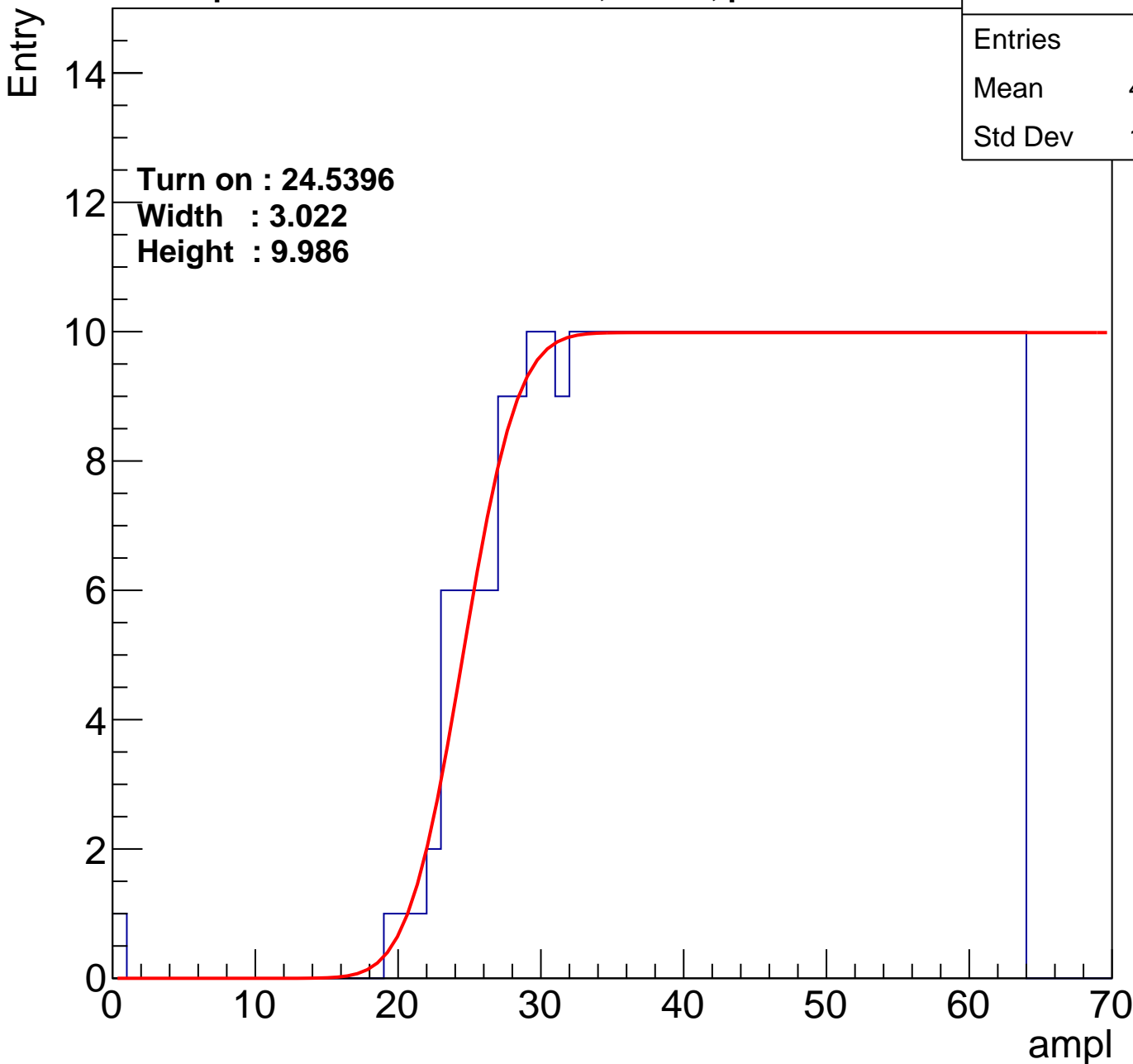
calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.47
Std Dev	11.83

Turn on : 24.5396

Width : 3.022

Height : 9.986



B0L102S, U6-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.5046

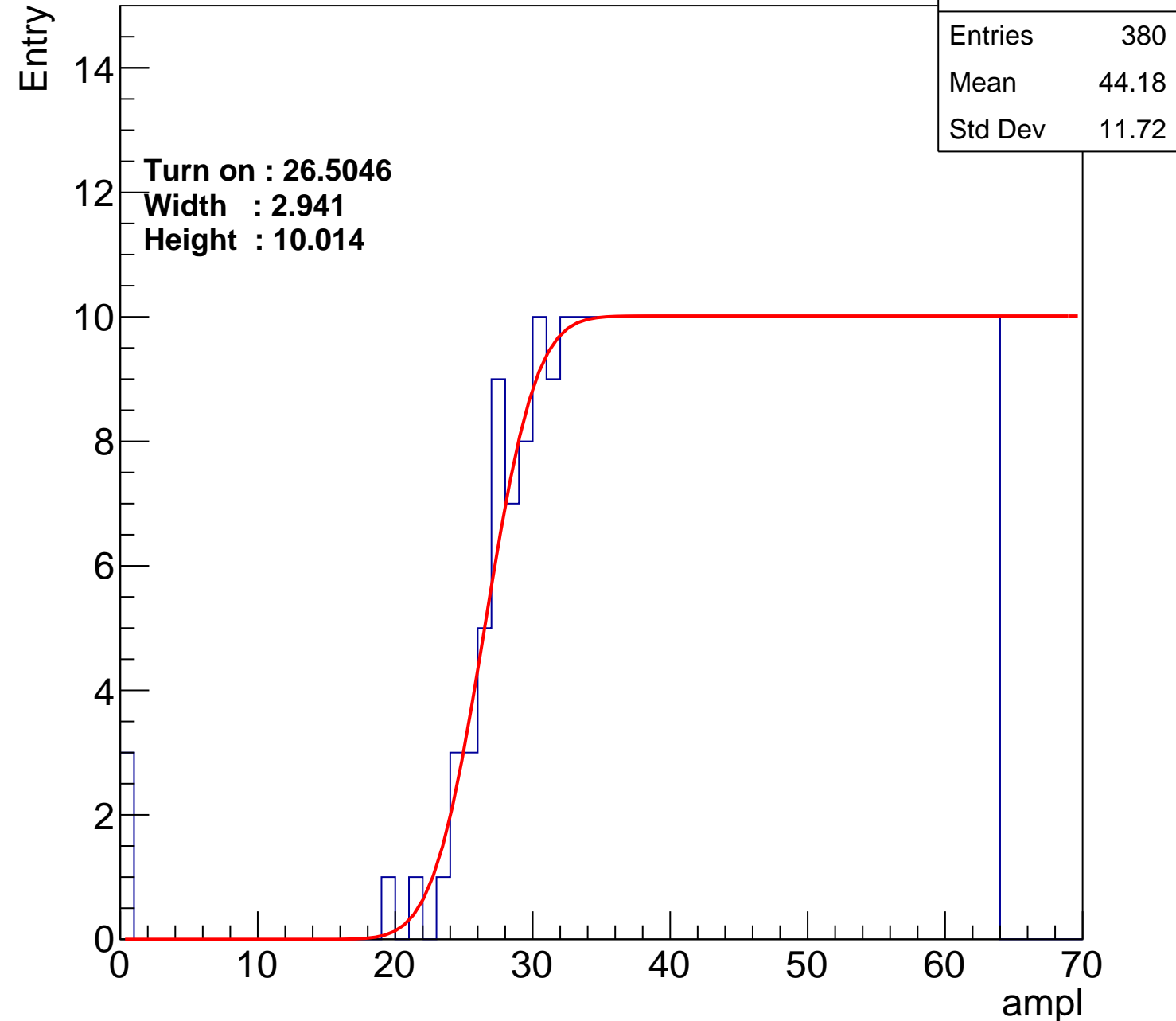
Width : 2.941

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.23
Std Dev	11.69

Turn on : 26.6667

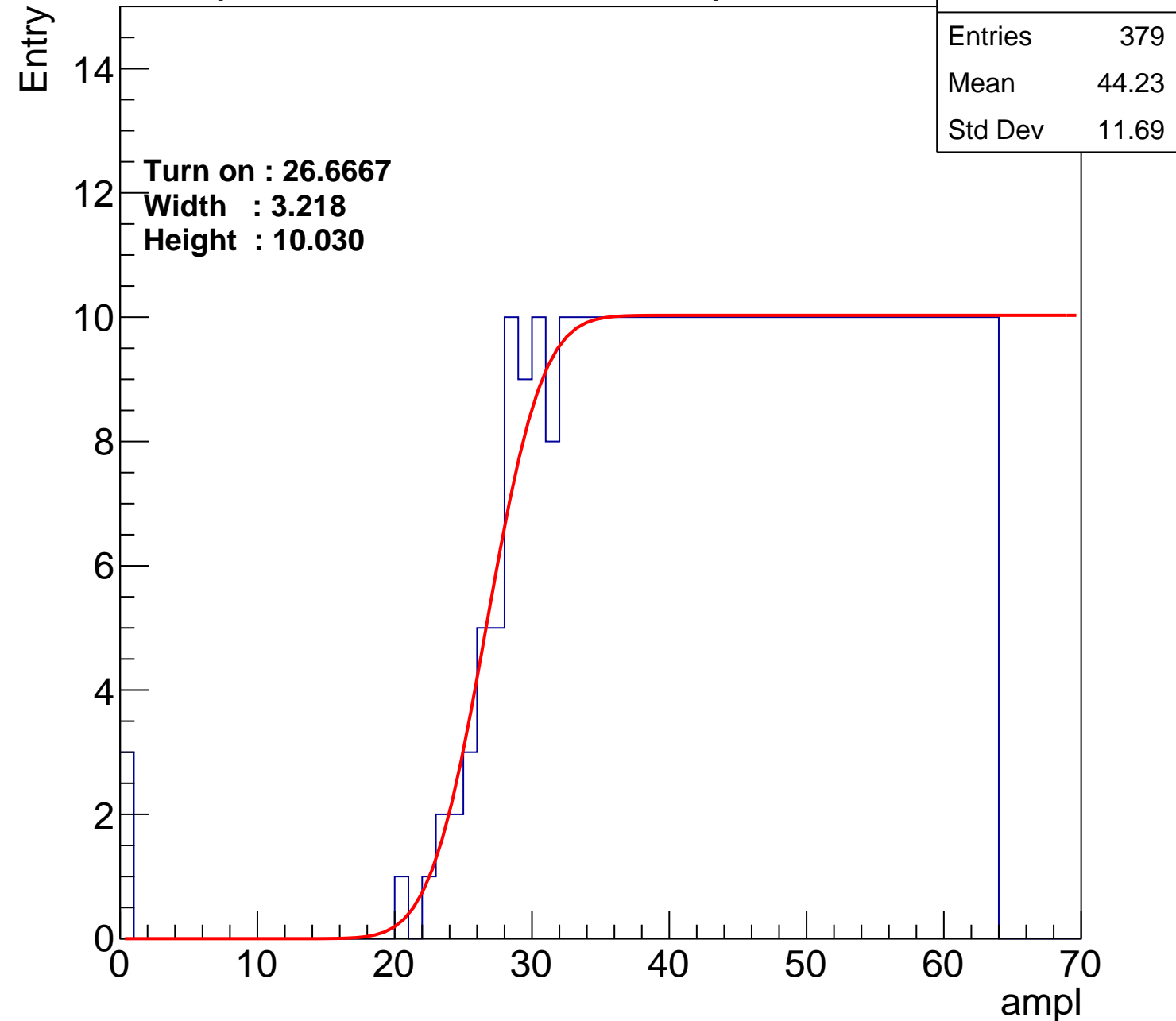
Width : 3.218

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 25.6207

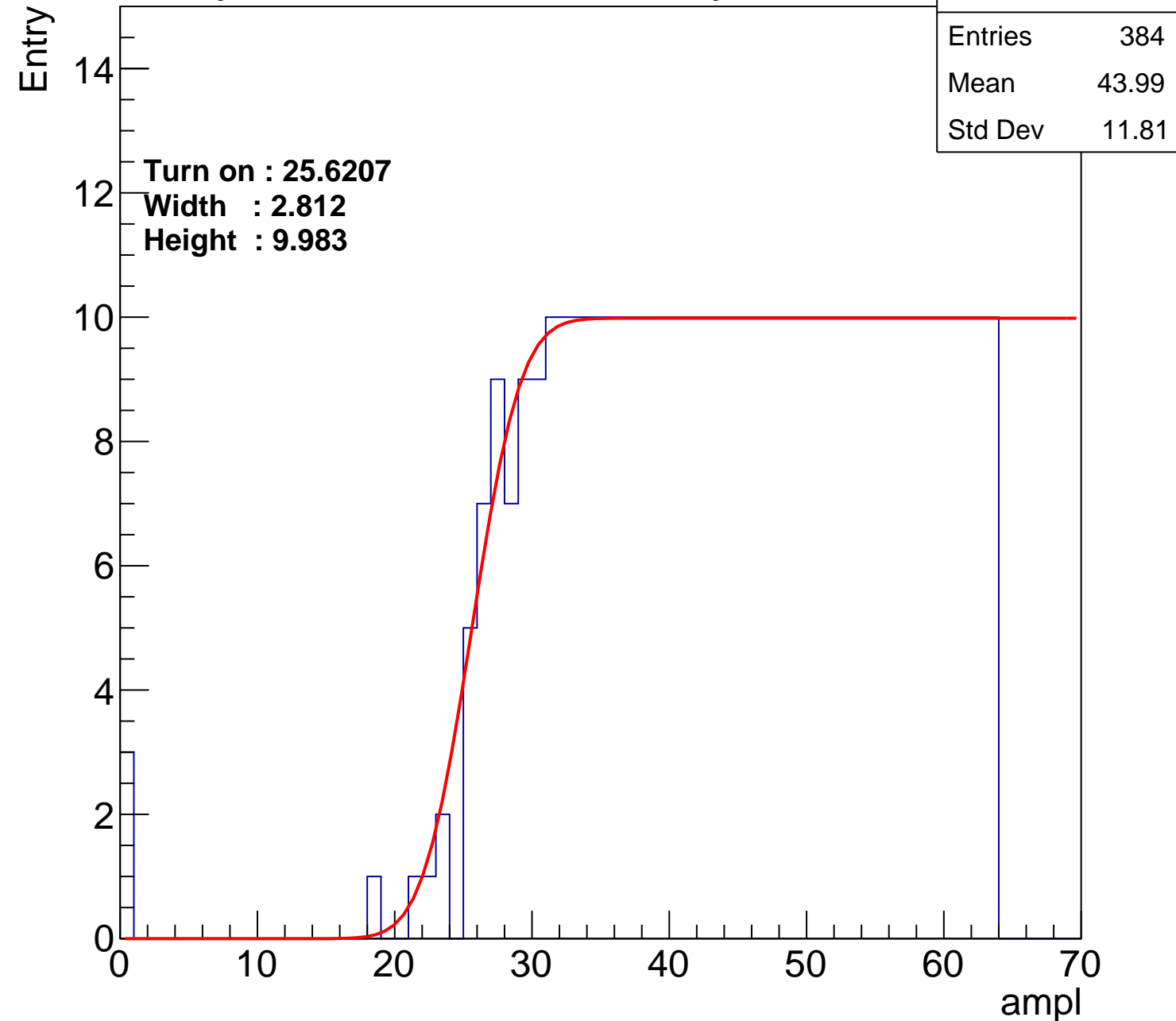
Width : 2.812

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch70

calib_packv5_042523_0143.root, FC#12, port B1

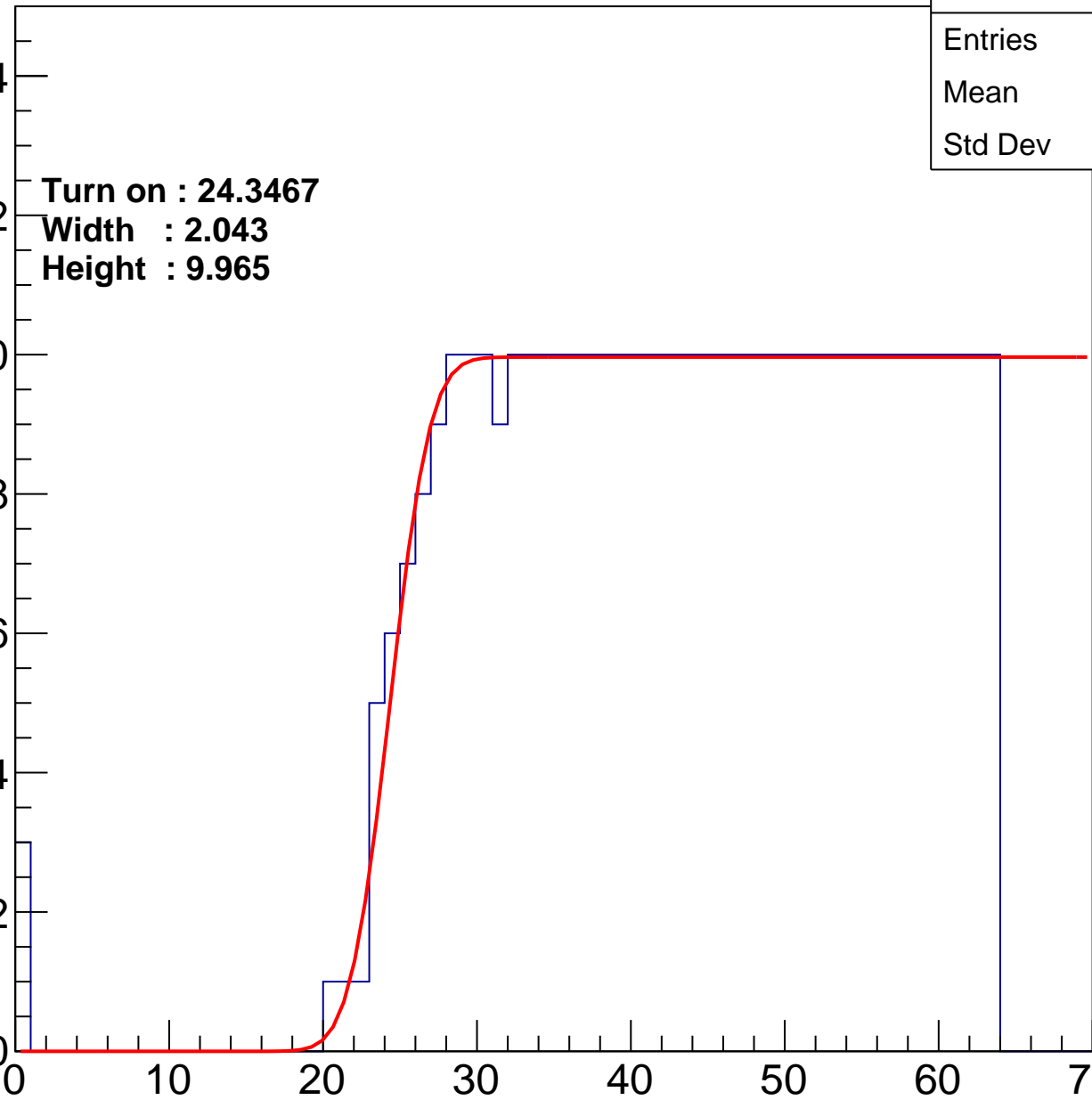
Entry

14
12
10
8
6
4
2
0

Turn on : 24.3467
Width : 2.043
Height : 9.965

Entries	400
Mean	43.24
Std Dev	12.14

ampl



B0L102S, U6-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.2652

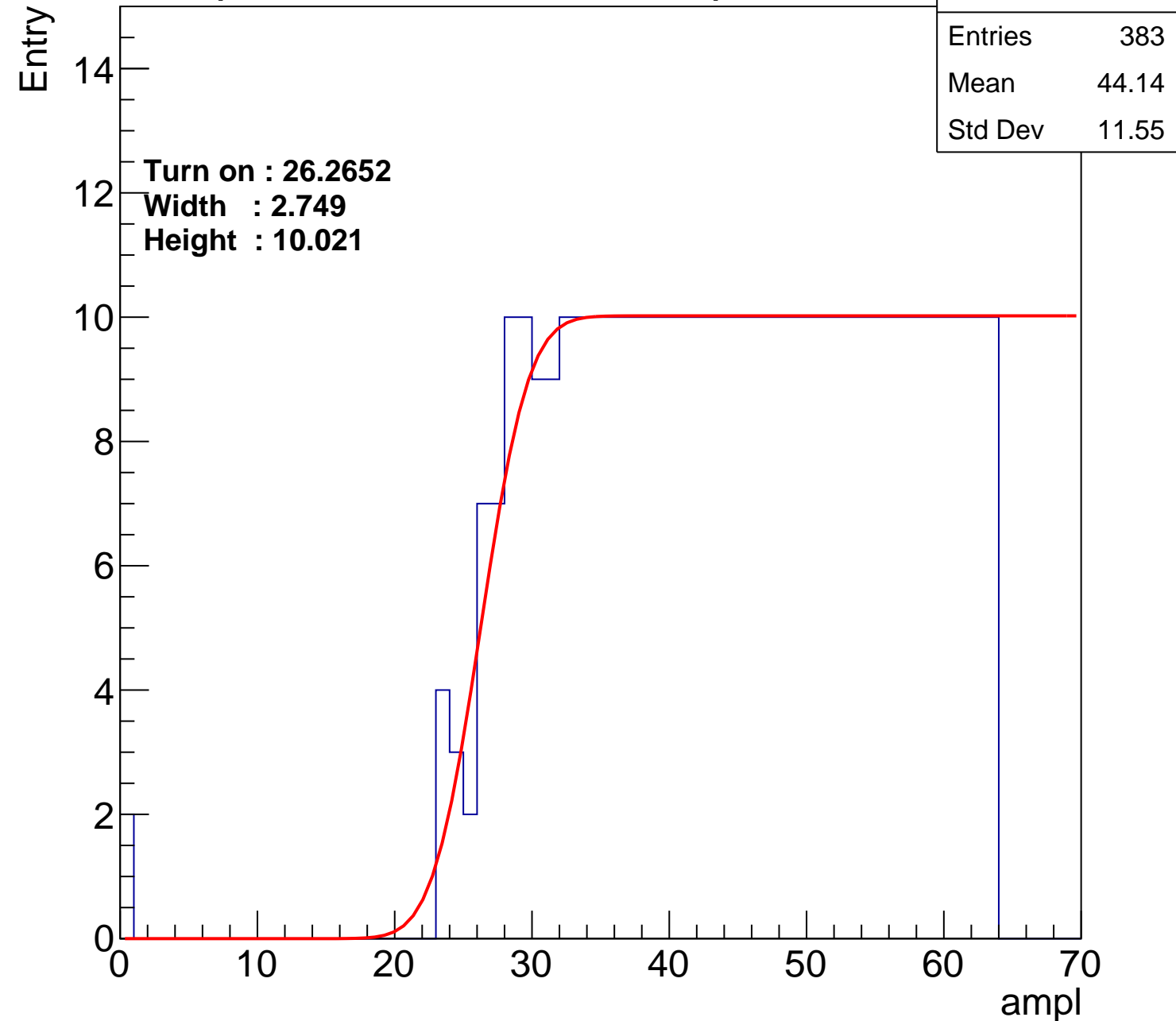
Width : 2.749

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch72

calib_packv5_042523_0143.root, FC#12, port B1

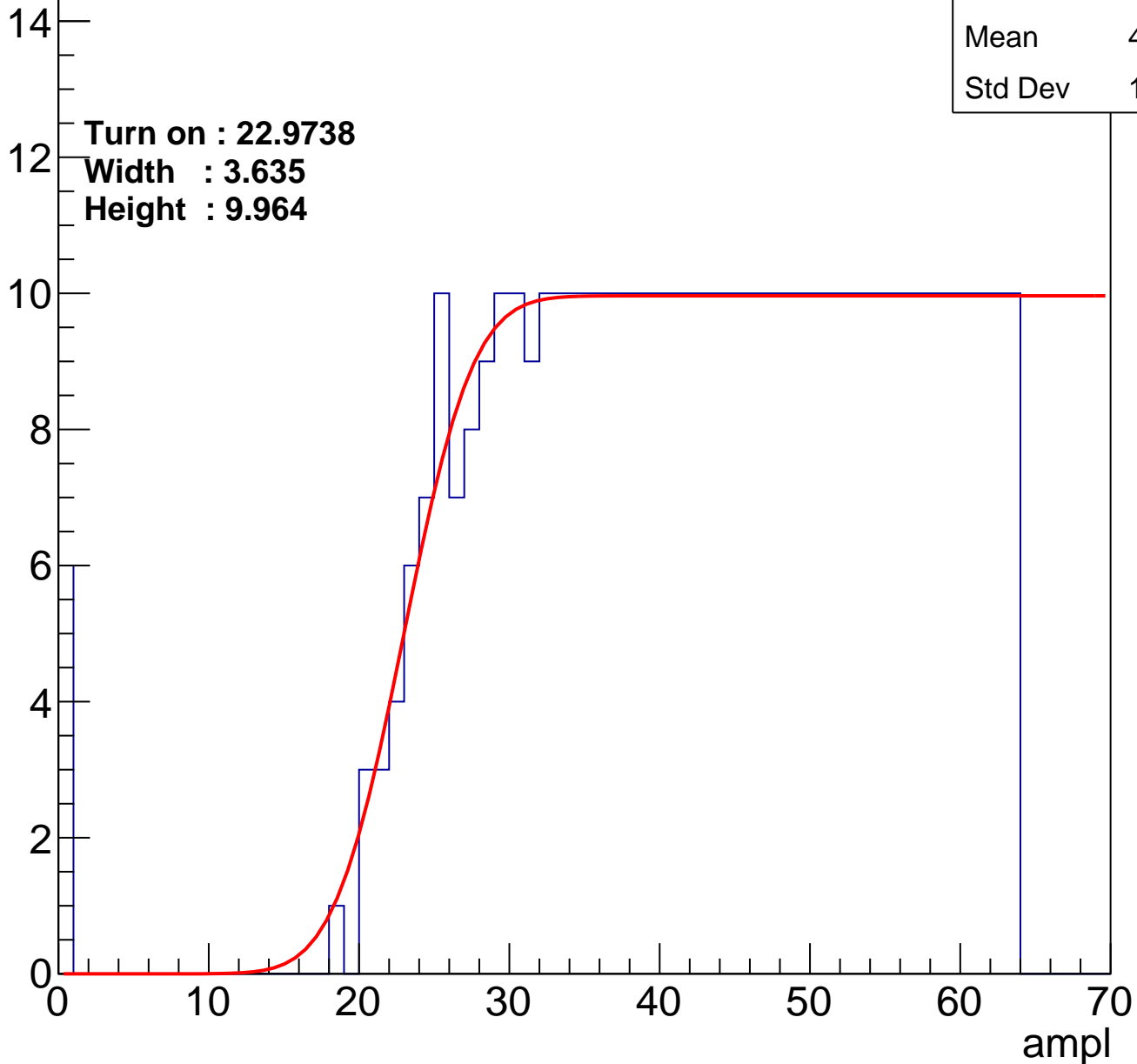
Entries	413
Mean	42.38
Std Dev	12.96

Turn on : 22.9738

Width : 3.635

Height : 9.964

Entry



B0L102S, U6-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.78
Std Dev	11.23

Turn on : 27.6554

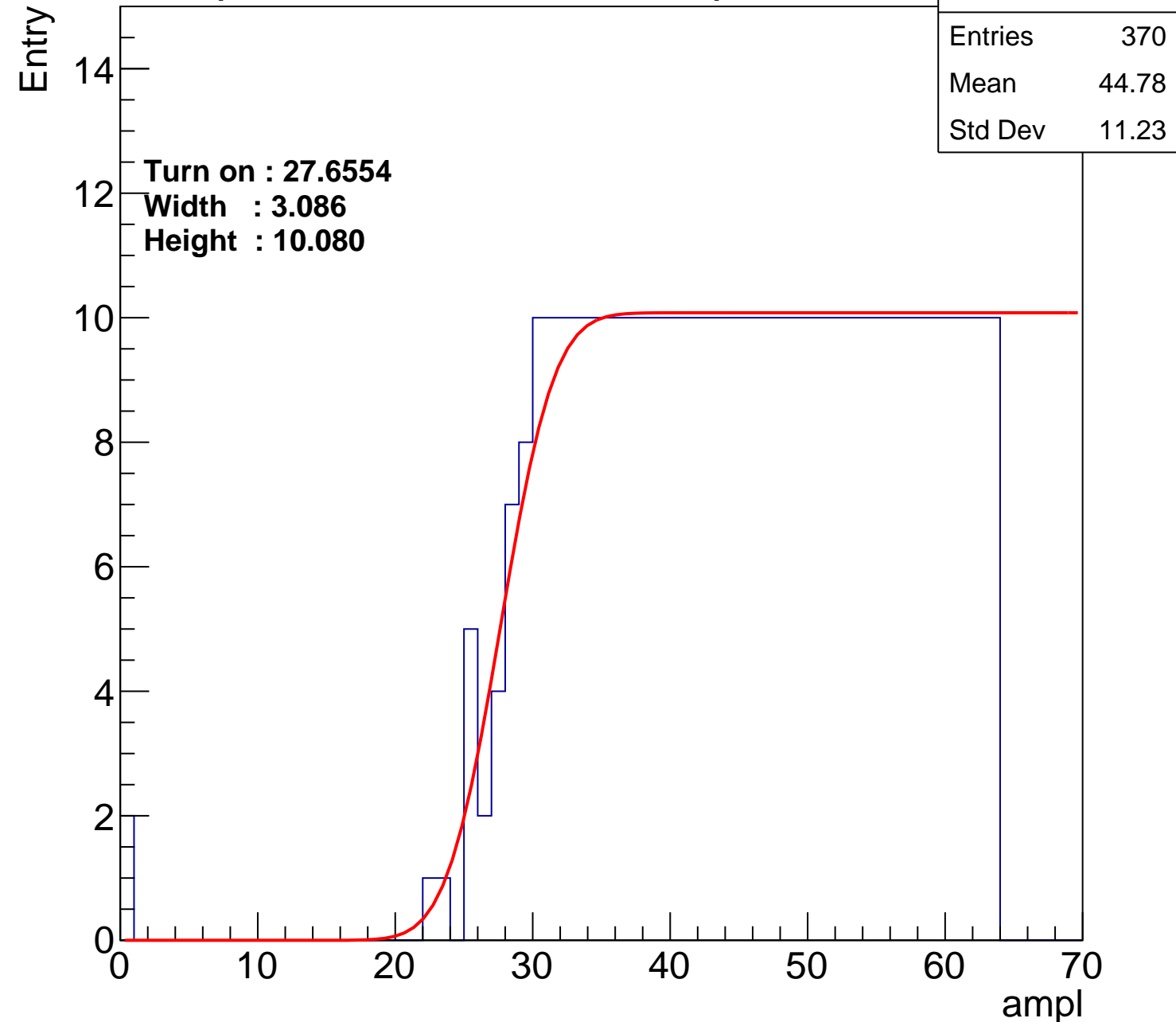
Width : 3.086

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.95
Std Dev	12.35

Turn on : 24.1893

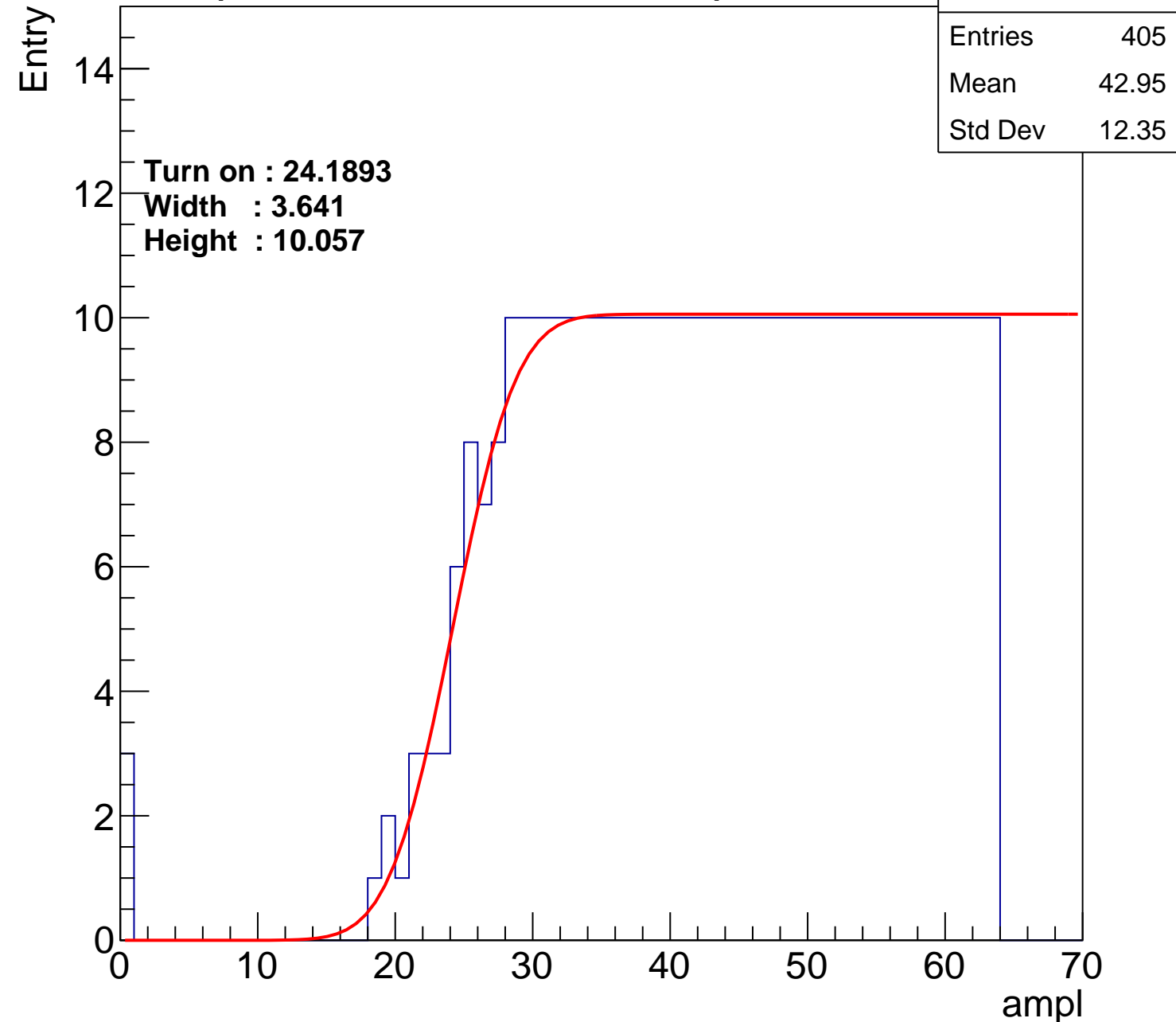
Width : 3.641

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.6647

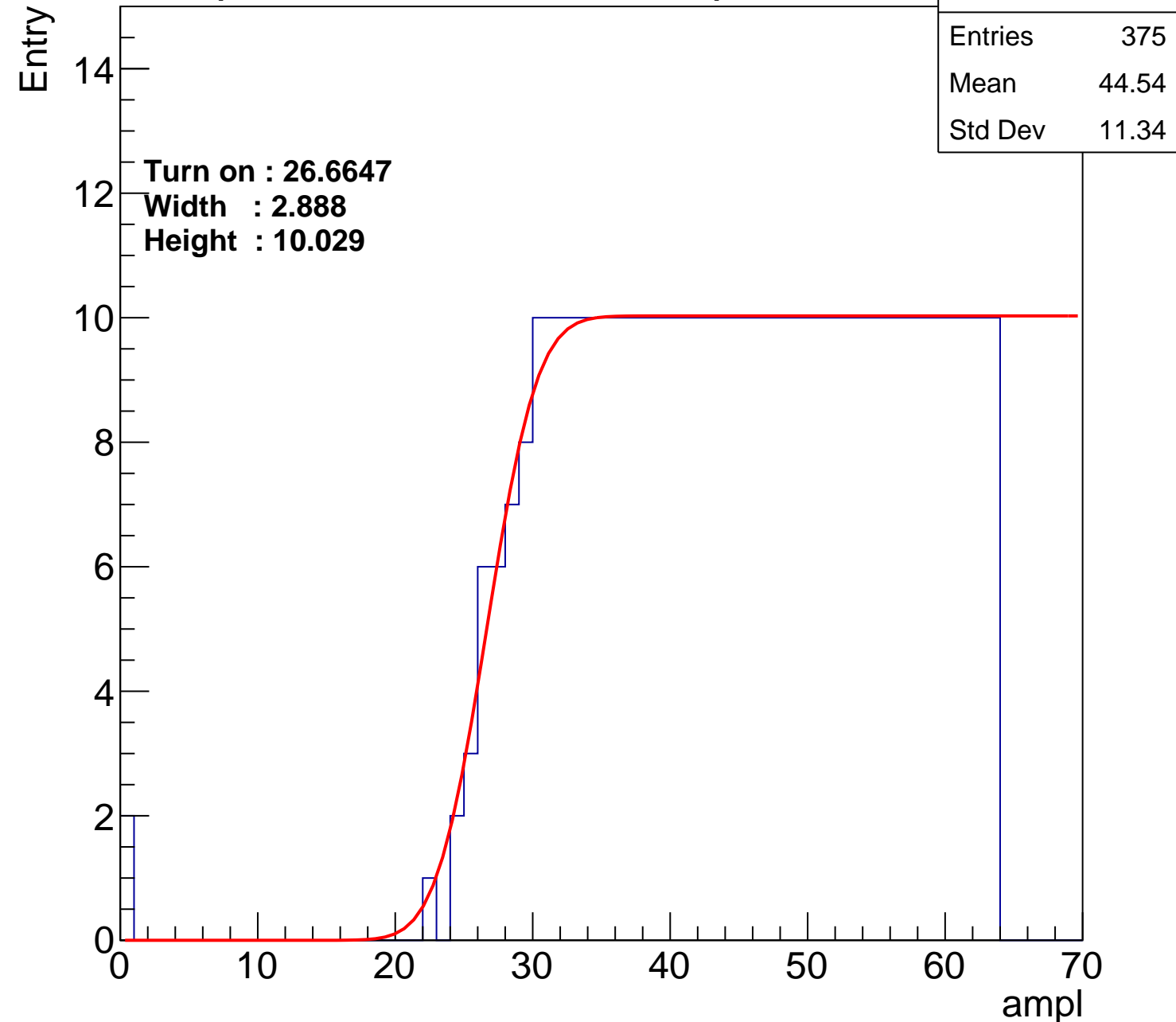
Width : 2.888

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 26.0356

Width : 2.310

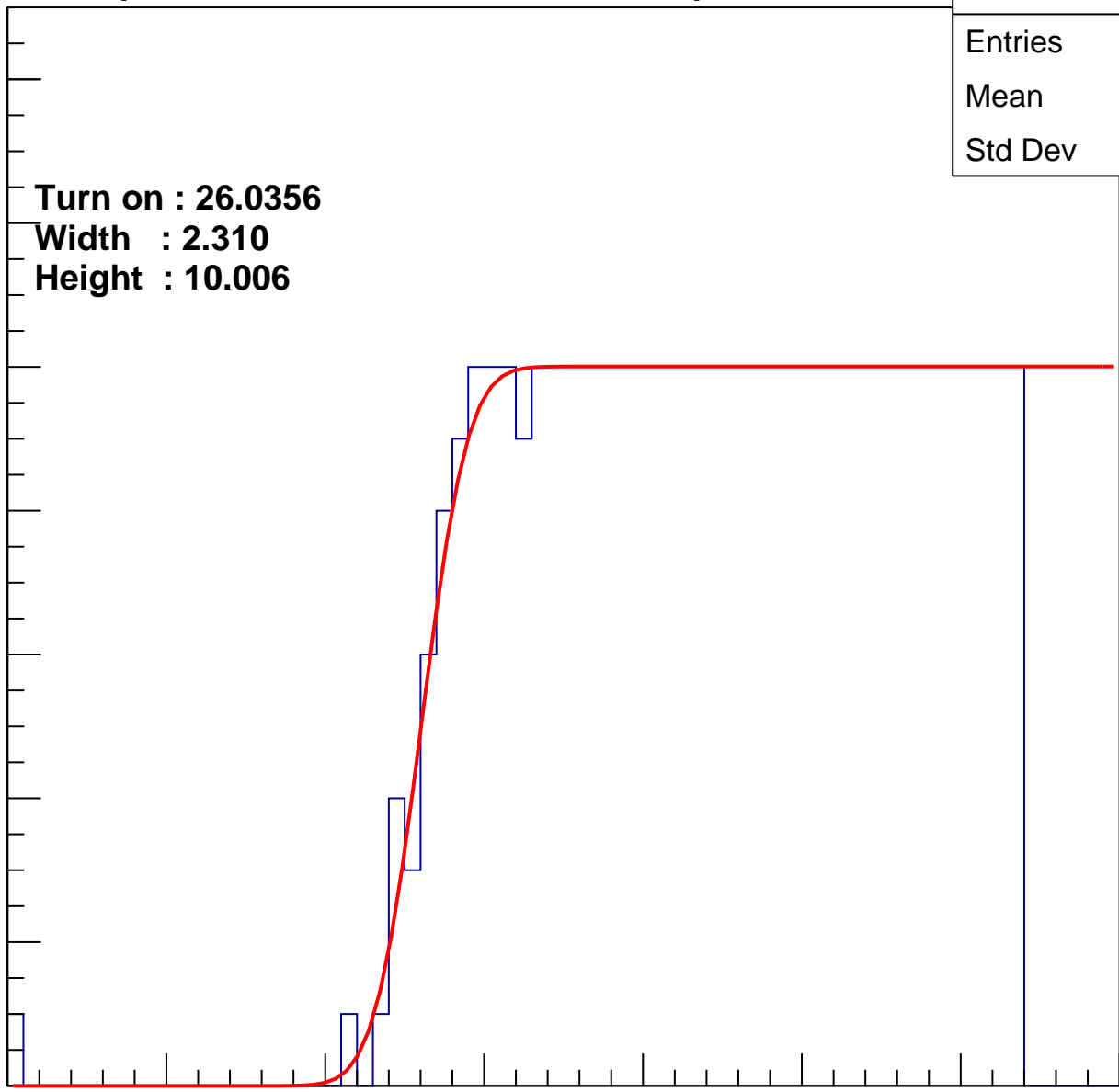
Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U6-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.3
Std Dev	11.99

Turn on : 24.2746

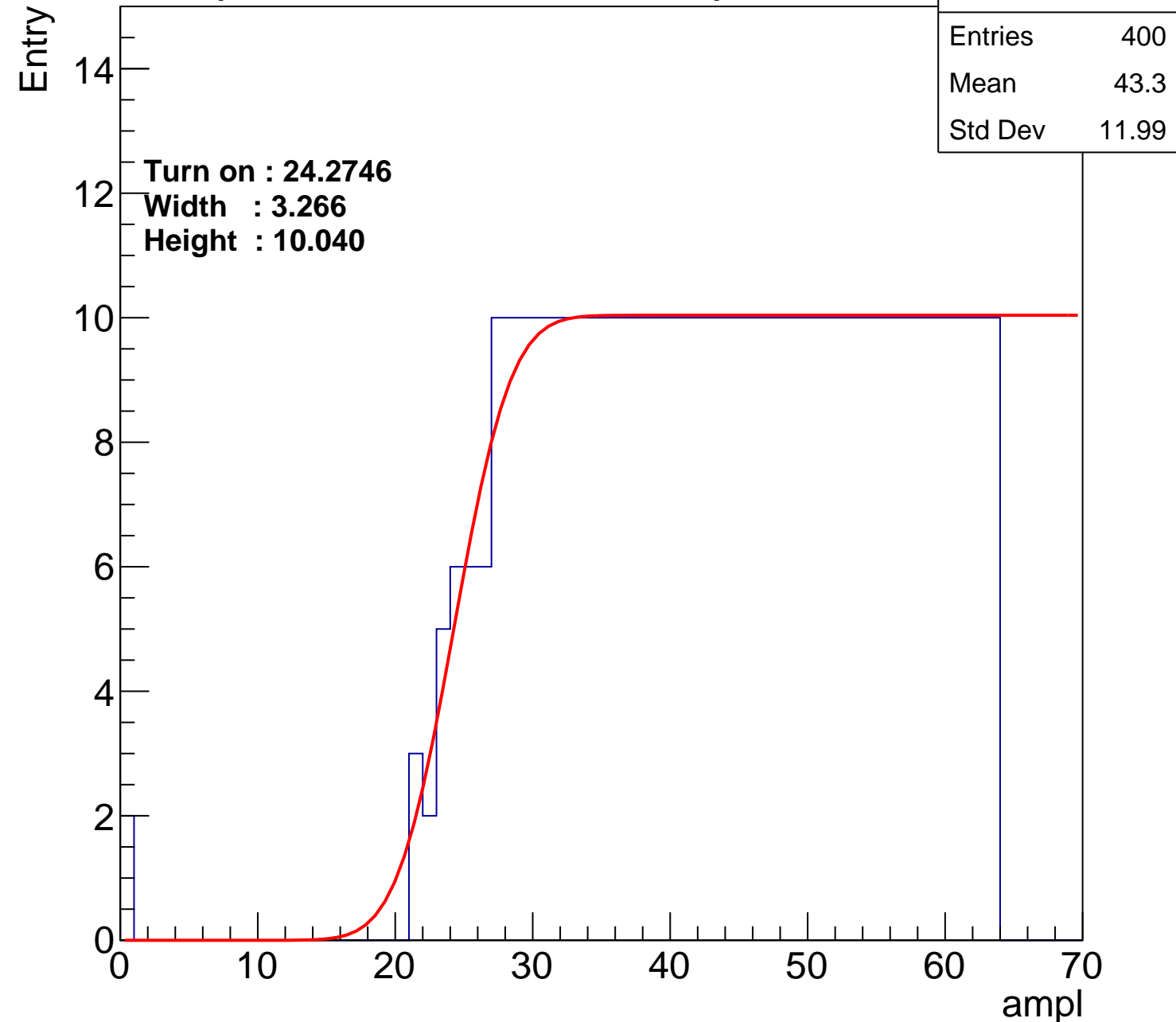
Width : 3.266

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.7
Std Dev	11.34

Turn on : 26.7859

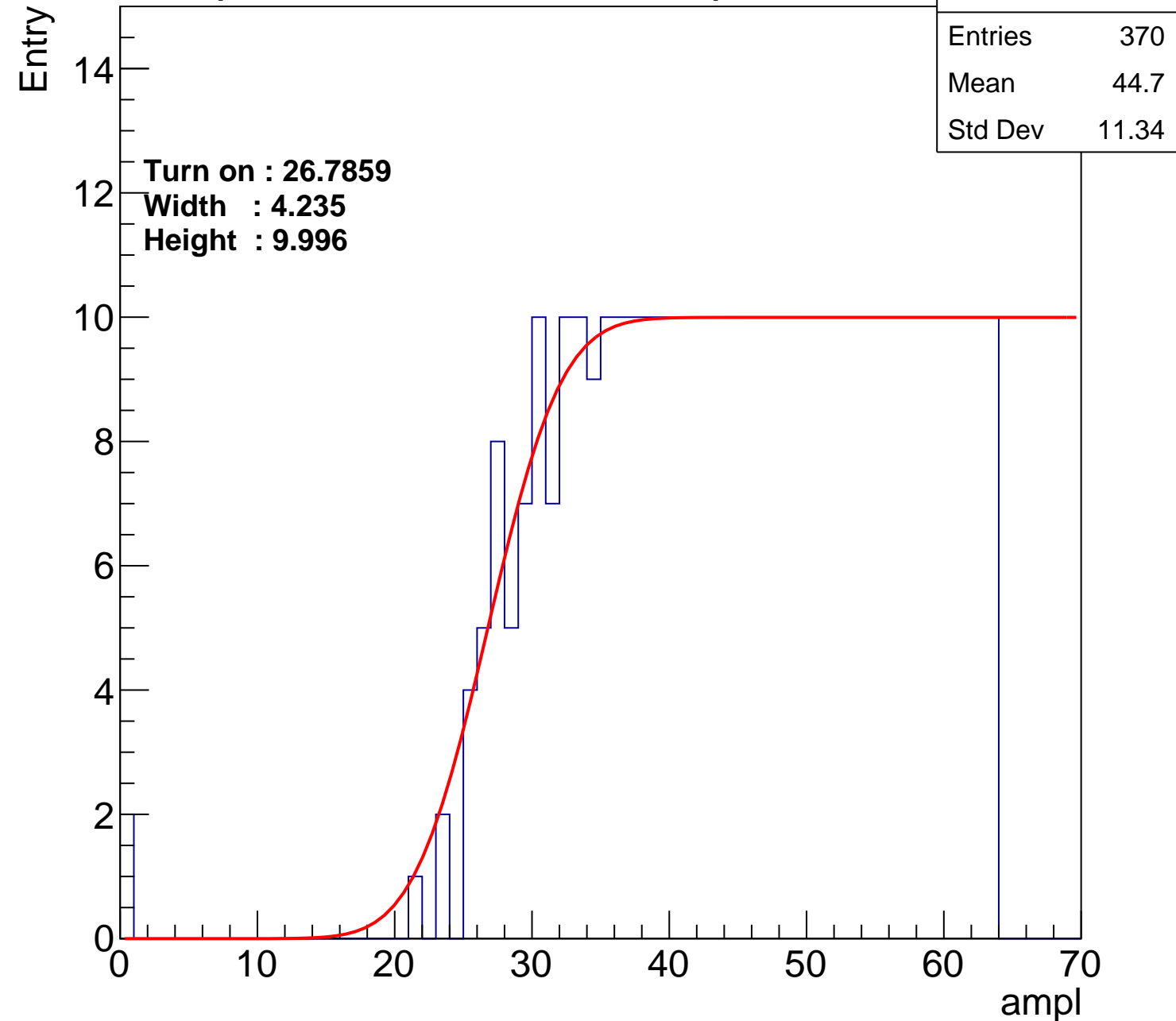
Width : 4.235

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.63
Std Dev	11.85

Turn on : 25.2494

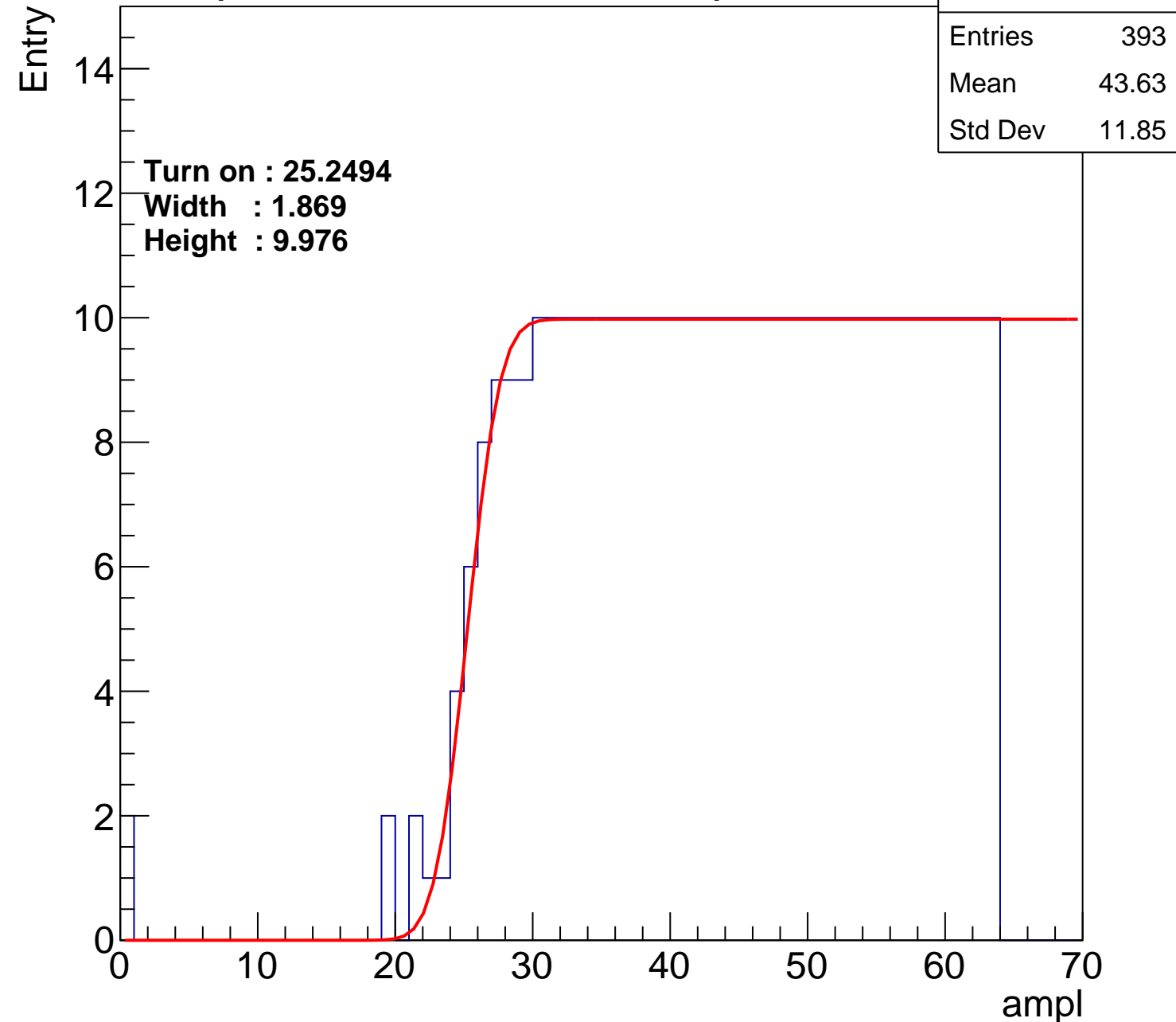
Width : 1.869

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.53
Std Dev	11.83

Turn on : 25.2687

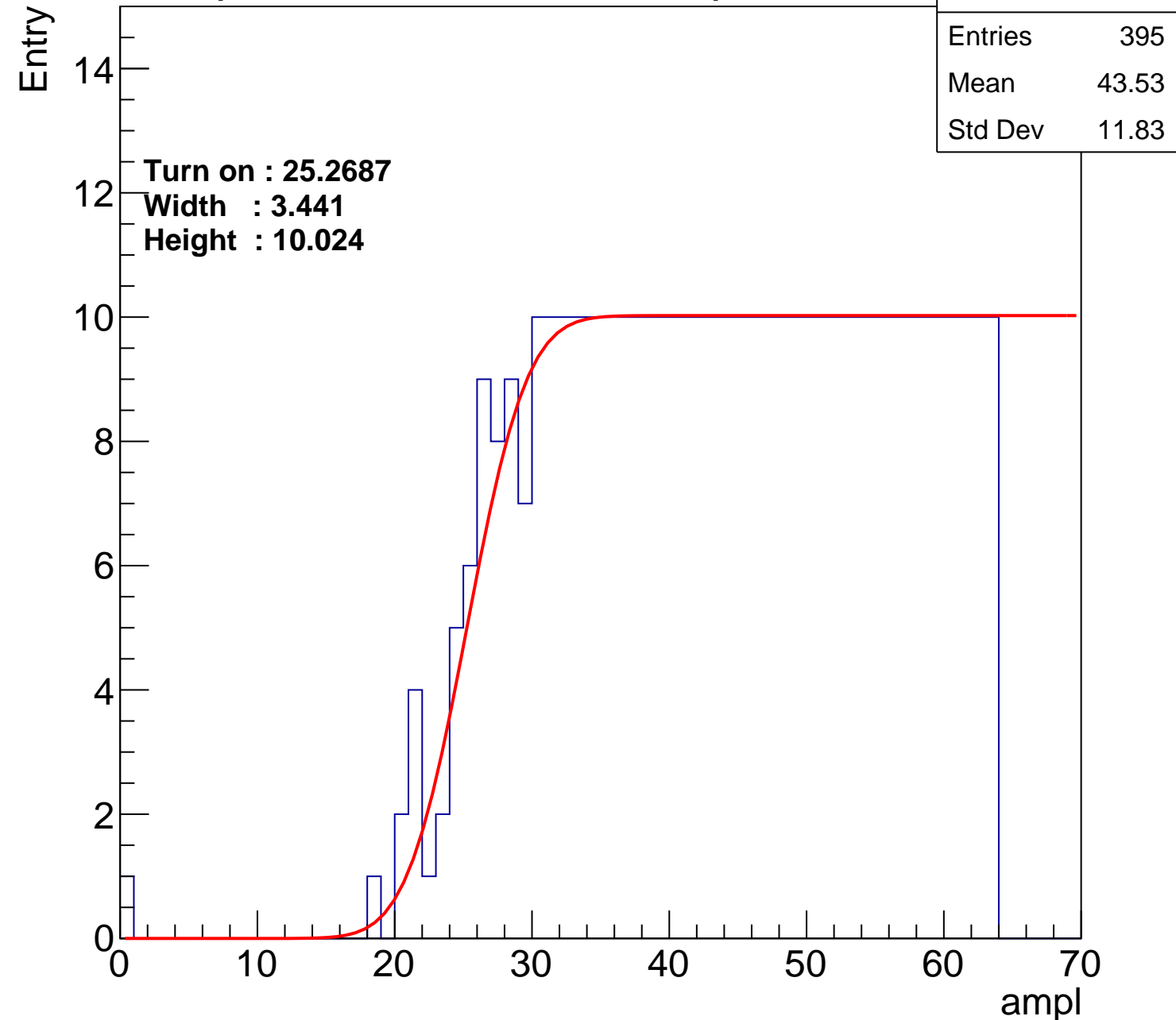
Width : 3.441

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.36
Std Dev	10.83

Turn on : 28.7021

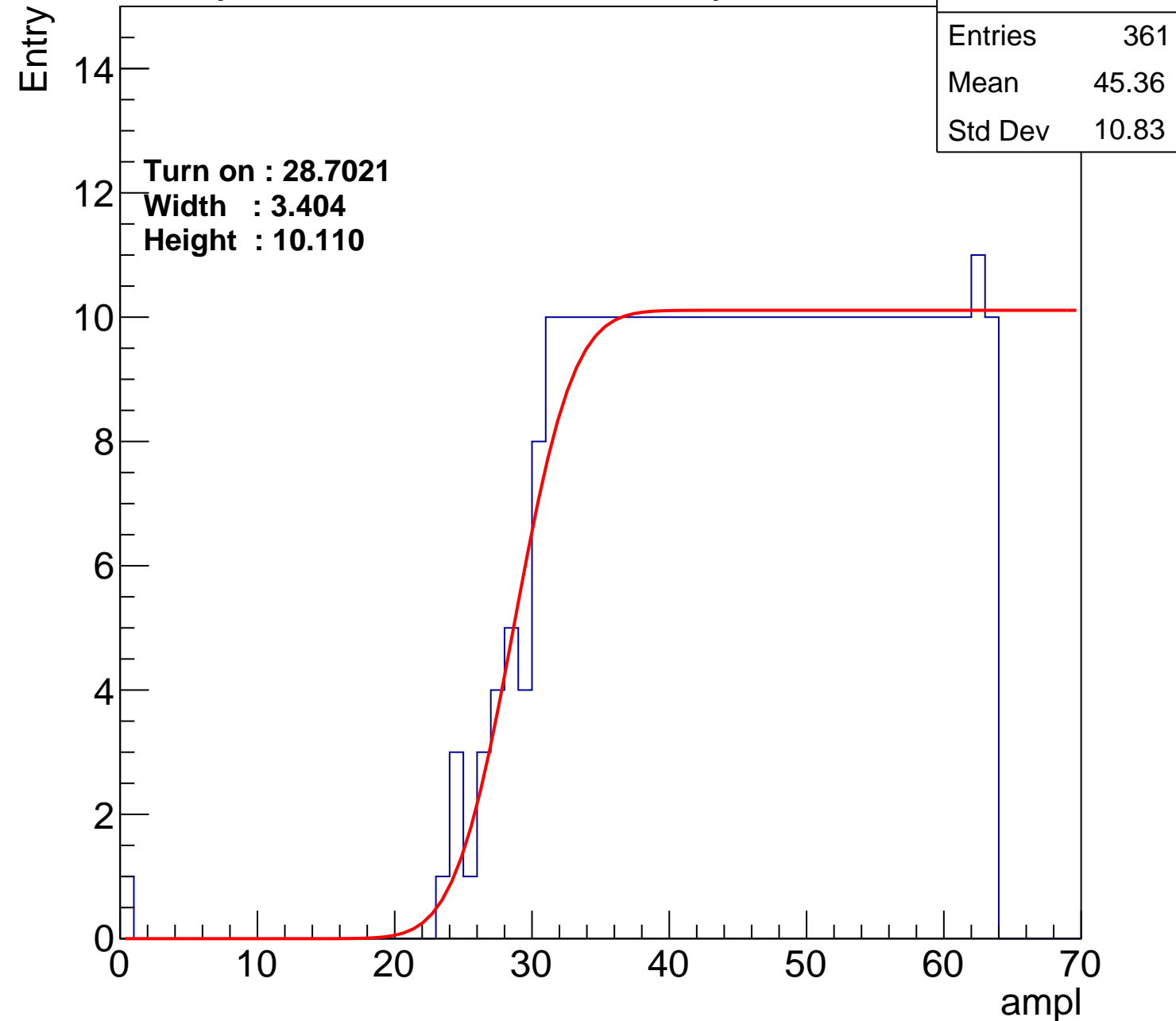
Width : 3.404

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch82

calib_packv5_042523_0143.root, FC#12, port B1

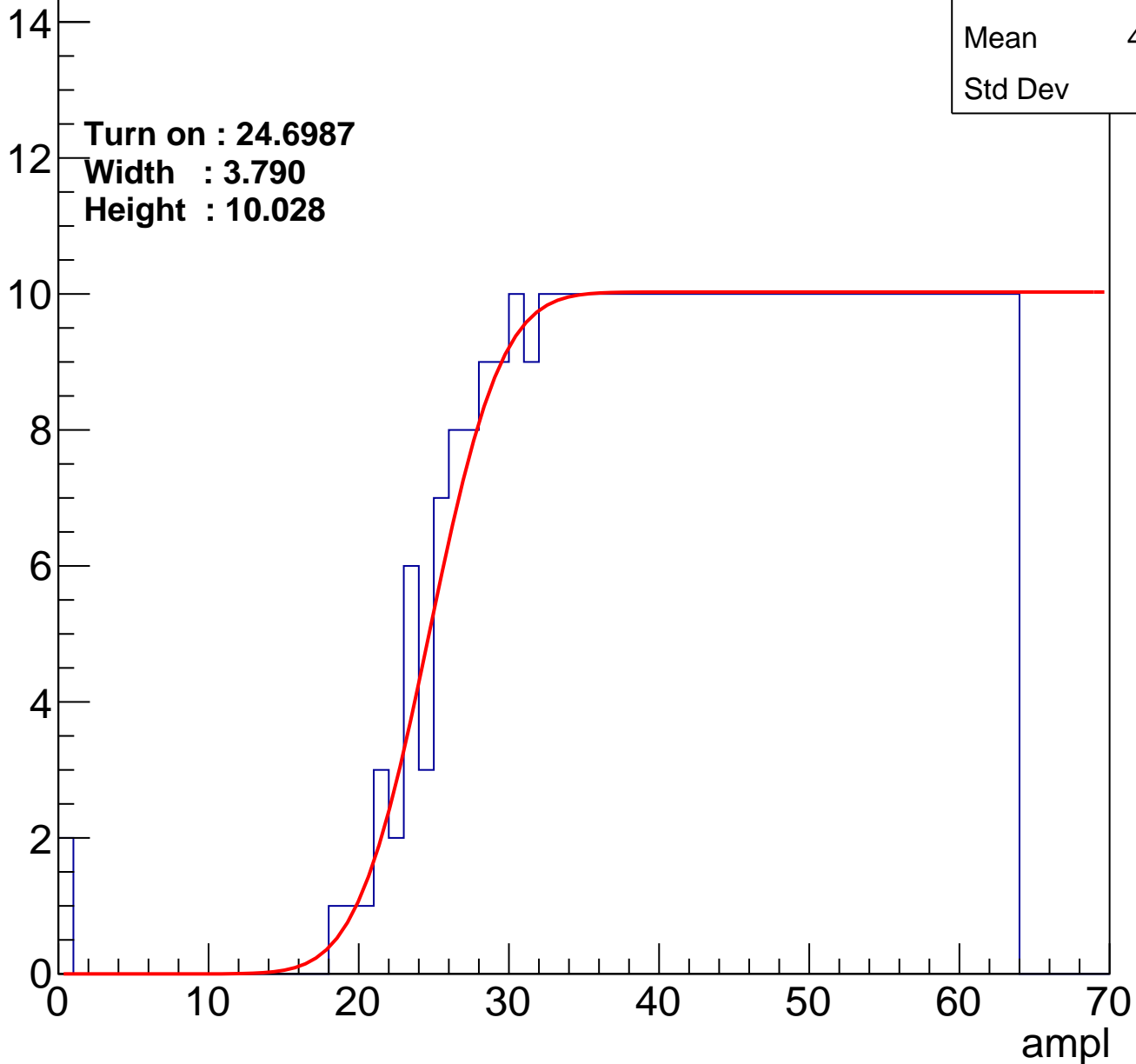
Entries	399
Mean	43.27
Std Dev	12.1

Turn on : 24.6987

Width : 3.790

Height : 10.028

Entry



B0L102S, U6-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.35
Std Dev	11.79

Turn on : 27.0944

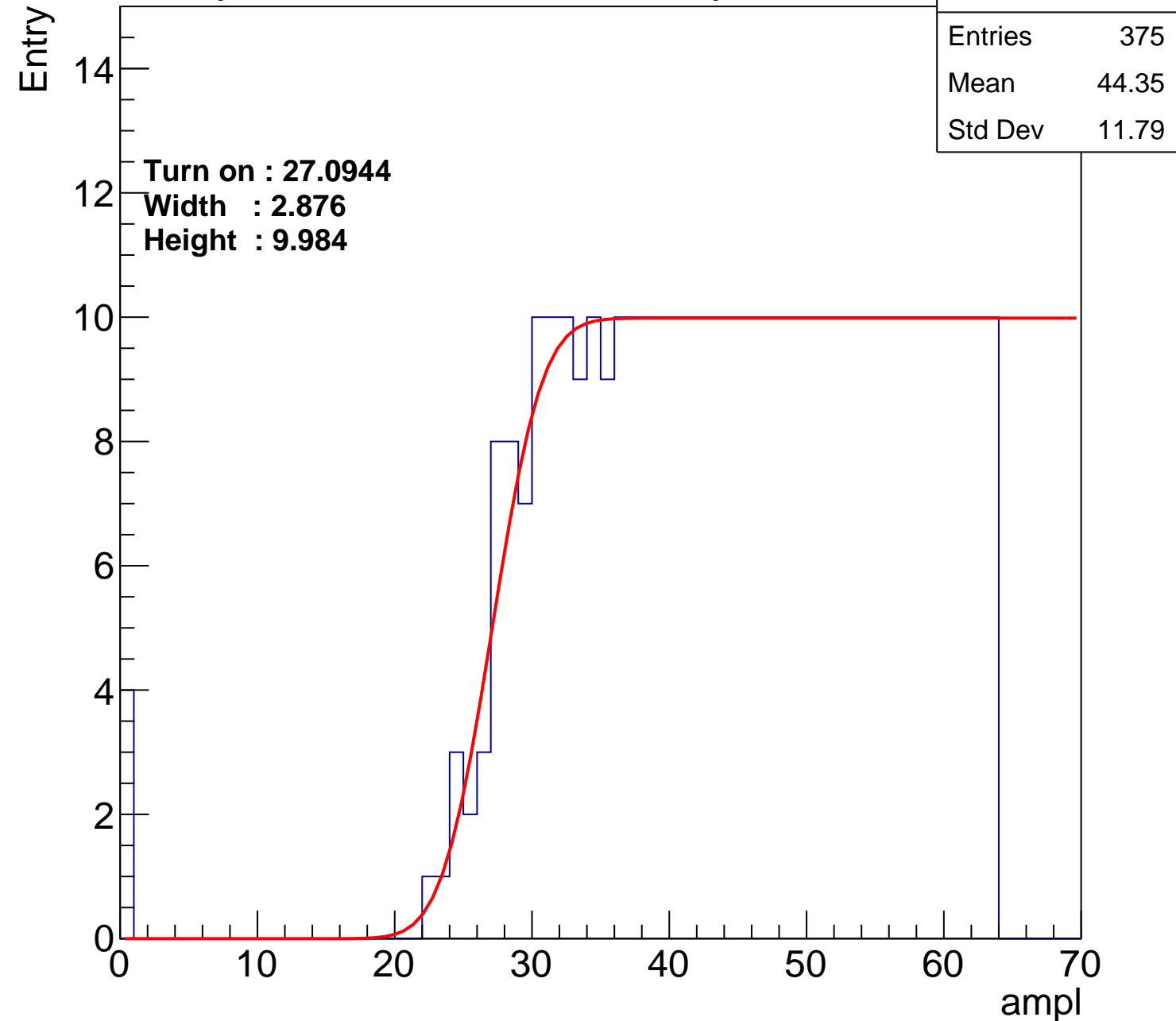
Width : 2.876

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.91
Std Dev	12.46

Turn on : 24.2519

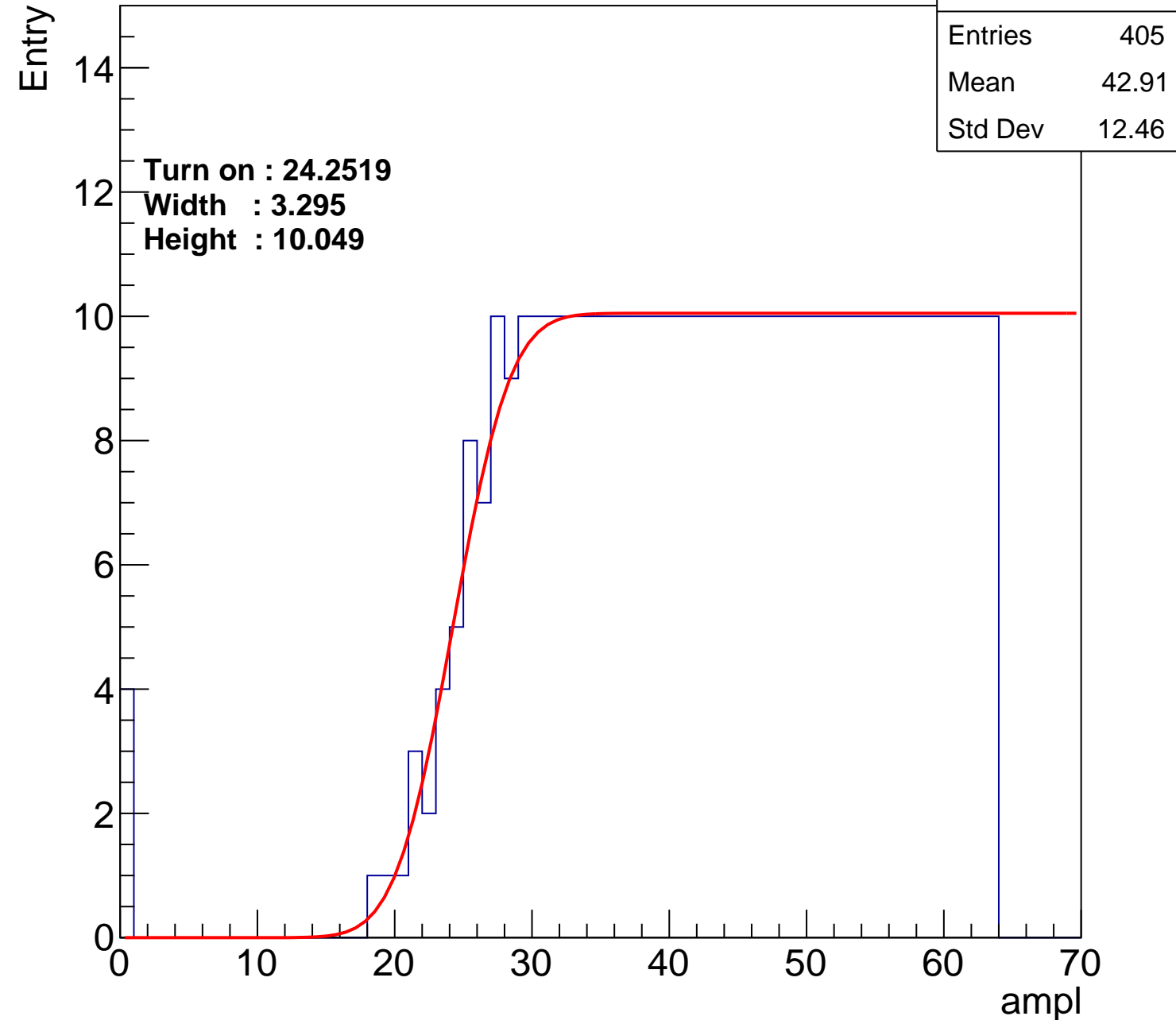
Width : 3.295

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.7845

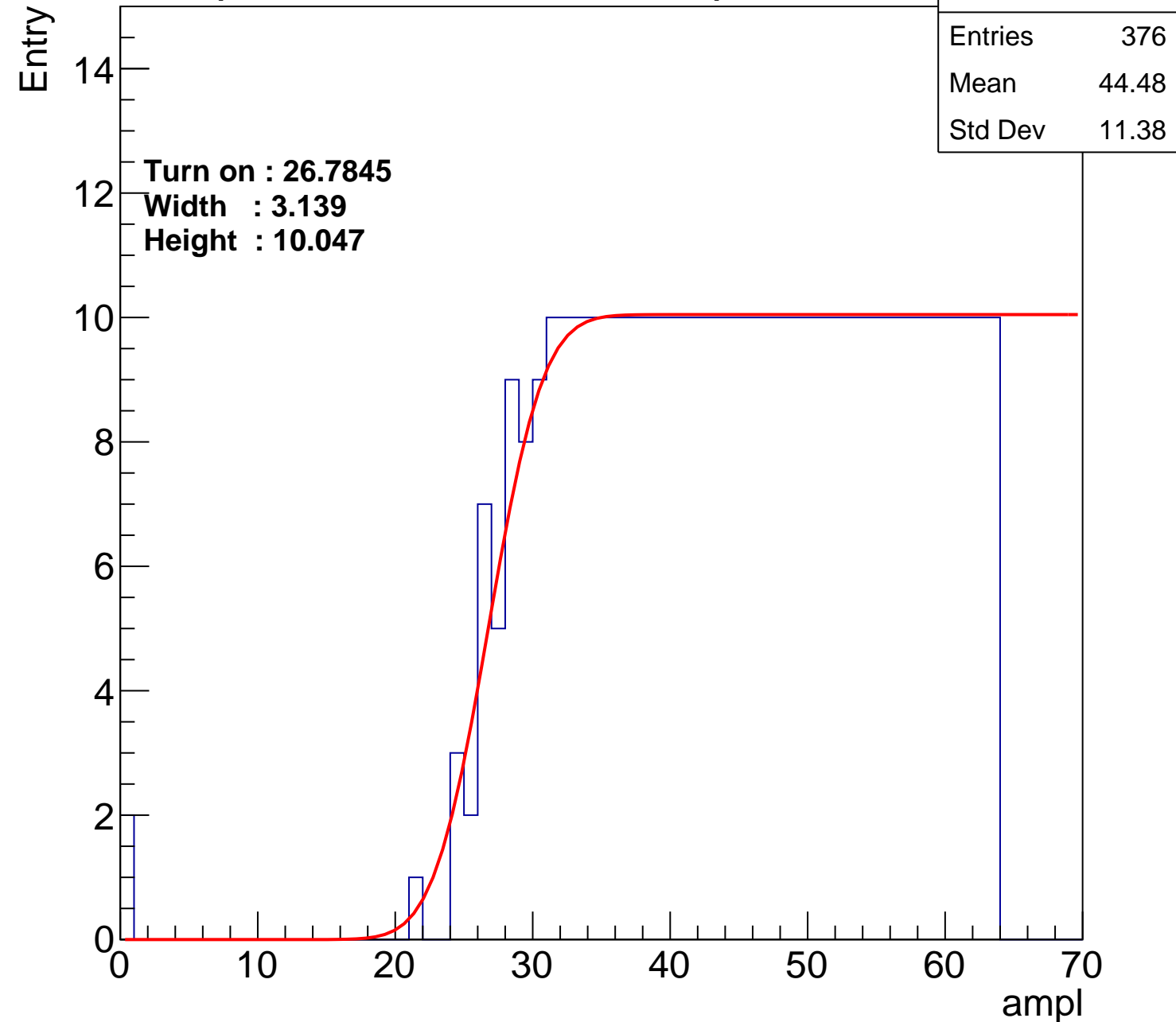
Width : 3.139

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch86

calib_packv5_042523_0143.root, FC#12, port B1

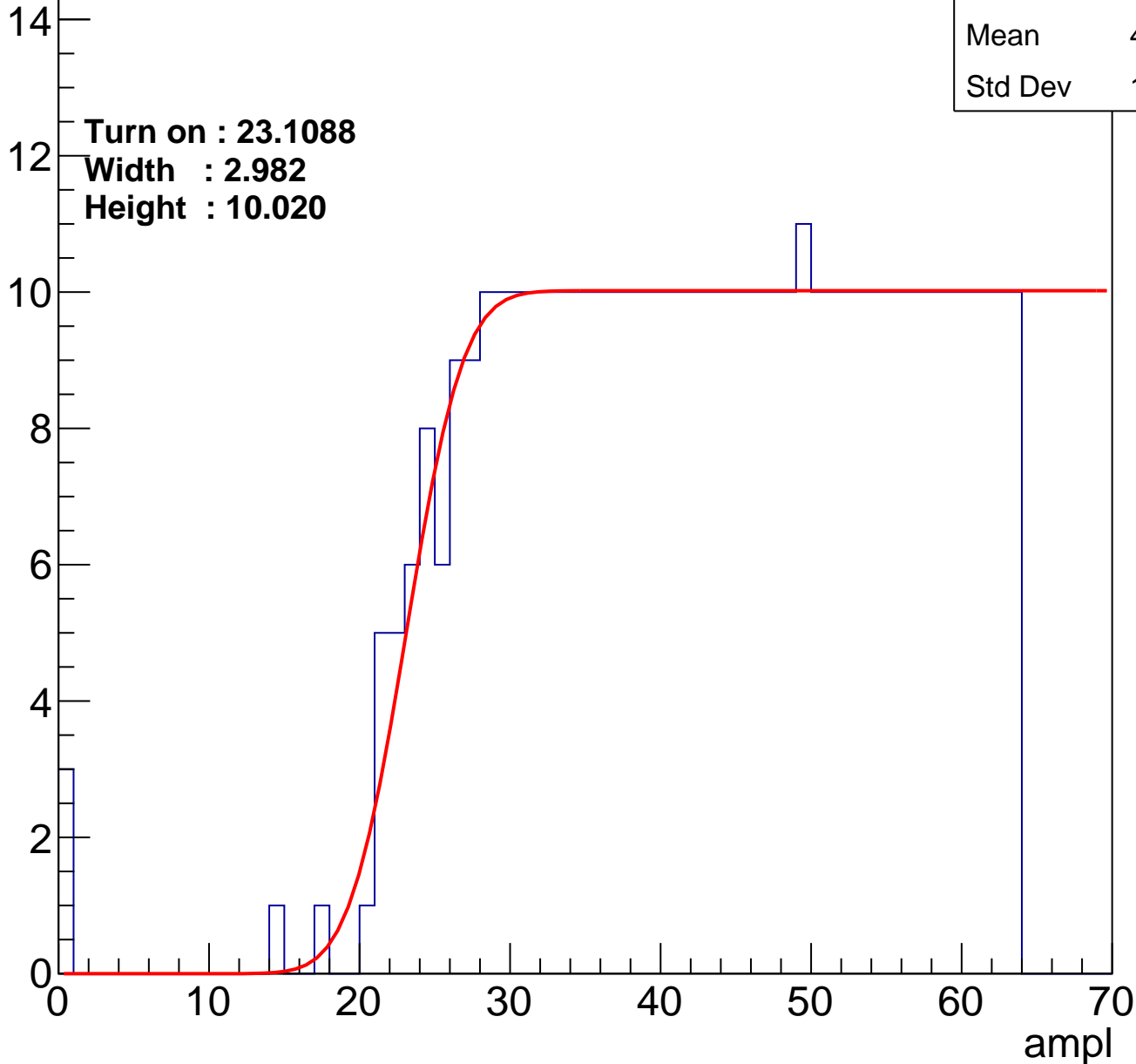
Entry

Entries	415
Mean	42.53
Std Dev	12.55

Turn on : 23.1088

Width : 2.982

Height : 10.020



B0L102S, U6-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.72
Std Dev	11.7

Turn on : 28.4254

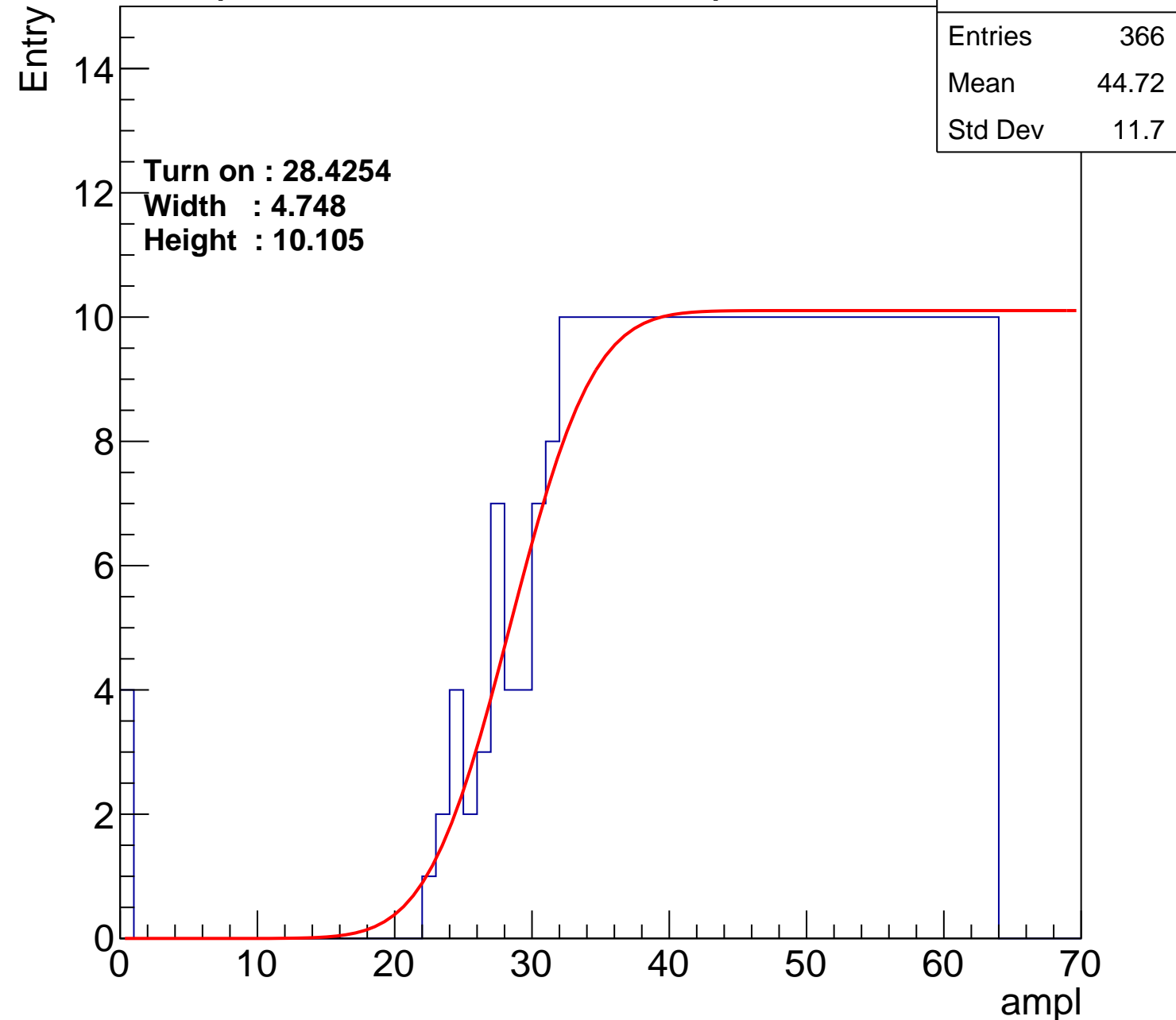
Width : 4.748

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.88
Std Dev	12.04

Turn on : 26.3531

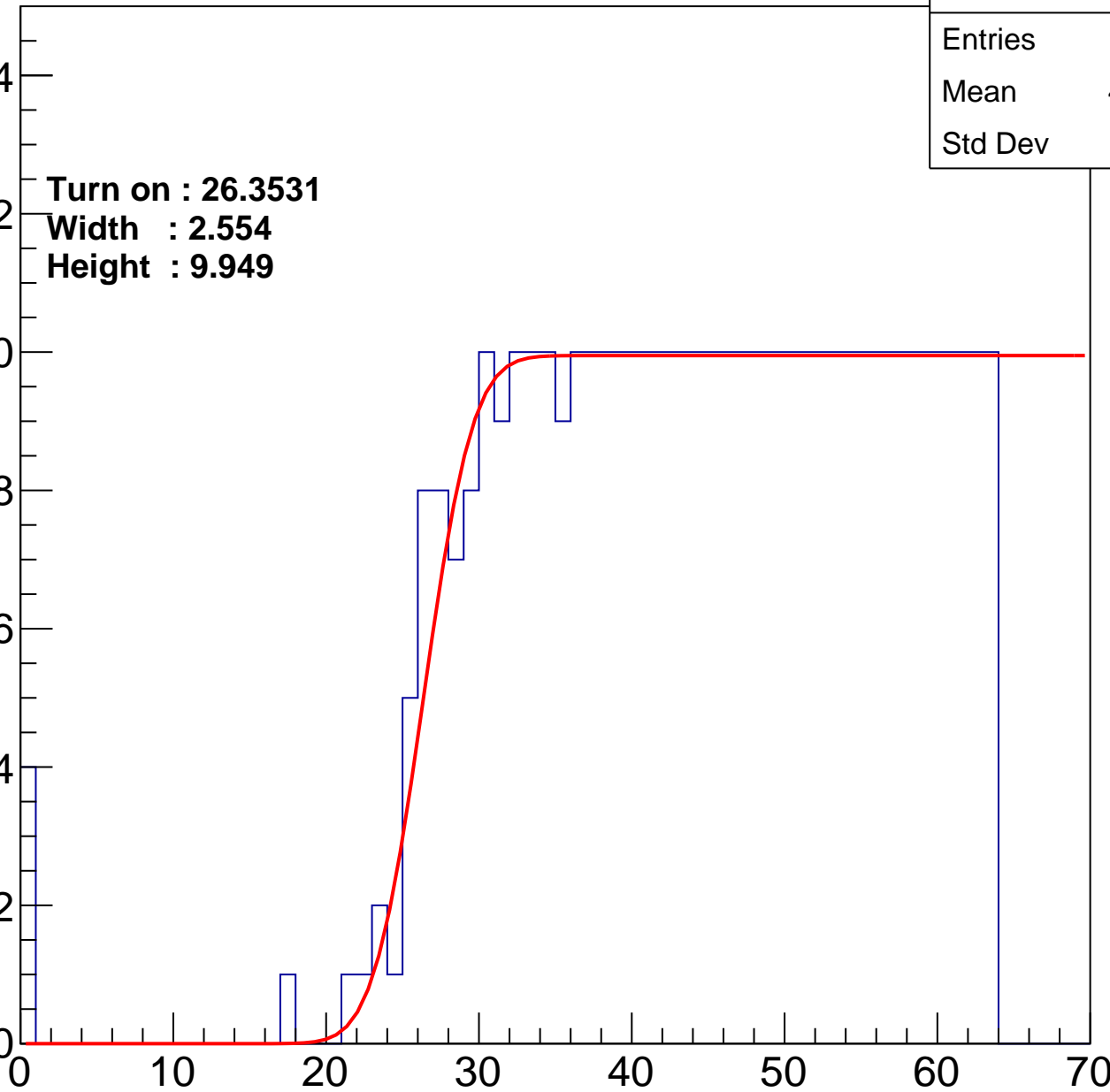
Width : 2.554

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch89

calib_packv5_042523_0143.root, FC#12, port B1

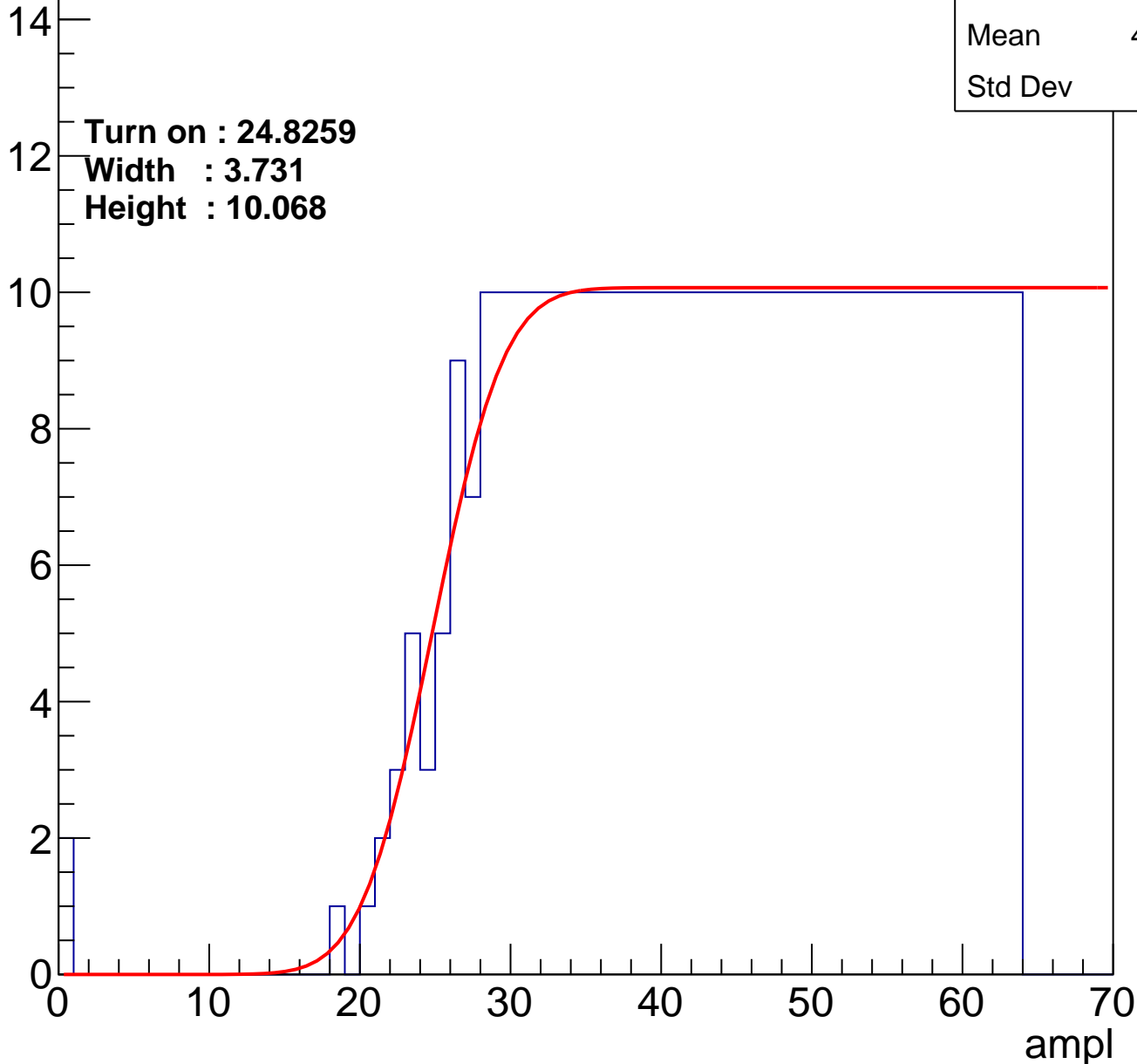
Entries	398
Mean	43.37
Std Dev	12

Turn on : 24.8259

Width : 3.731

Height : 10.068

Entry



B0L102S, U6-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.37
Std Dev	12.11

Turn on : 24.7238

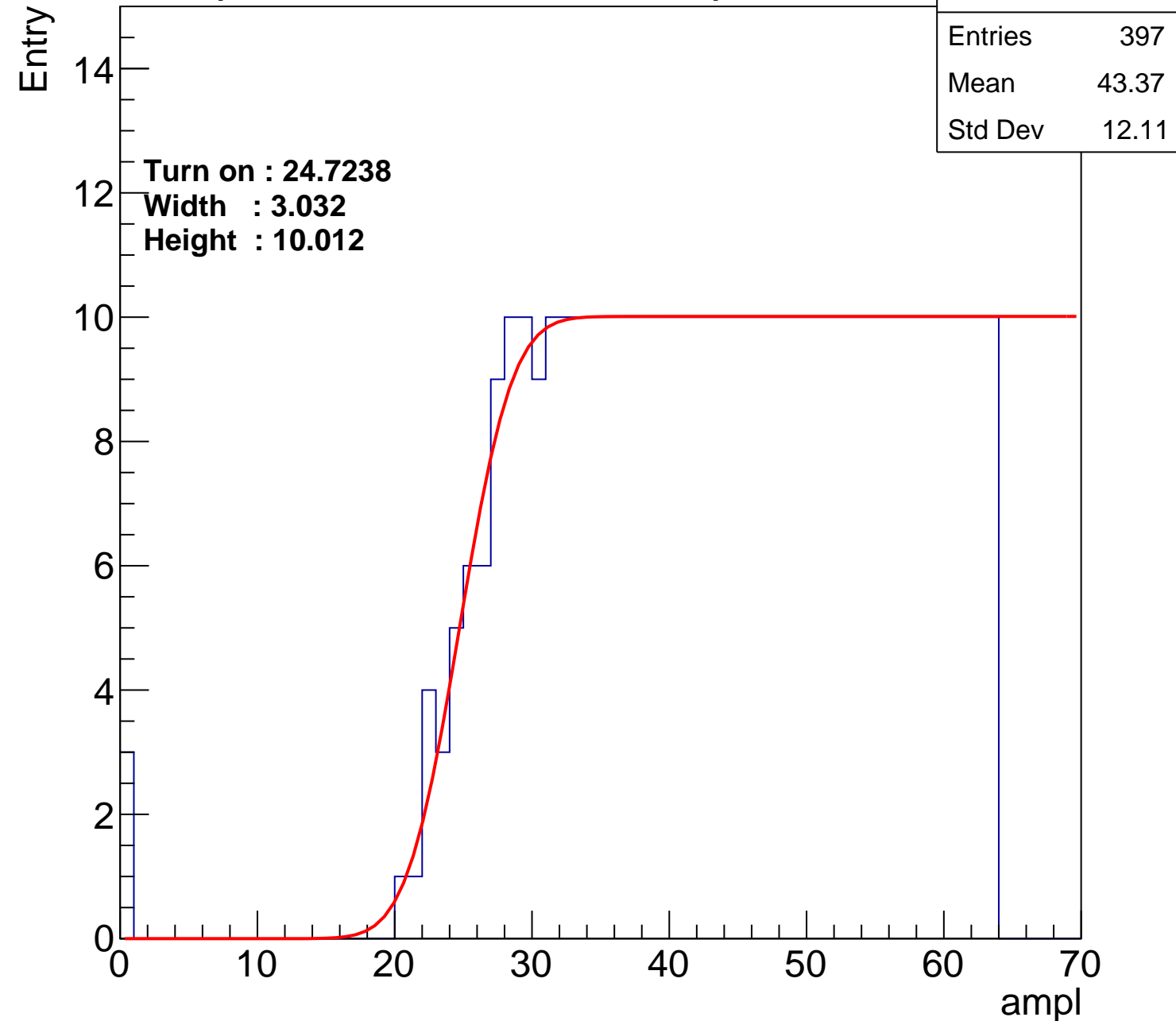
Width : 3.032

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.21
Std Dev	12.32

Turn on : 24.6701

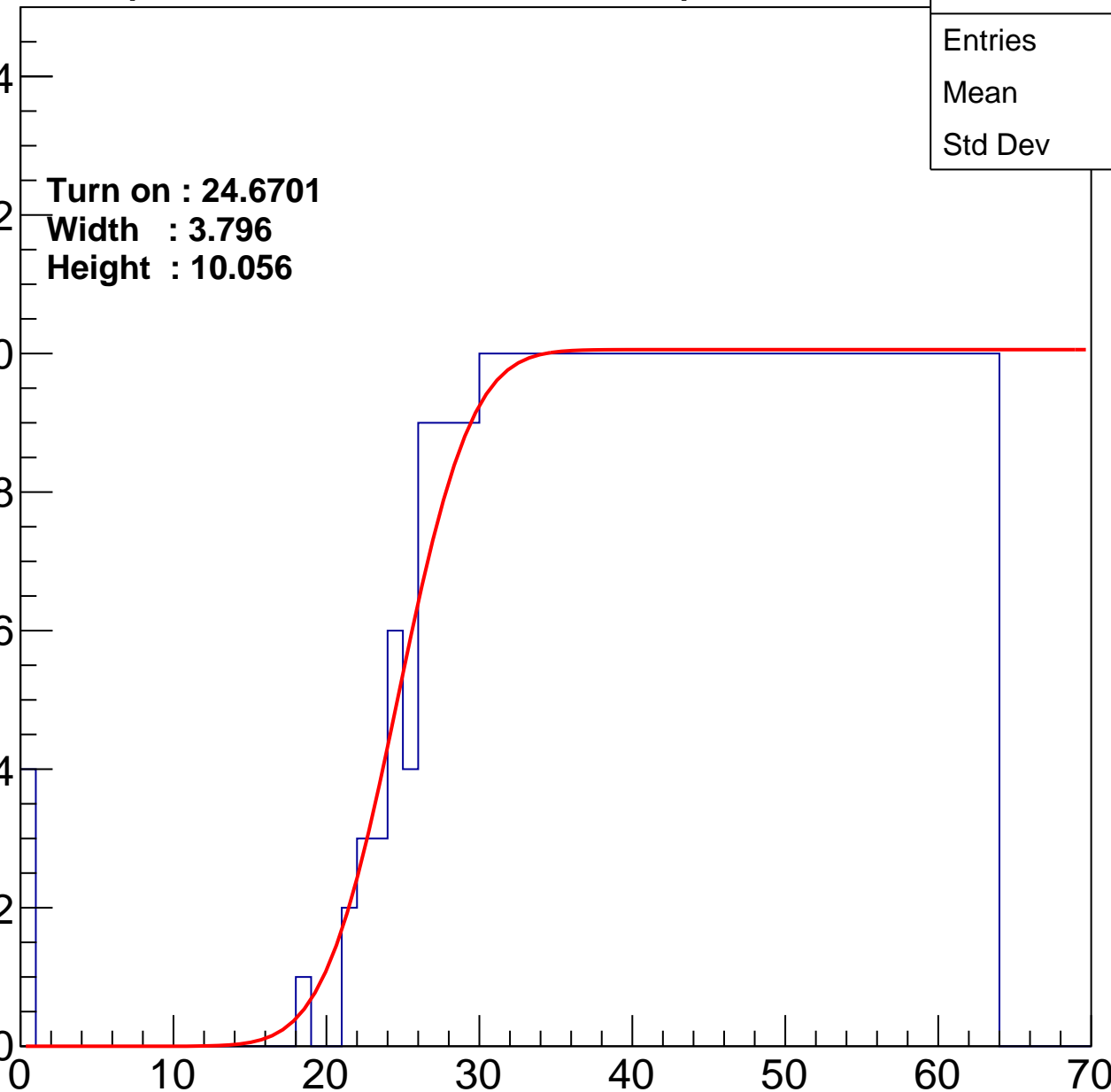
Width : 3.796

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	43.95
Std Dev	12.17

Turn on : 27.5622

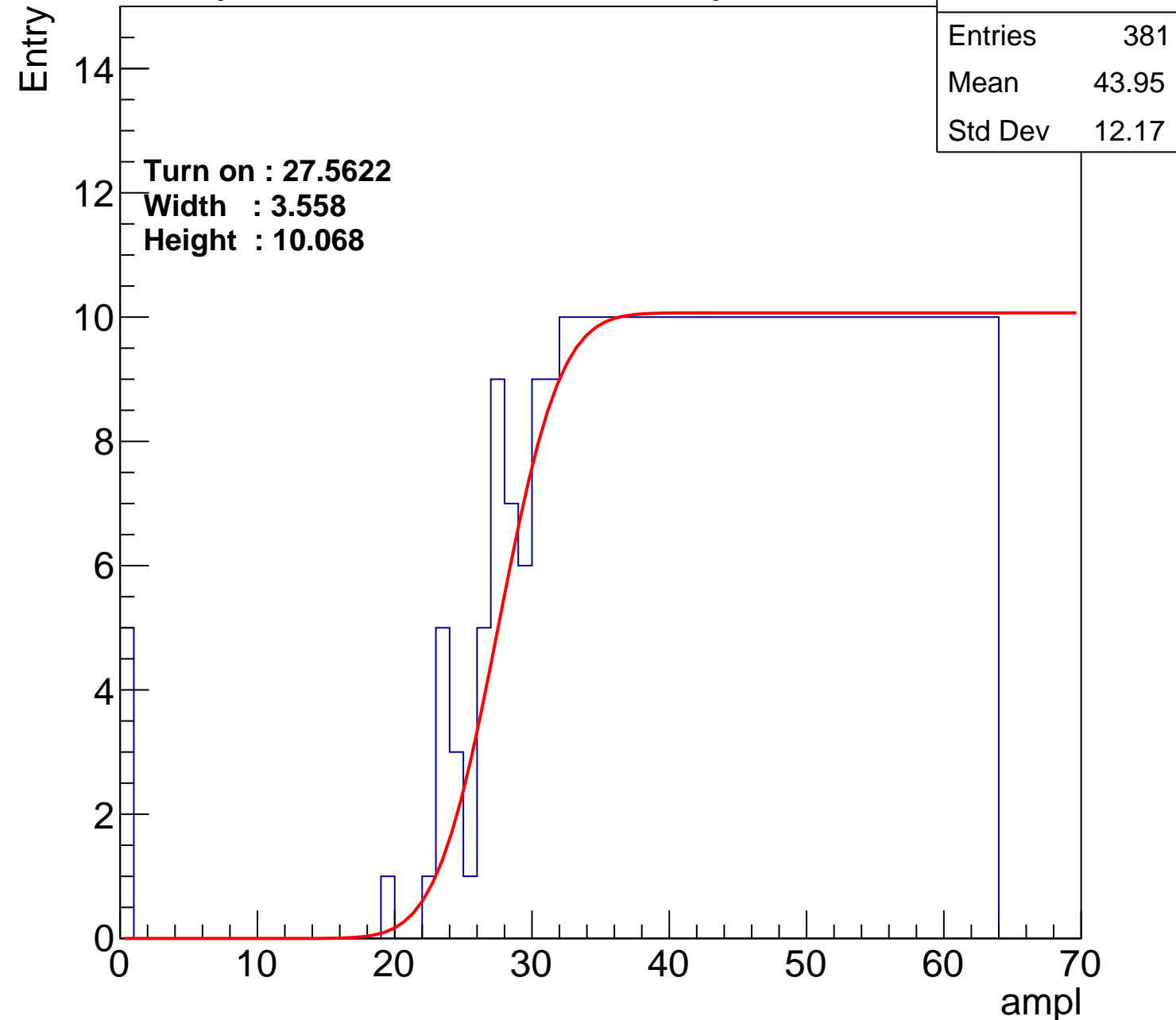
Width : 3.558

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 27.2403

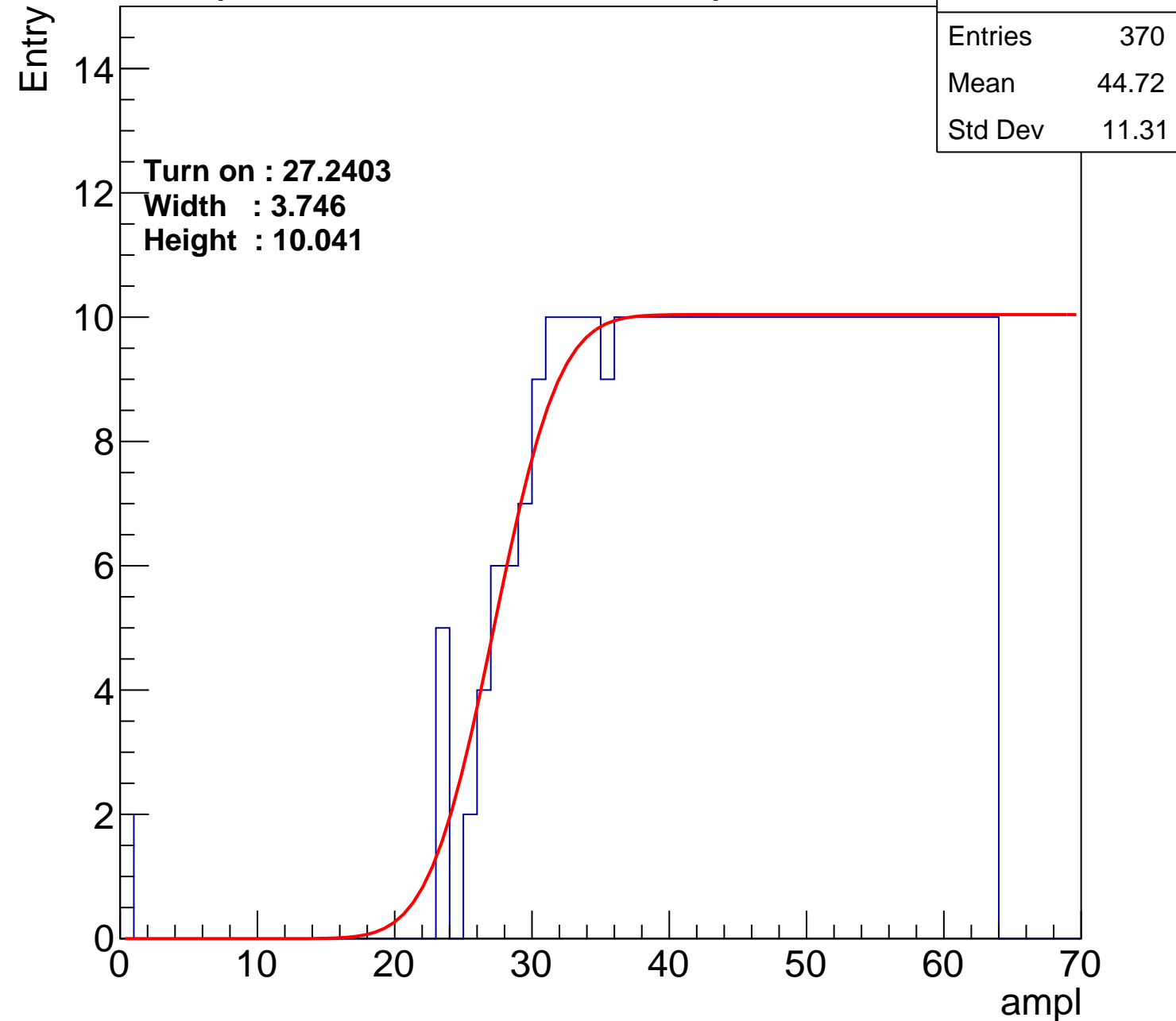
Width : 3.746

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	414
Mean	42.41
Std Dev	12.83

Turn on : 23.7850

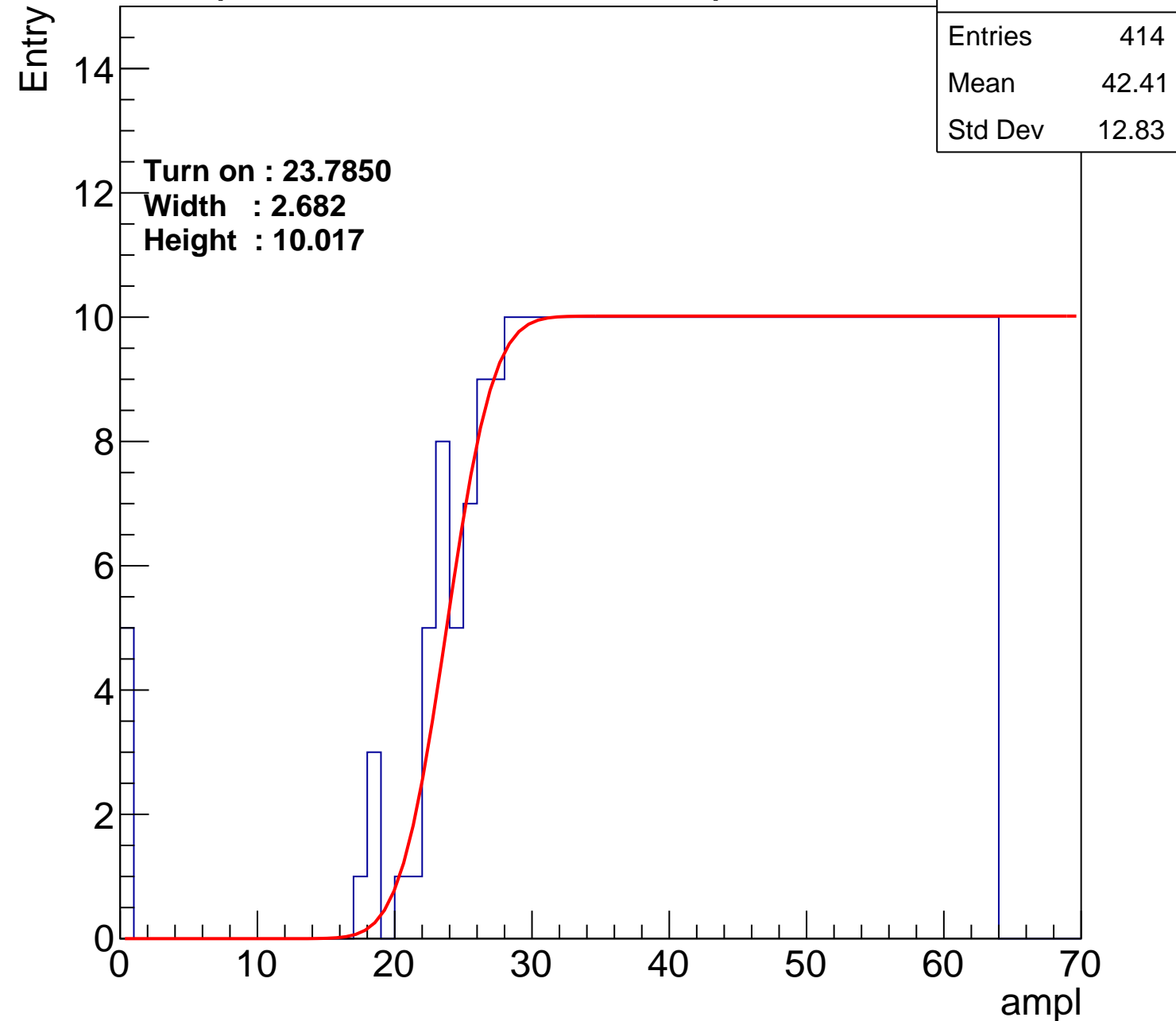
Width : 2.682

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.63
Std Dev	11.34

Turn on : 26.8110

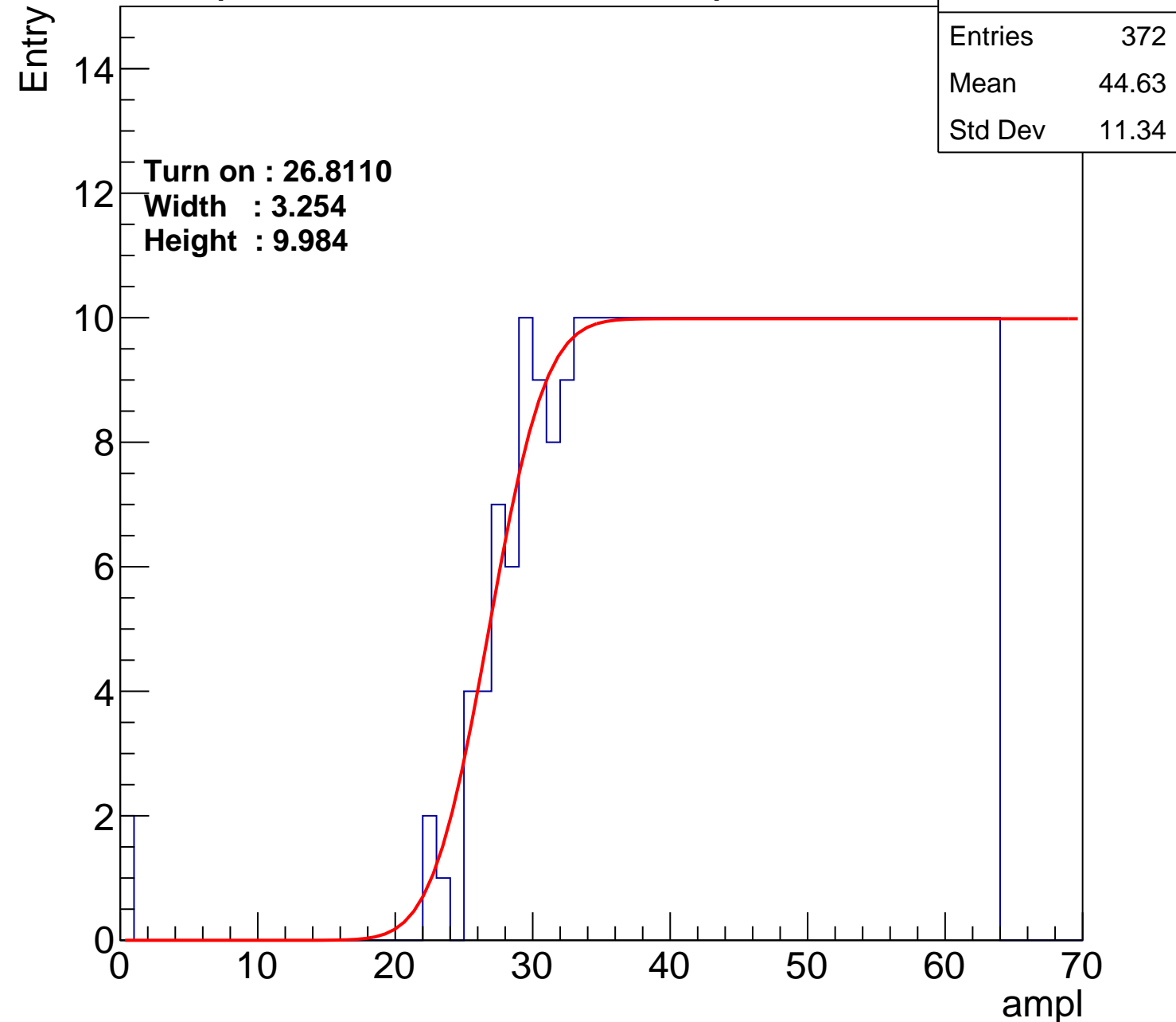
Width : 3.254

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.84
Std Dev	11.64

Turn on : 25.5226

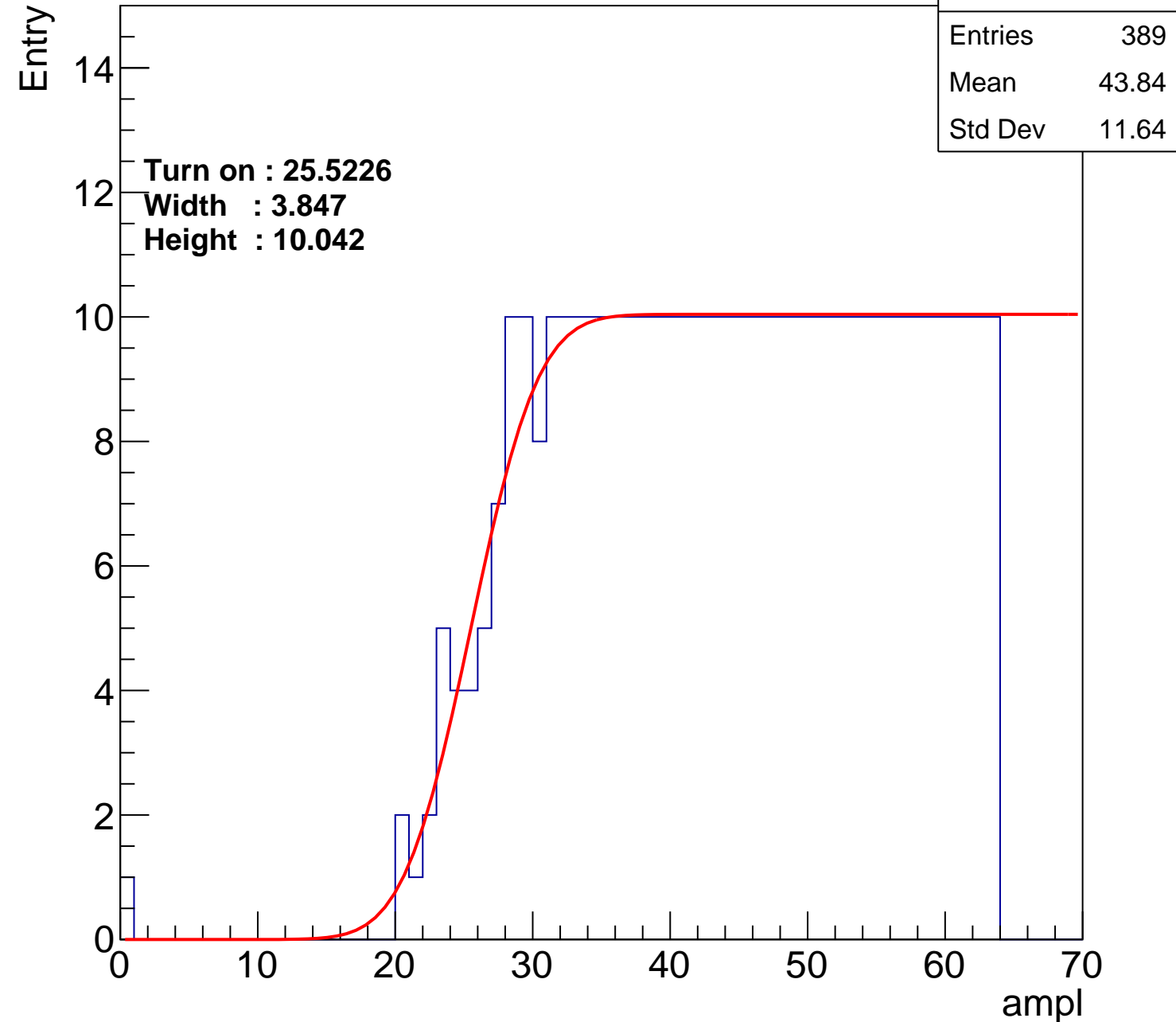
Width : 3.847

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.68
Std Dev	11.7

Turn on : 23.8826

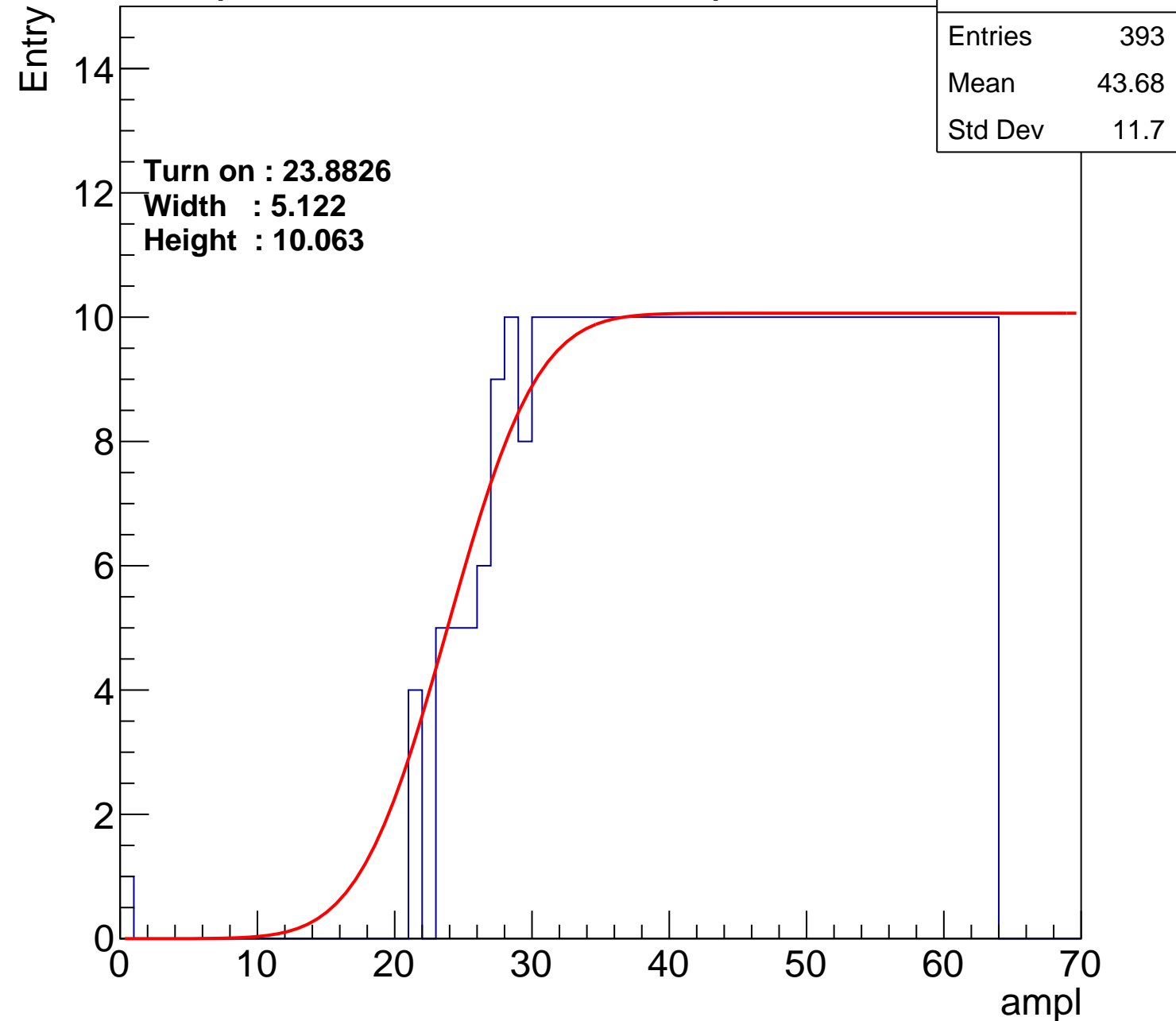
Width : 5.122

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.5
Std Dev	12.79

Turn on : 23.9893

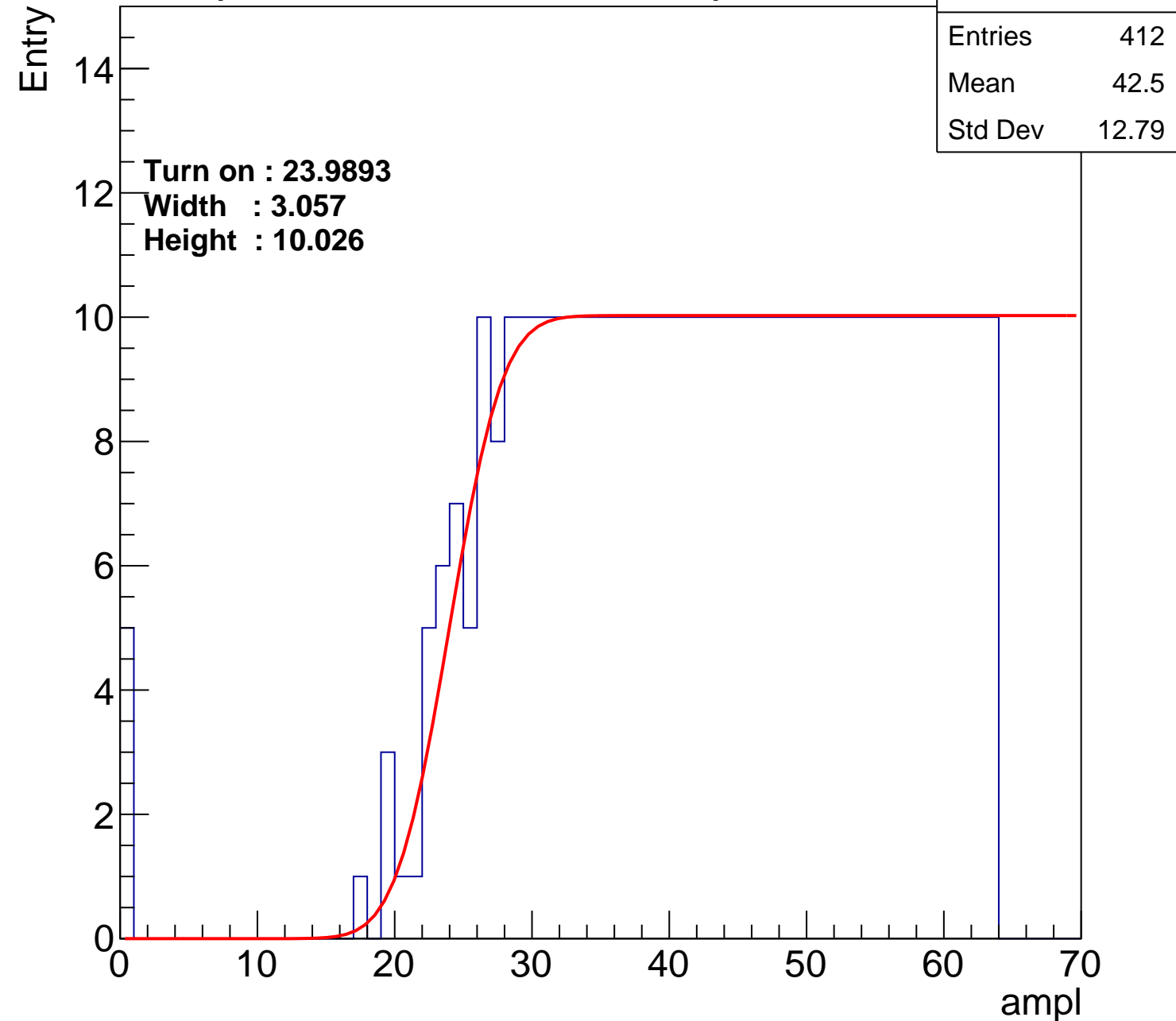
Width : 3.057

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.88
Std Dev	12.01

Turn on : 26.6143

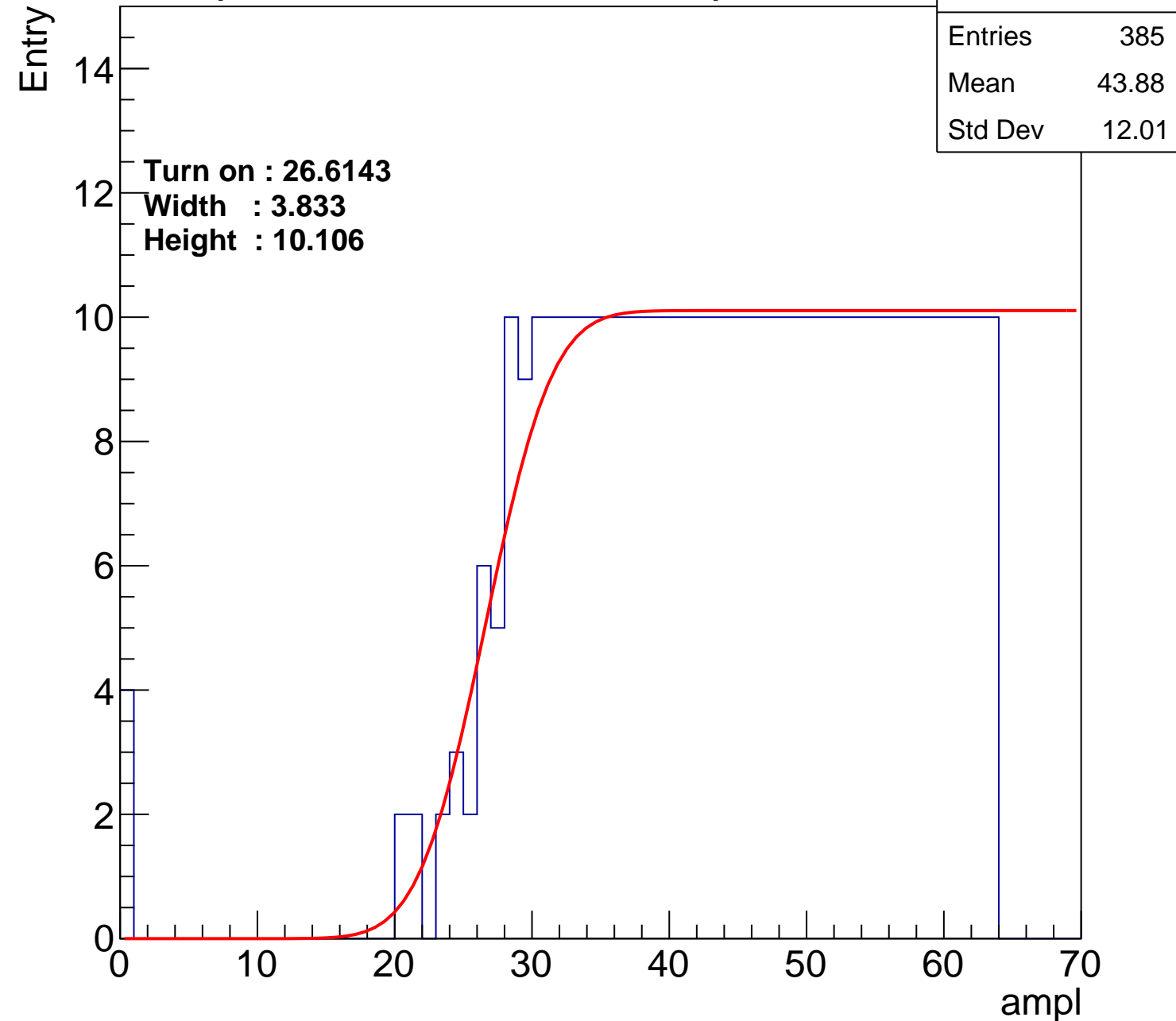
Width : 3.833

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.43
Std Dev	11.76

Turn on : 27.5754

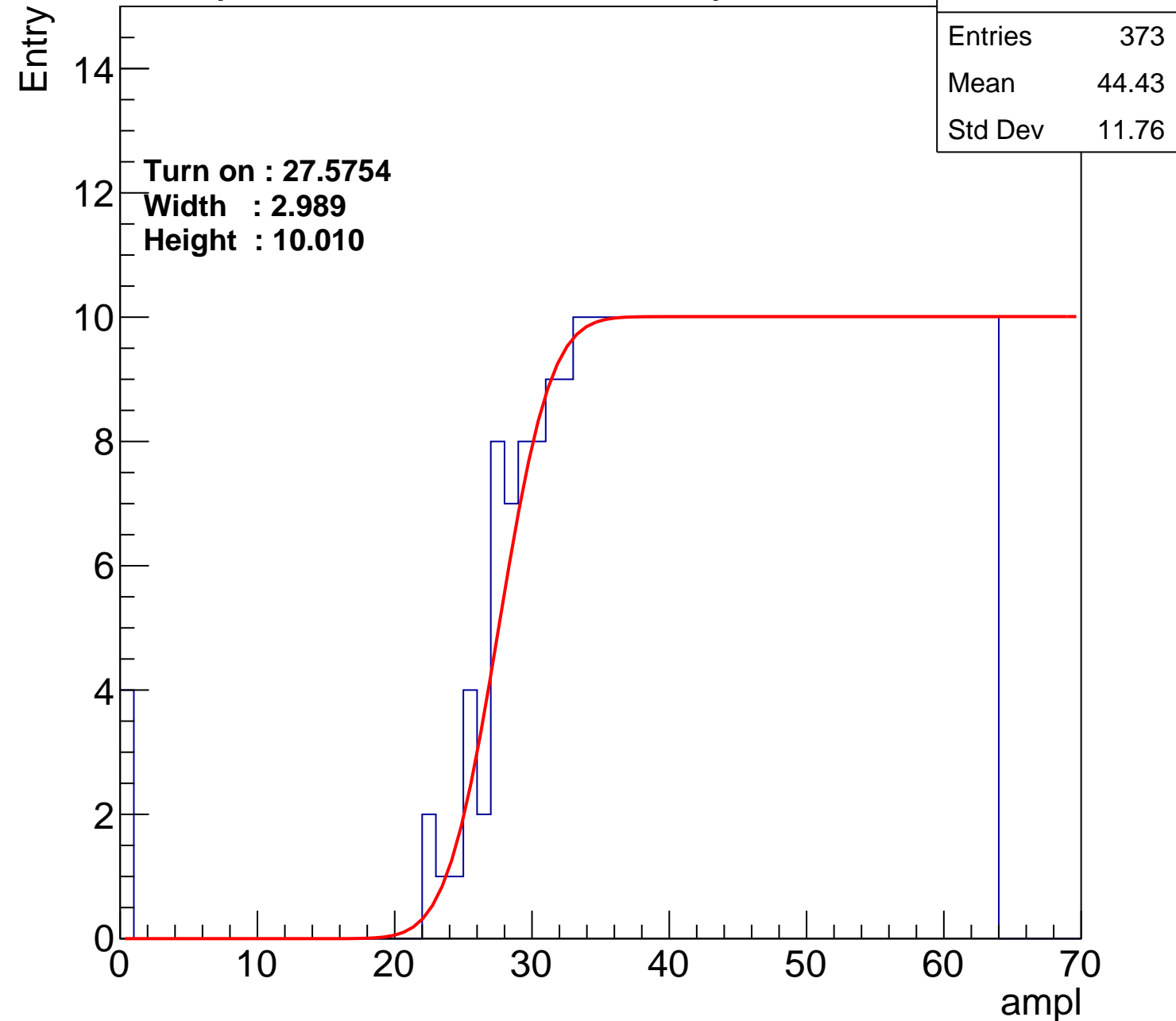
Width : 2.989

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.28
Std Dev	11.91

Turn on : 24.0668

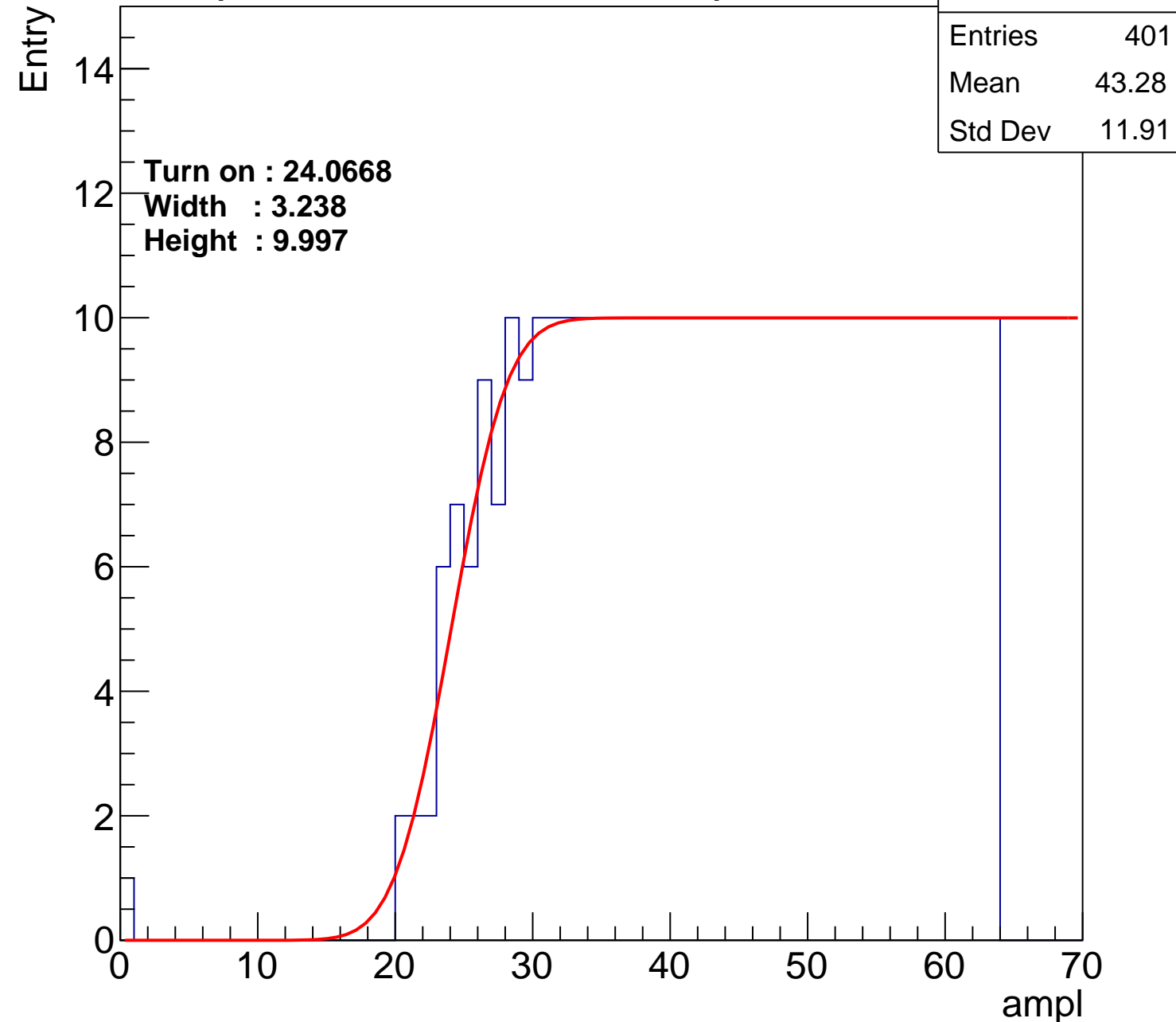
Width : 3.238

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch102

calib_packv5_042523_0143.root, FC#12, port B1

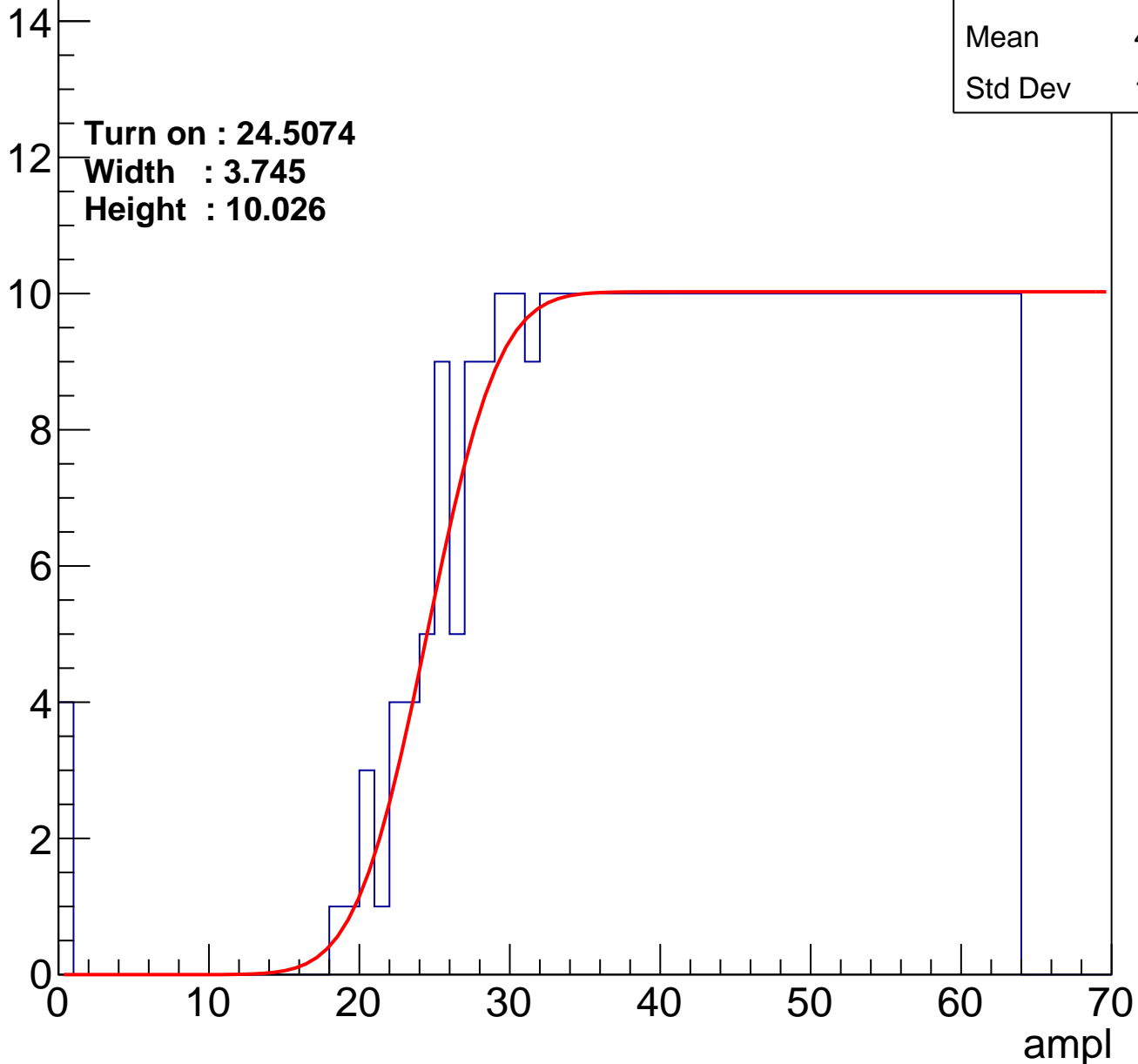
Entries	404
Mean	42.91
Std Dev	12.51

Turn on : 24.5074

Width : 3.745

Height : 10.026

Entry



B0L102S, U6-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	44
Std Dev	11.62

Turn on : 26.0947

Width : 3.269

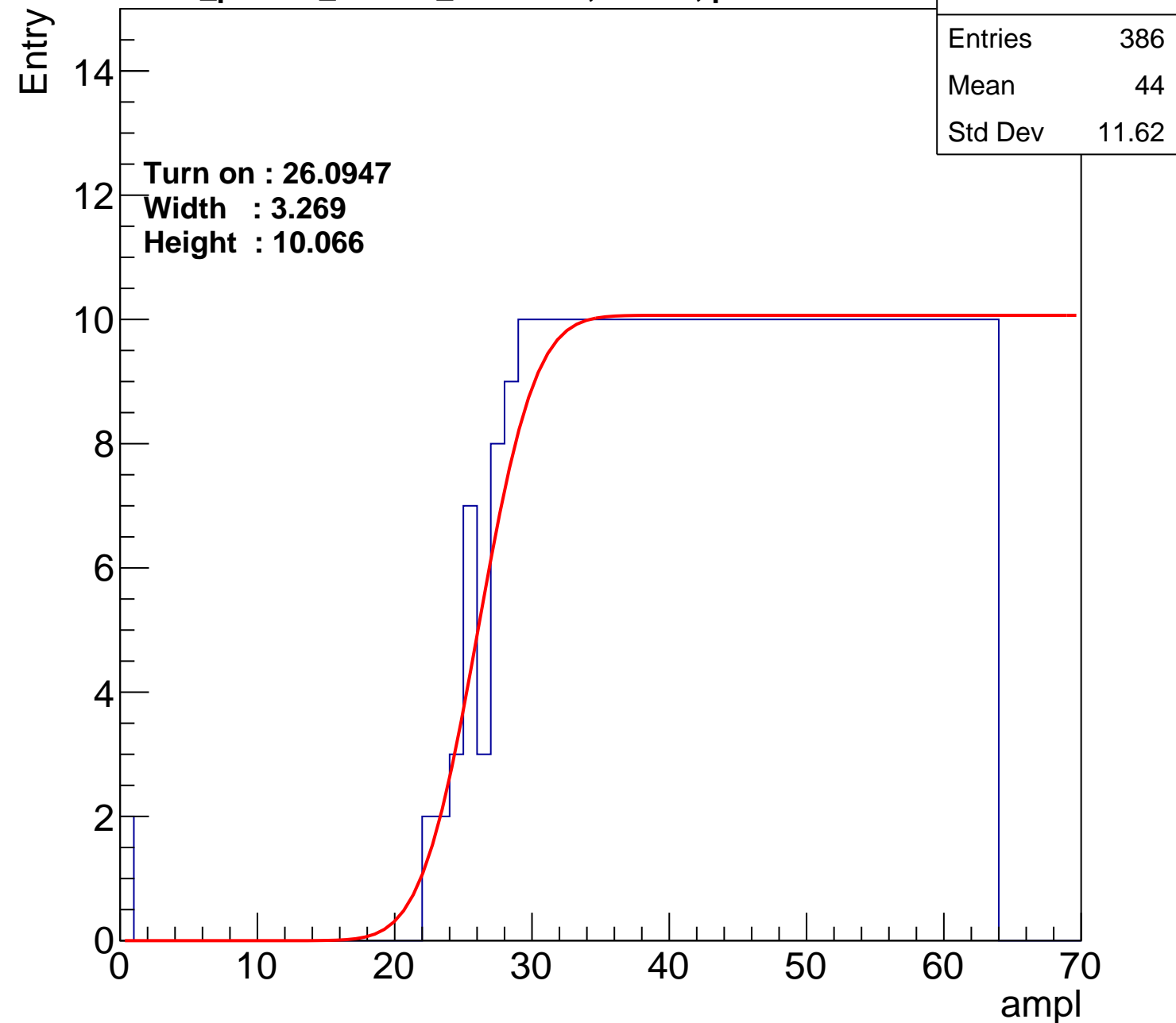
Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U6-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	433
Mean	41.63
Std Dev	12.98

Turn on : 21.2717

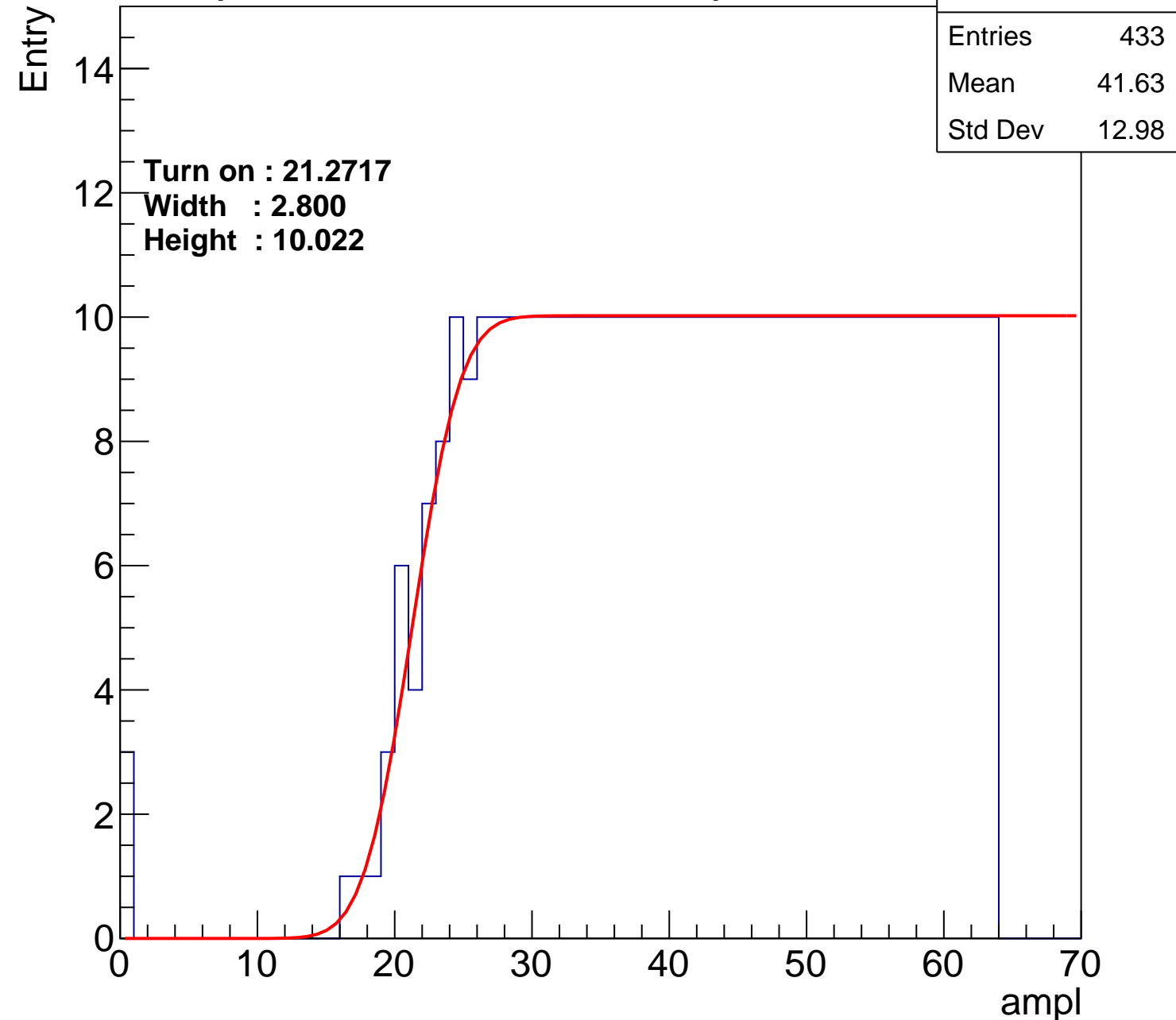
Width : 2.800

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.89

Turn on : 24.6737

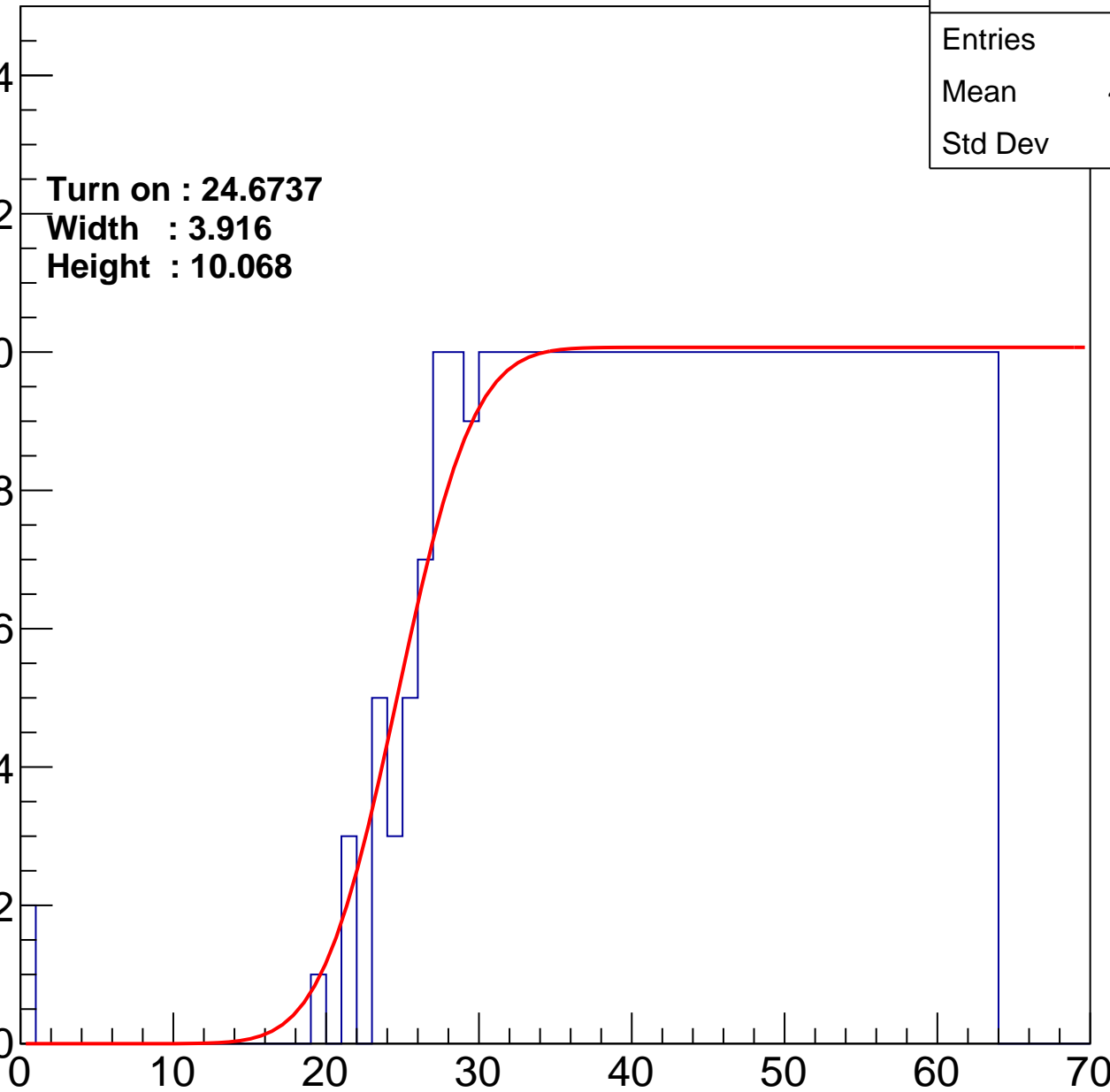
Width : 3.916

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.51
Std Dev	12.33

Turn on : 26.0985

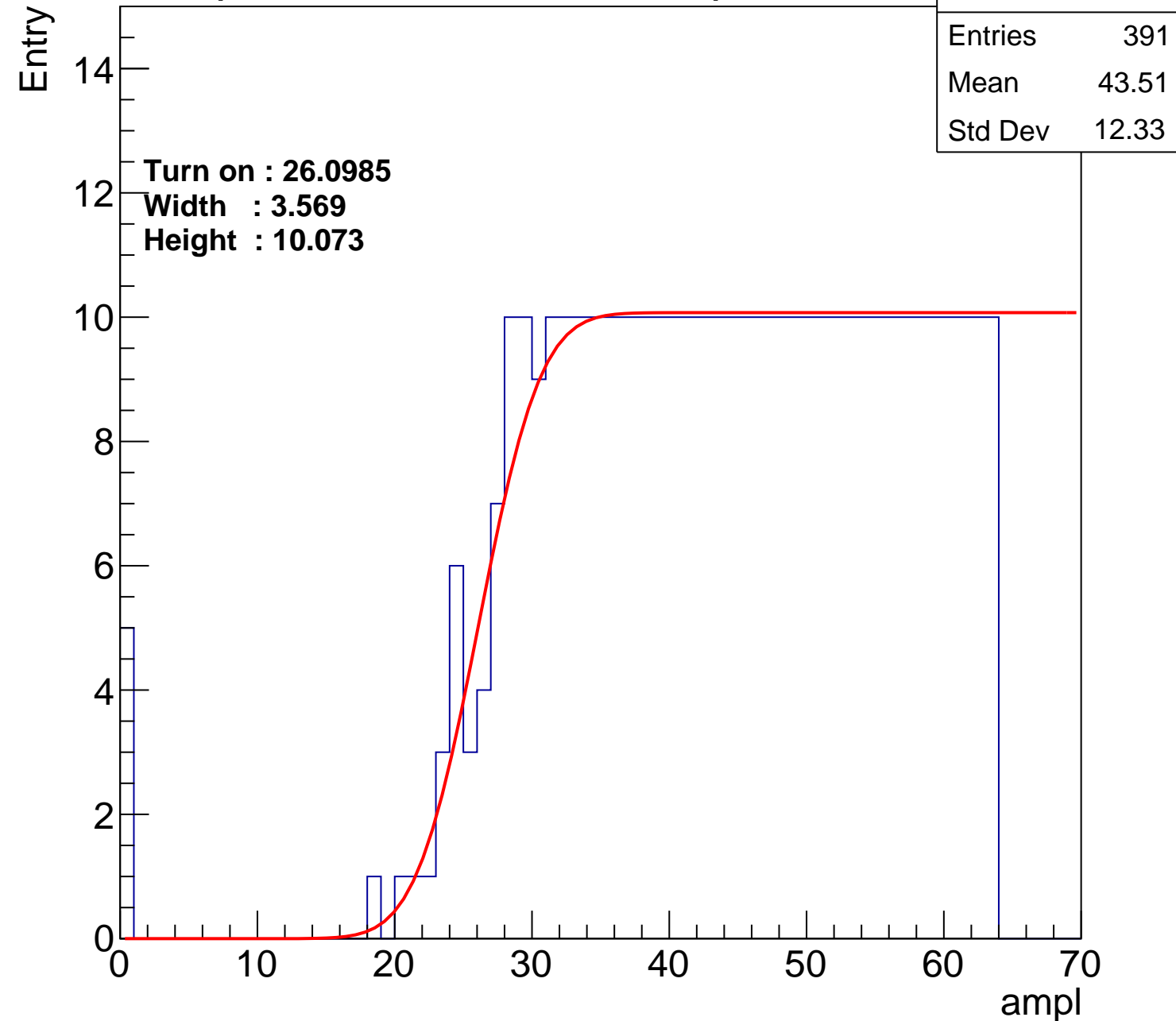
Width : 3.569

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.3404

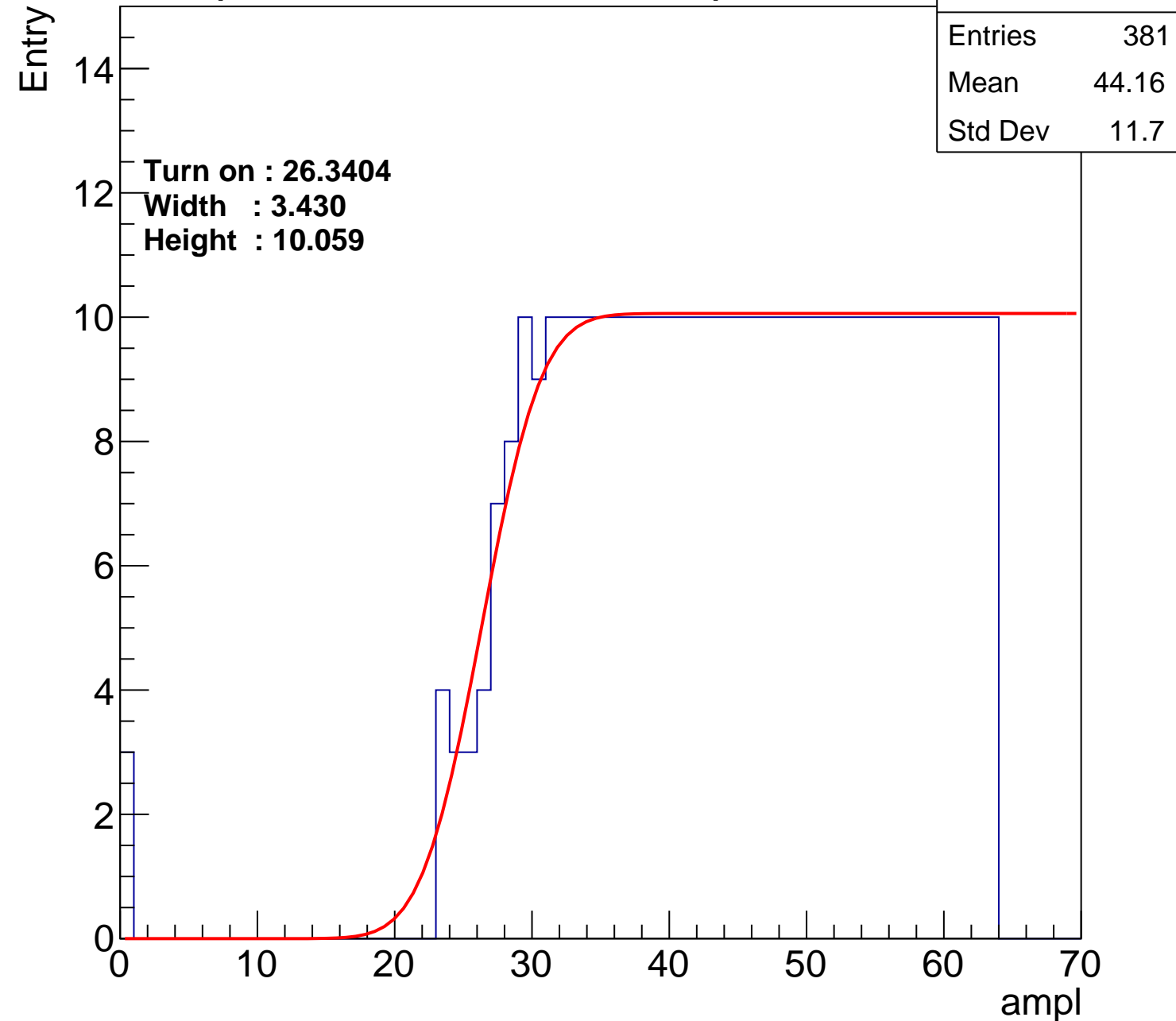
Width : 3.430

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	425
Mean	42
Std Dev	13.01

Turn on : 23.0364

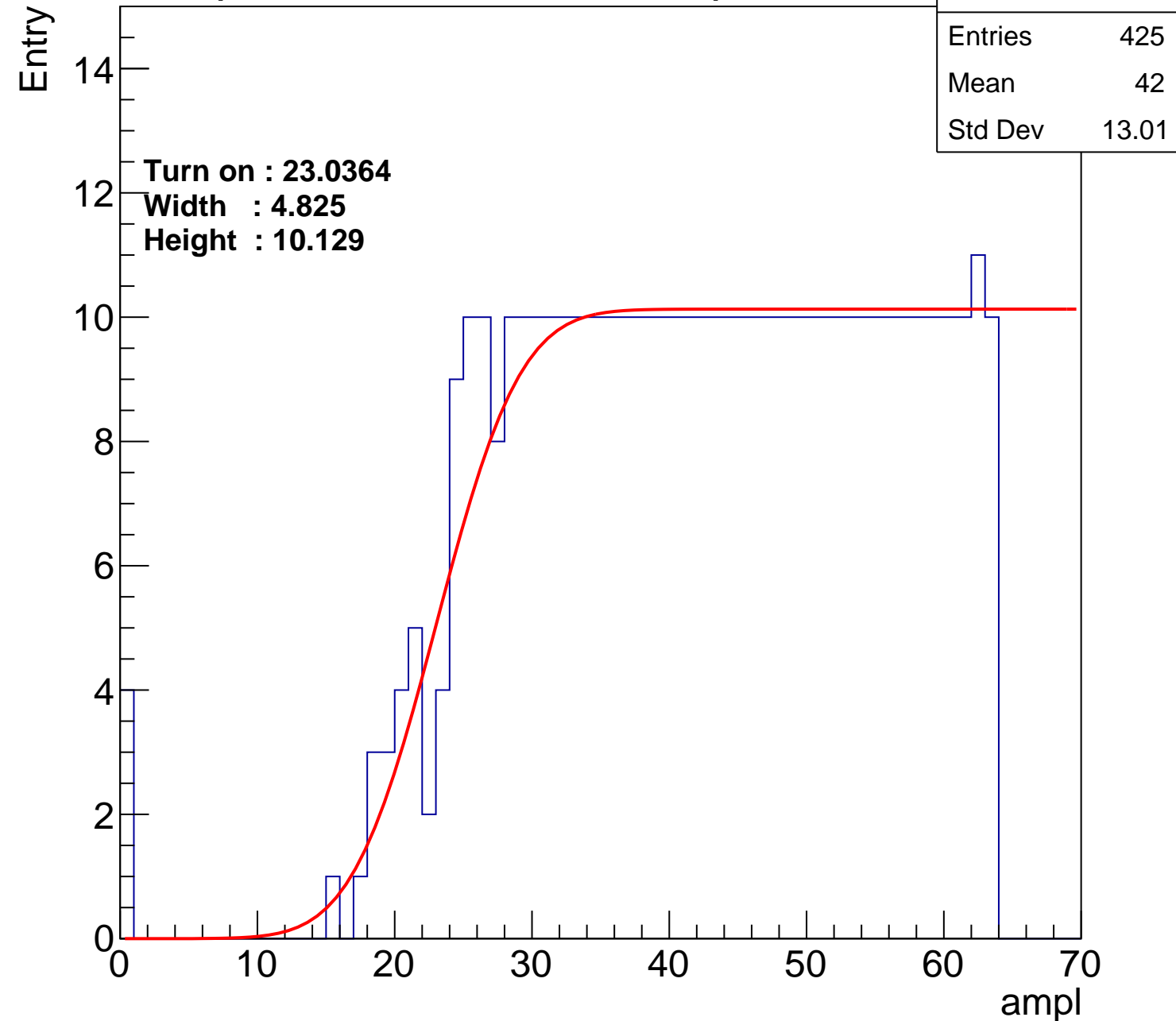
Width : 4.825

Height : 10.129

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.9309

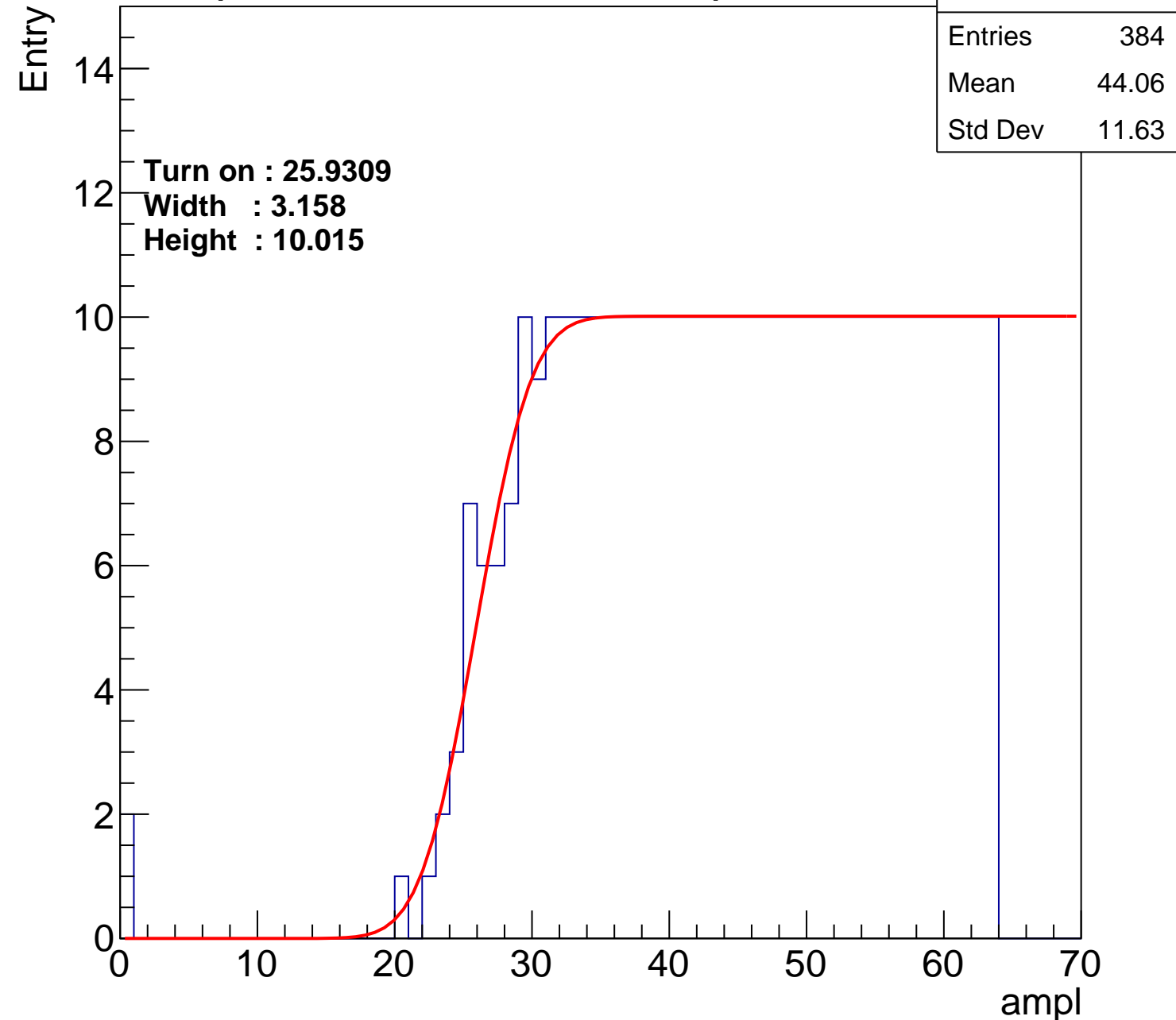
Width : 3.158

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.52
Std Dev	12.09

Turn on : 25.4949

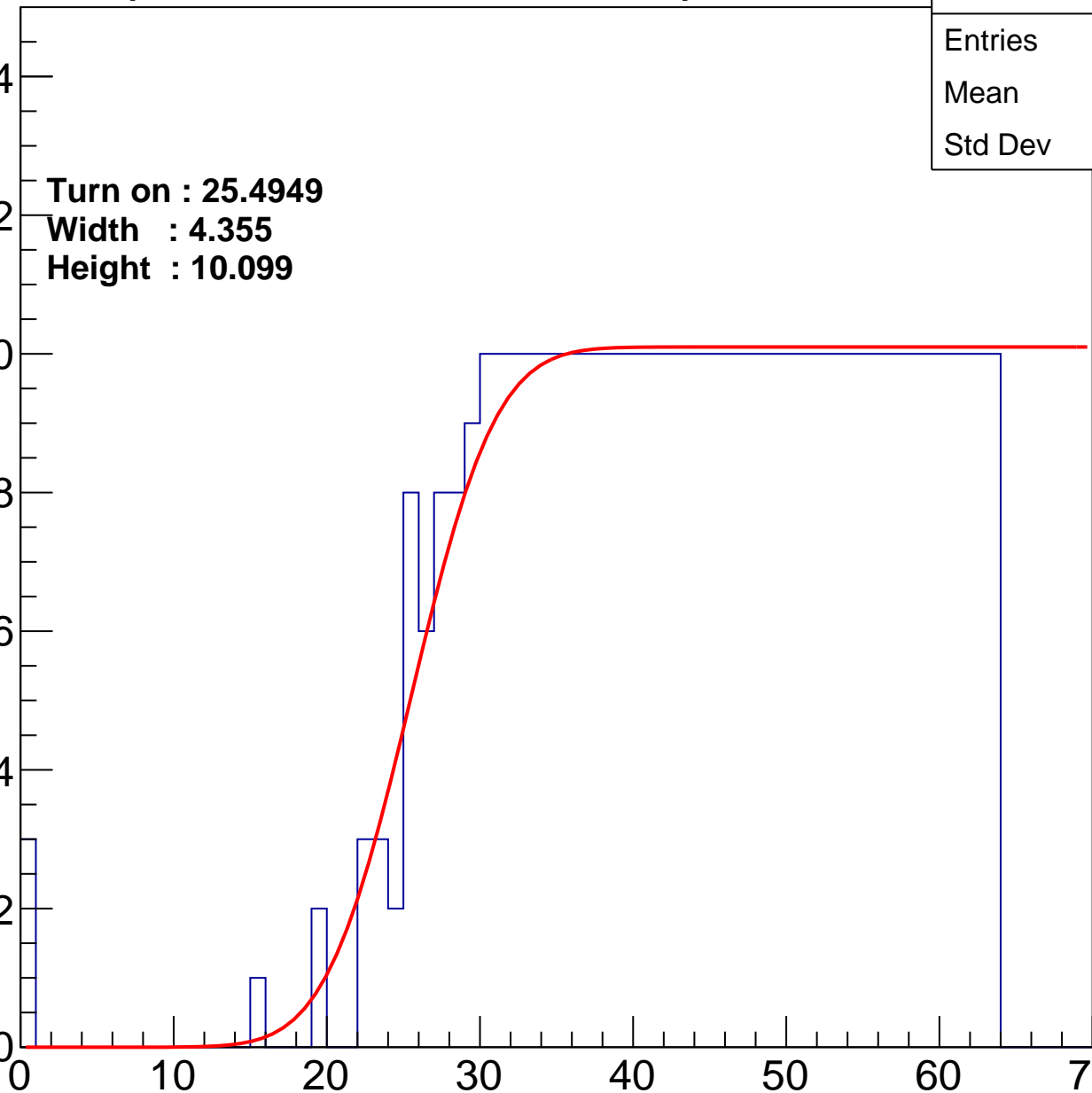
Width : 4.355

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 24.4183

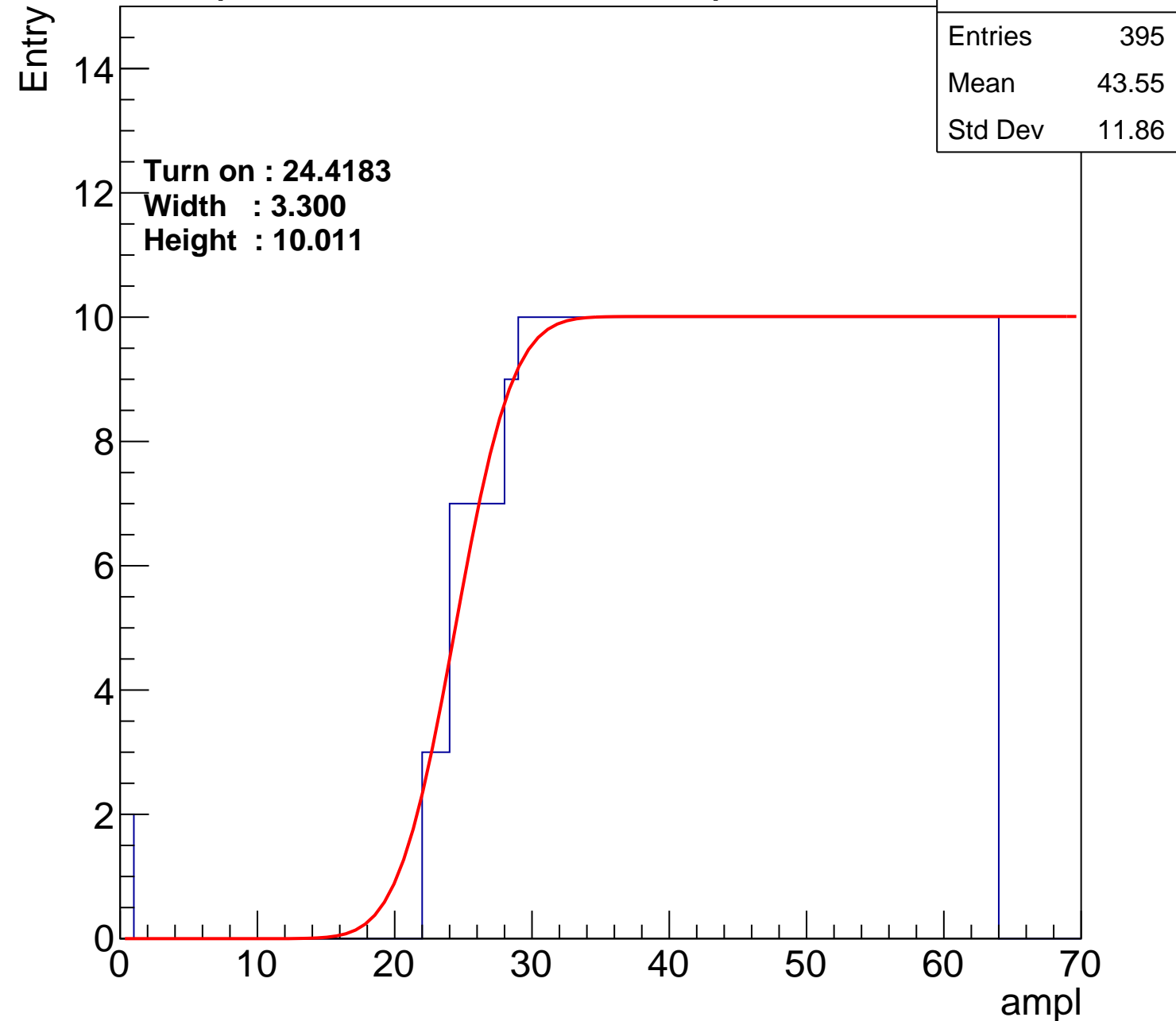
Width : 3.300

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.49
Std Dev	12.05

Turn on : 25.1655

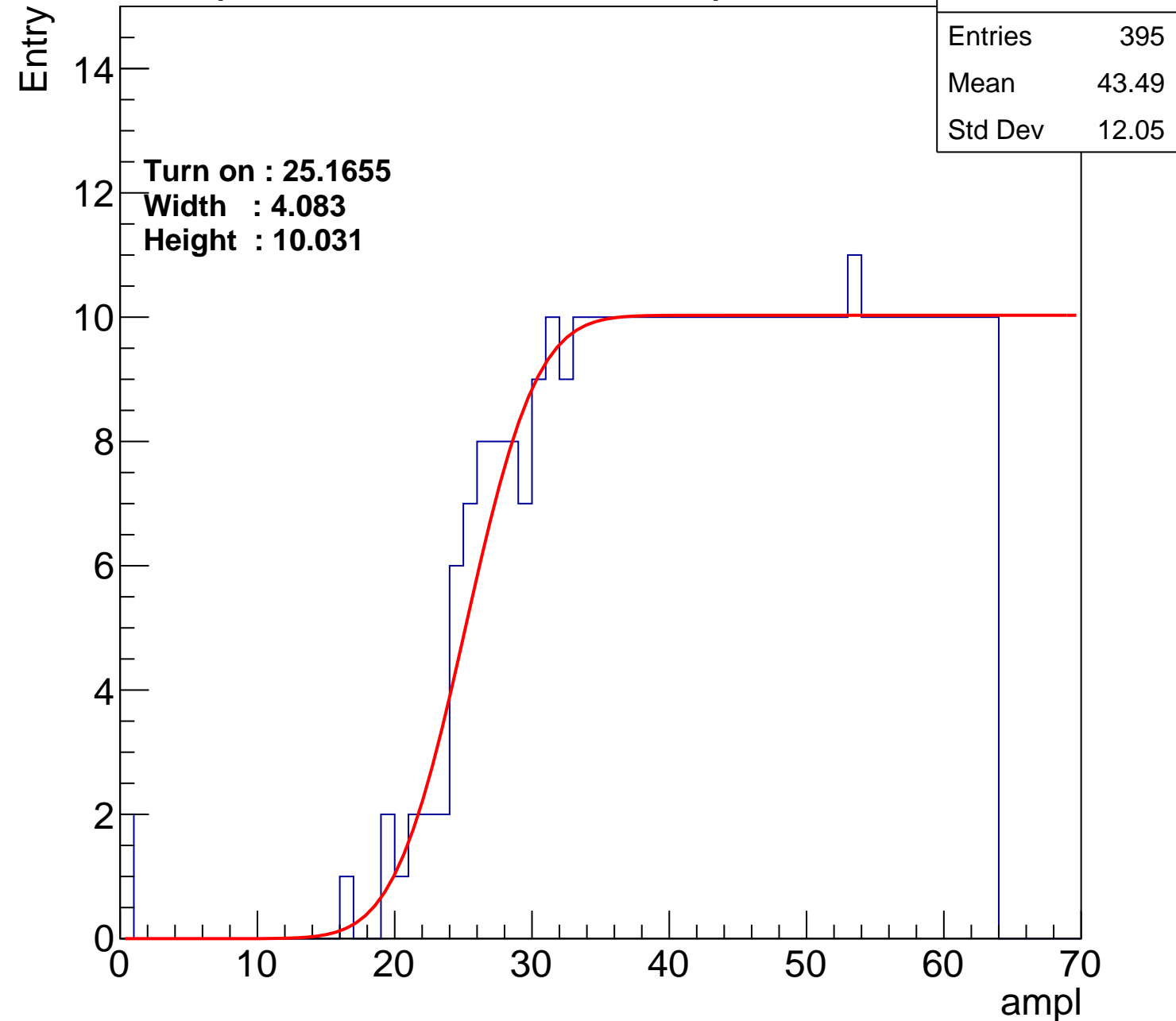
Width : 4.083

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.38
Std Dev	12.18

Turn on : 26.0649

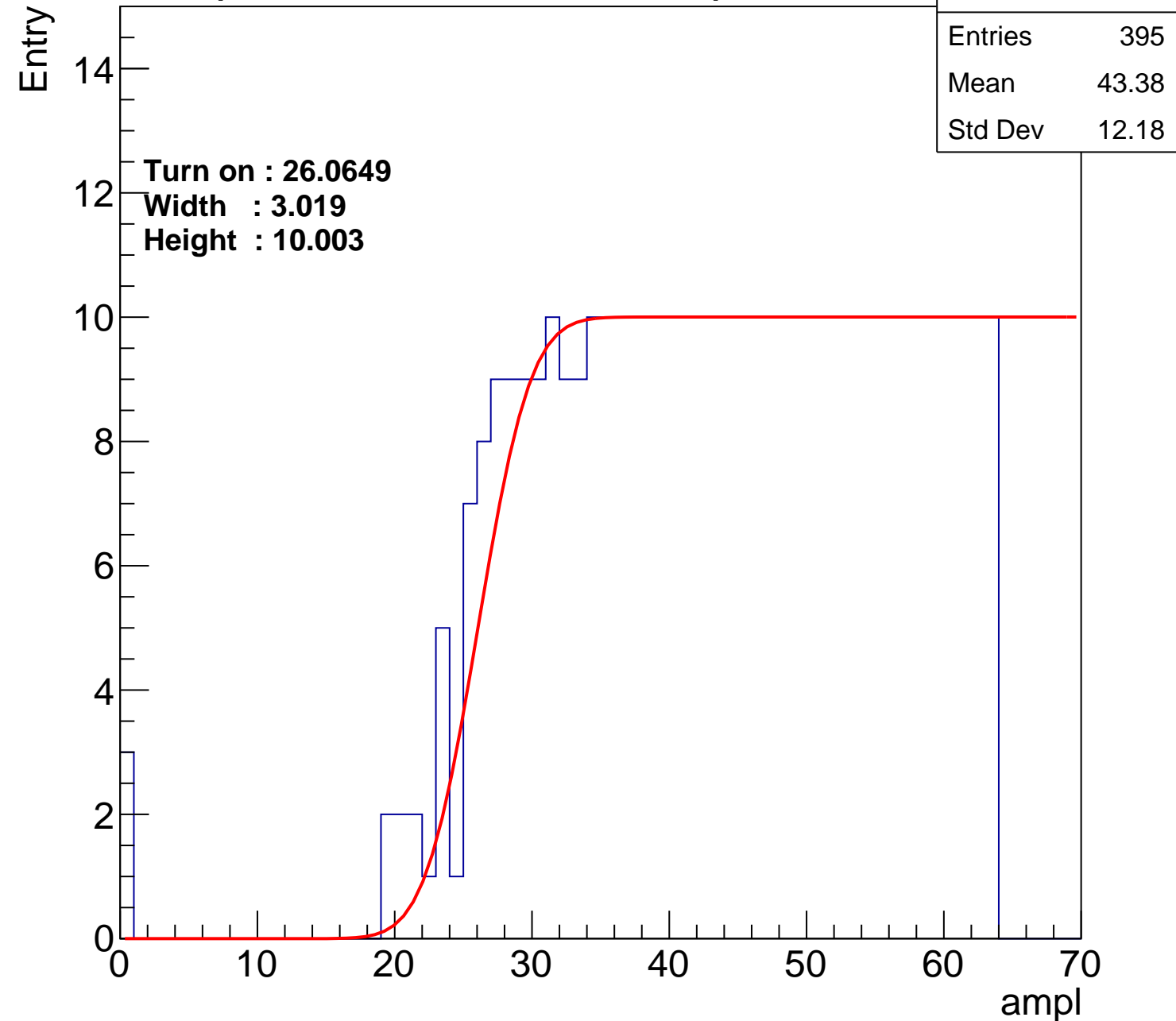
Width : 3.019

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.17
Std Dev	12.25

Turn on : 24.9882

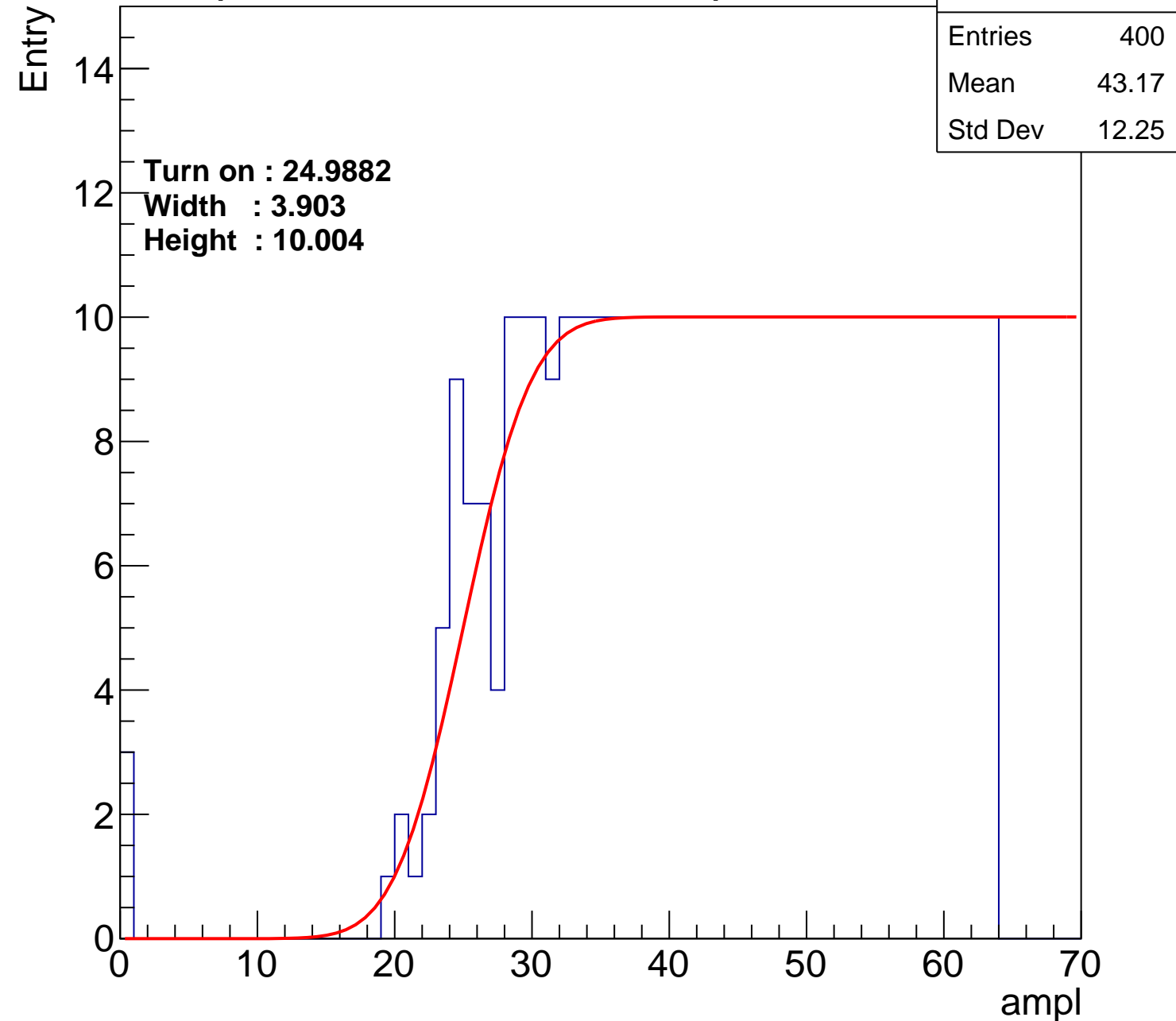
Width : 3.903

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.16
Std Dev	11.77

Turn on : 27.3804

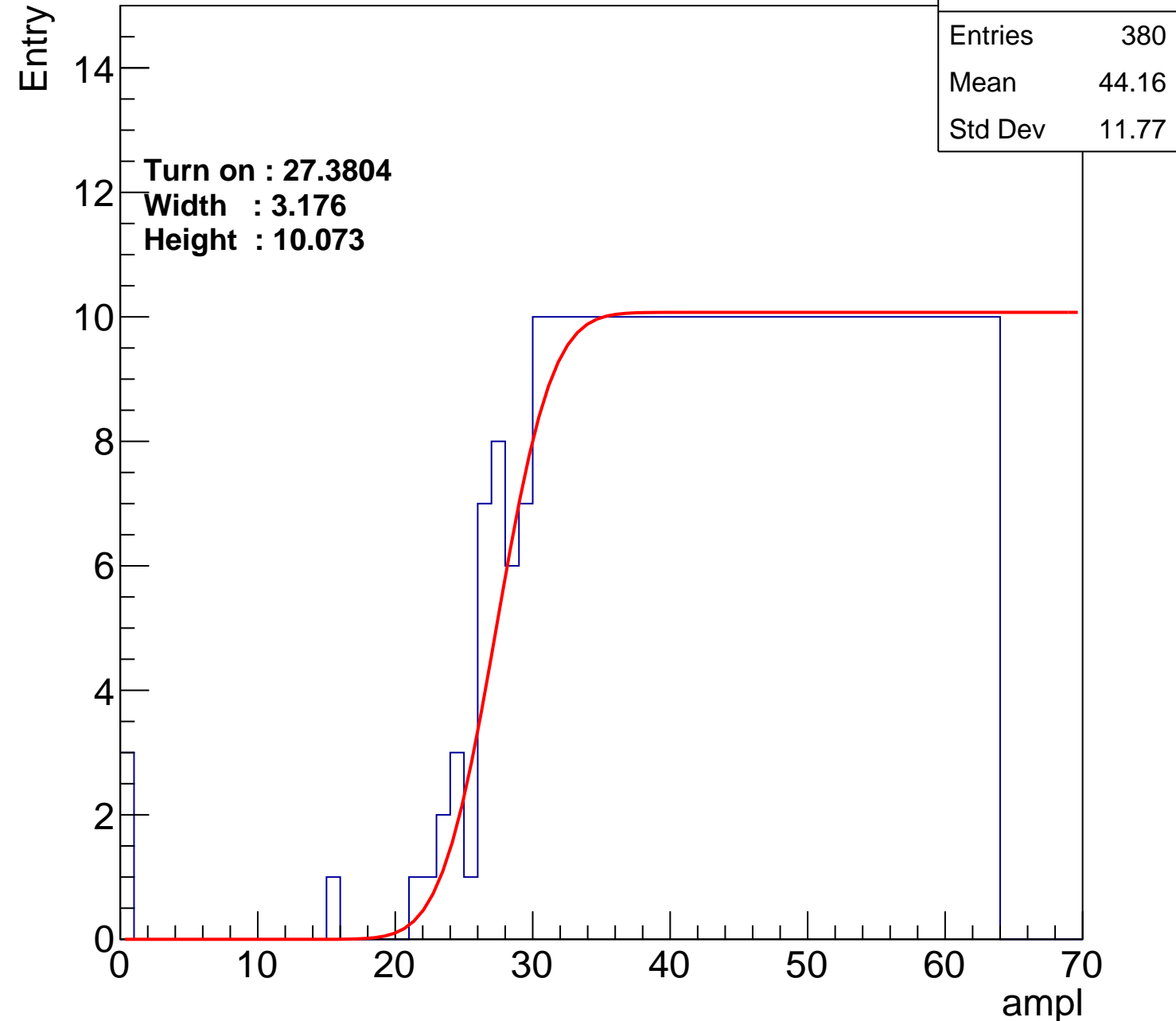
Width : 3.176

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.22
Std Dev	12.14

Turn on : 24.4565

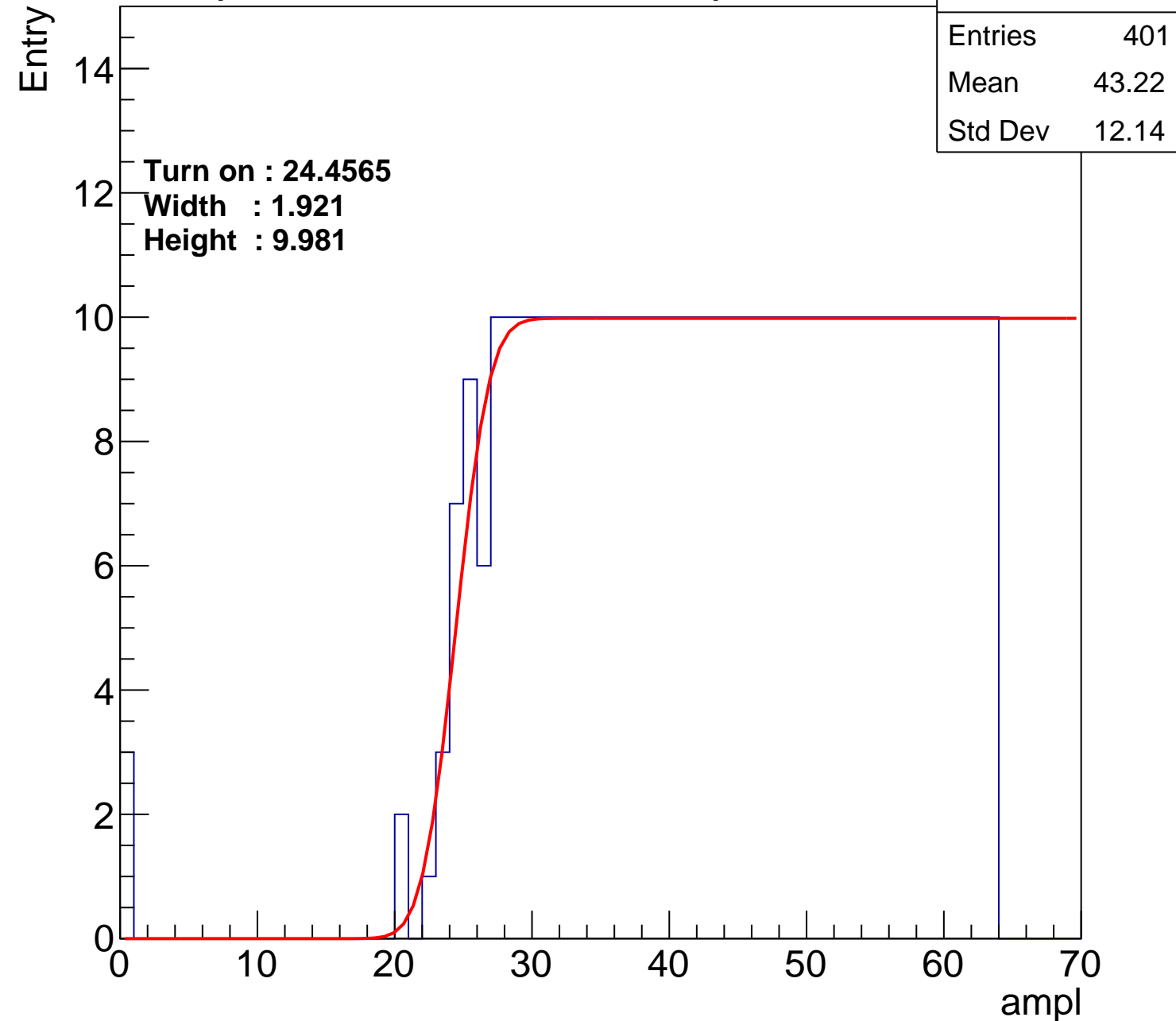
Width : 1.921

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.31
Std Dev	12.14

Turn on : 24.6133

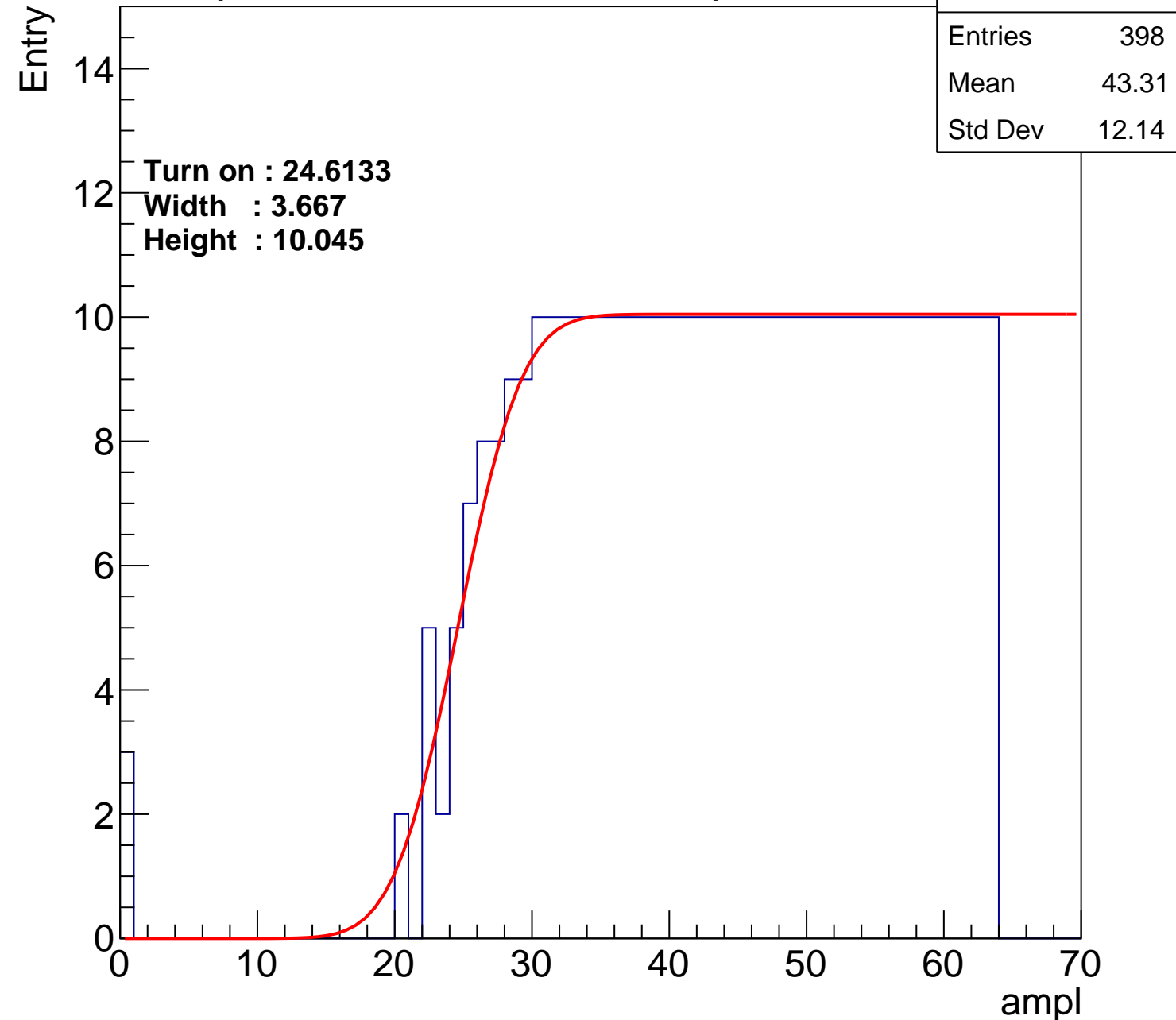
Width : 3.667

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.79
Std Dev	11.98

Turn on : 25.9942

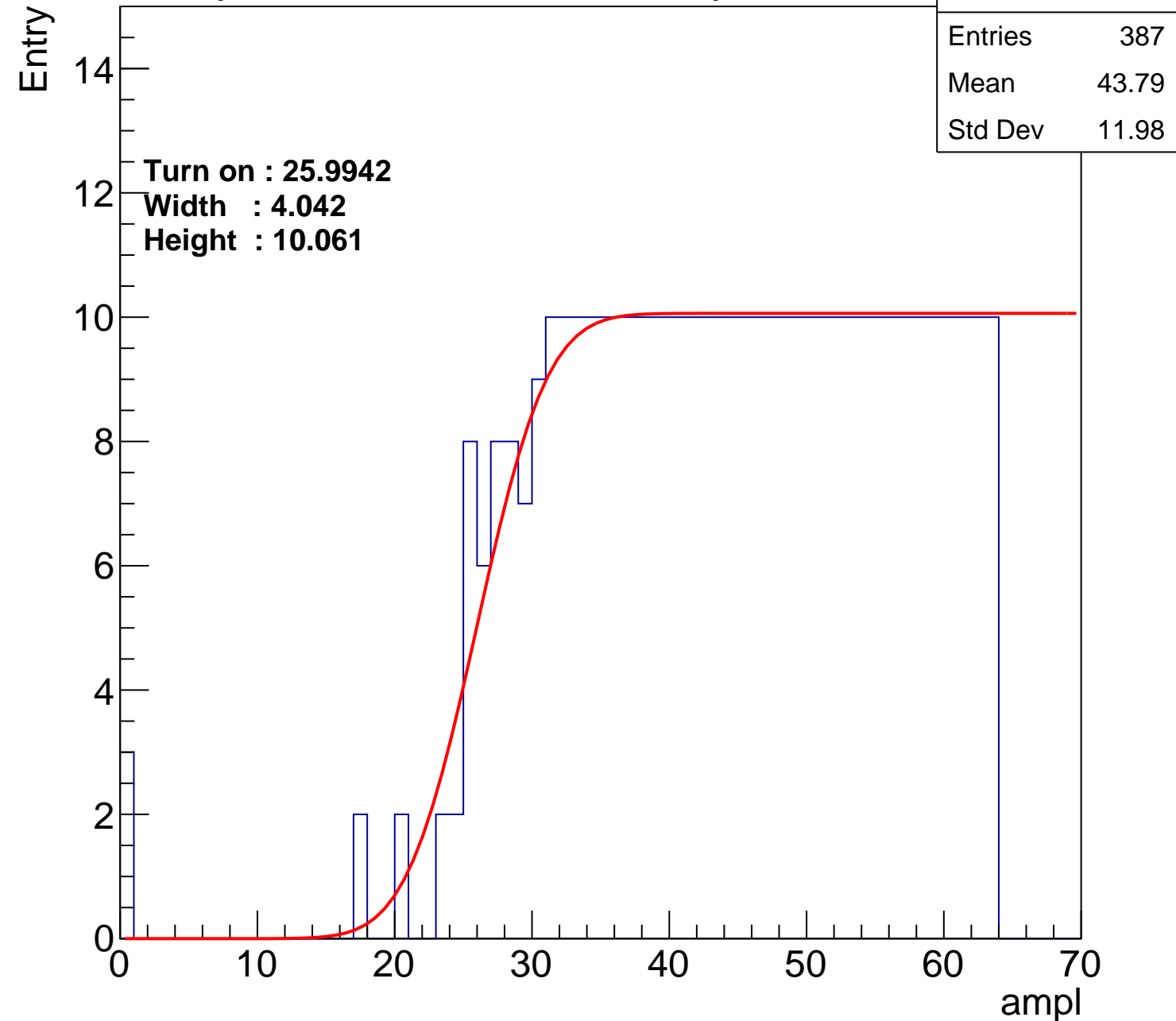
Width : 4.042

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.94
Std Dev	12.36

Turn on : 24.1658

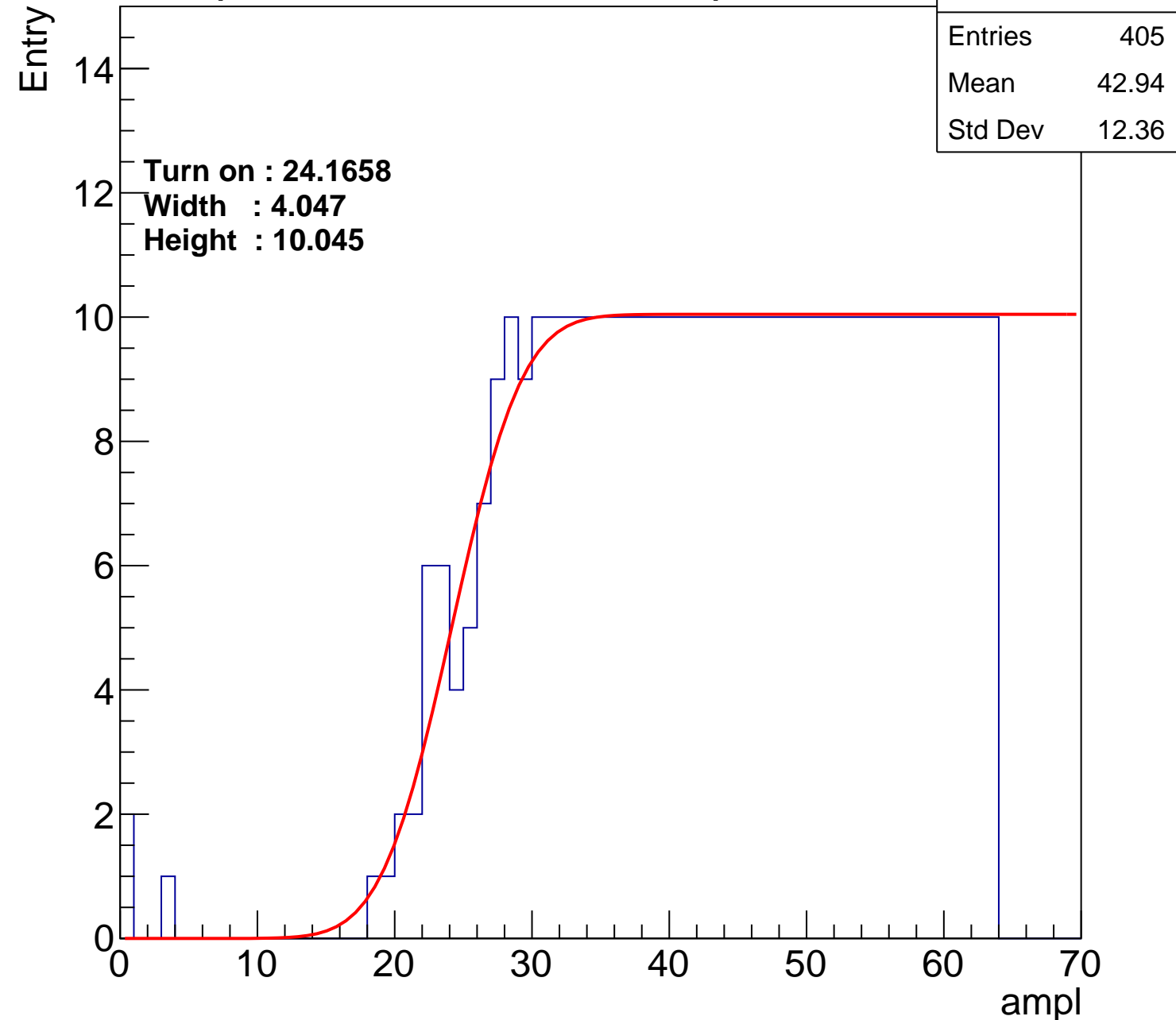
Width : 4.047

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.36
Std Dev	12.07

Turn on : 25.0106

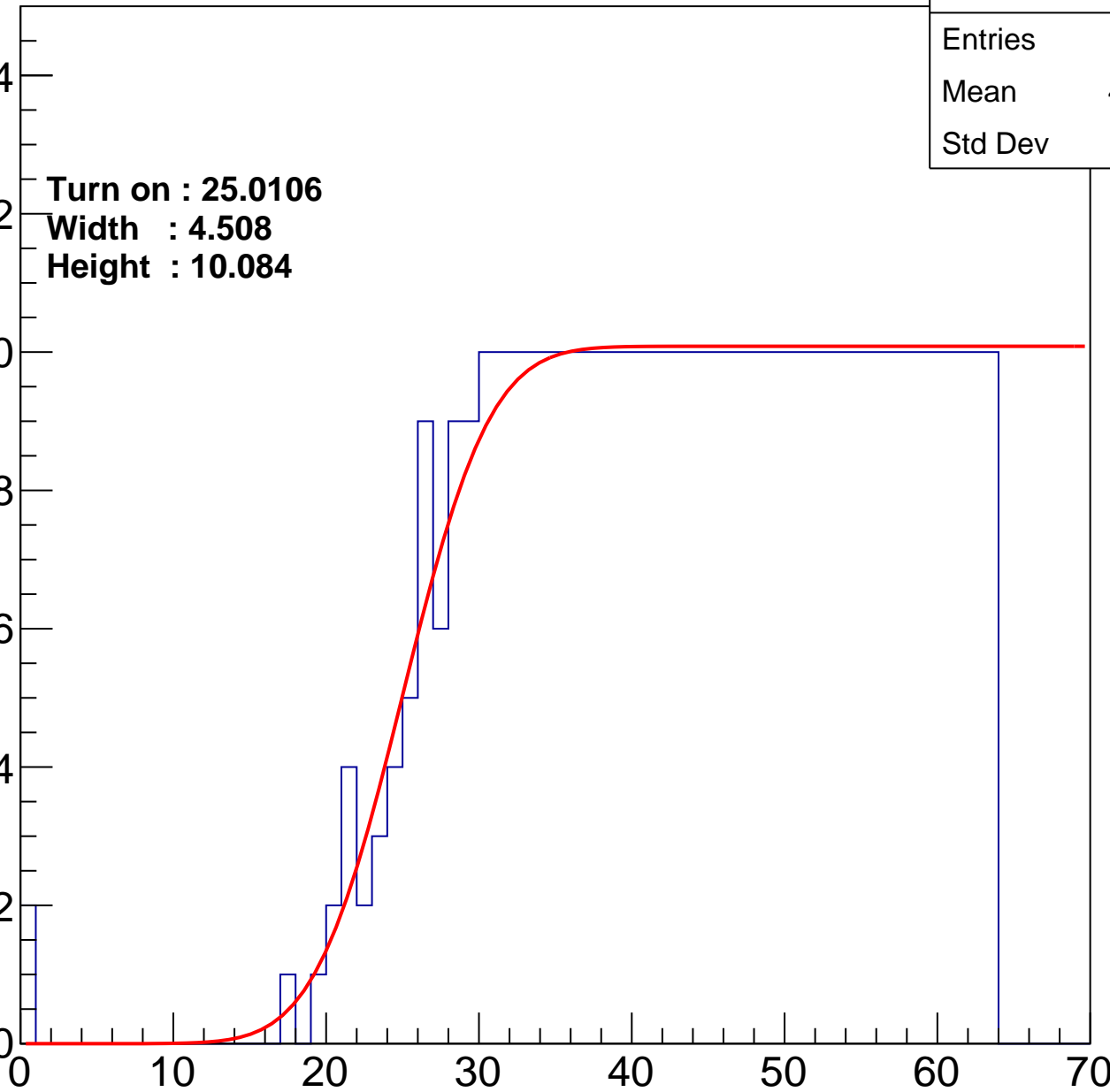
Width : 4.508

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch121

calib_packv5_042523_0143.root, FC#12, port B1

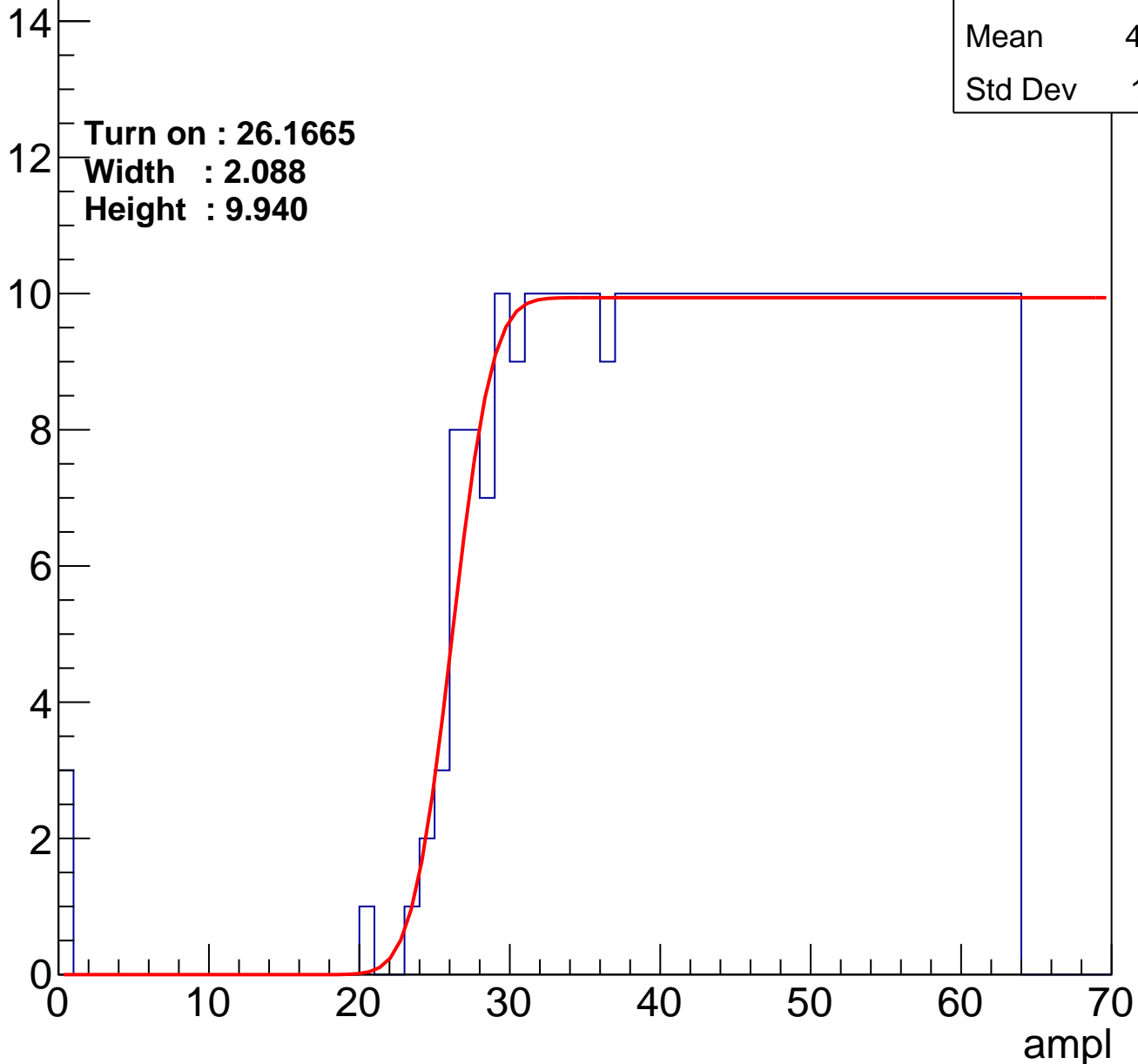
Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.1665

Width : 2.088

Height : 9.940

Entry



B0L102S, U6-ch122

calib_packv5_042523_0143.root, FC#12, port B1

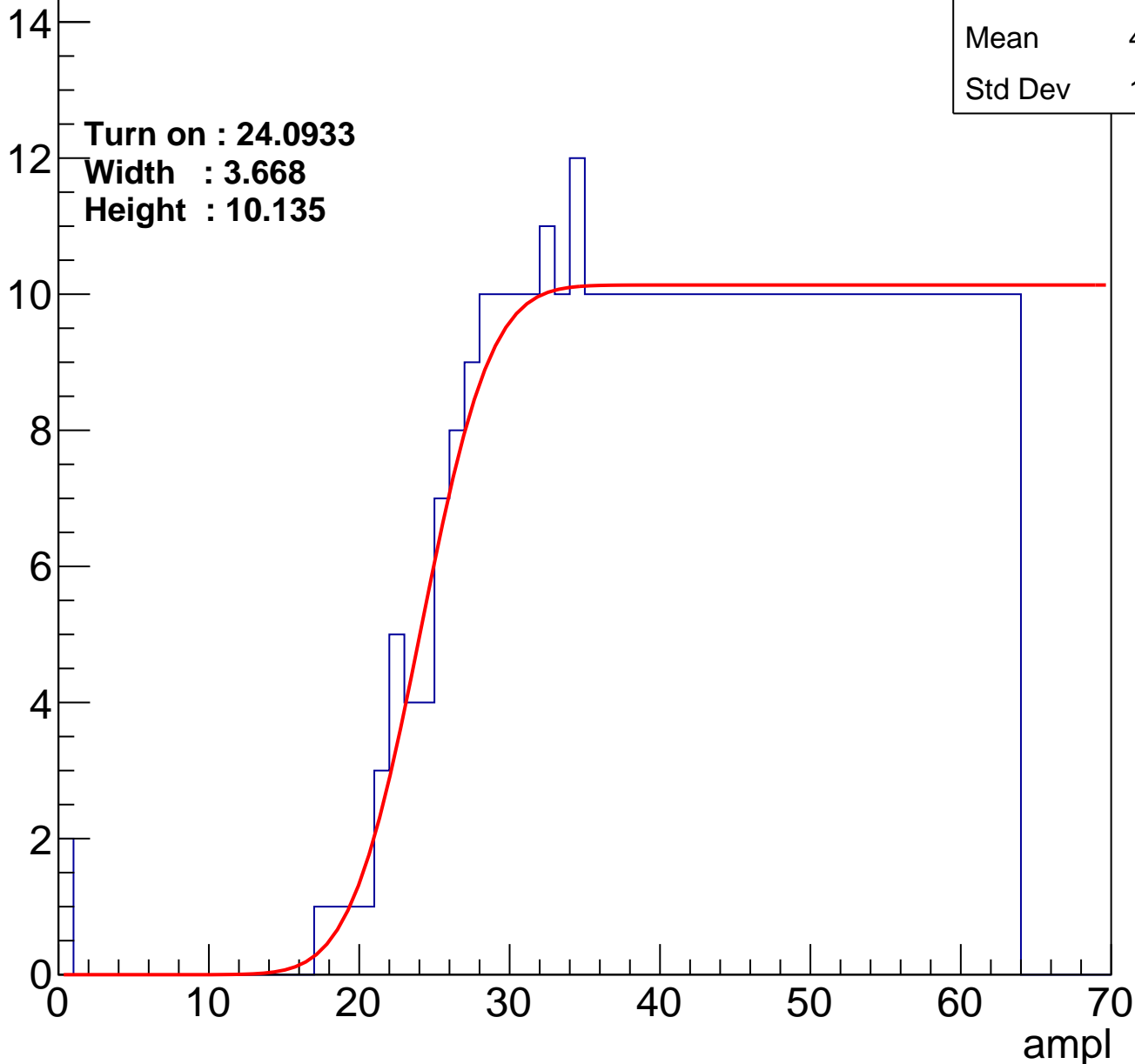
Entries	409
Mean	42.89
Std Dev	12.22

Turn on : 24.0933

Width : 3.668

Height : 10.135

Entry



B0L102S, U6-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.25
Std Dev	12.19

Turn on : 25.0781

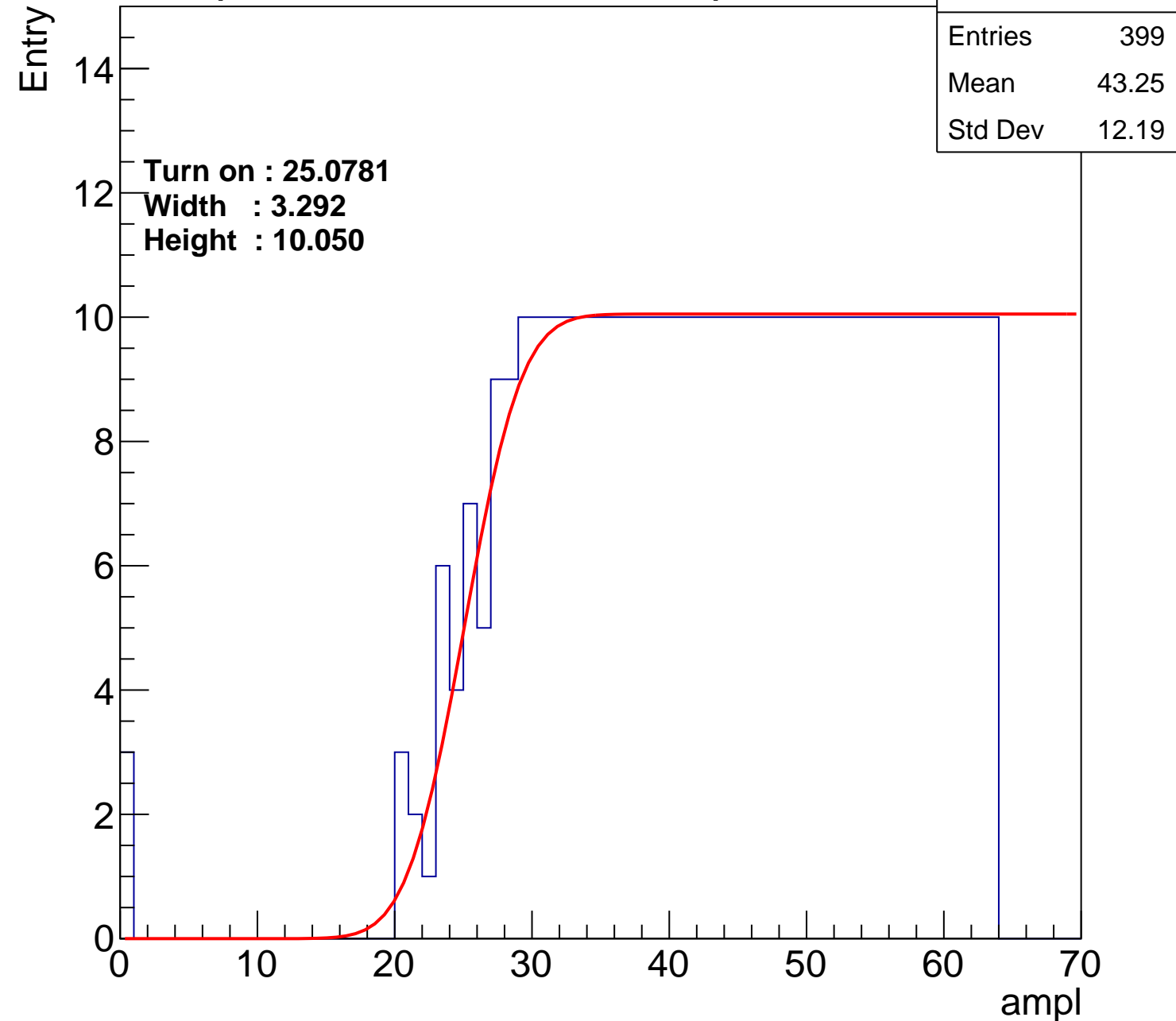
Width : 3.292

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	423
Mean	41.98
Std Dev	12.98

Turn on : 22.4622

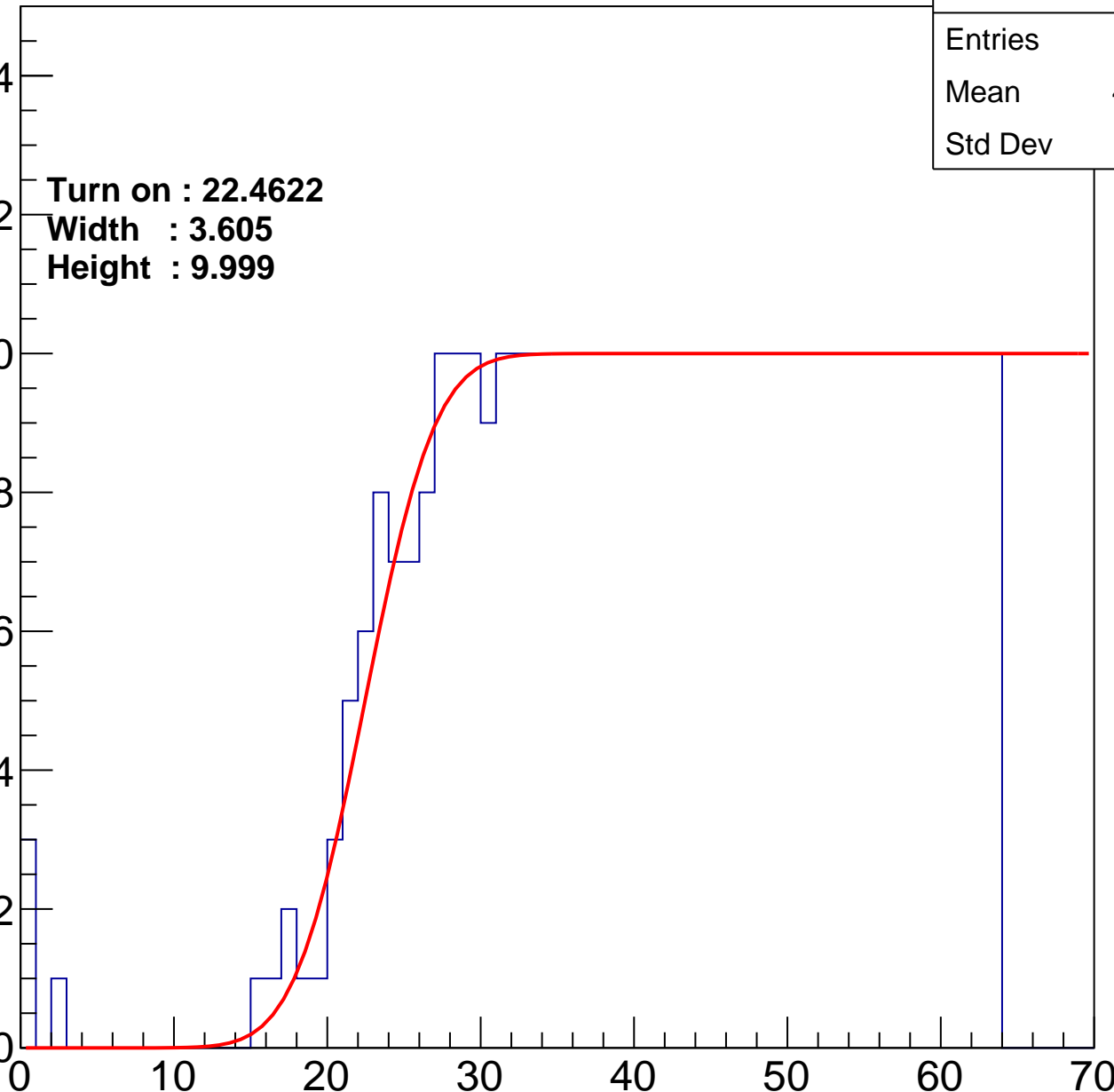
Width : 3.605

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.14
Std Dev	11.73

Turn on : 26.0468

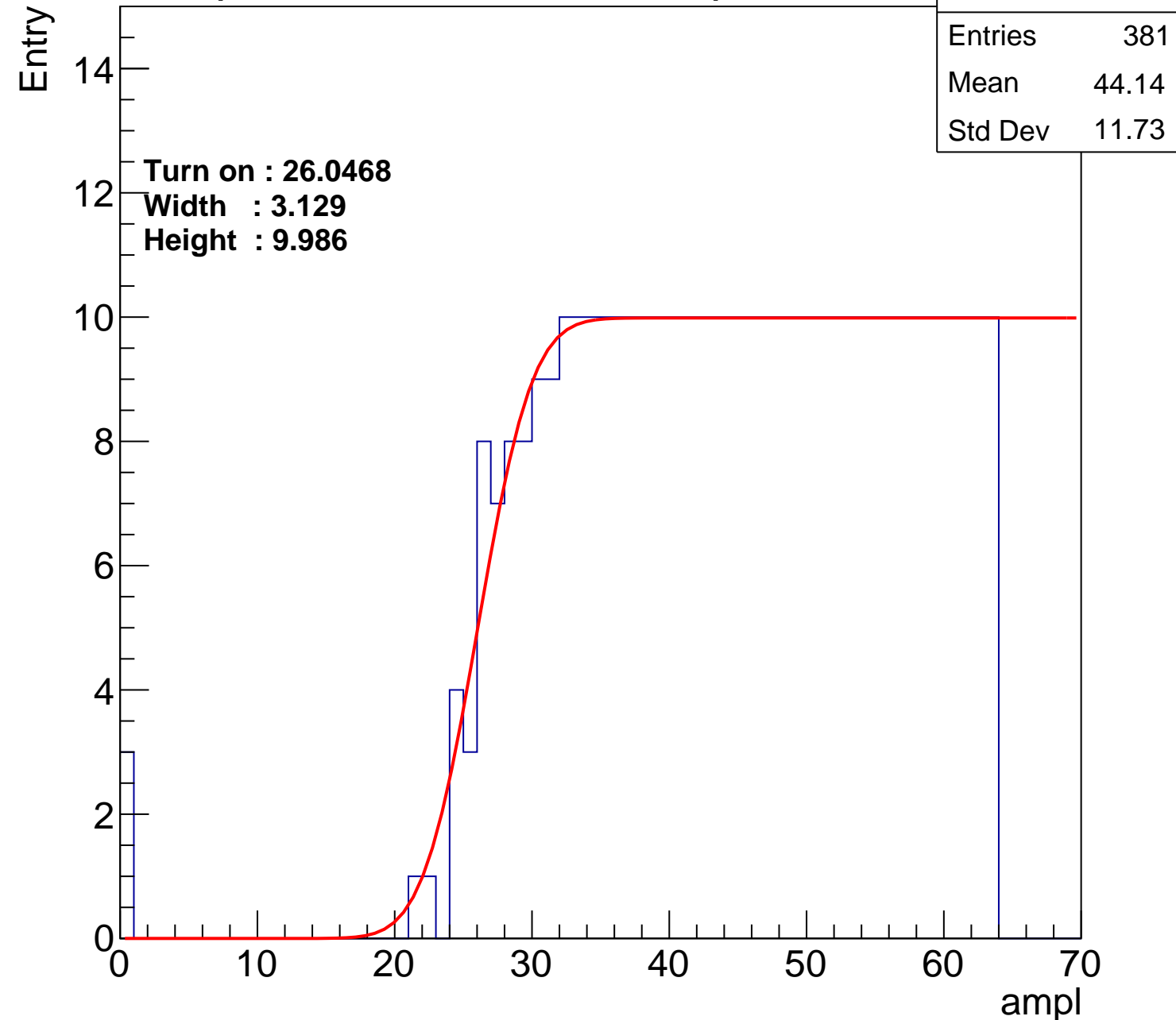
Width : 3.129

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch126

calib_packv5_042523_0143.root, FC#12, port B1

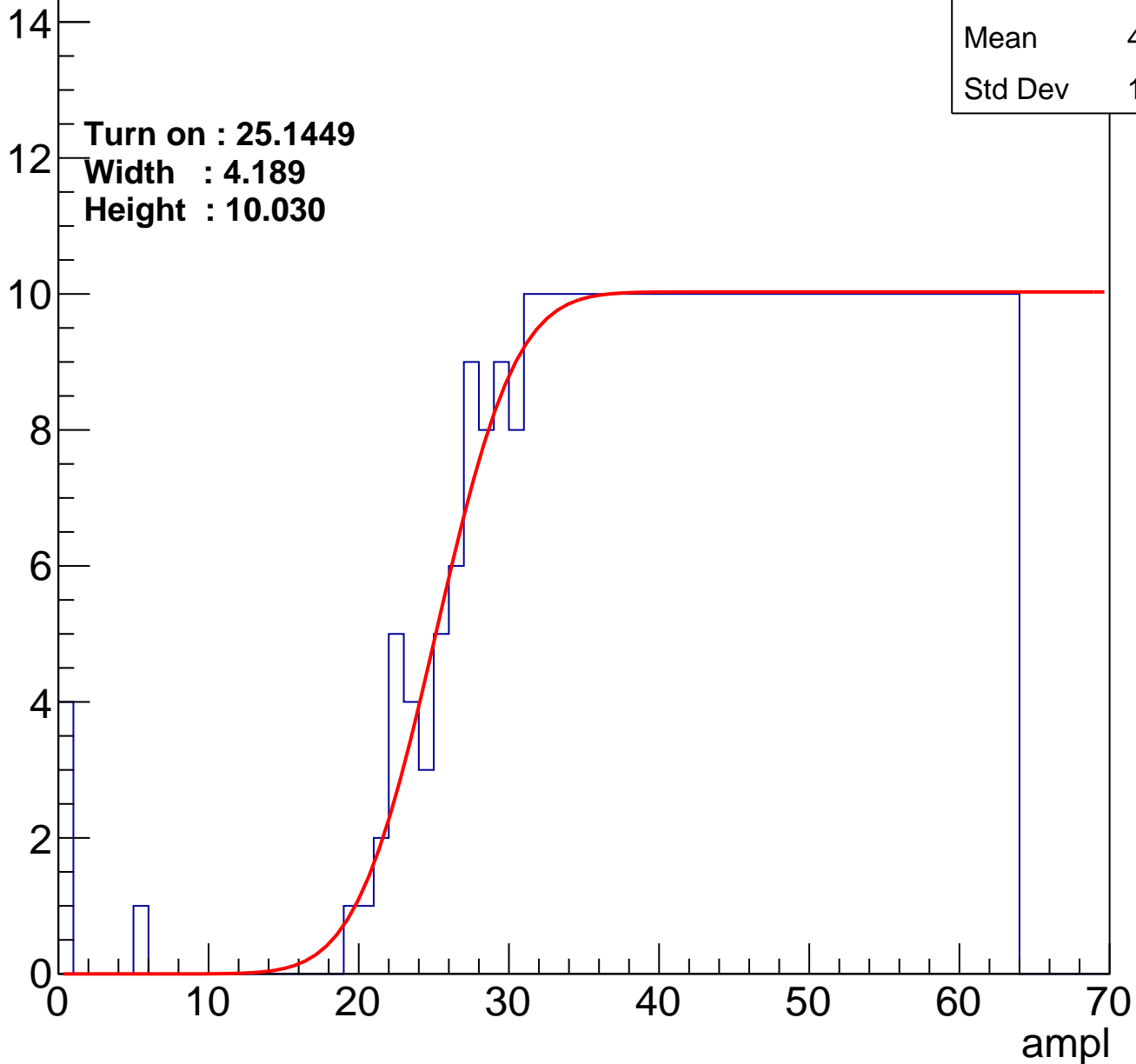
Entries	396
Mean	43.23
Std Dev	12.47

Turn on : 25.1449

Width : 4.189

Height : 10.030

Entry



B0L102S, U6-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	422
Mean	42.08
Std Dev	13.02

Turn on : 23.0031

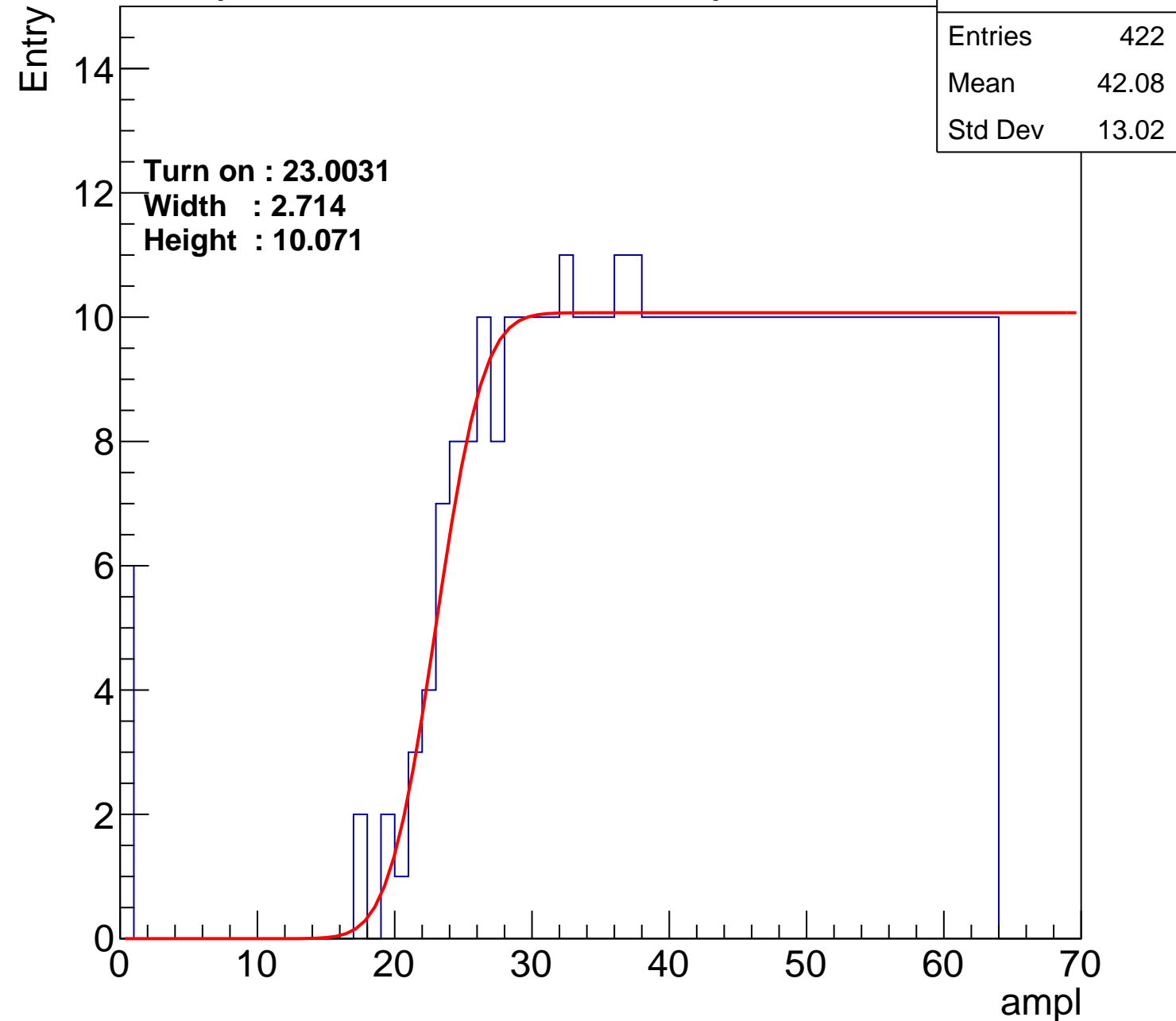
Width : 2.714

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U6-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	422
Mean	42.08
Std Dev	13.02

Turn on : 23.0031

Width : 2.714

Height : 10.071

Entry

