



# B0L103S, U9-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

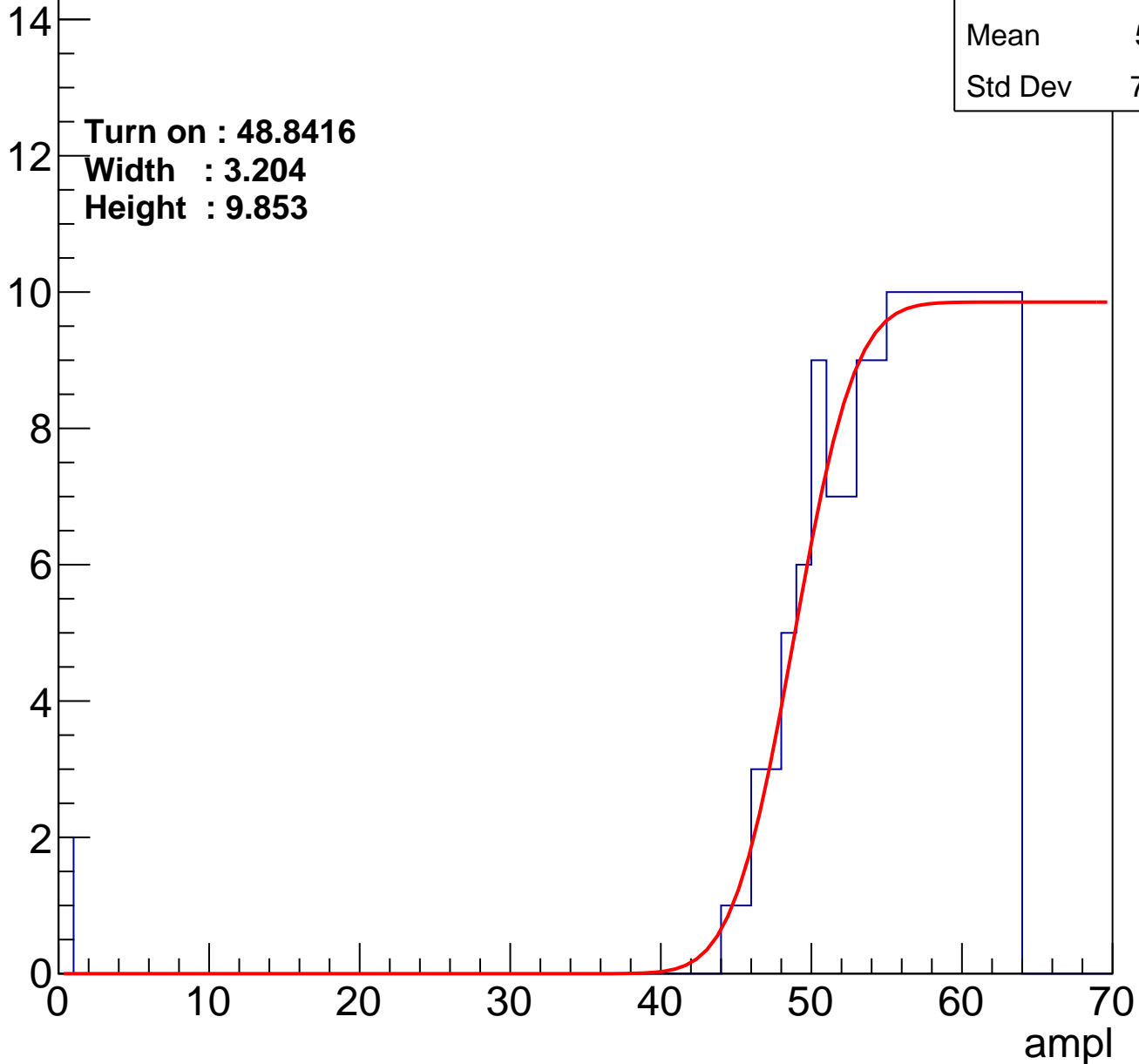
Entries	152
Mean	54.91
Std Dev	7.968

Turn on : 48.8416

Width : 3.204

Height : 9.853

Entry



# B0L103S, U9-ch1

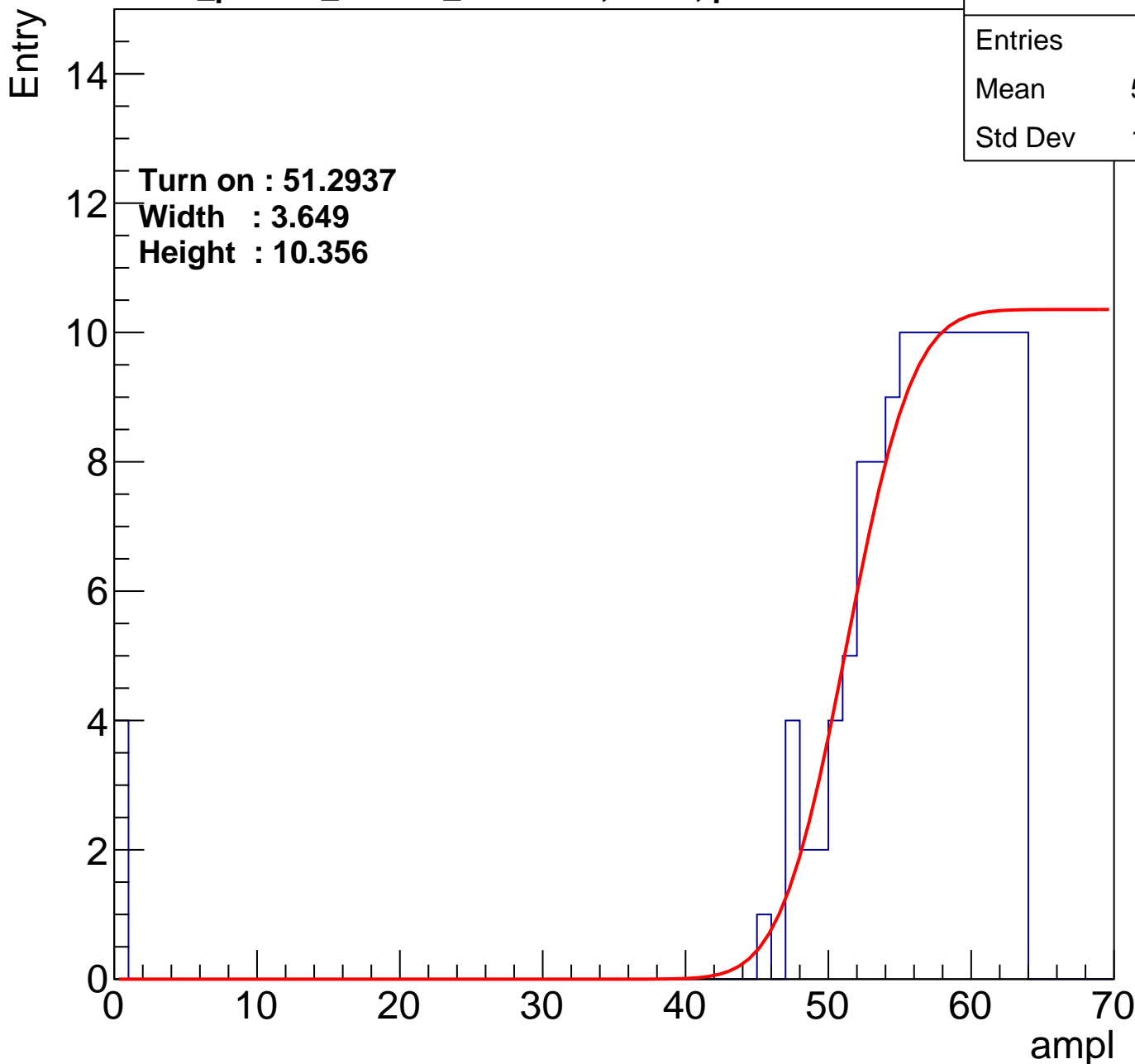
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	137
Mean	54.88
Std Dev	10.45

Turn on : 51.2937

**Width : 3.649**

**Height : 10.356**



# B0L103S, U9-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

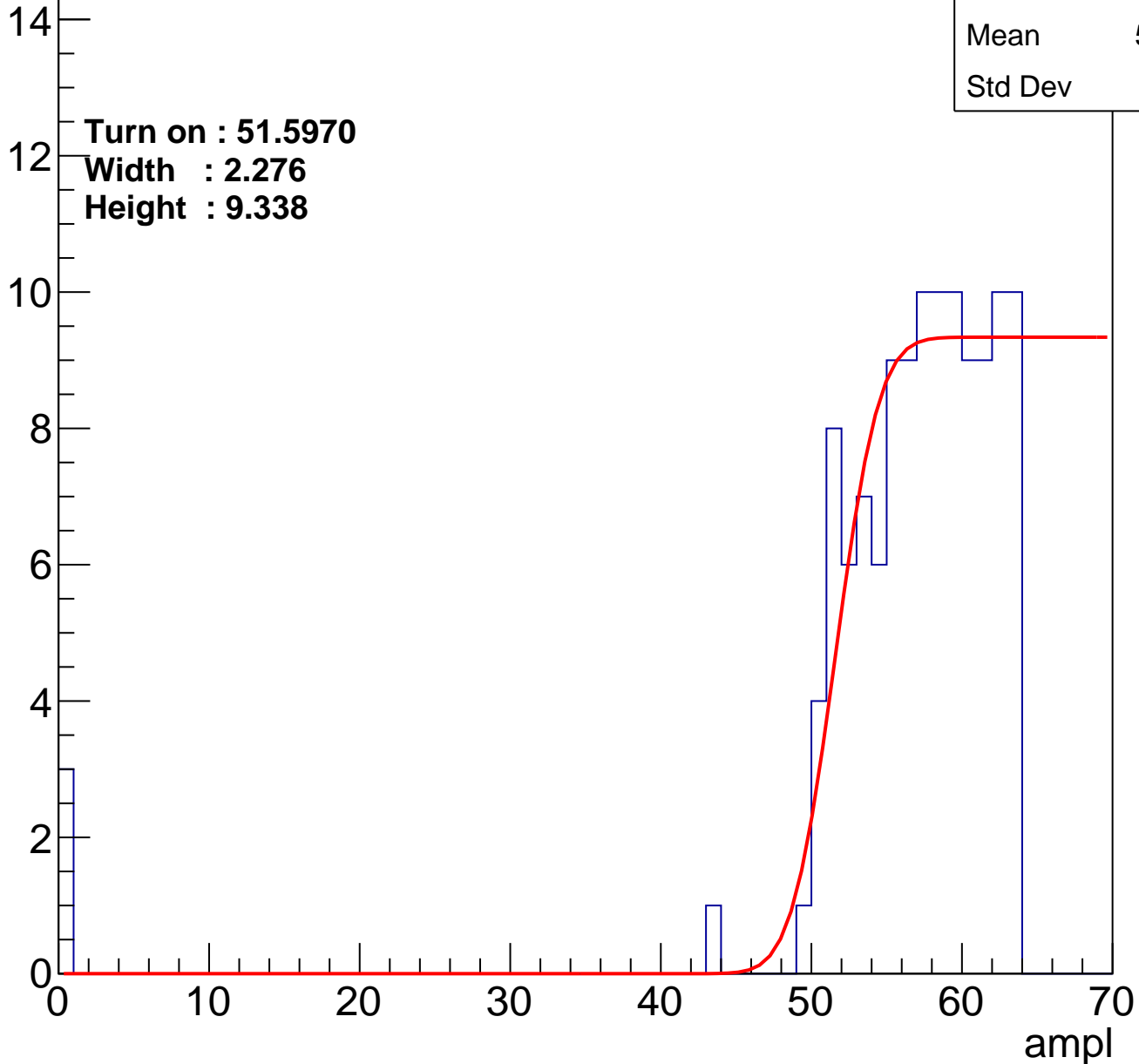
Entries	122
Mean	55.61
Std Dev	9.71

Turn on : 51.5970

Width : 2.276

Height : 9.338

Entry



# B0L103S, U9-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	55.26
Std Dev	7.927

Turn on : 49.4301

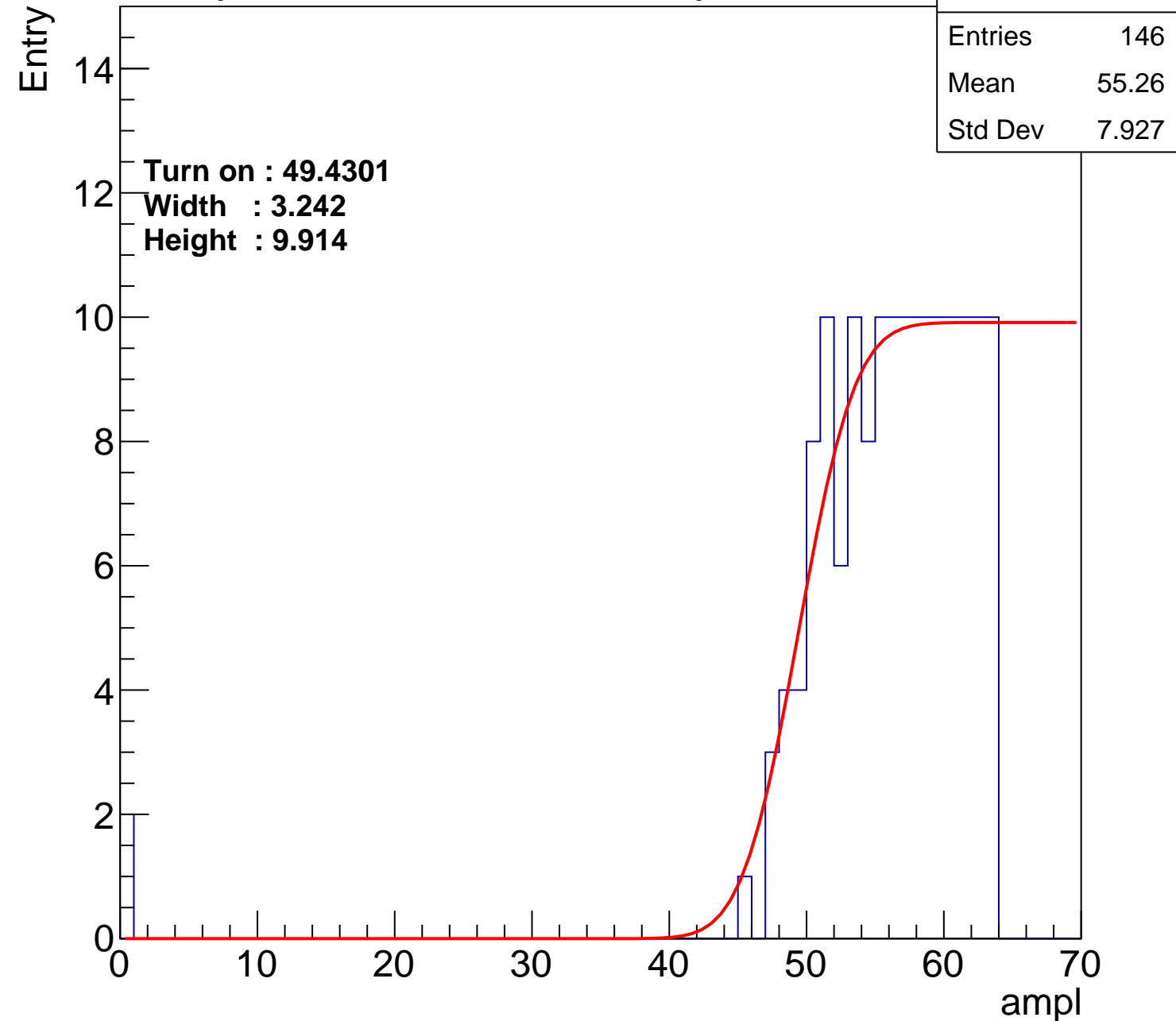
Width : 3.242

Height : 9.914

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch4

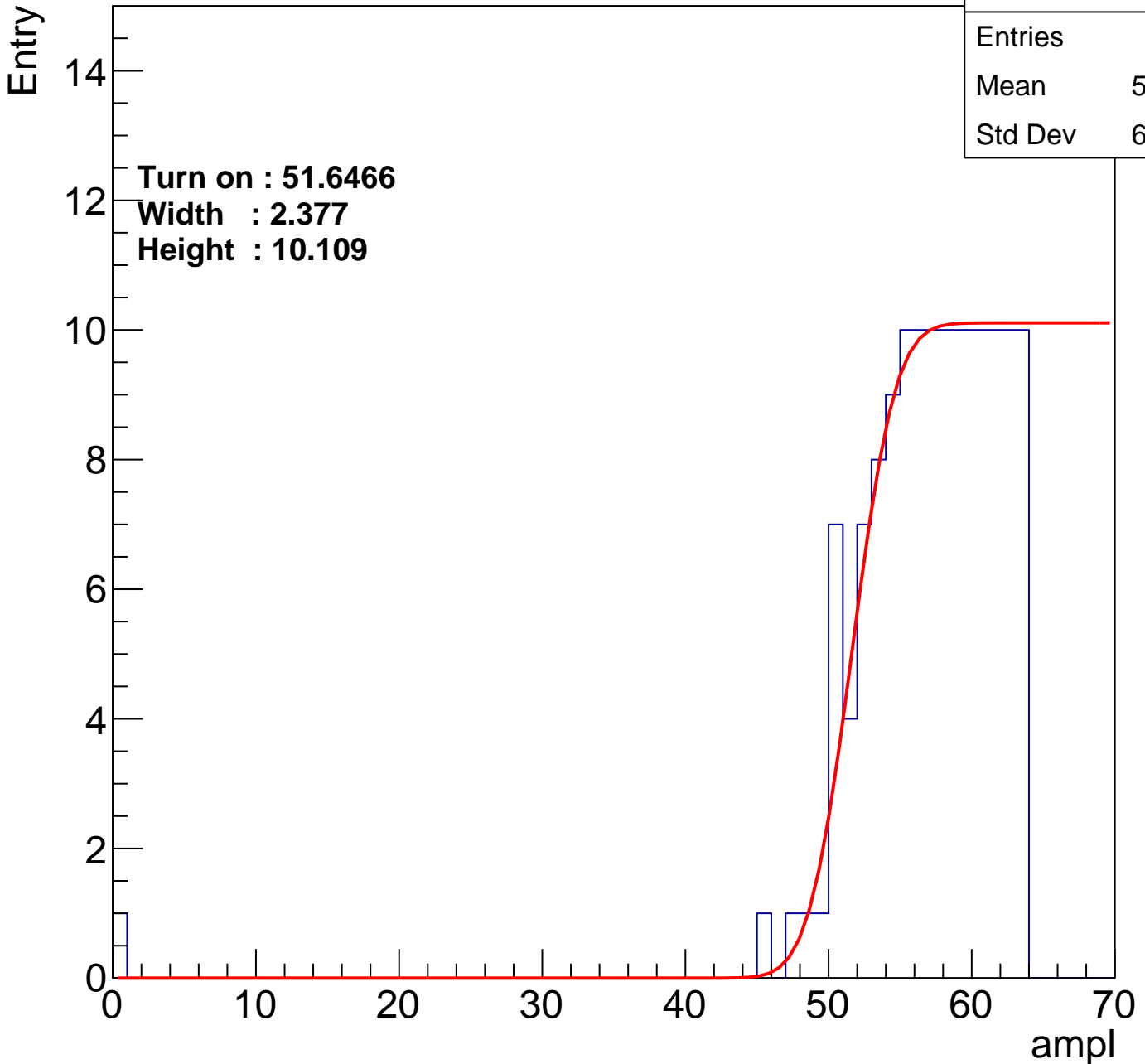
calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	56.36
Std Dev	6.455

Turn on : 51.6466

Width : 2.377

Height : 10.109



# B0L103S, U9-ch5

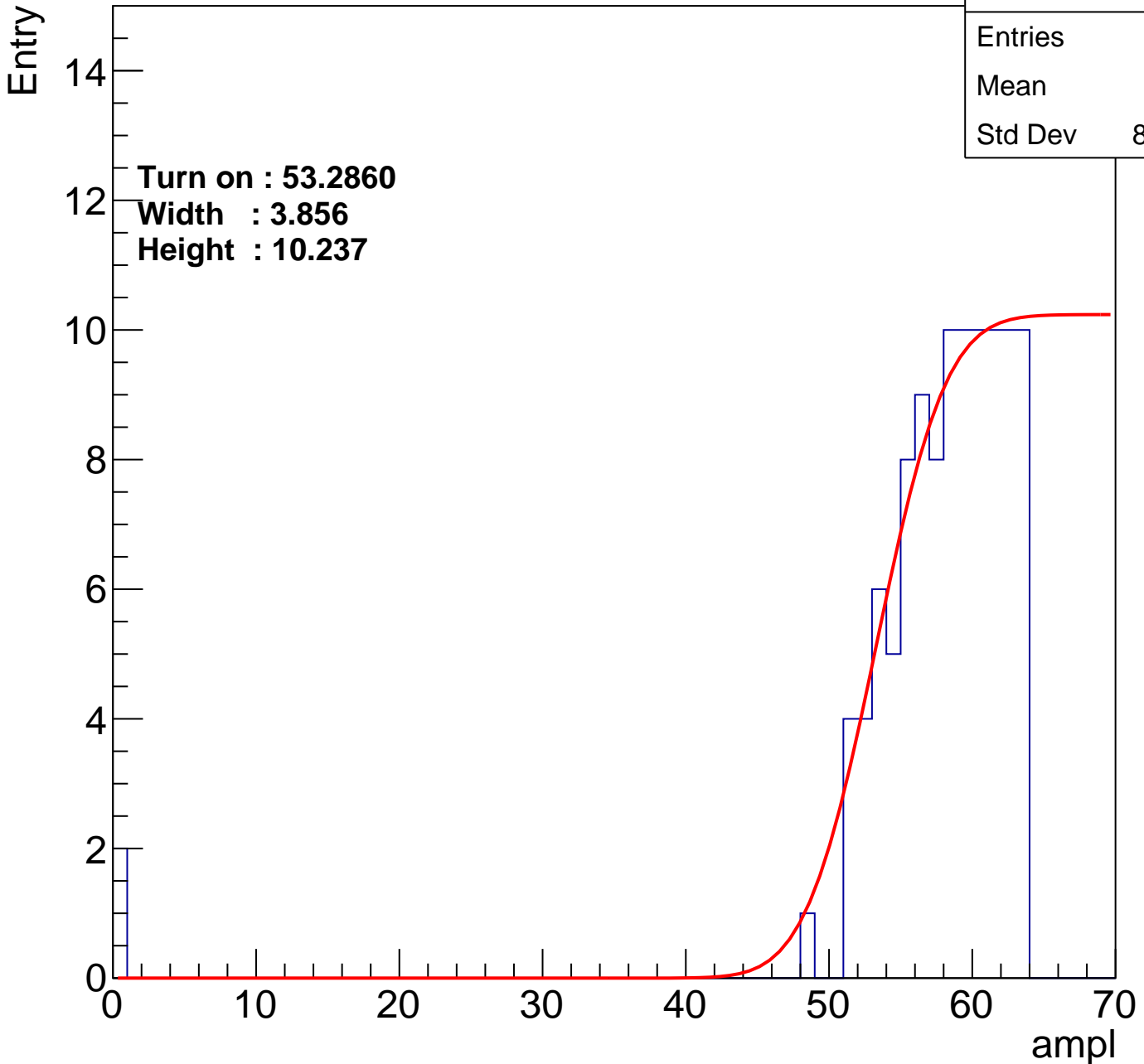
calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	56.8
Std Dev	8.598

**Turn on : 53.2860**

**Width : 3.856**

**Height : 10.237**



# B0L103S, U9-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

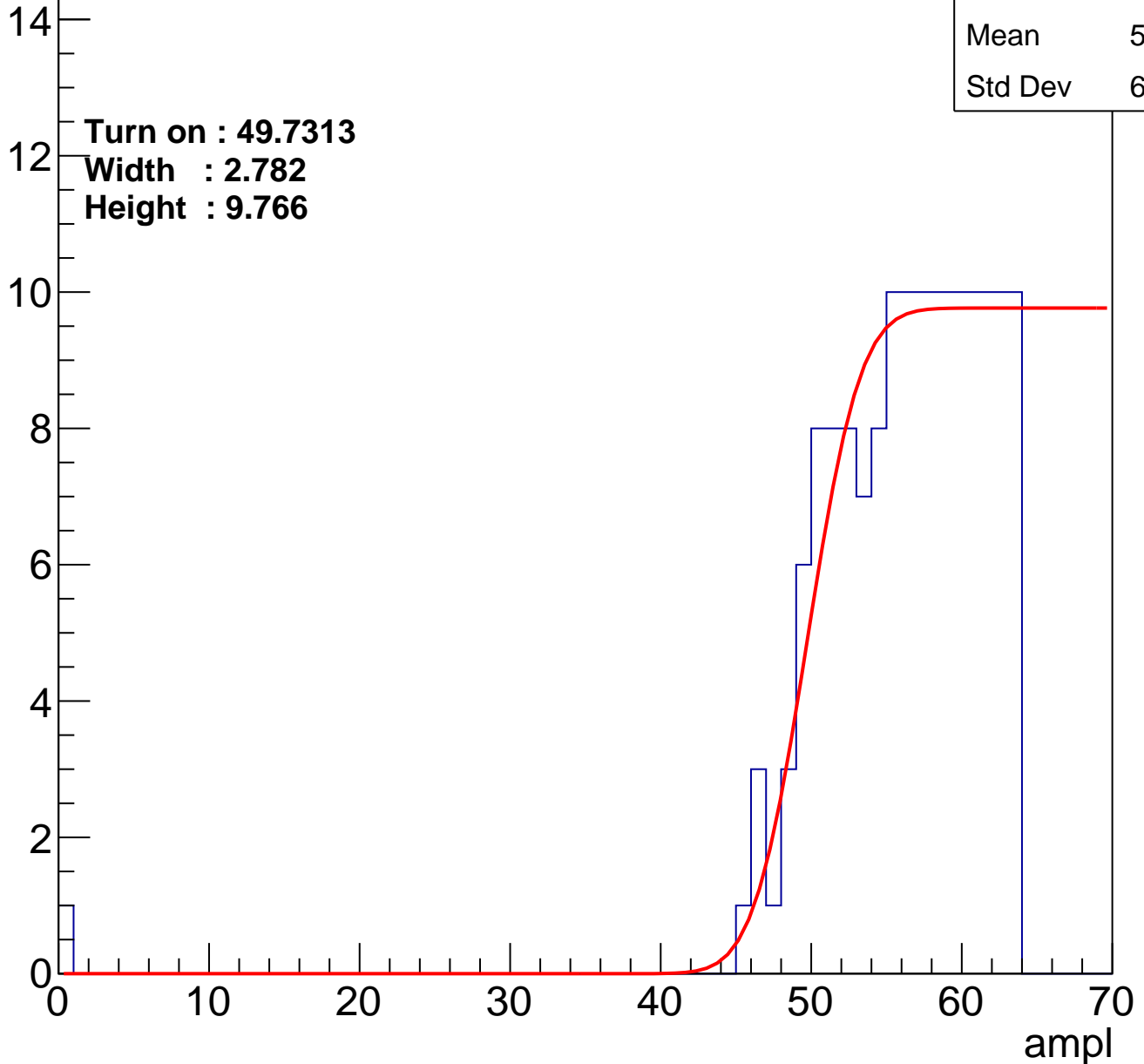
Entries	144
Mean	55.59
Std Dev	6.573

Turn on : 49.7313

Width : 2.782

Height : 9.766

Entry





# B0L103S, U9-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

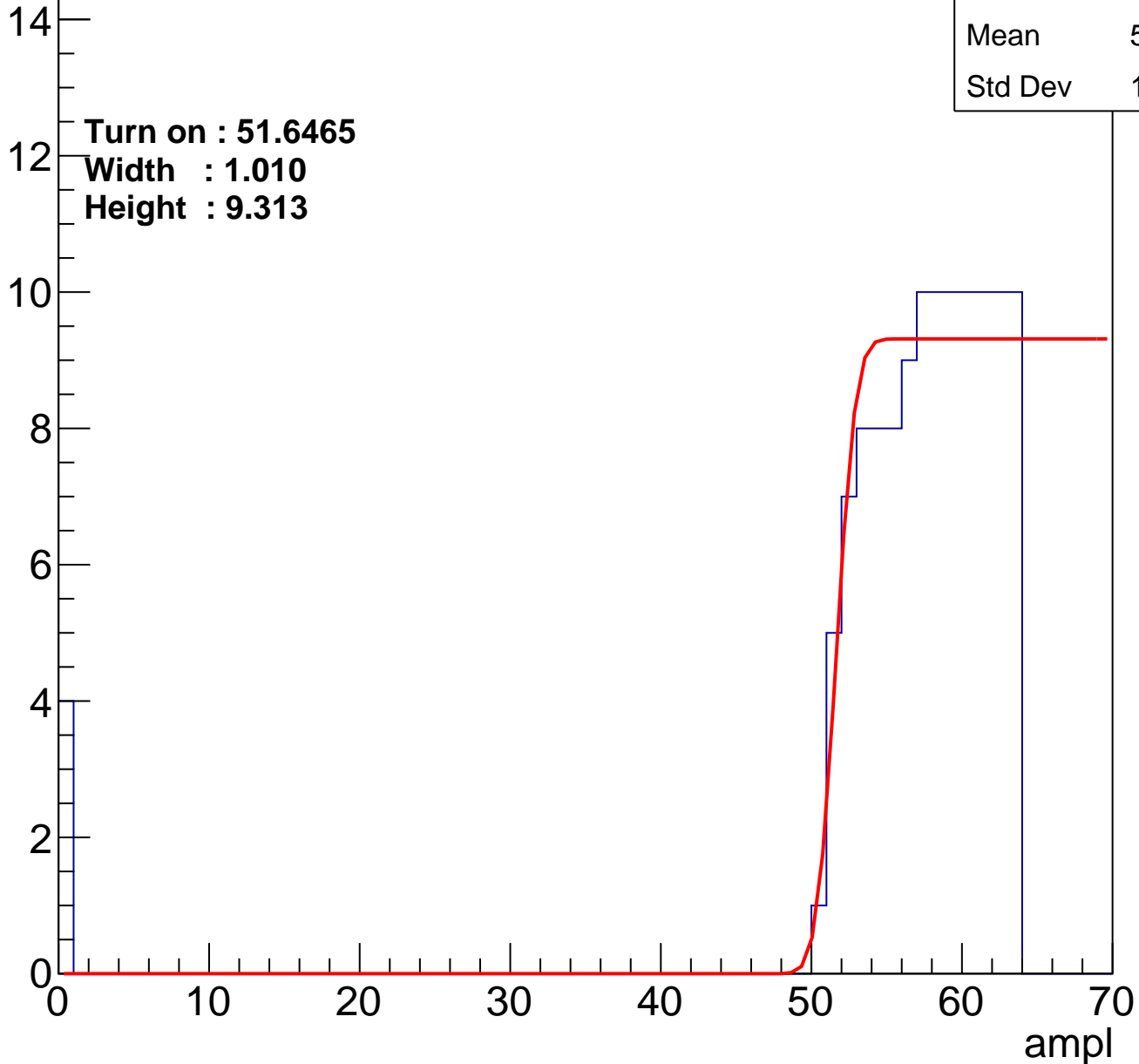
Entries	120
Mean	55.58
Std Dev	10.92

Turn on : 51.6465

Width : 1.010

Height : 9.313

Entry



# B0L103S, U9-ch8

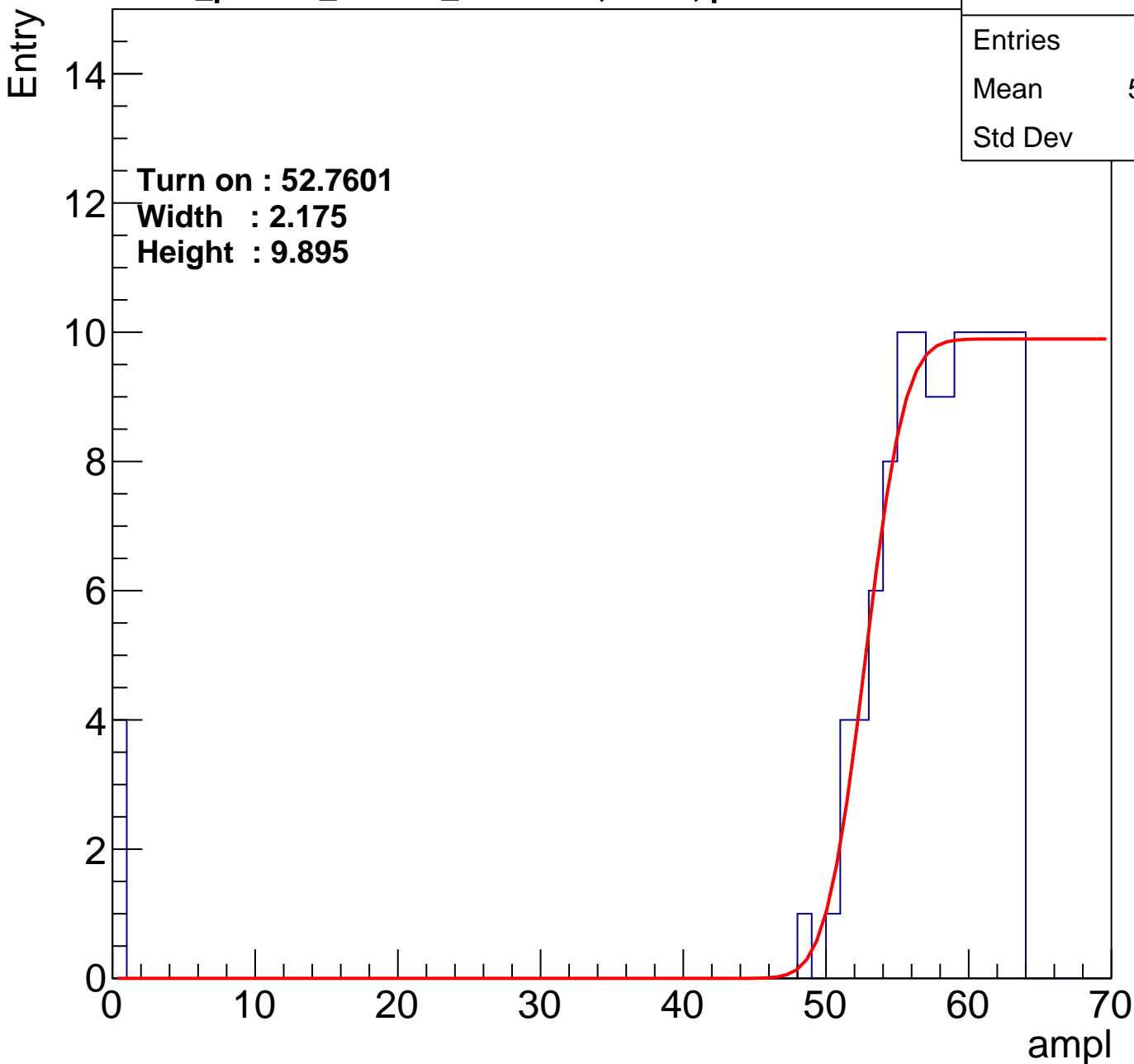
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	116
Mean	55.65
Std Dev	11.1

**Turn on : 52.7601**

**Width : 2.175**

**Height : 9.895**



# B0L103S, U9-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

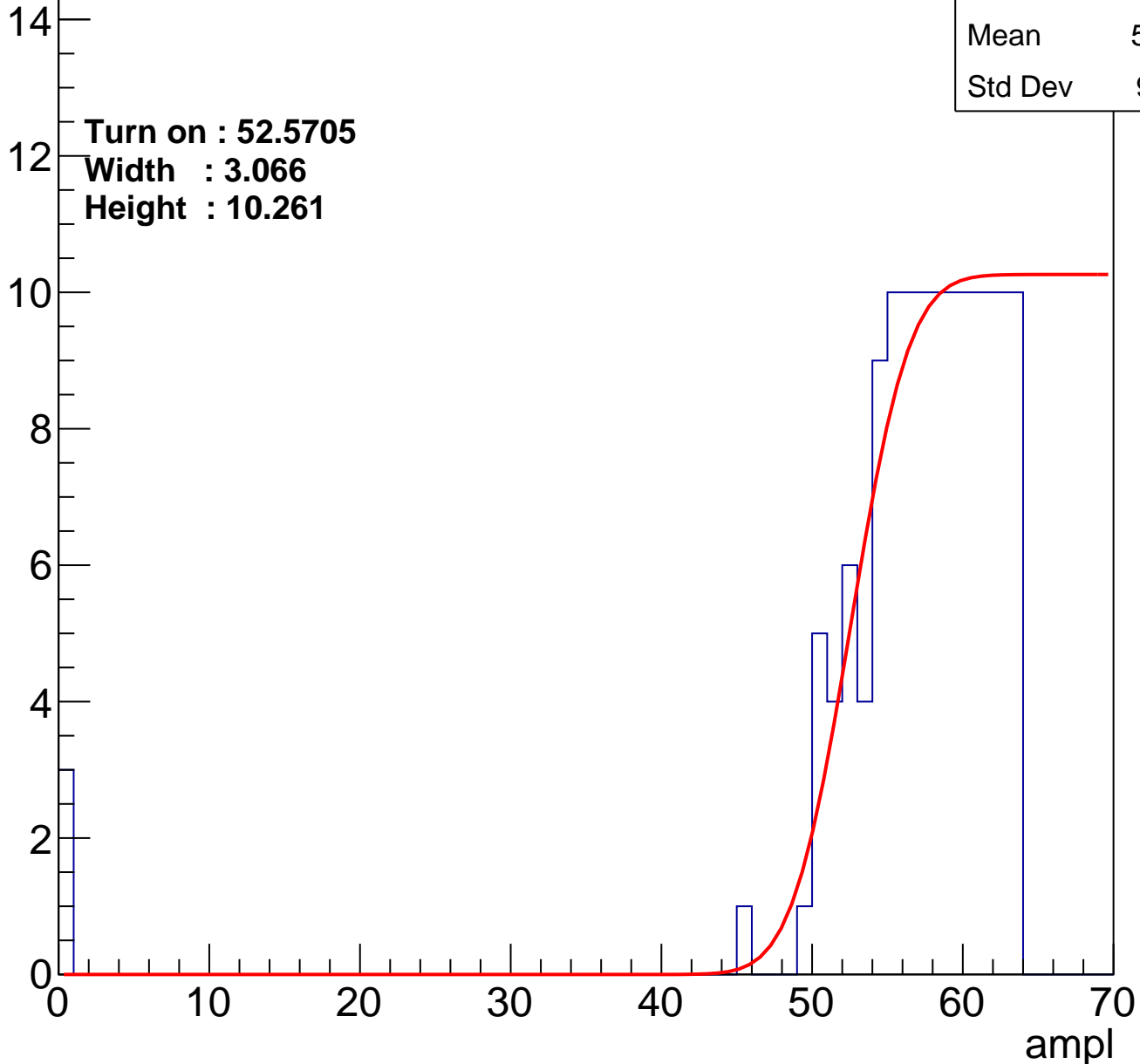
Entries	123
Mean	55.84
Std Dev	9.641

Turn on : 52.5705

Width : 3.066

Height : 10.261

Entry



# B0L103S, U9-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	57.13
Std Dev	6.571

Turn on : 52.8400

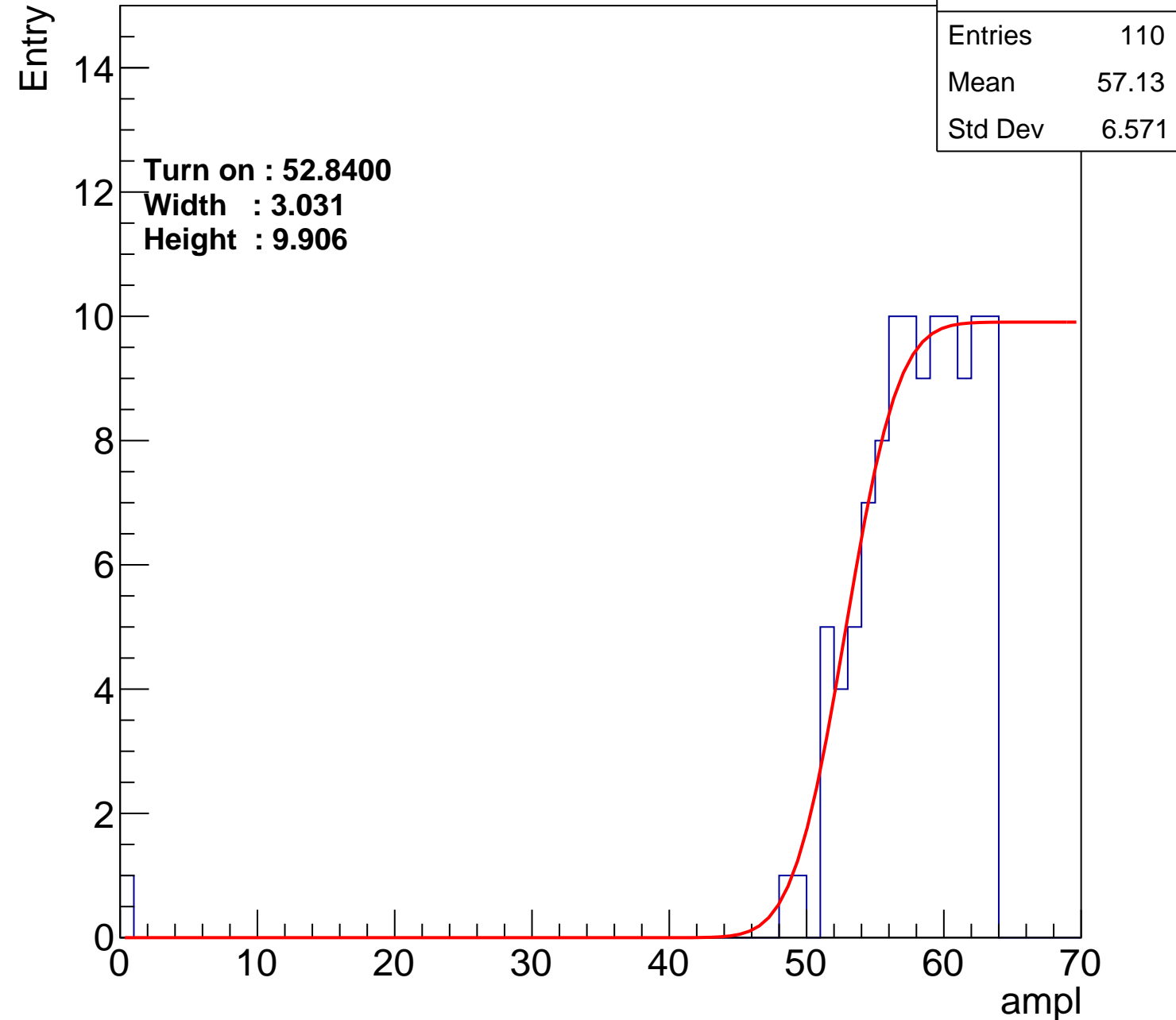
Width : 3.031

Height : 9.906

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.53
Std Dev	9.427

Turn on : 51.6300

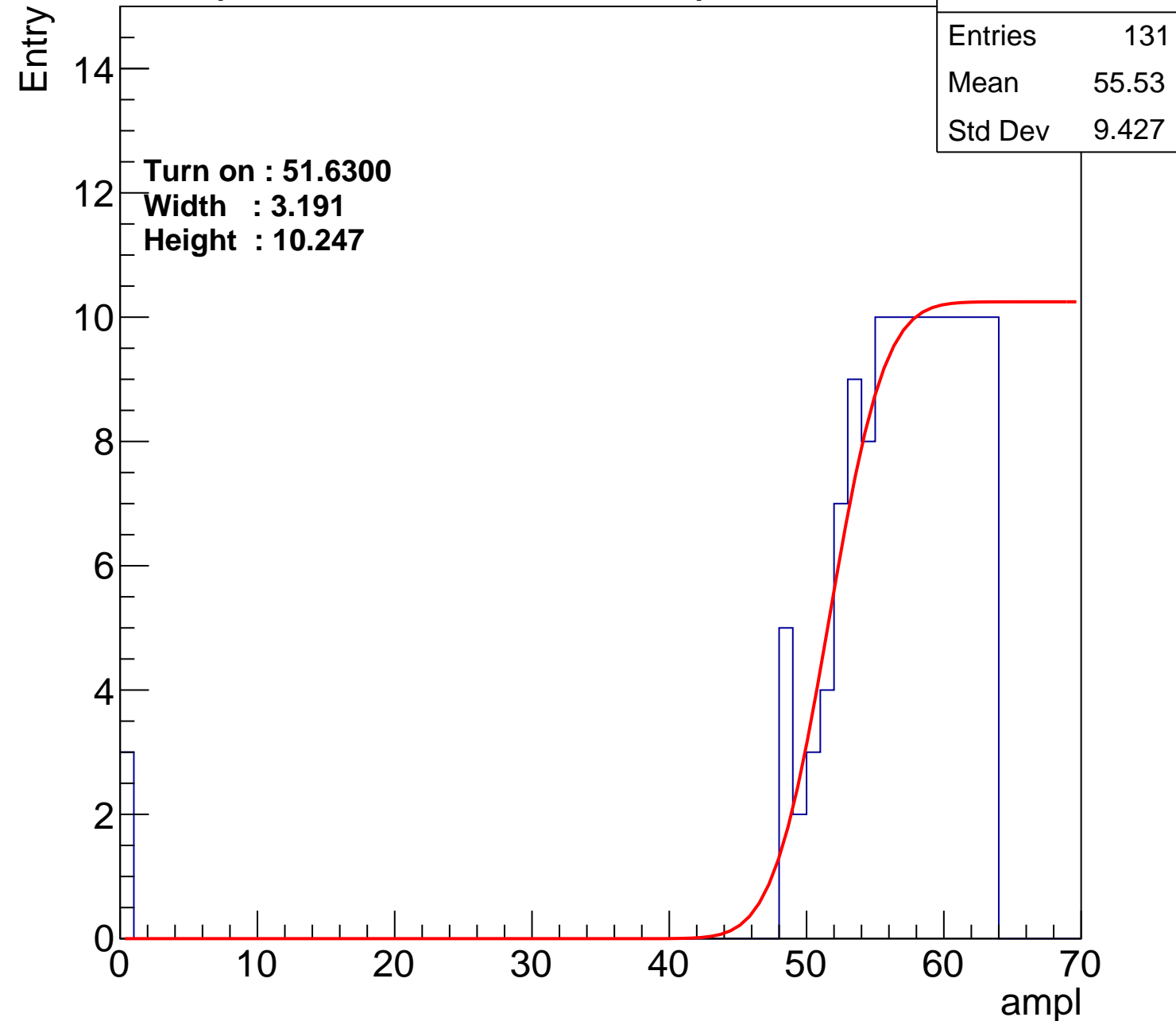
Width : 3.191

Height : 10.247

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	54.71
Std Dev	10.2

Turn on : 50.3942

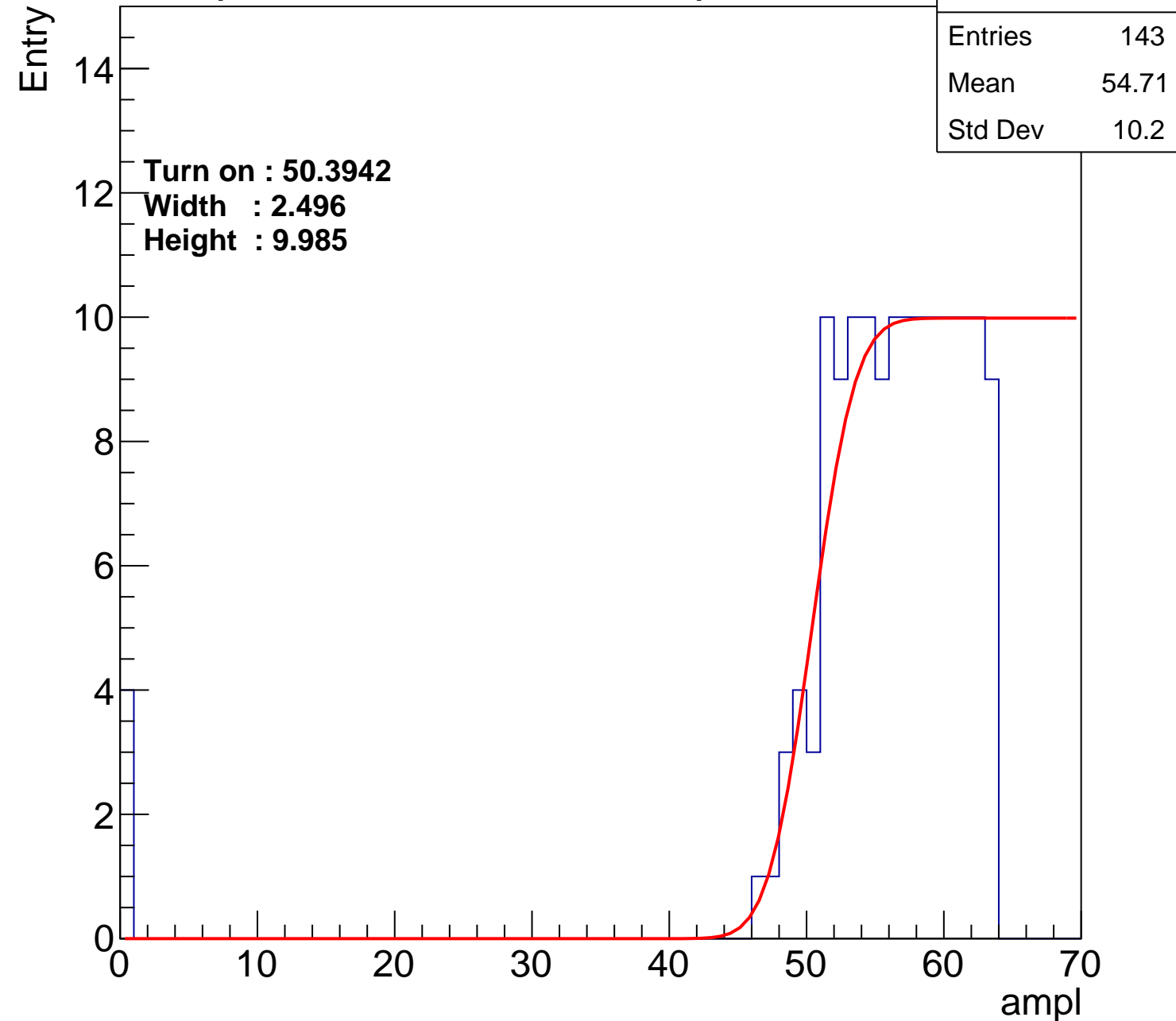
Width : 2.496

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch13

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.33
Std Dev	9.221

Turn on : 50.2634

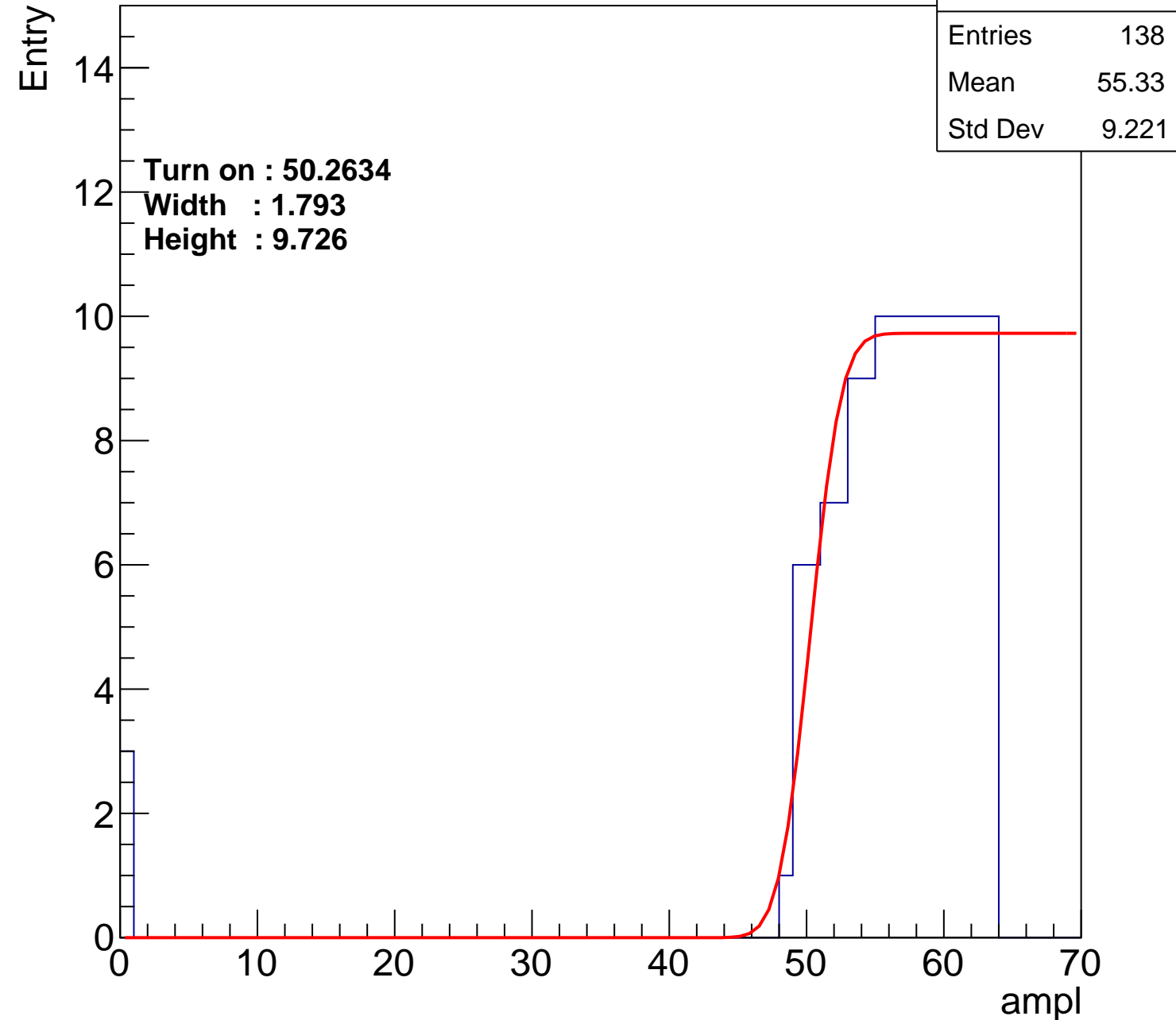
Width : 1.793

Height : 9.726

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch14

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.59
Std Dev	9.403

Turn on : 51.4643

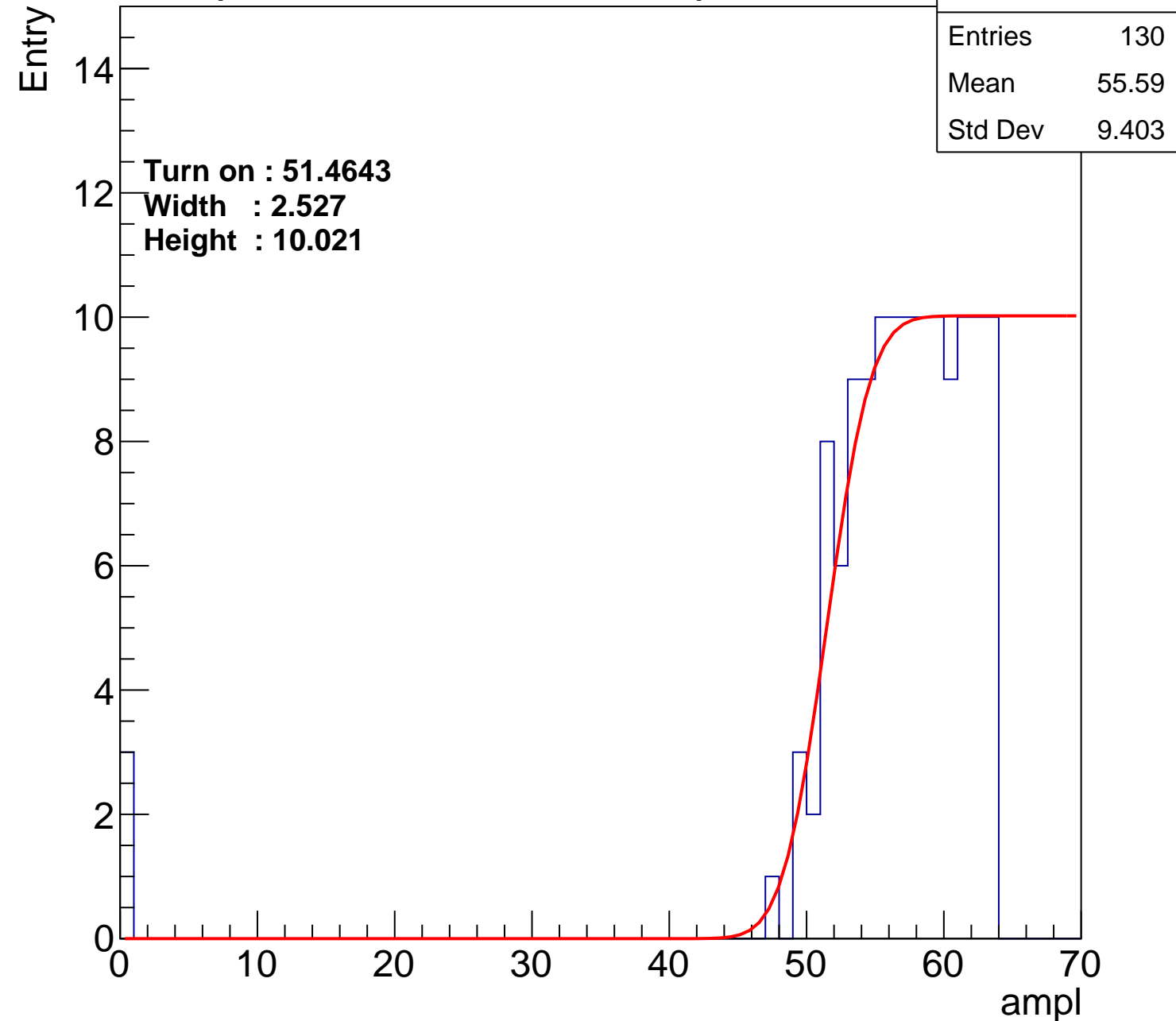
Width : 2.527

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch15

calib\_packv5\_040323\_1717.root, FC#2, port C3

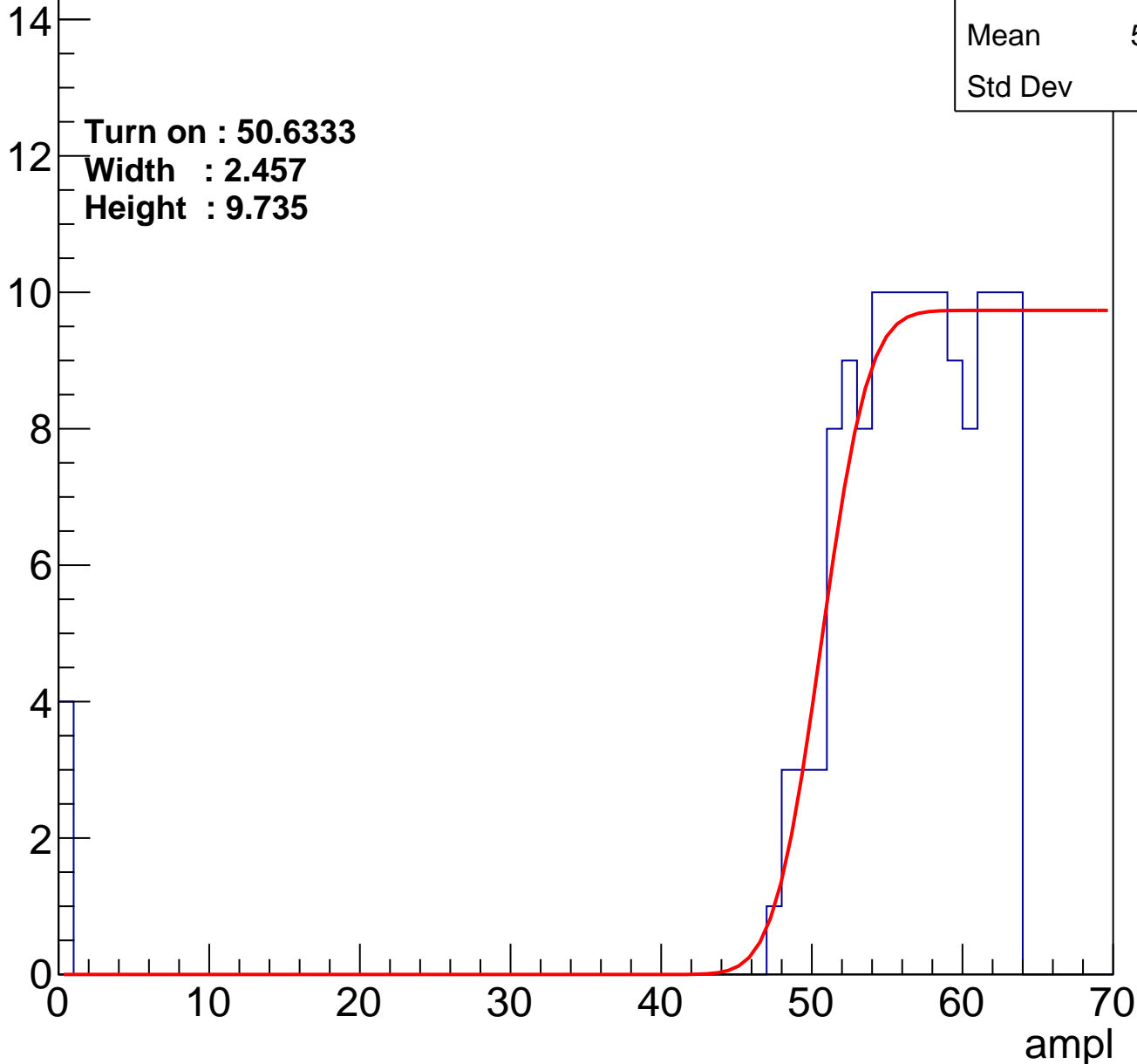
Entry

Entries	136
Mean	54.85
Std Dev	10.4

Turn on : 50.6333

Width : 2.457

Height : 9.735



# B0L103S, U9-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	54.99
Std Dev	10.34

Turn on : 50.7885

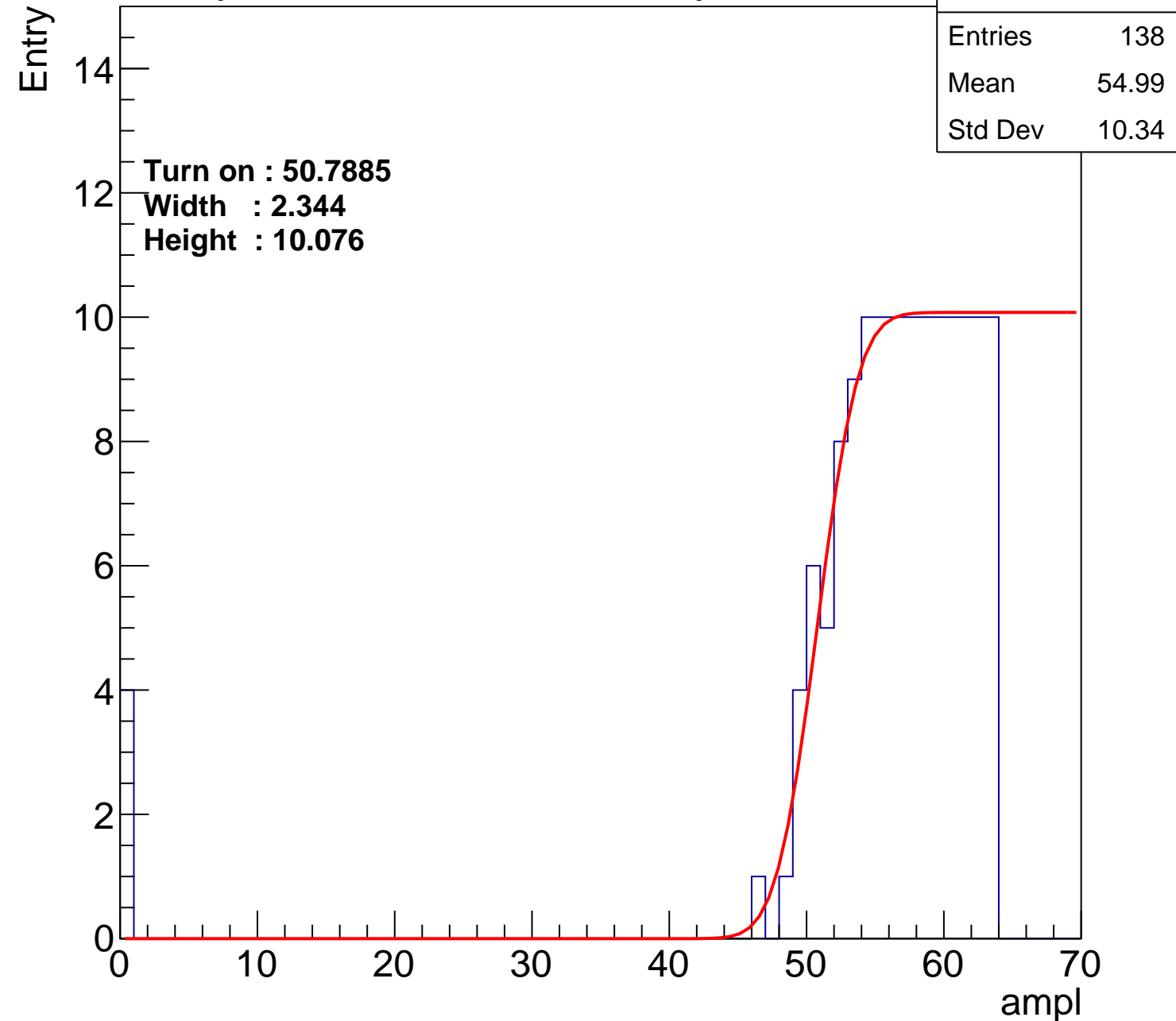
Width : 2.344

Height : 10.076

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.97
Std Dev	9.082

Turn on : 49.4230

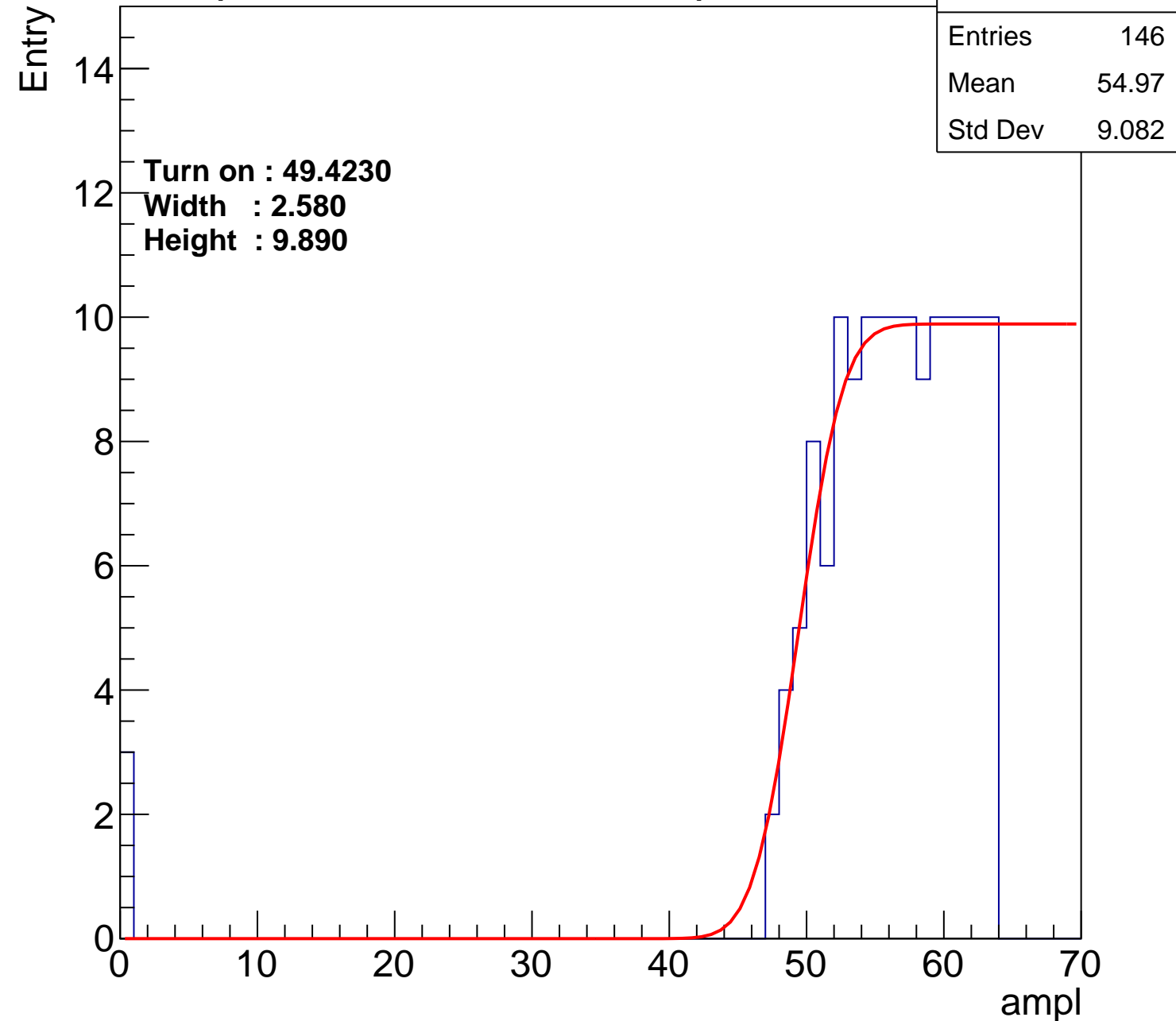
Width : 2.580

Height : 9.890

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch18

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.22
Std Dev	9.406

Turn on : 50.6562

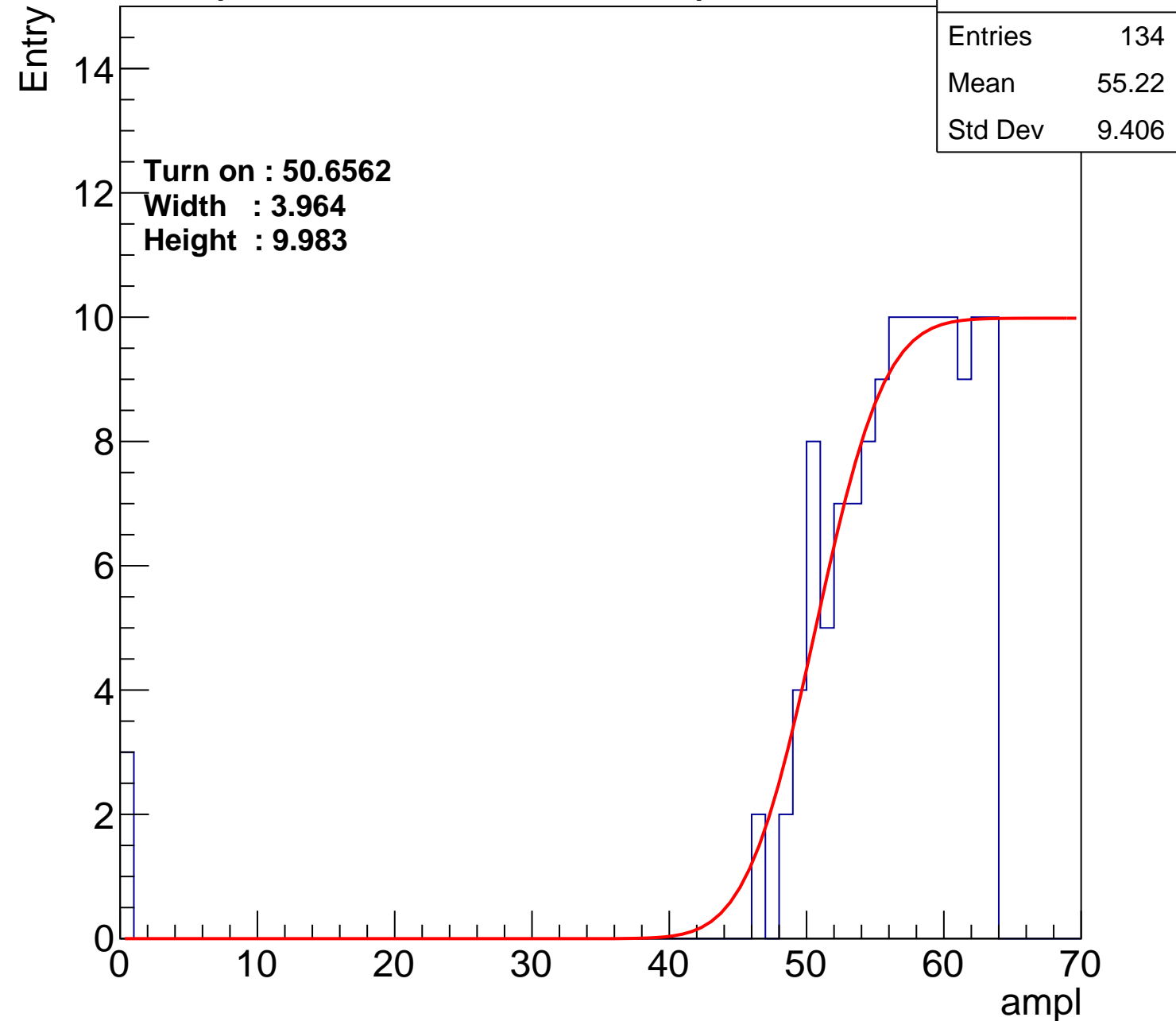
Width : 3.964

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

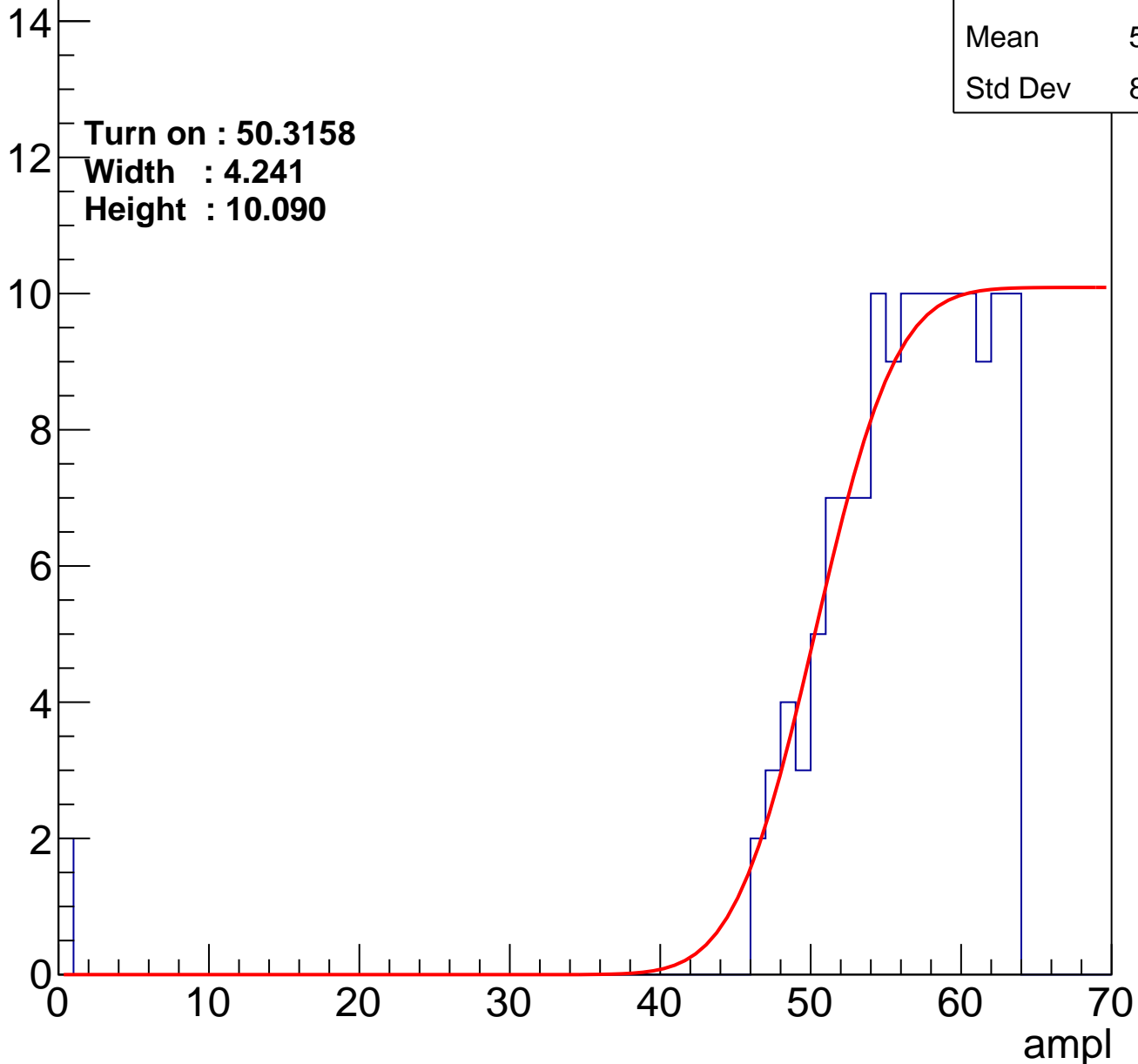
Entry

Entries	138
Mean	55.42
Std Dev	8.087

Turn on : 50.3158

Width : 4.241

Height : 10.090



# B0L103S, U9-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

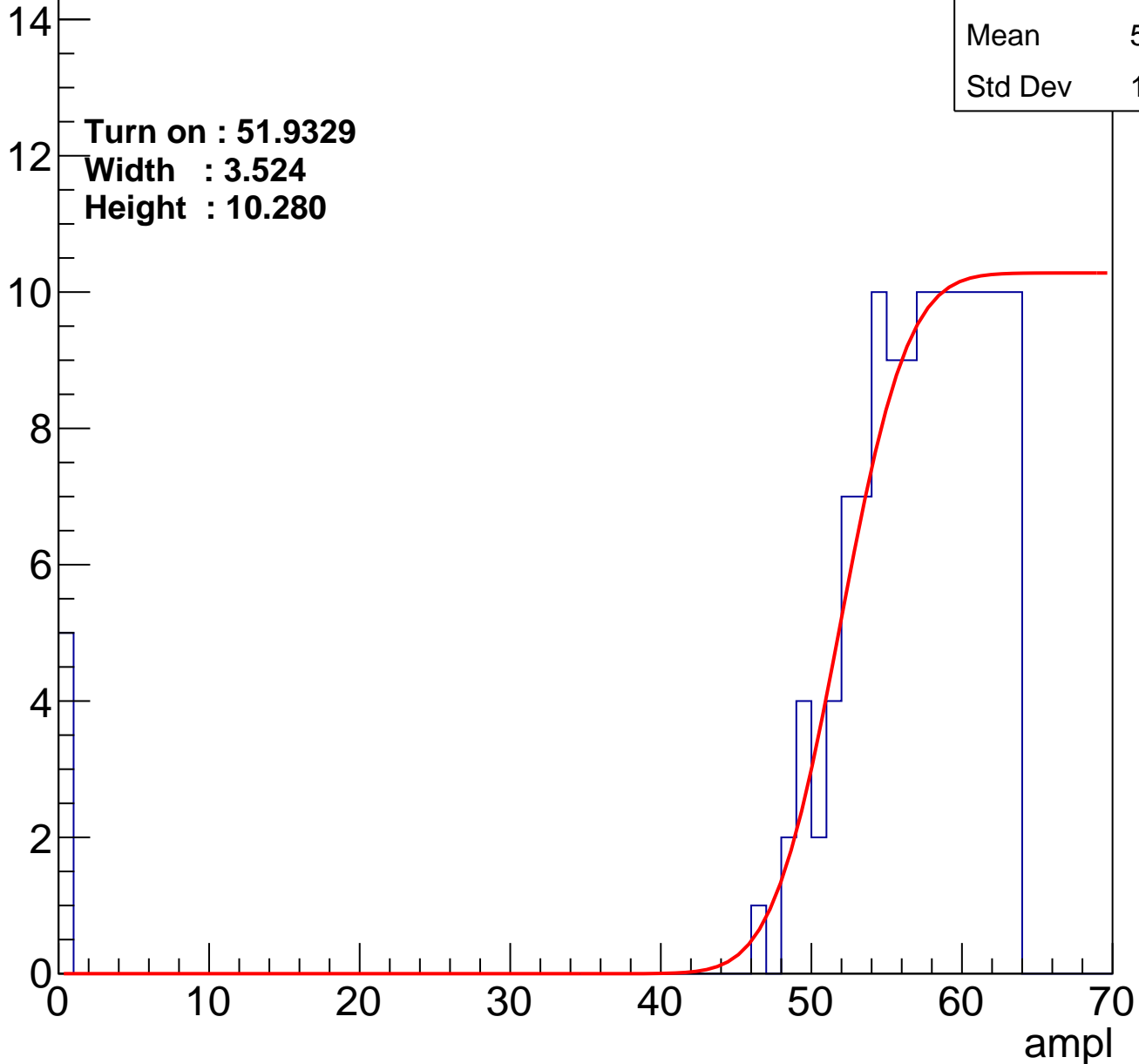
Entry

Entries	130
Mean	54.74
Std Dev	11.67

Turn on : 51.9329

Width : 3.524

Height : 10.280



# B0L103S, U9-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.78
Std Dev	8.124

Turn on : 51.0067

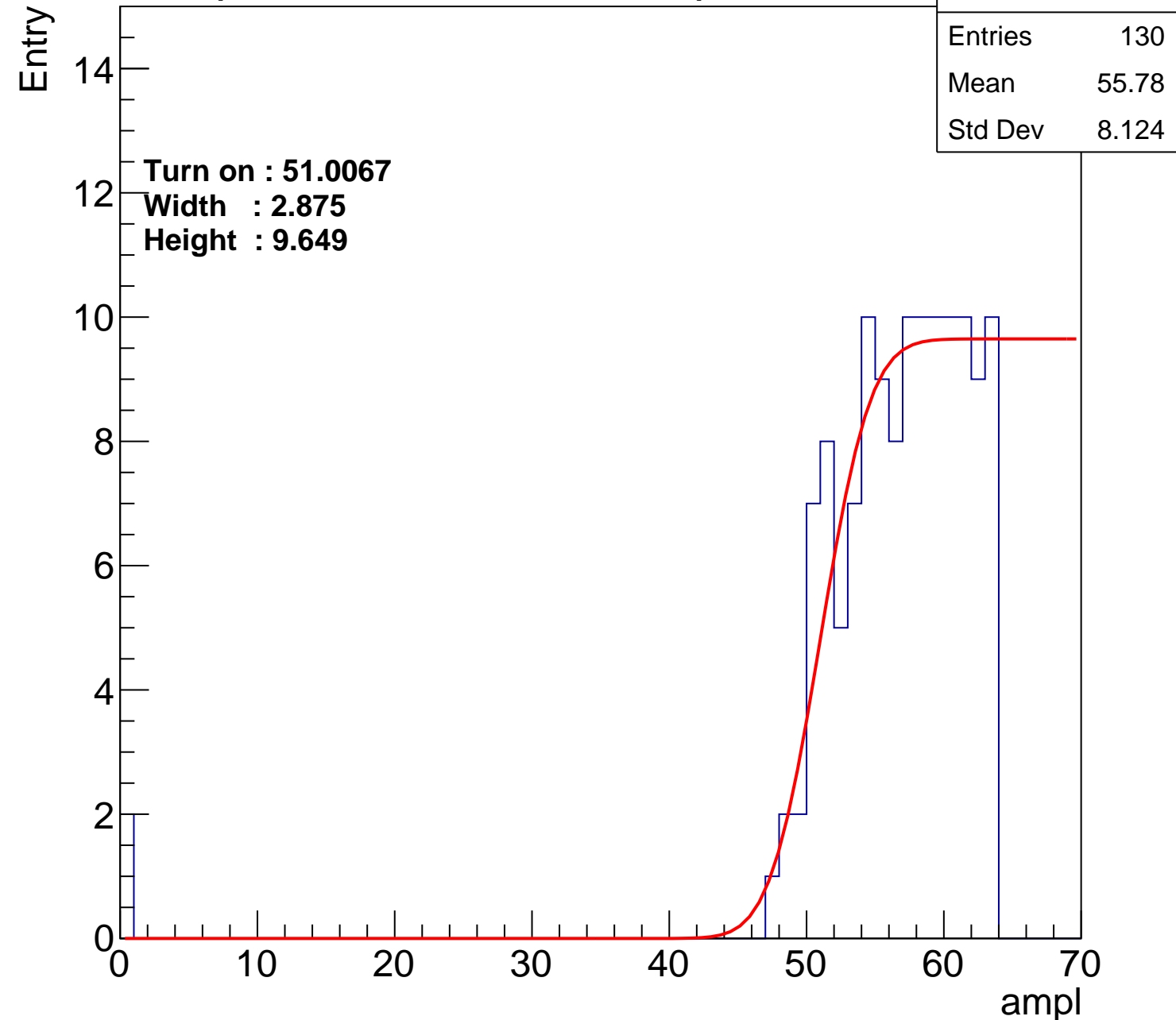
Width : 2.875

Height : 9.649

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch22

**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries 118

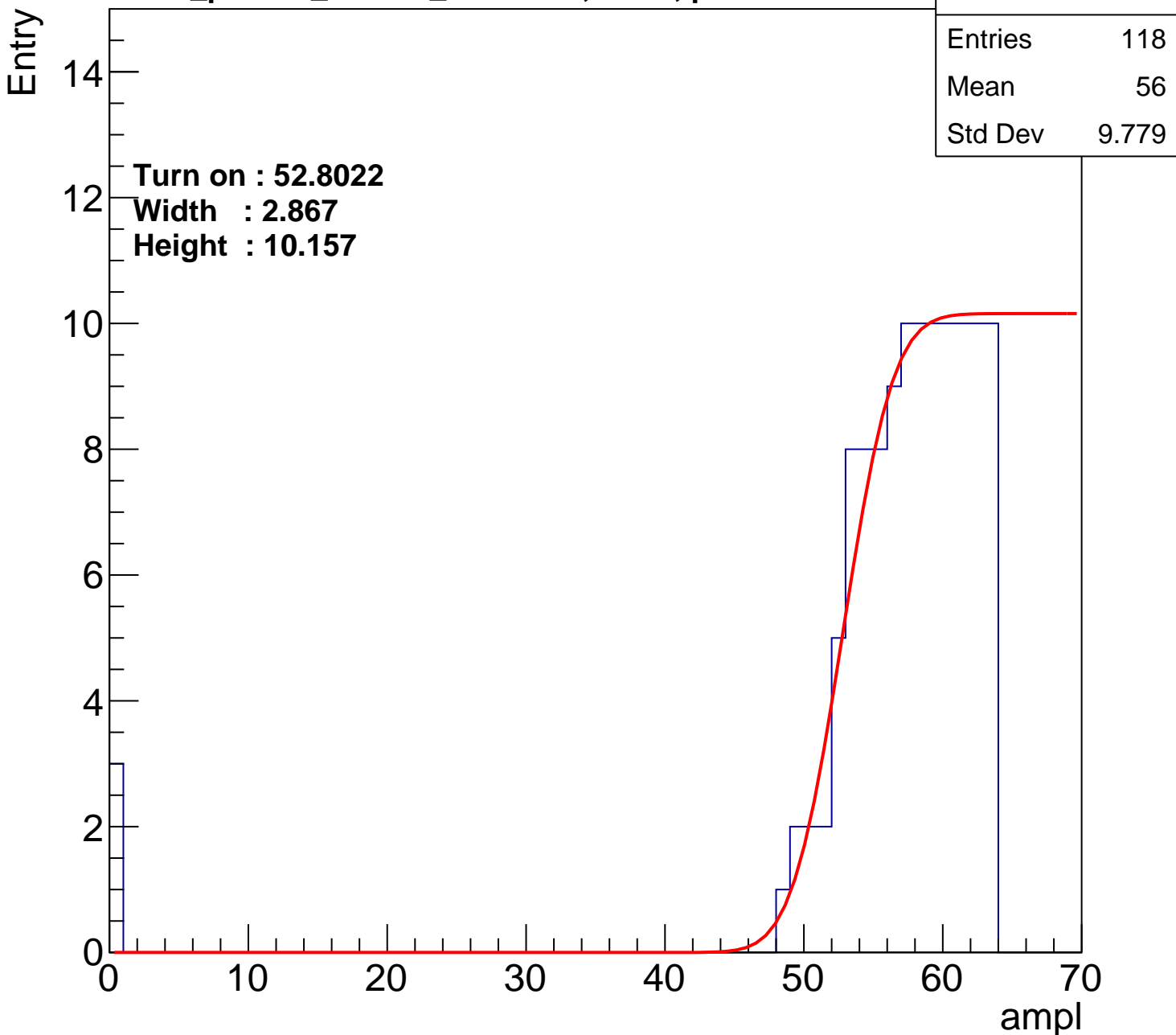
Mean 56

Std Dev 9.779

**Turn on : 52.8022**

**Width : 2.867**

**Height : 10.157**





# B0L103S, U9-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

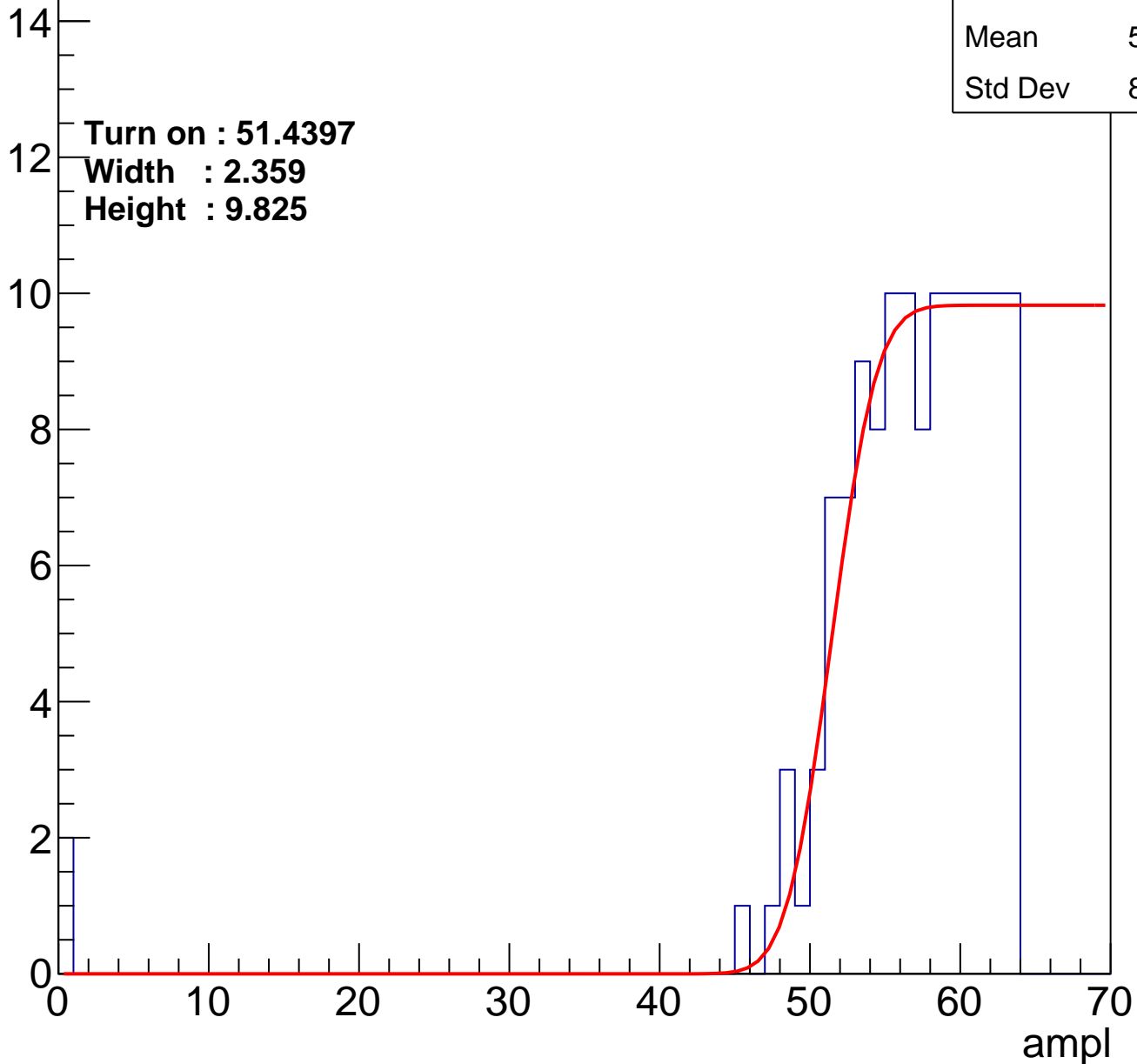
Entry

Entries	130
Mean	55.85
Std Dev	8.147

Turn on : 51.4397

Width : 2.359

Height : 9.825



# B0L103S, U9-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	149
Mean	55.13
Std Dev	7.925

Turn on : 50.2642

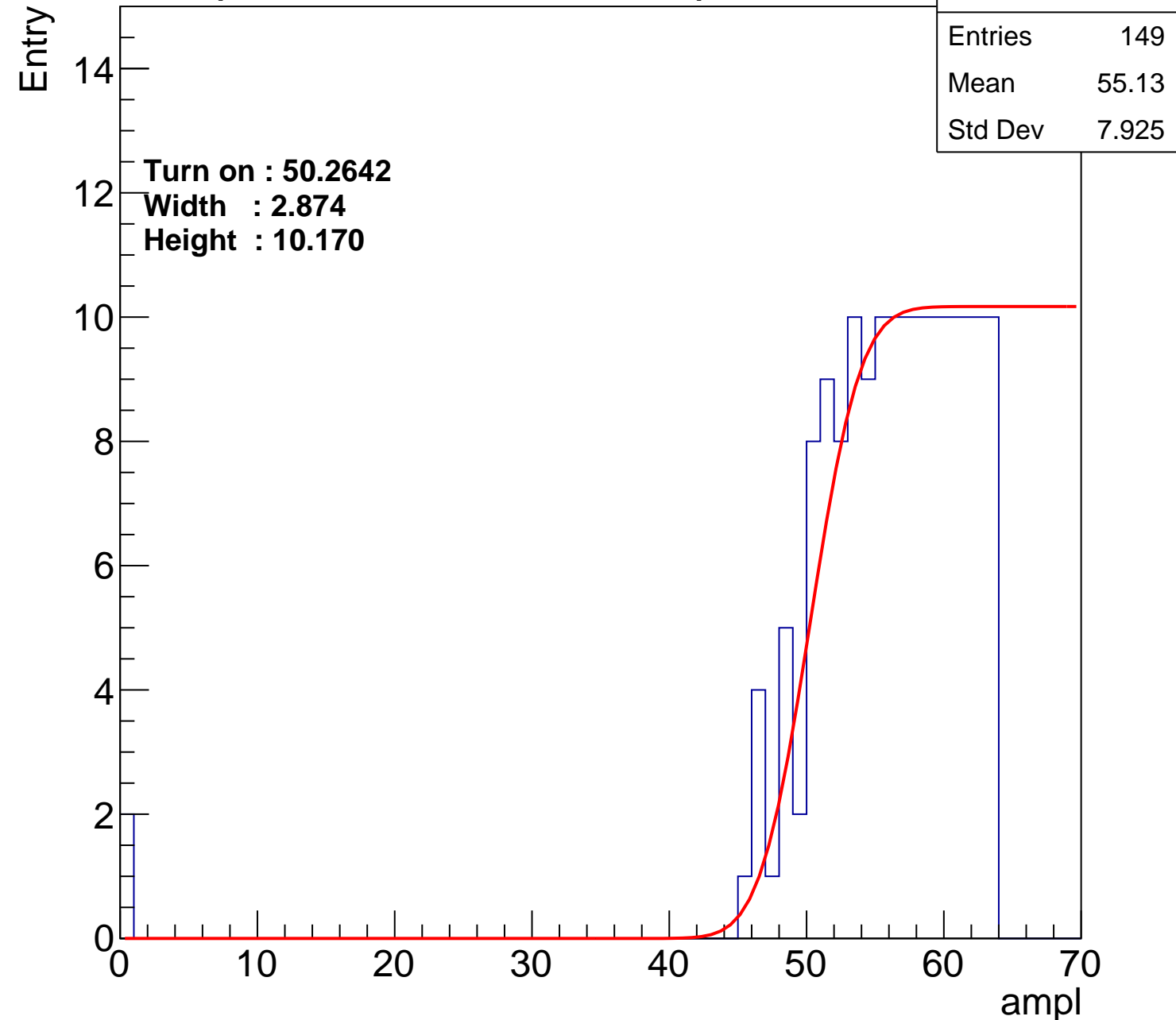
Width : 2.874

Height : 10.170

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.87
Std Dev	6.459

Turn on : 53.1569

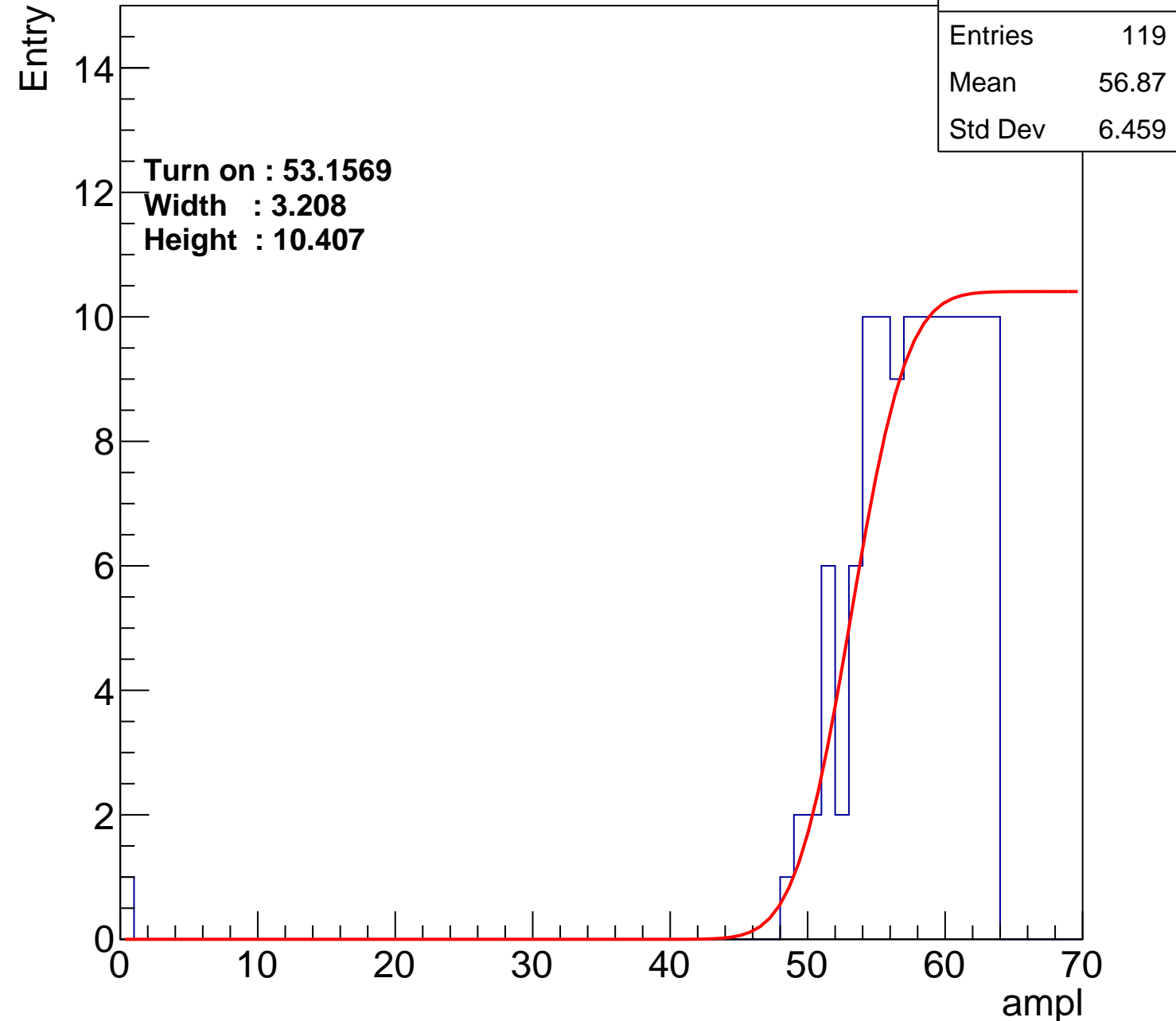
Width : 3.208

Height : 10.407

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.52
Std Dev	9.564

Turn on : 52.1041

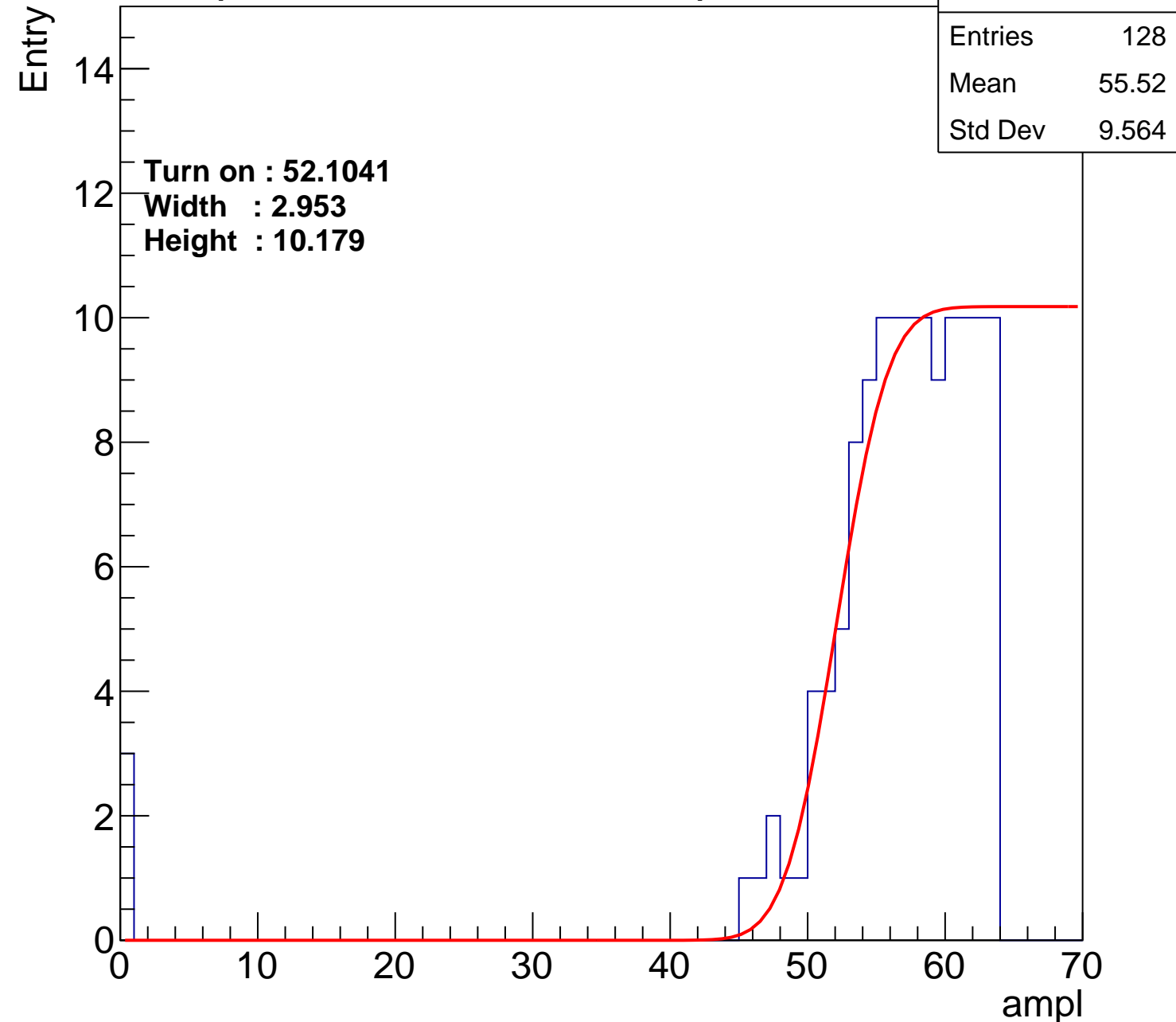
Width : 2.953

Height : 10.179

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	55.34
Std Dev	7.877

Turn on : 49.2525

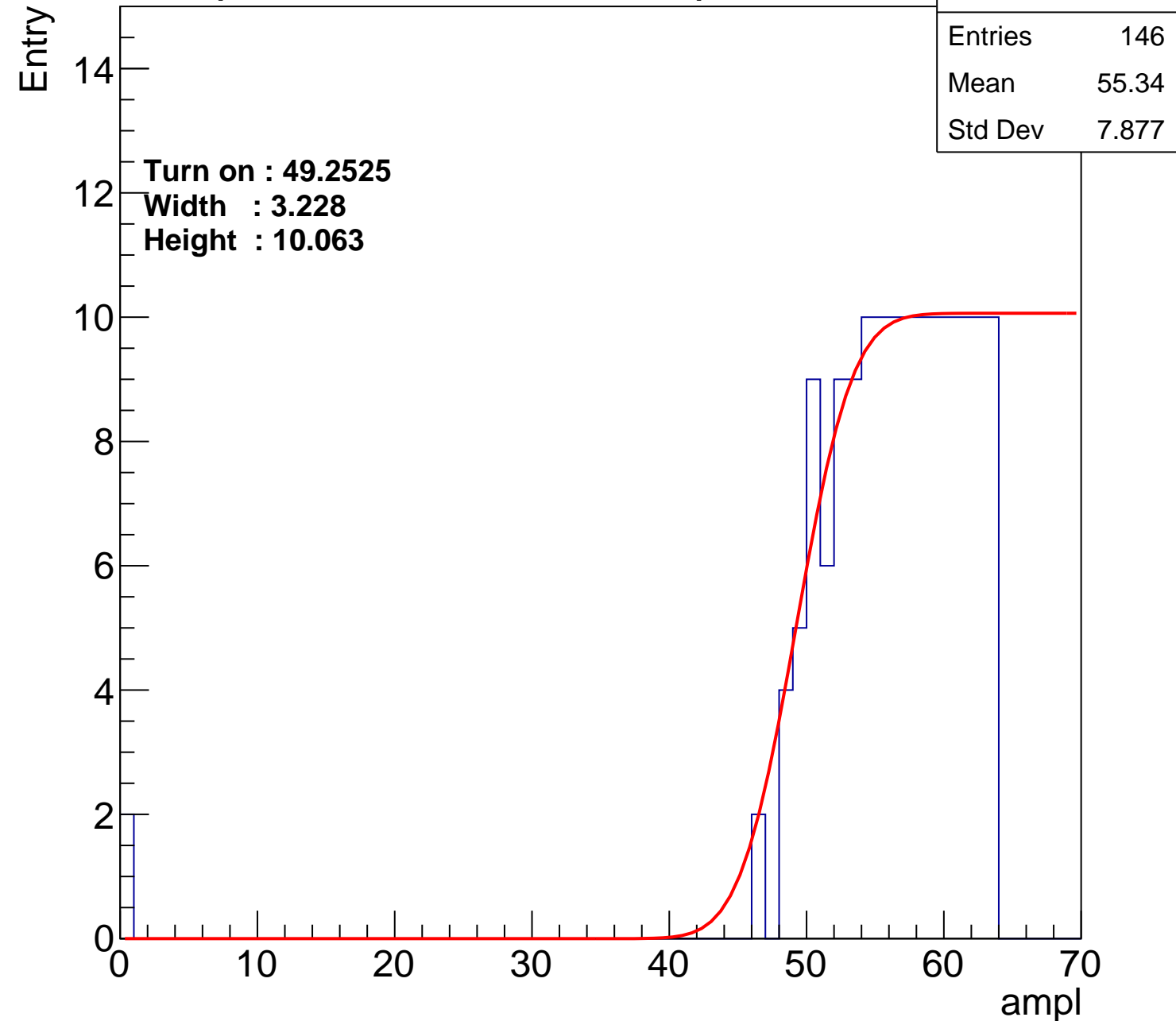
Width : 3.228

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

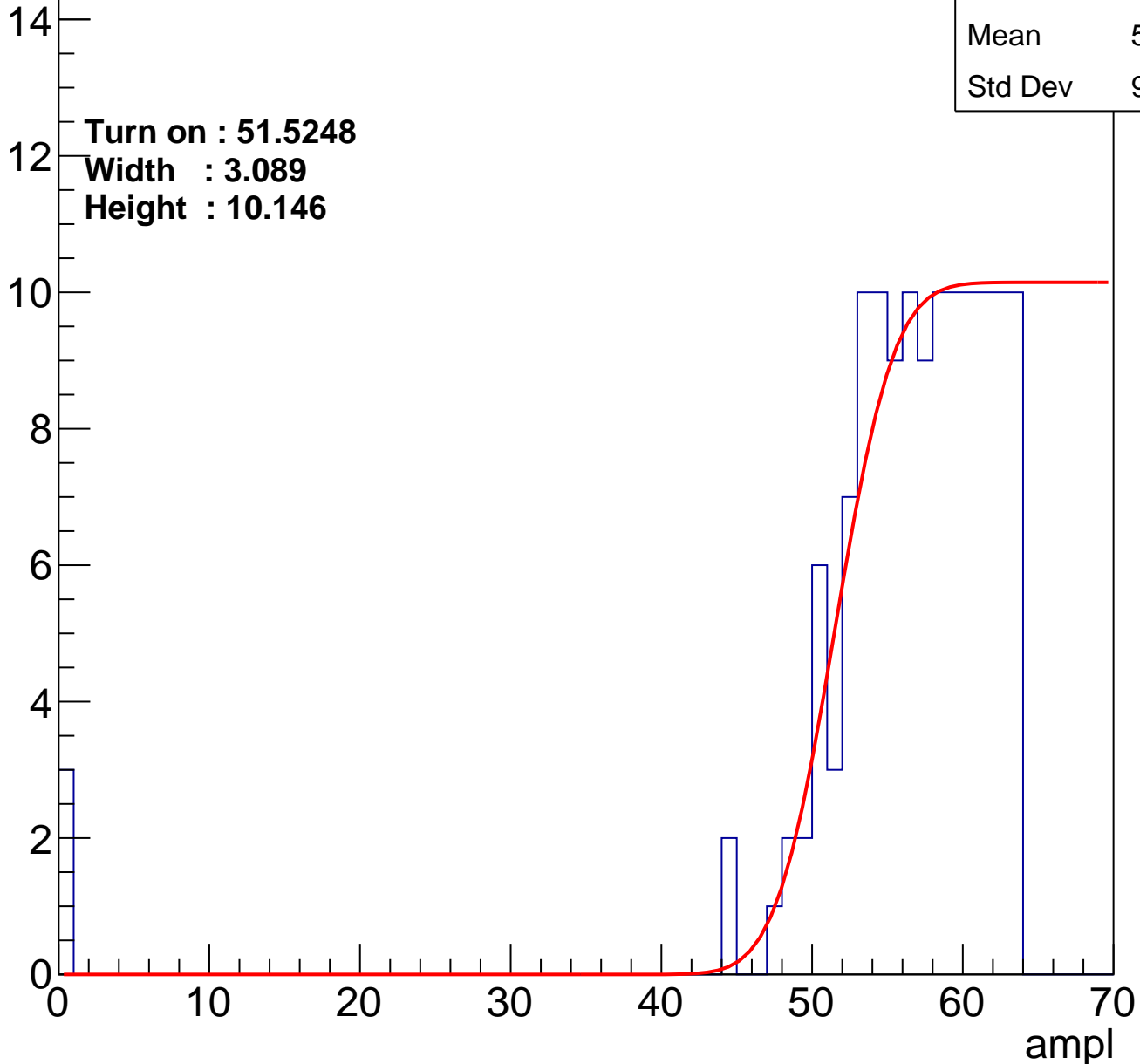
Entries	134
Mean	55.33
Std Dev	9.417

Turn on : 51.5248

Width : 3.089

Height : 10.146

Entry



# B0L103S, U9-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.75
Std Dev	8.07

Turn on : 51.5971

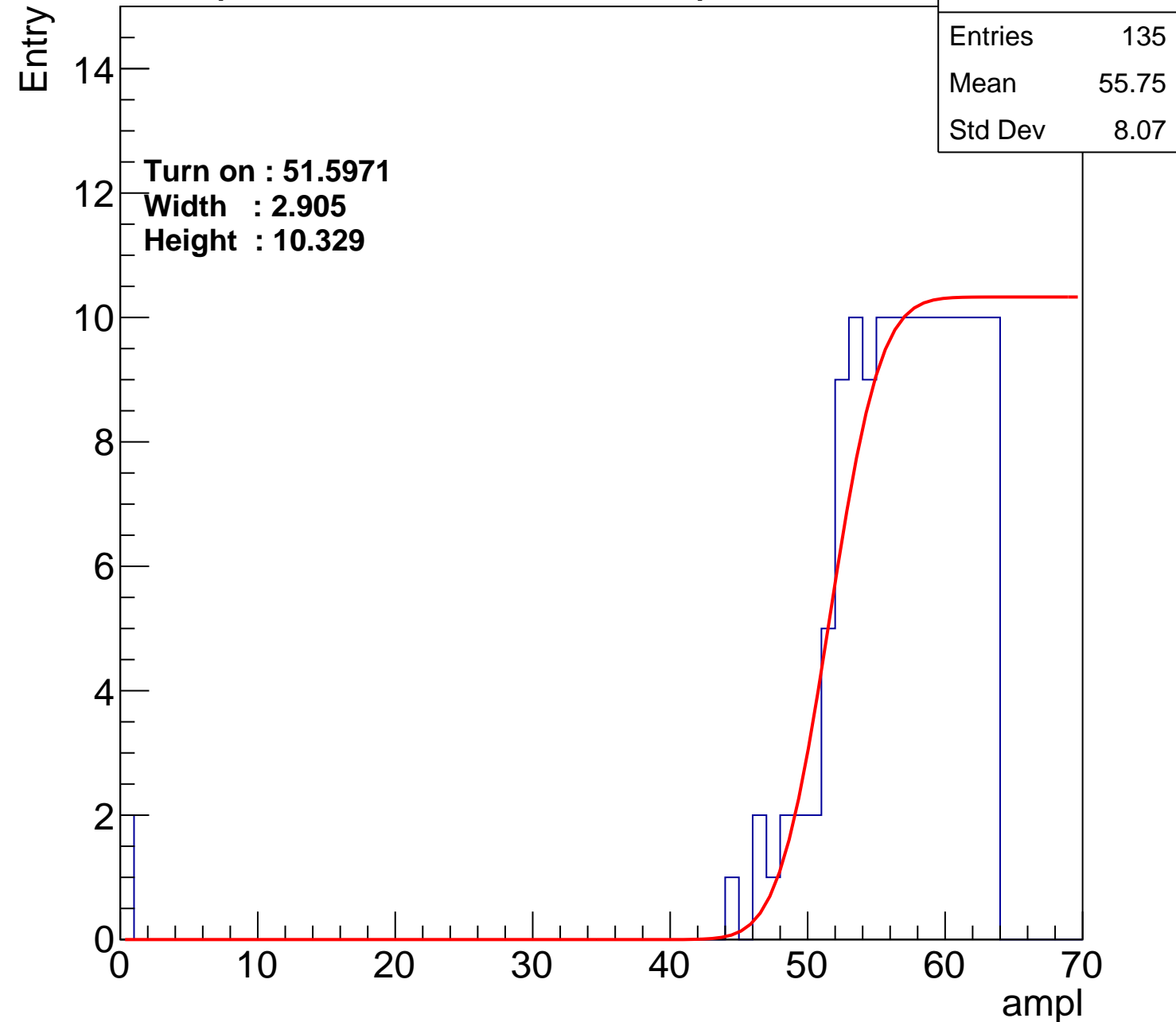
Width : 2.905

Height : 10.329

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 53.1570

Width : 3.929

Height : 10.161

Entries

110

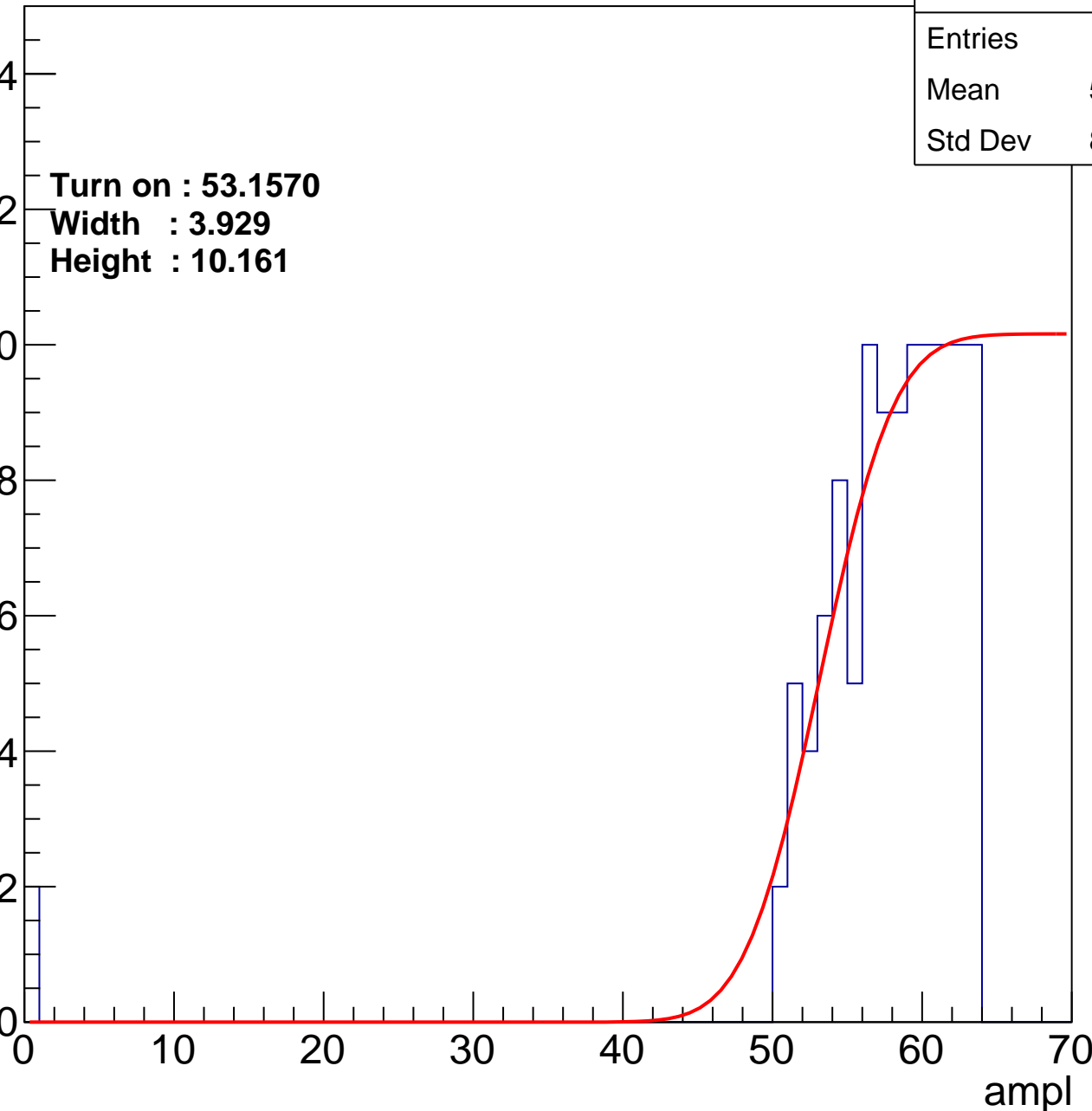
Mean

56.66

Std Dev

8.512

ampl





# B0L103S, U9-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	55.78
Std Dev	6.473

Turn on : 50.0300

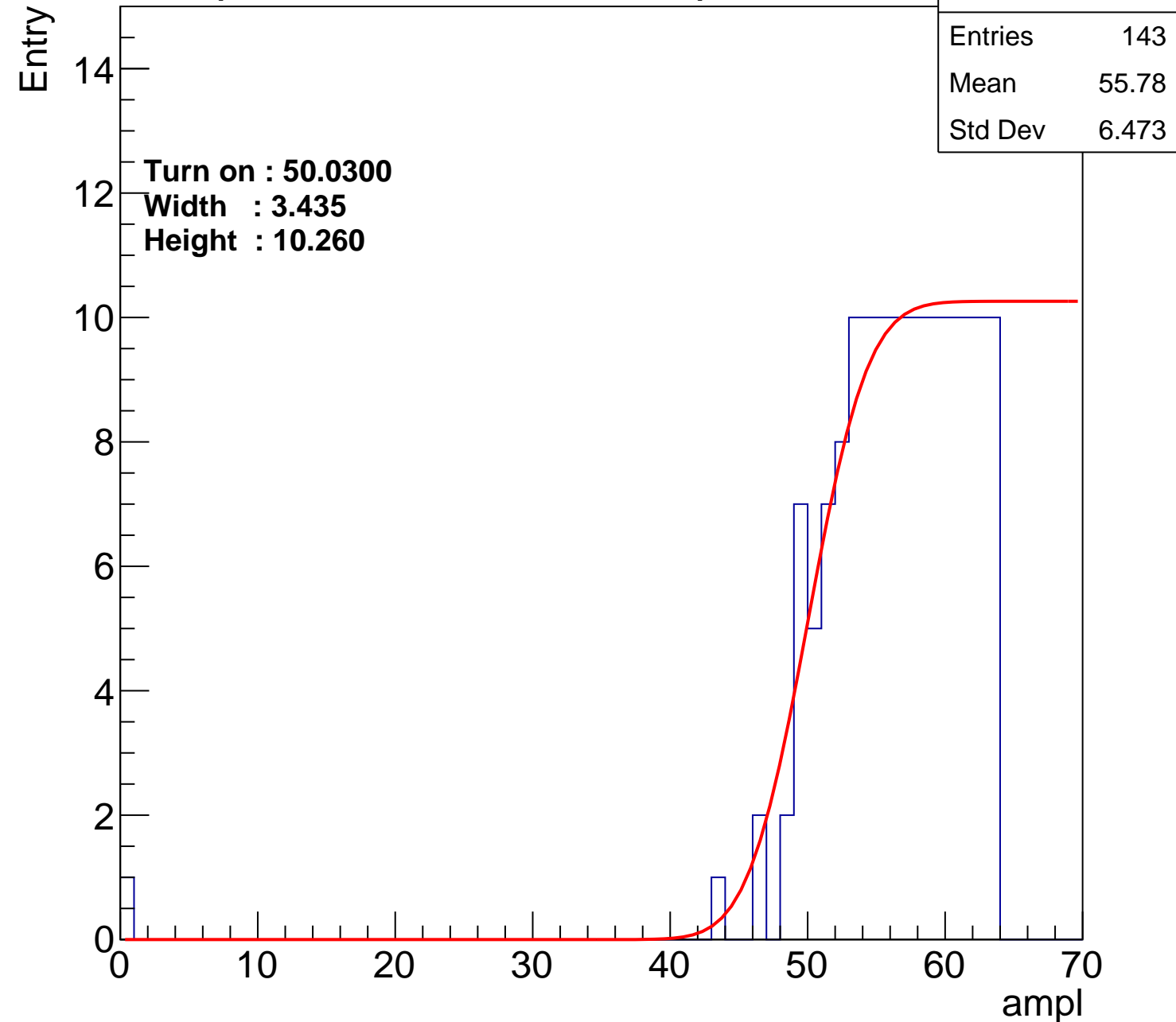
Width : 3.435

Height : 10.260

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

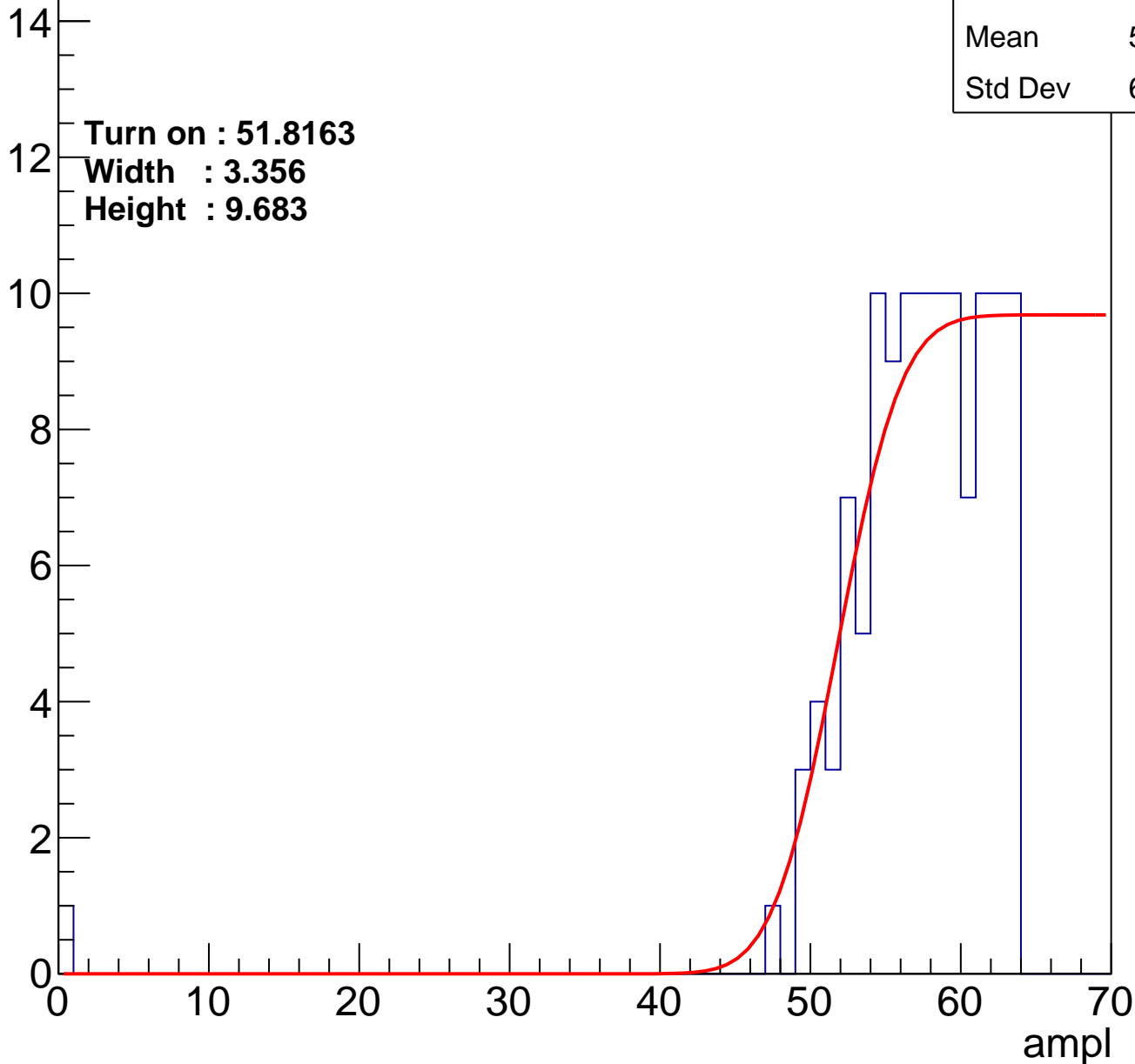
Entry

Entries	120
Mean	56.59
Std Dev	6.519

Turn on : 51.8163

Width : 3.356

Height : 9.683



# B0L103S, U9-ch33

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.77
Std Dev	9.782

Turn on : 52.4371

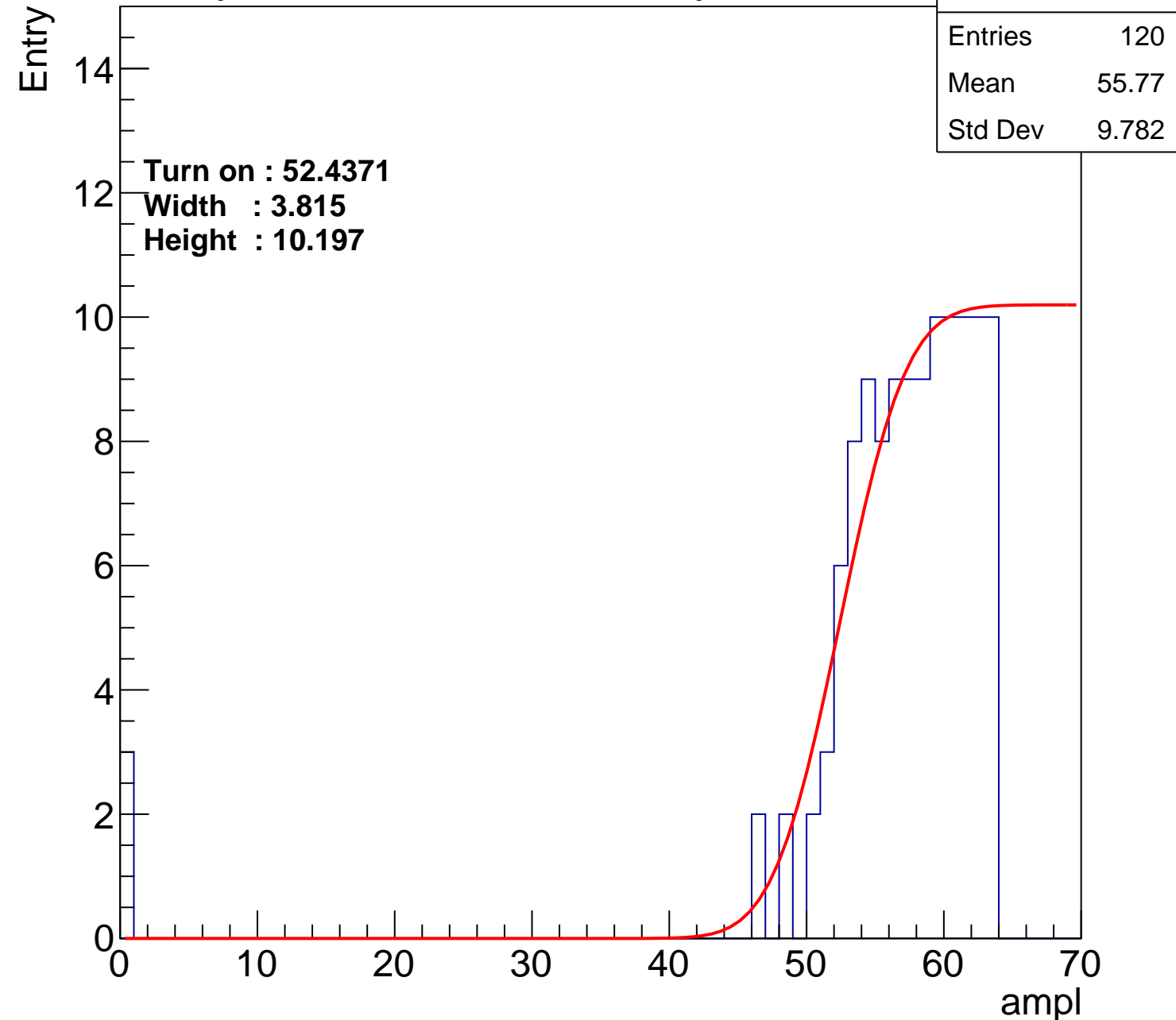
Width : 3.815

Height : 10.197

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

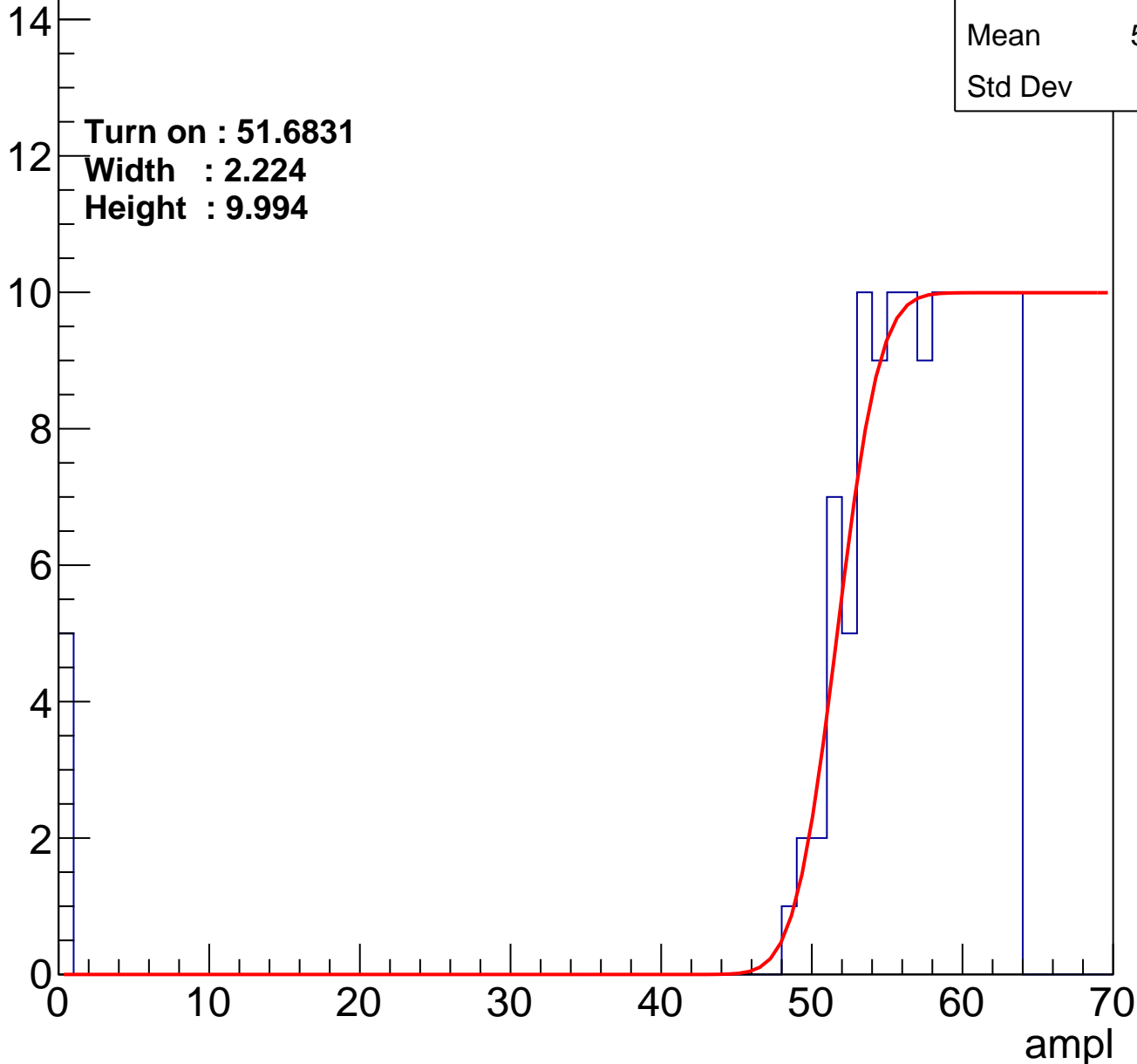
Entry

Entries	130
Mean	54.86
Std Dev	11.61

Turn on : 51.6831

Width : 2.224

Height : 9.994



# B0L103S, U9-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	56.14
Std Dev	8.182

Turn on : 51.6519

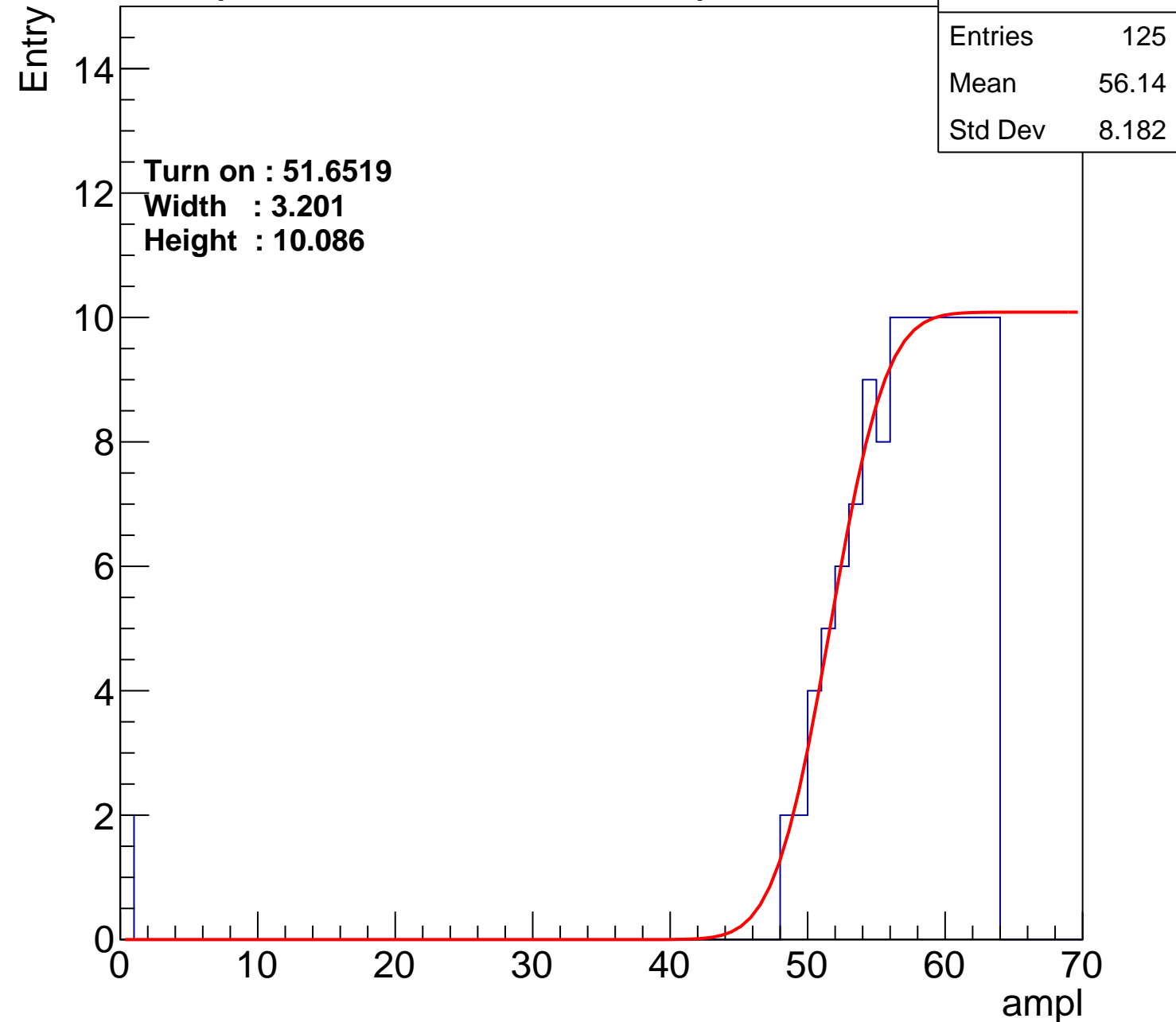
Width : 3.201

Height : 10.086

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.36
Std Dev	9.399

Turn on : 51.4218

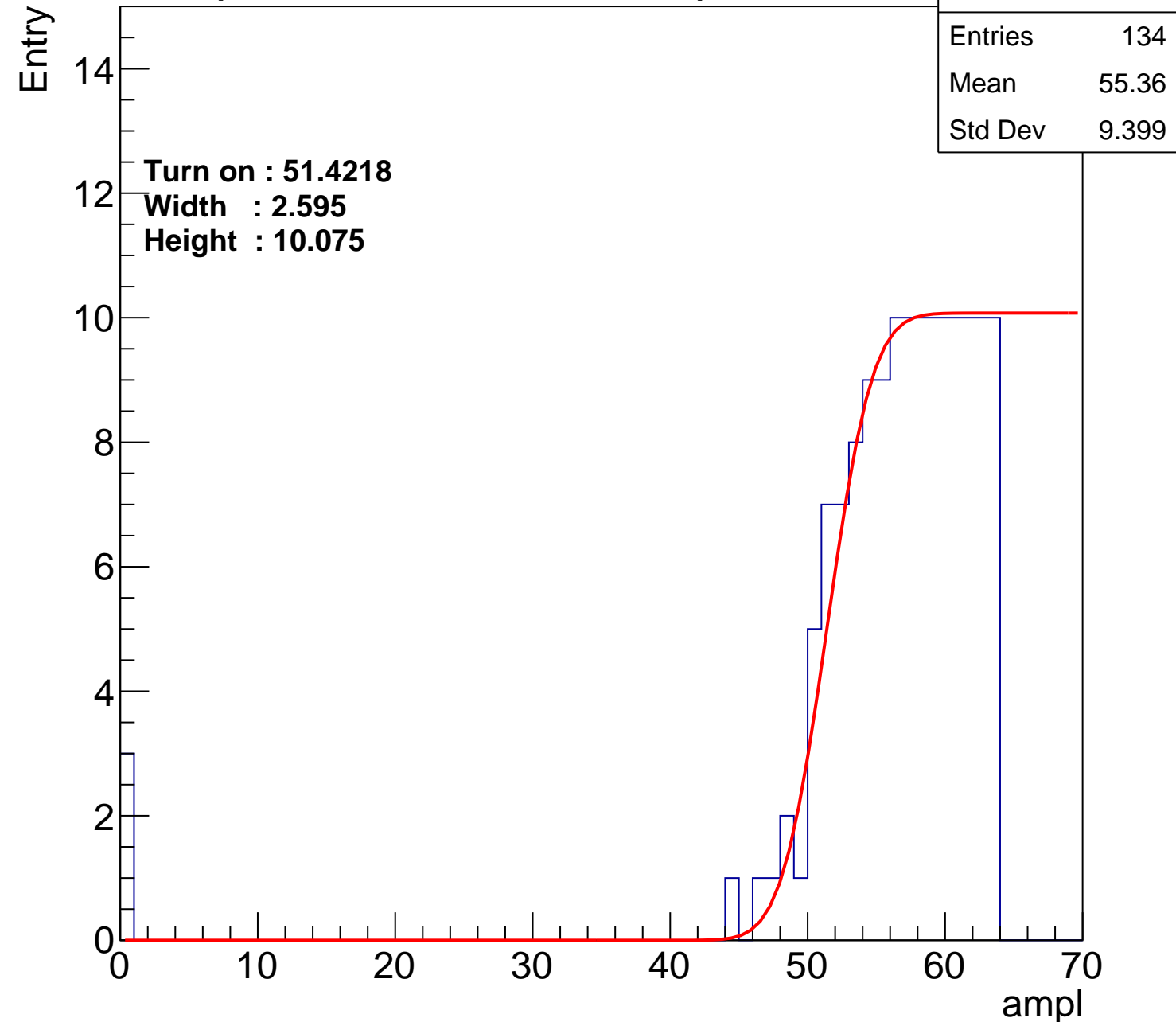
Width : 2.595

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 50.7300

Width : 2.611

Height : 9.865

Entries

134

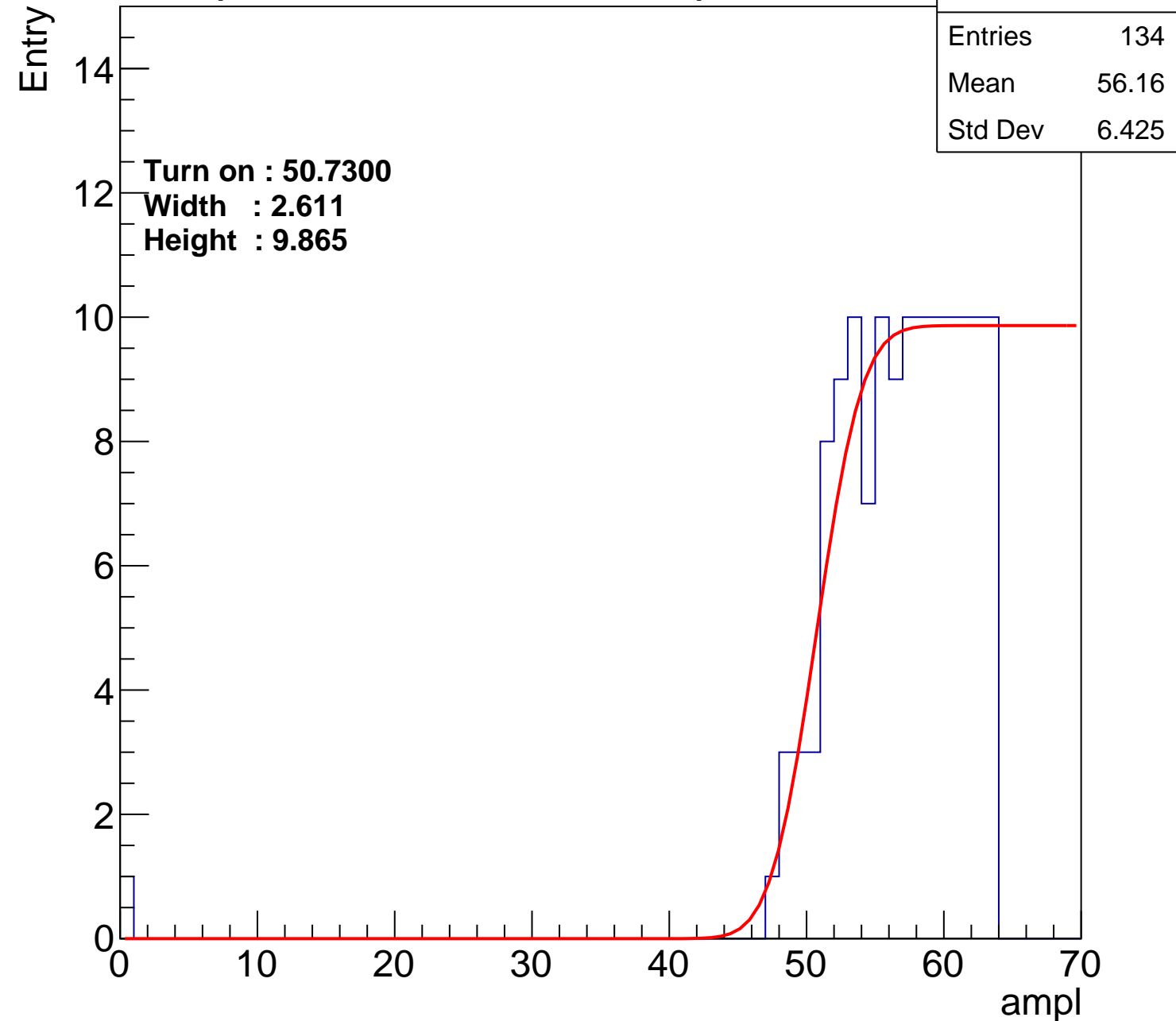
Mean

56.16

Std Dev

6.425

ampl



# B0L103S, U9-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	54.79
Std Dev	11.69

Turn on : 51.6161

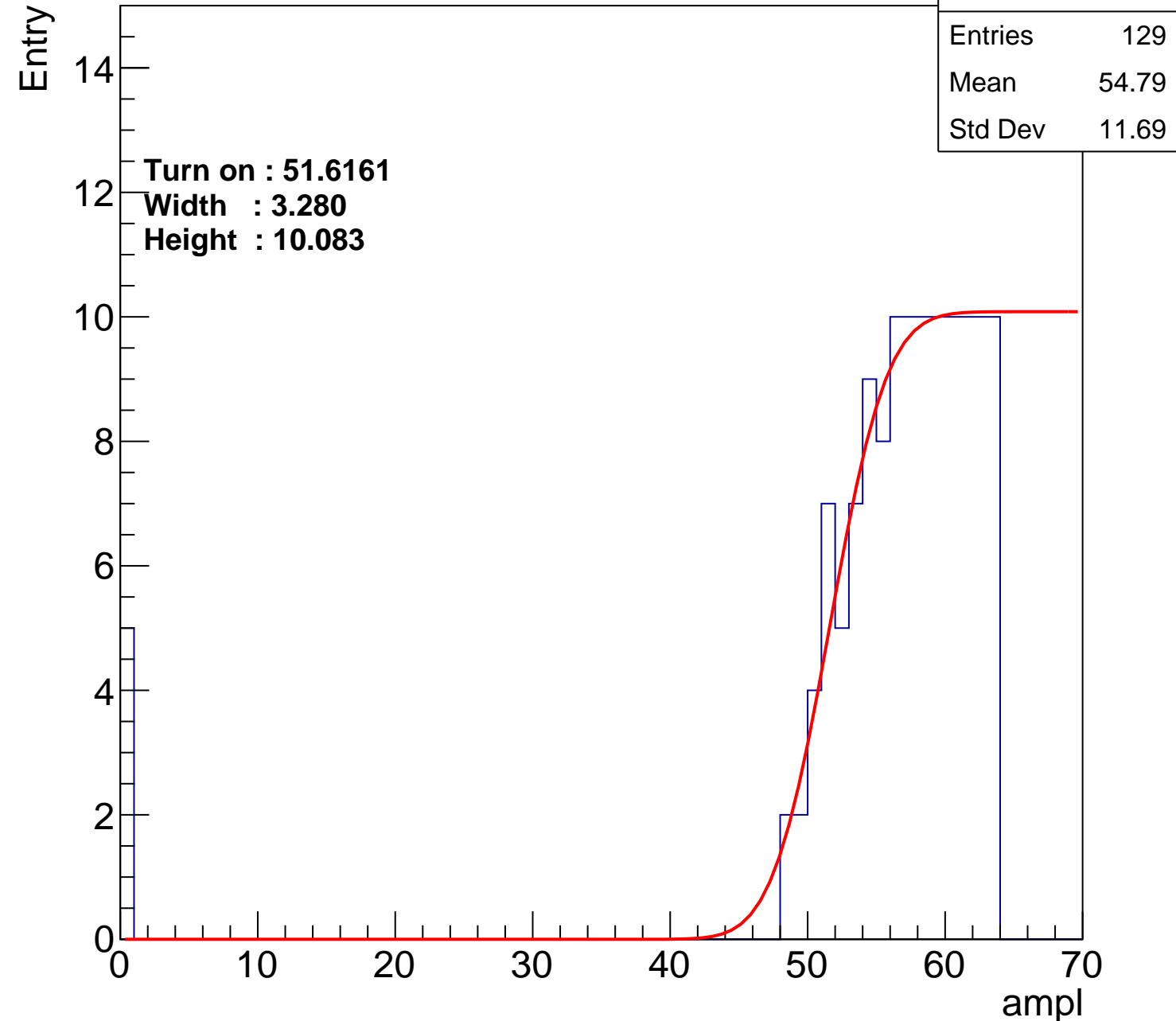
Width : 3.280

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.76
Std Dev	8.071

Turn on : 50.7726

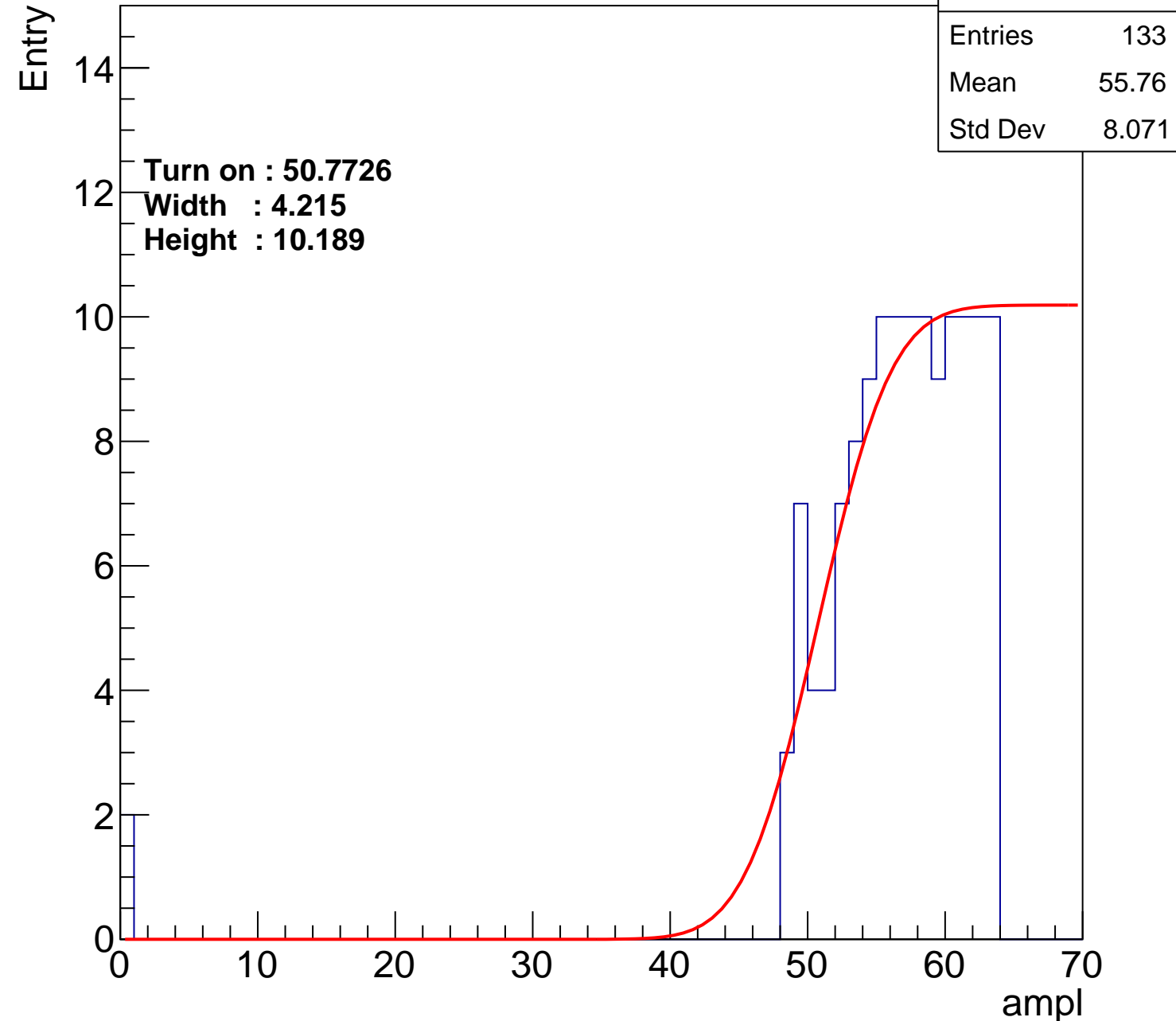
Width : 4.215

Height : 10.189

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	160
Mean	53.26
Std Dev	12.27

Turn on : 48.7760

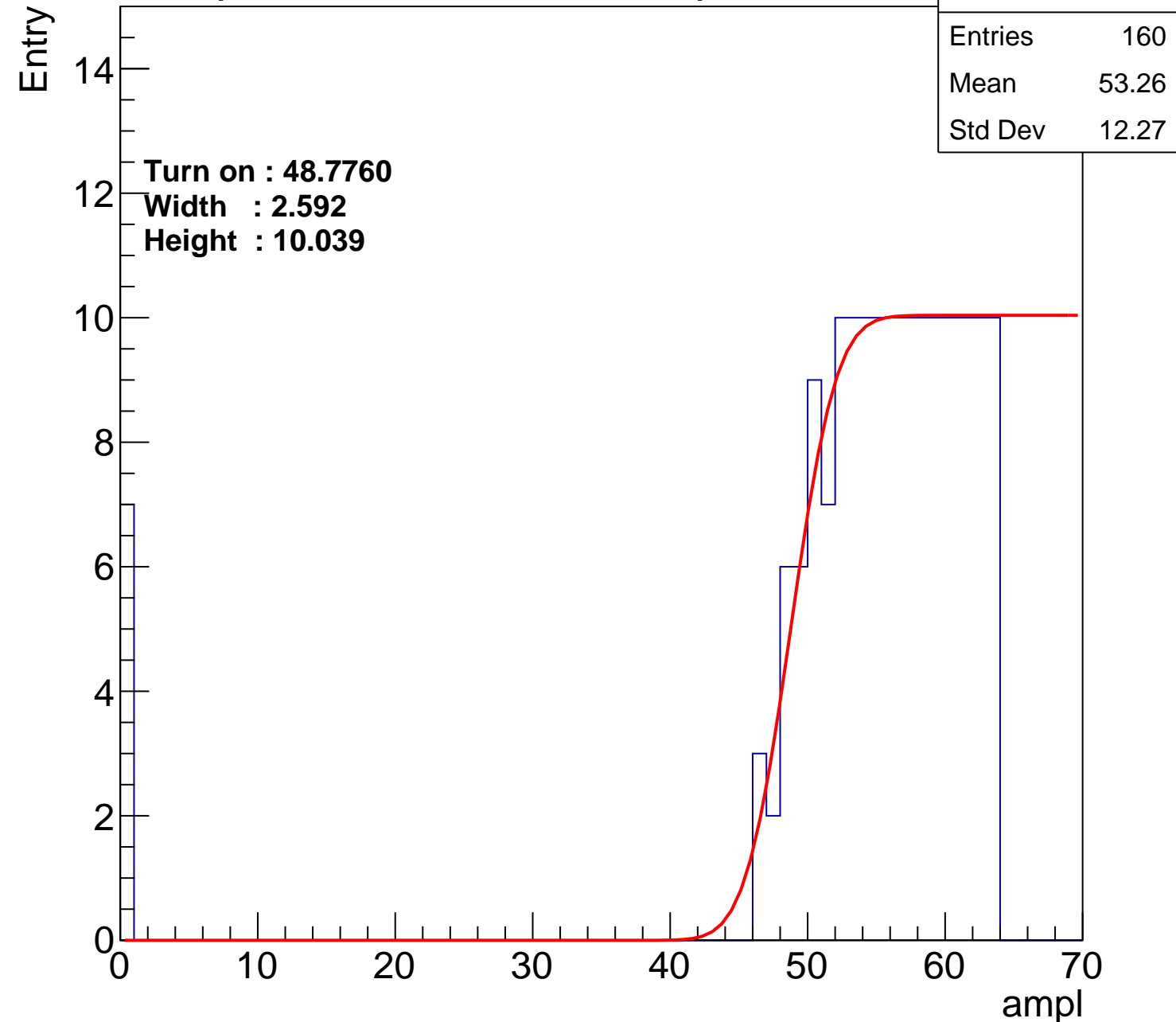
Width : 2.592

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	54.33
Std Dev	12.78

Turn on : 51.4756

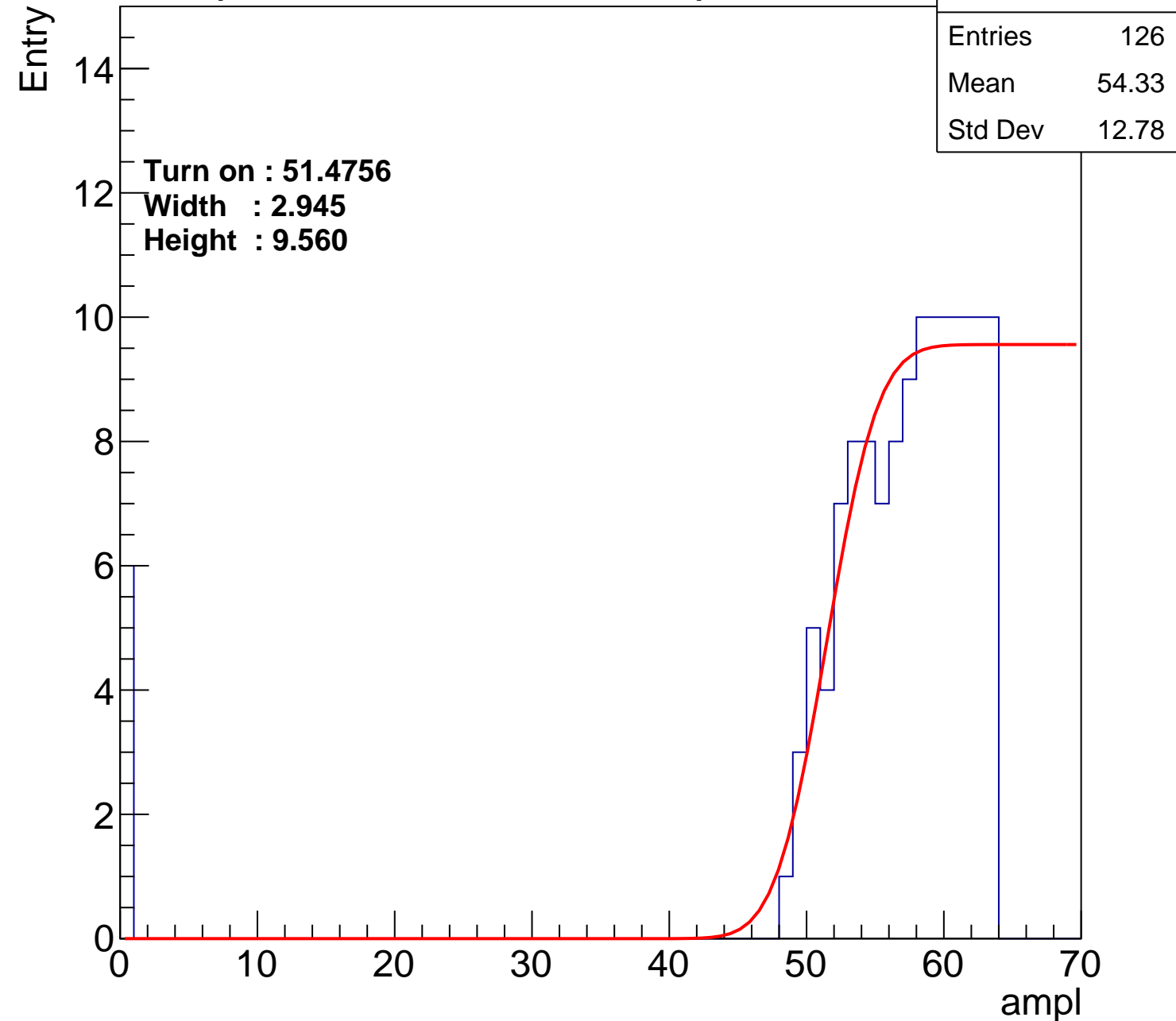
Width : 2.945

Height : 9.560

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.87
Std Dev	8.149

Turn on : 50.9604

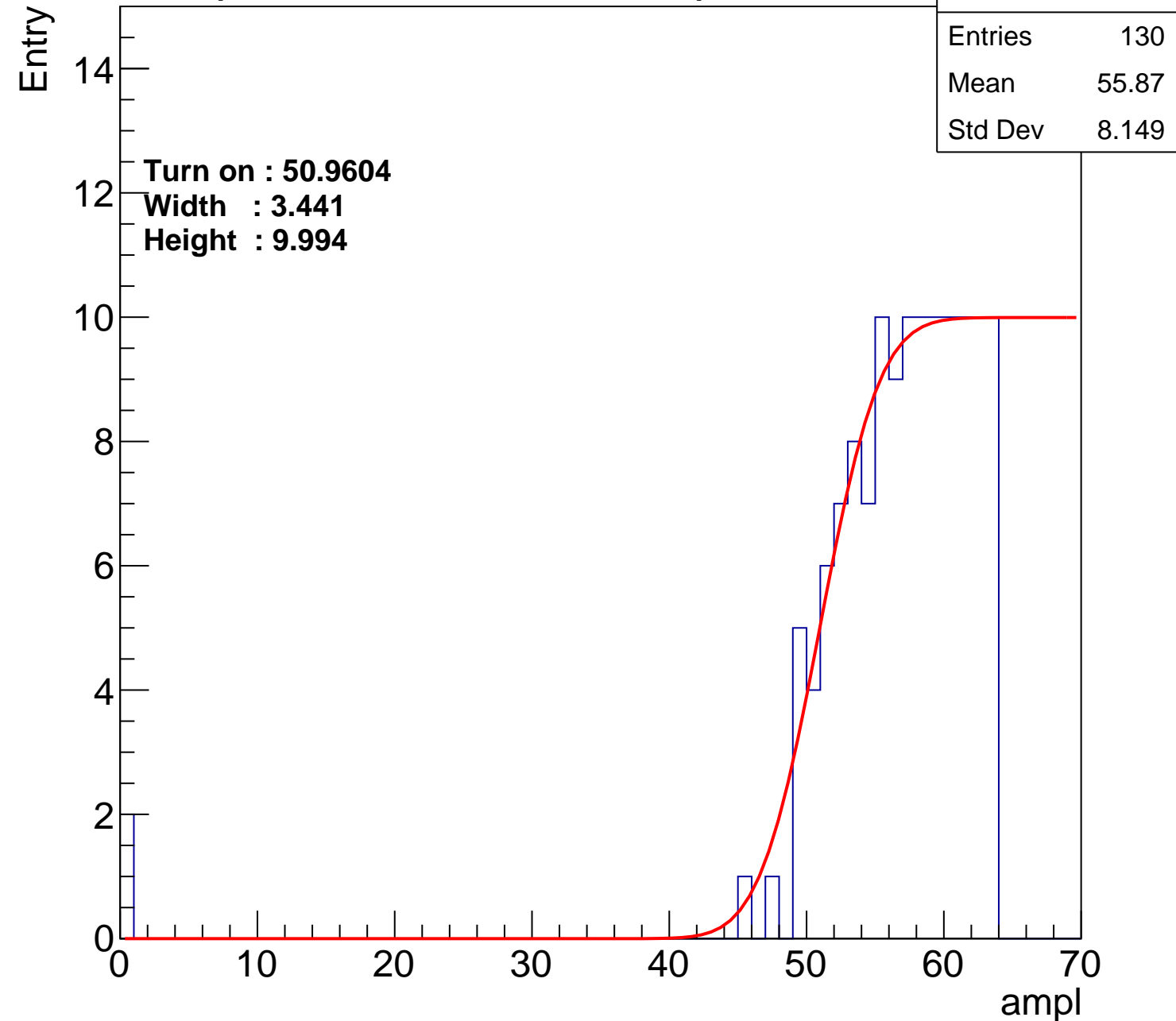
Width : 3.441

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	54.42
Std Dev	12.67

Turn on : 51.7121

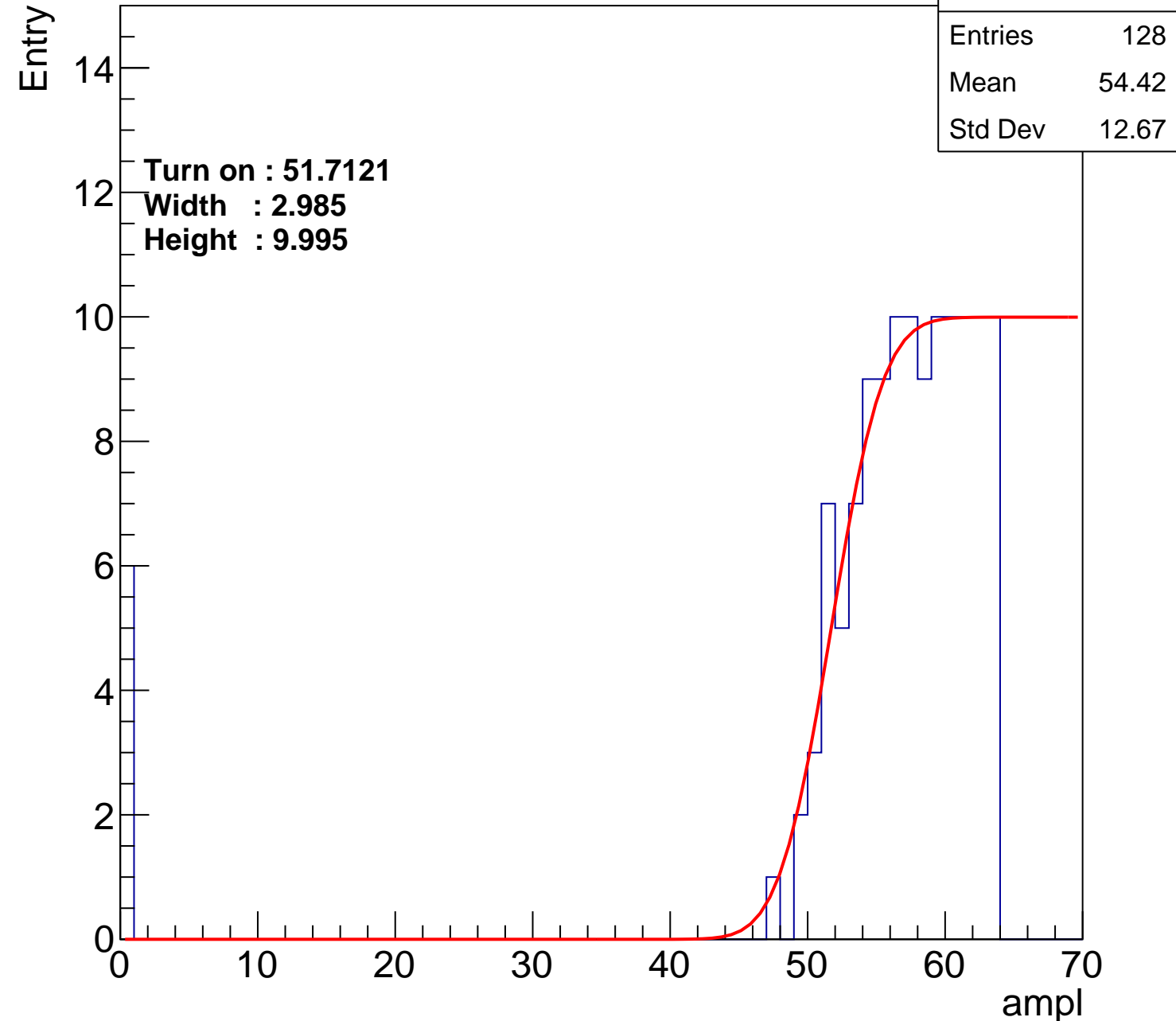
Width : 2.985

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch44

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	56.17
Std Dev	6.438

Turn on : 50.8083

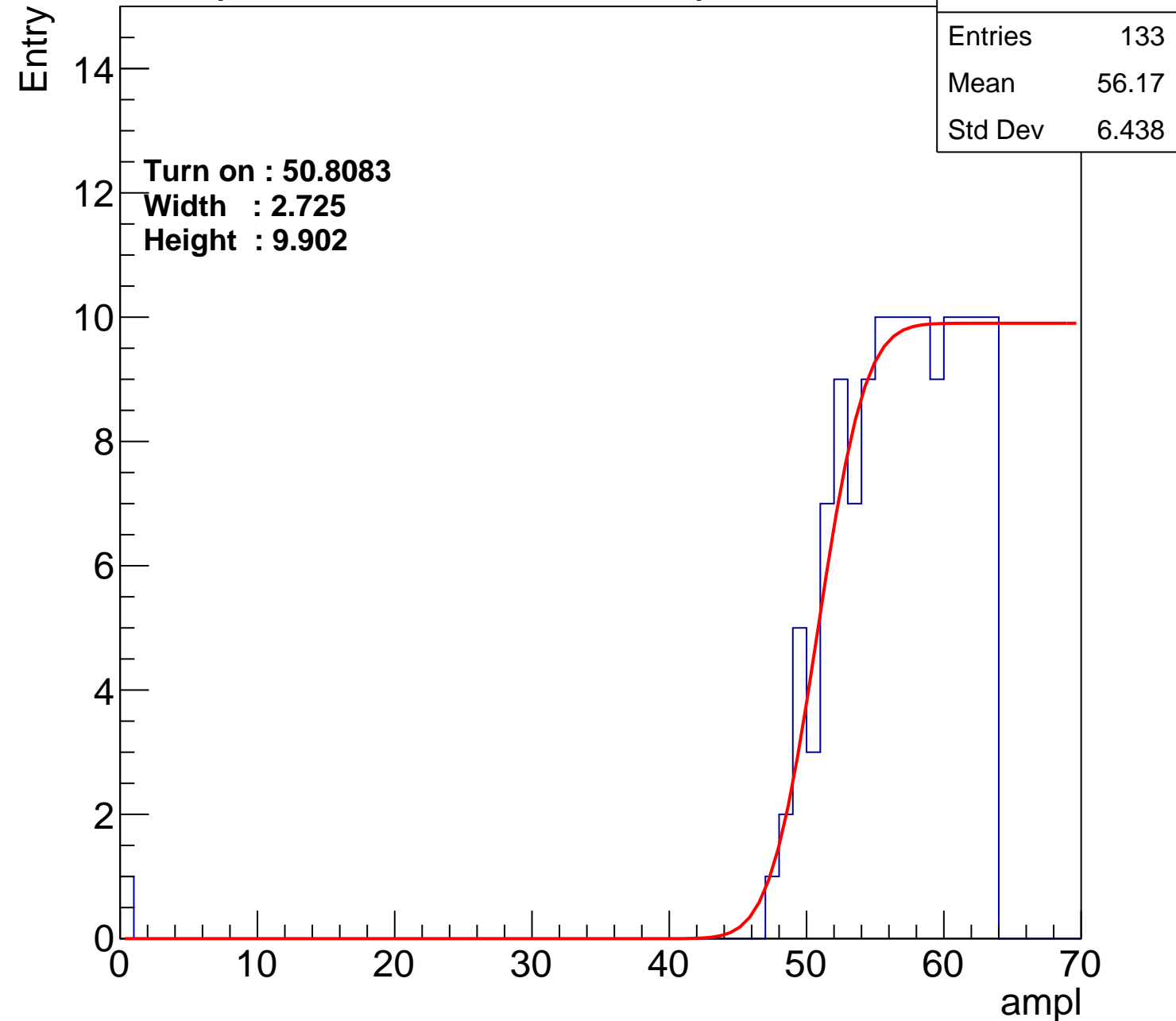
Width : 2.725

Height : 9.902

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

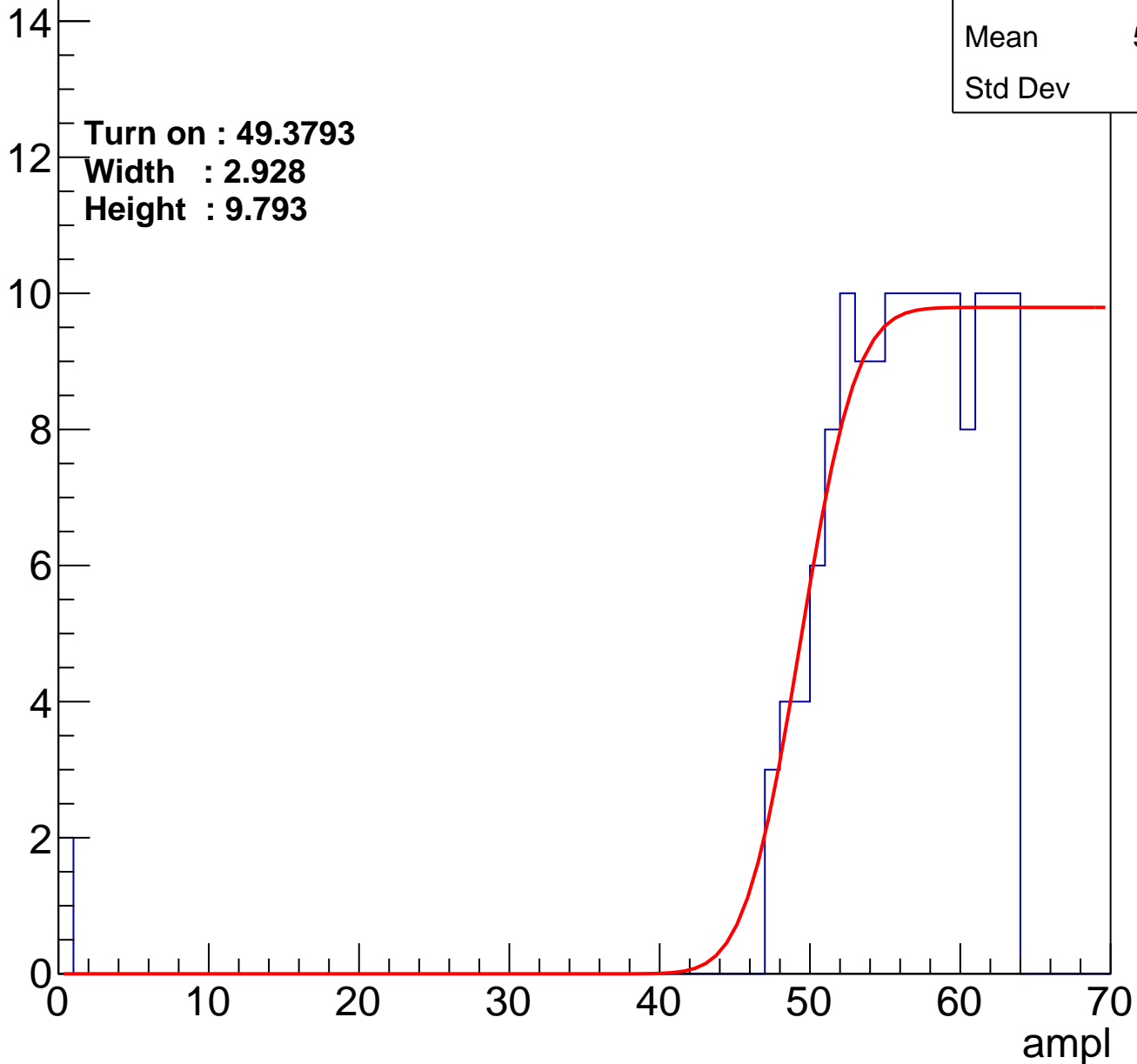
Entry

Entries	143
Mean	55.31
Std Dev	7.92

Turn on : 49.3793

Width : 2.928

Height : 9.793



# B0L103S, U9-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

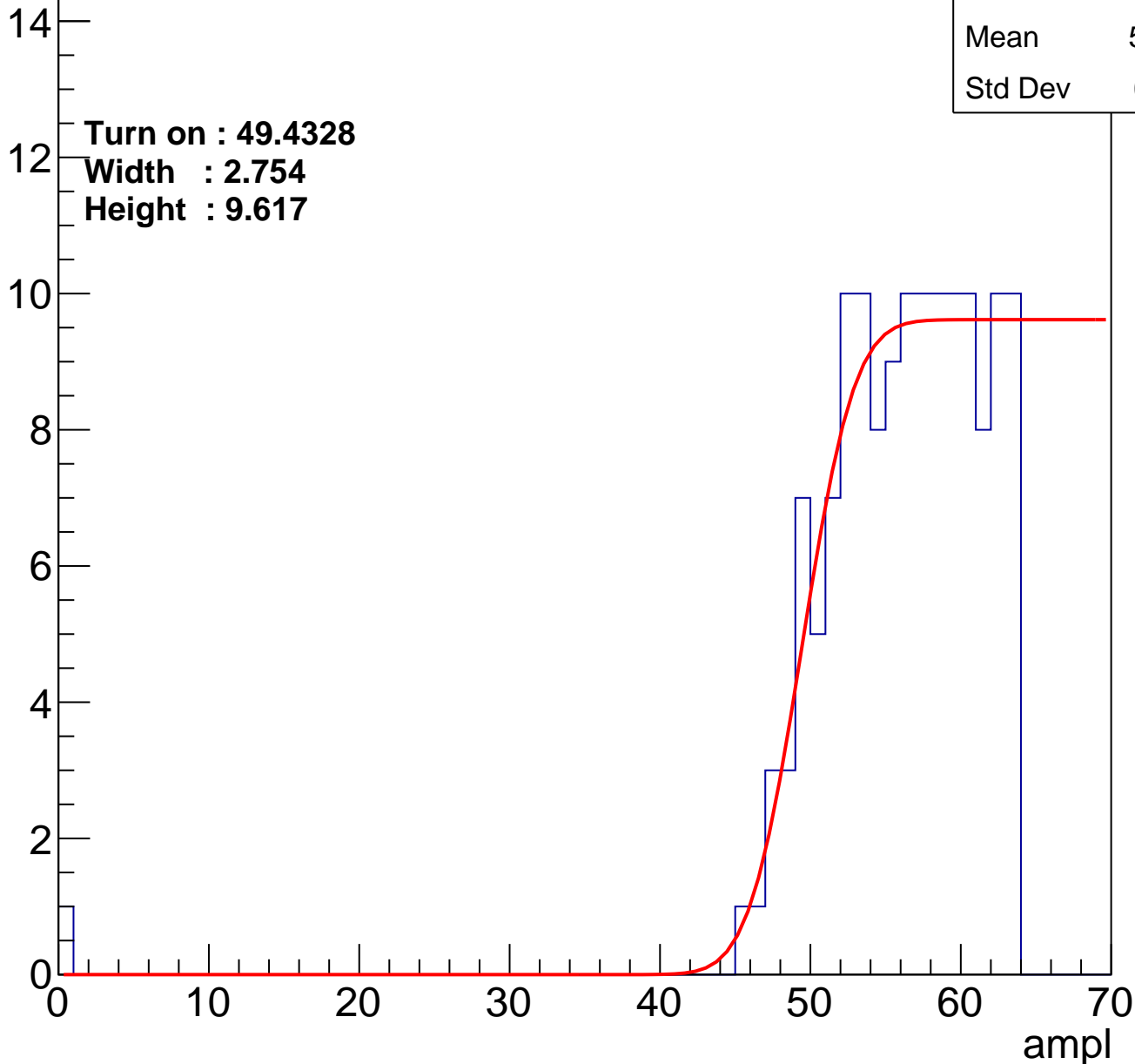
Entry

Entries	143
Mean	55.53
Std Dev	6.531

Turn on : 49.4328

Width : 2.754

Height : 9.617





# B0L103S, U9-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 51.8380

Width : 2.942

Height : 9.850

Entries

124

Mean

56.49

Std Dev

6.533

ampl

0

10

20

30

40

50

60

70

# B0L103S, U9-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	152
Mean	54.67
Std Dev	9.076

Turn on : 49.5190

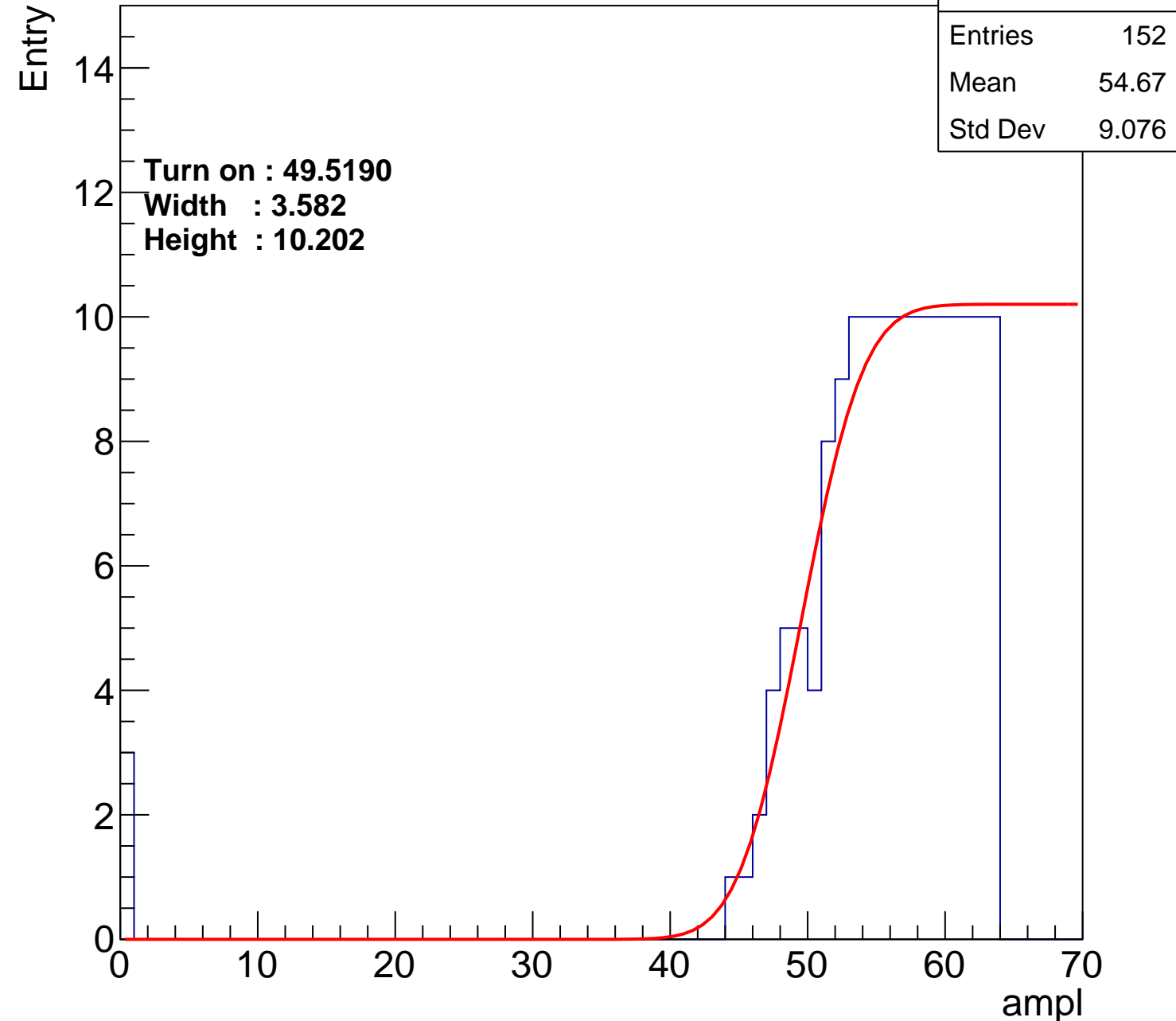
Width : 3.582

Height : 10.202

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.36
Std Dev	10.96

Turn on : 53.0990

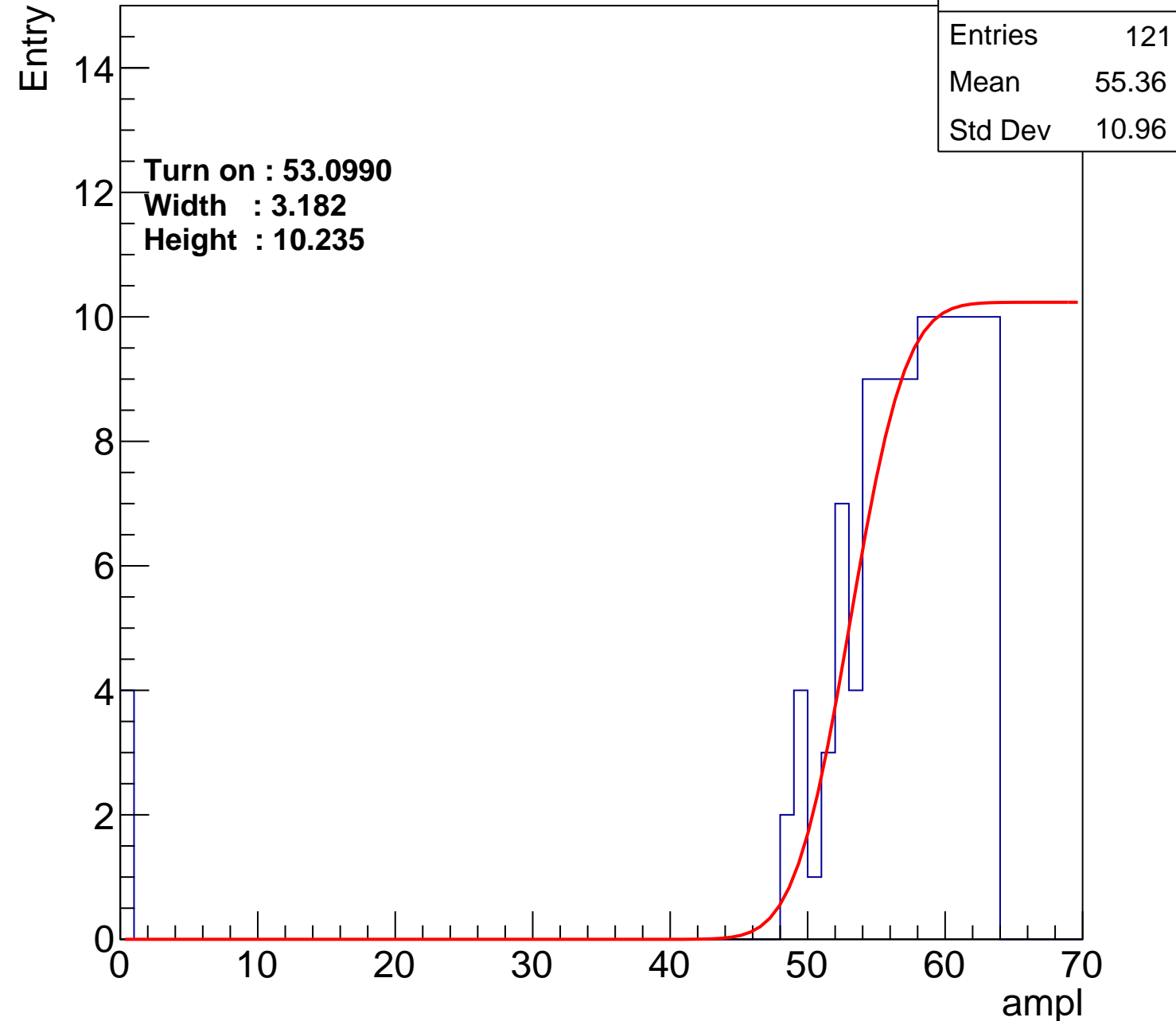
Width : 3.182

Height : 10.235

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	53.5
Std Dev	14.37

**Turn on : 52.0770**

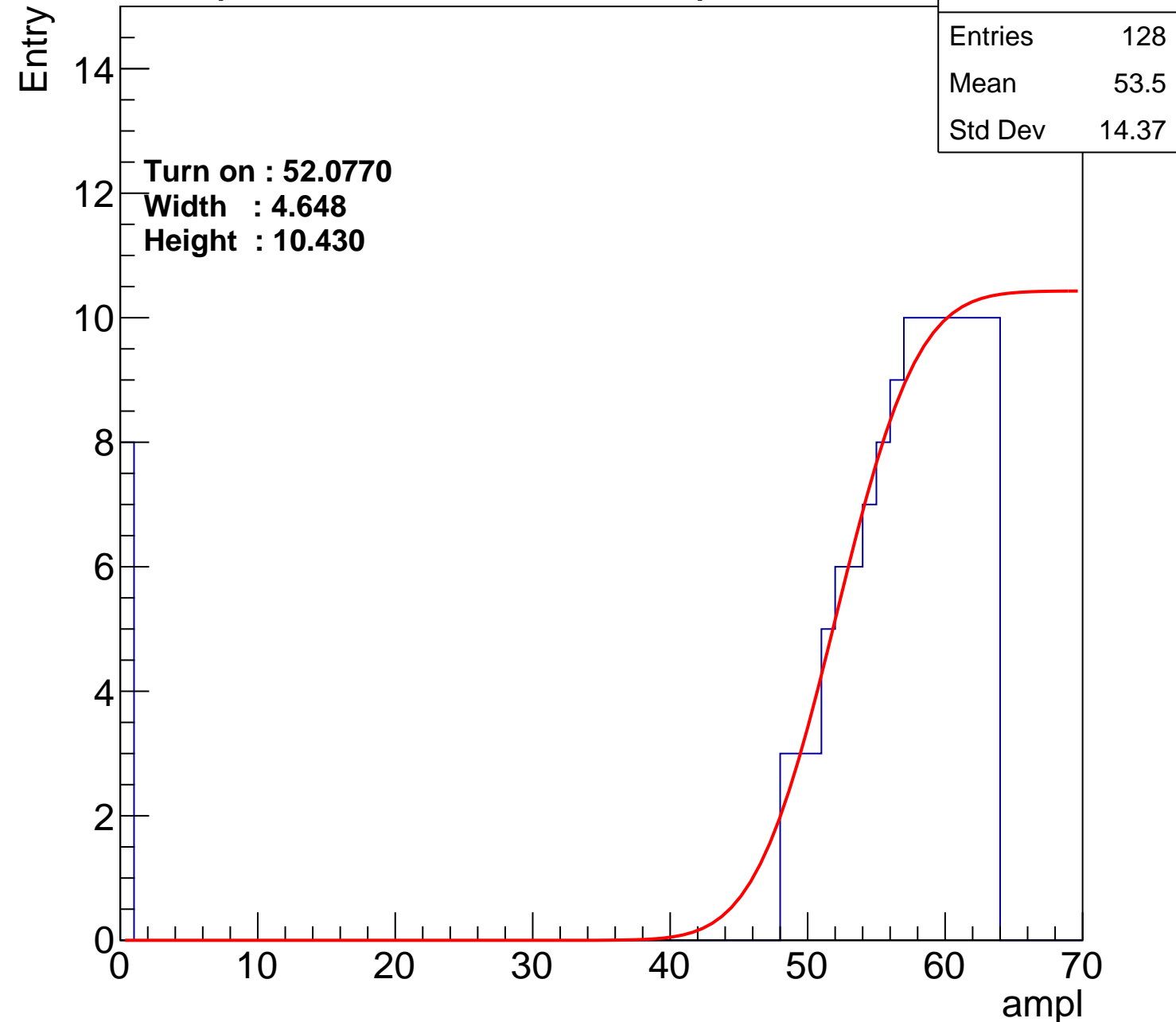
**Width : 4.648**

**Height : 10.430**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch51

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.16
Std Dev	12.13

Turn on : 53.1940

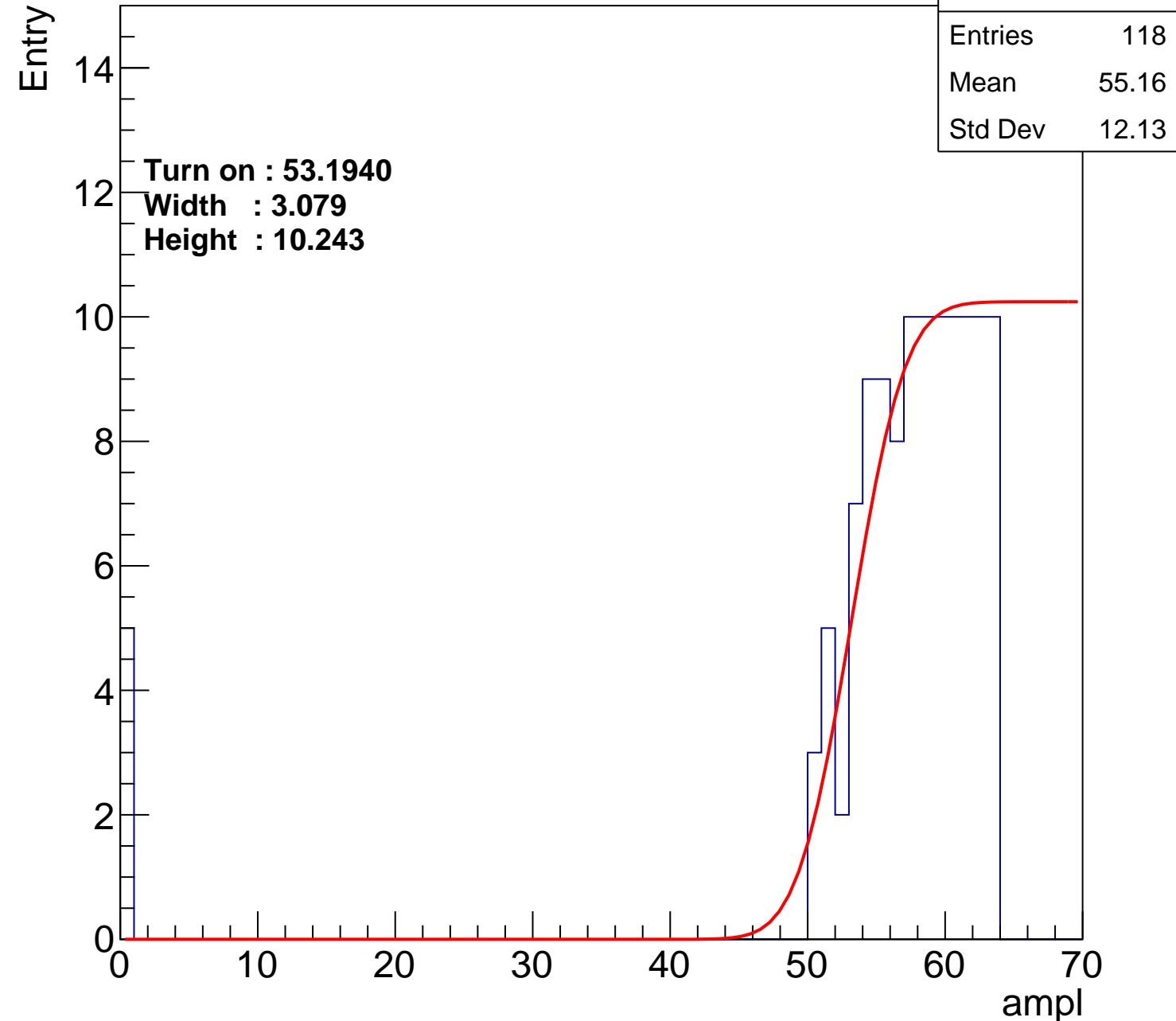
Width : 3.079

Height : 10.243

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.86
Std Dev	6.494

Turn on : 51.0647

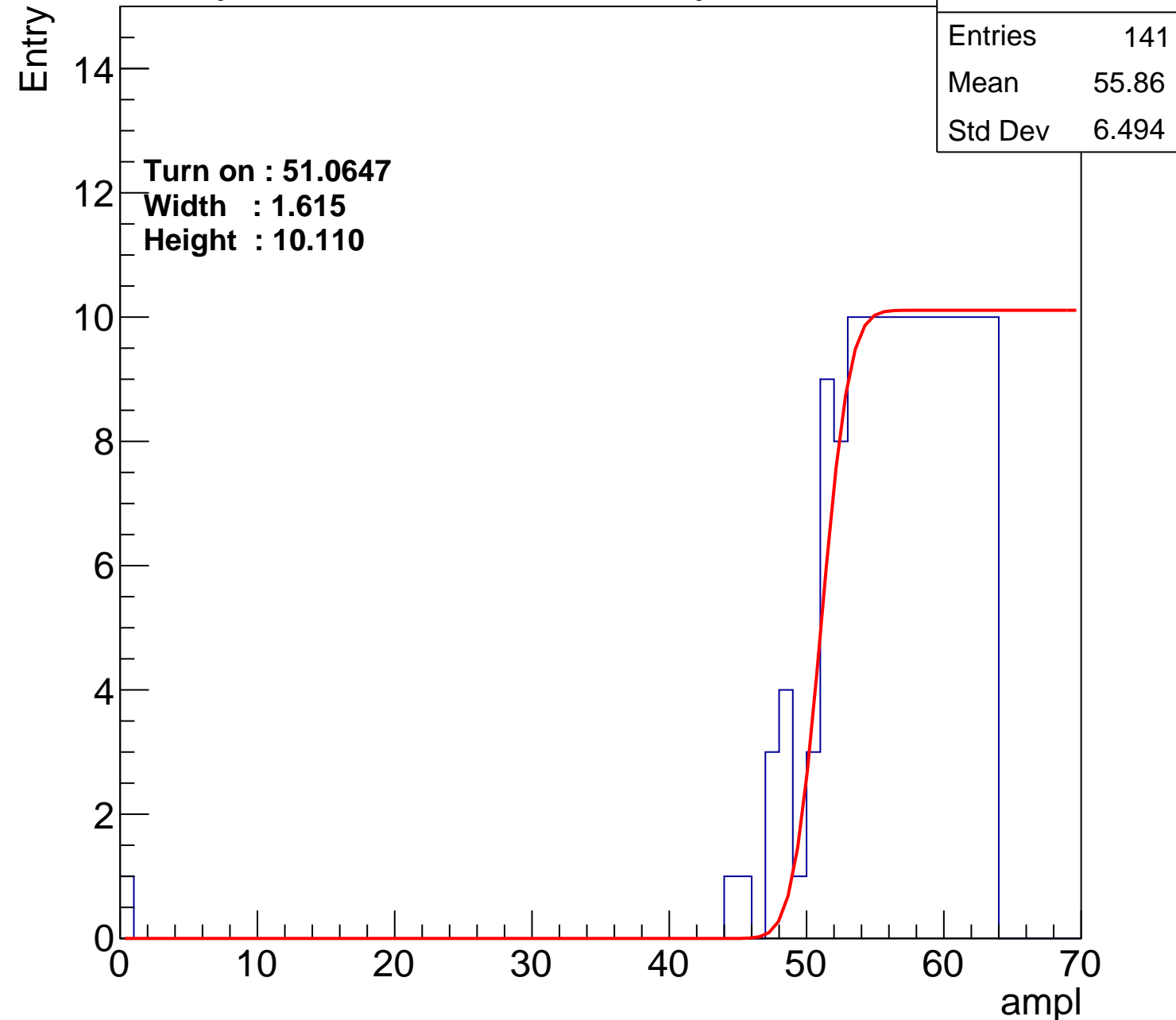
Width : 1.615

Height : 10.110

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

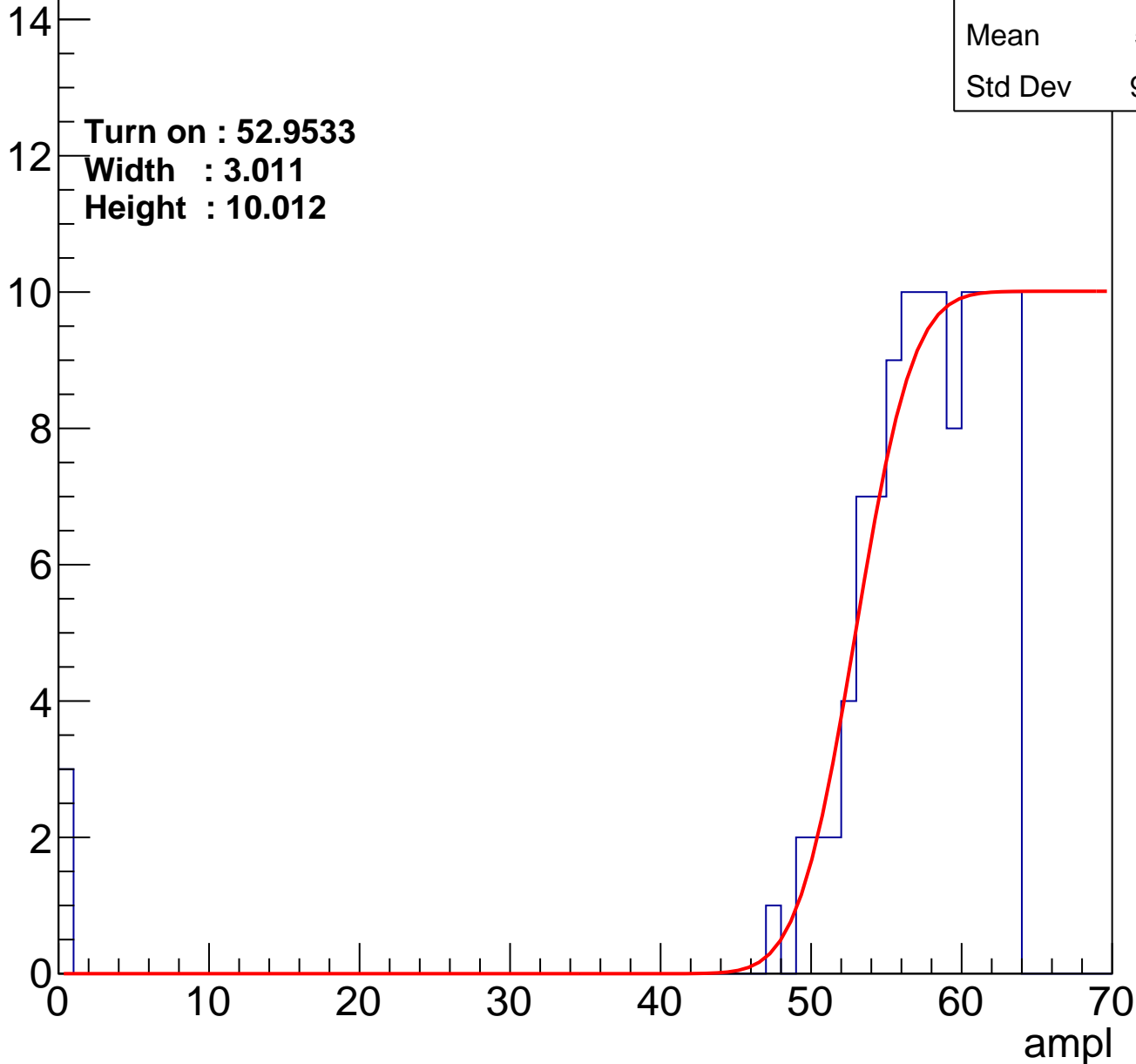
Entry

Entries	115
Mean	56.01
Std Dev	9.893

Turn on : 52.9533

Width : 3.011

Height : 10.012



# B0L103S, U9-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

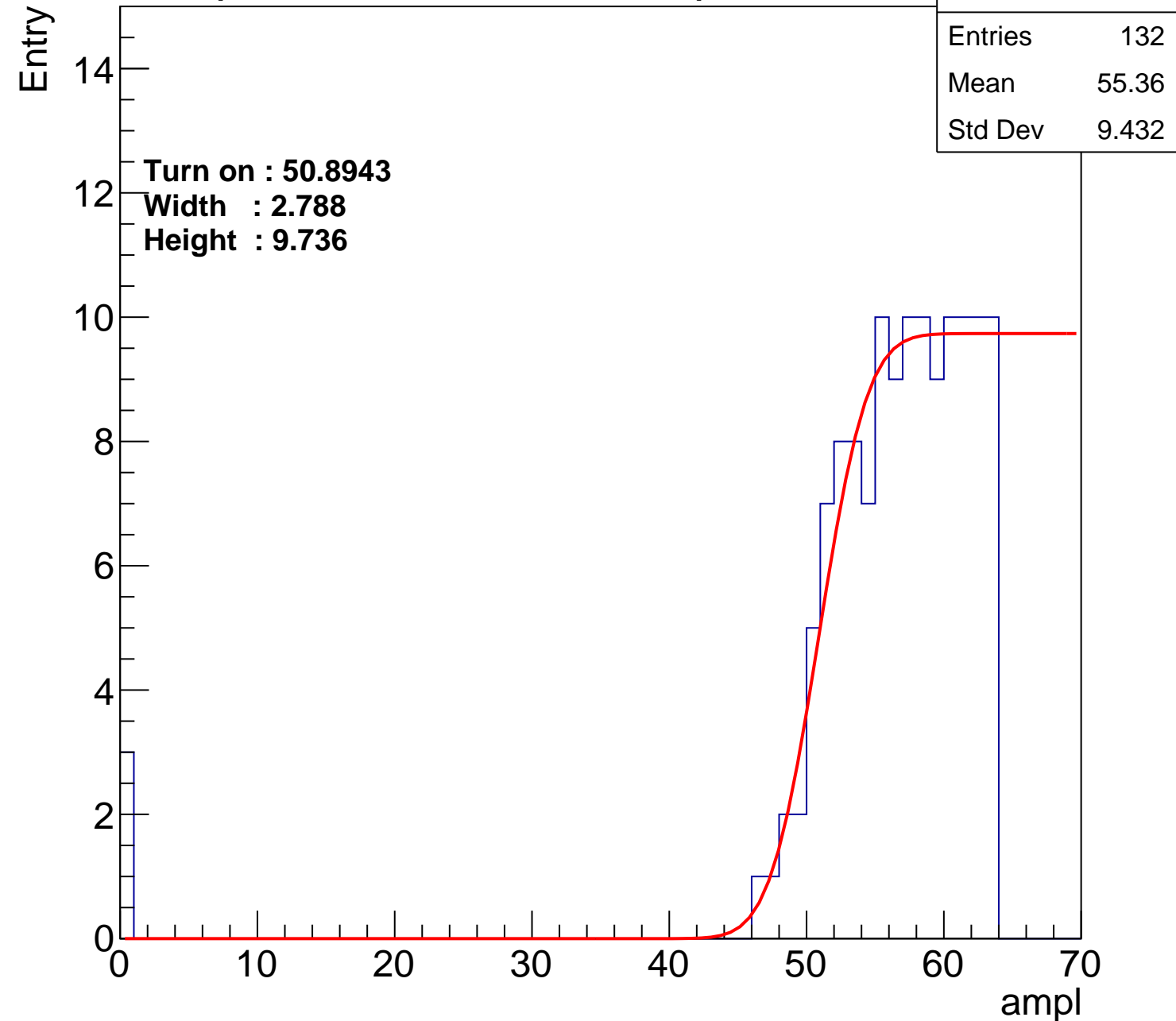
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.8943  
Width : 2.788  
Height : 9.736

Entries	132
Mean	55.36
Std Dev	9.432

ampl





# B0L103S, U9-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	56.77
Std Dev	6.516

Turn on : 52.5117

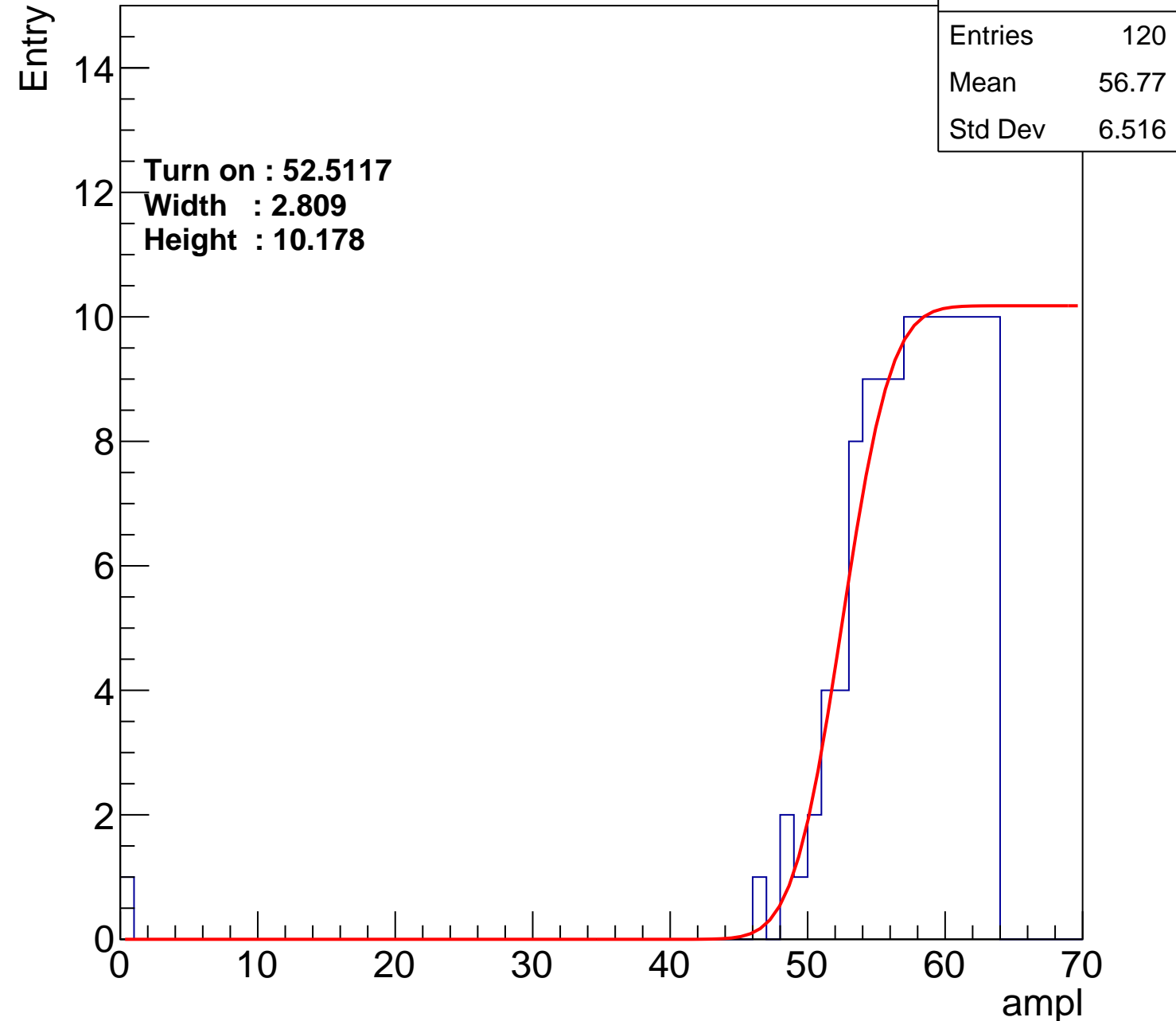
Width : 2.809

Height : 10.178

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	148
Mean	54.89
Std Dev	9.091

Turn on : 49.5020

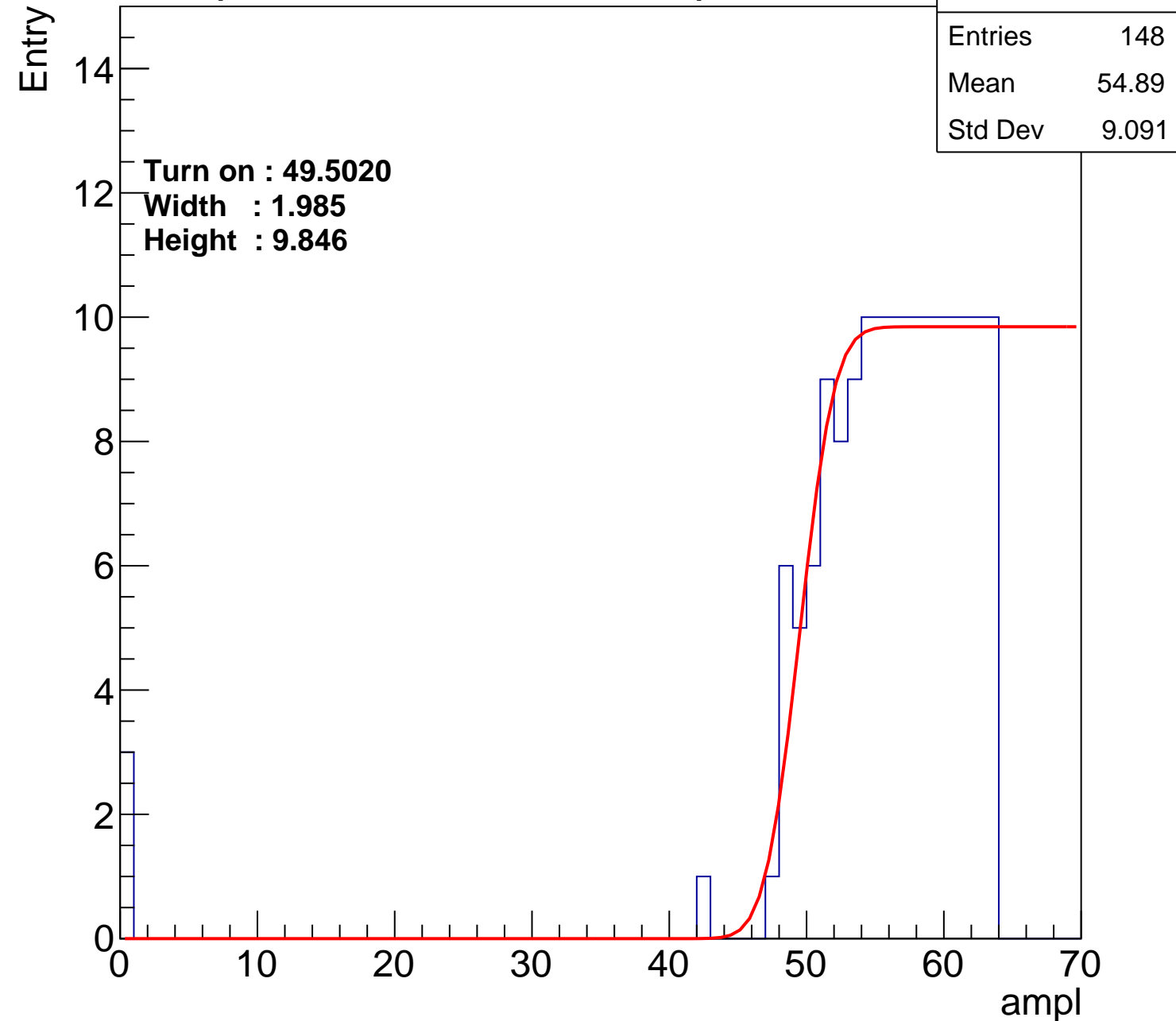
Width : 1.985

Height : 9.846

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	154
Mean	54.79
Std Dev	7.924

Turn on : 49.0307

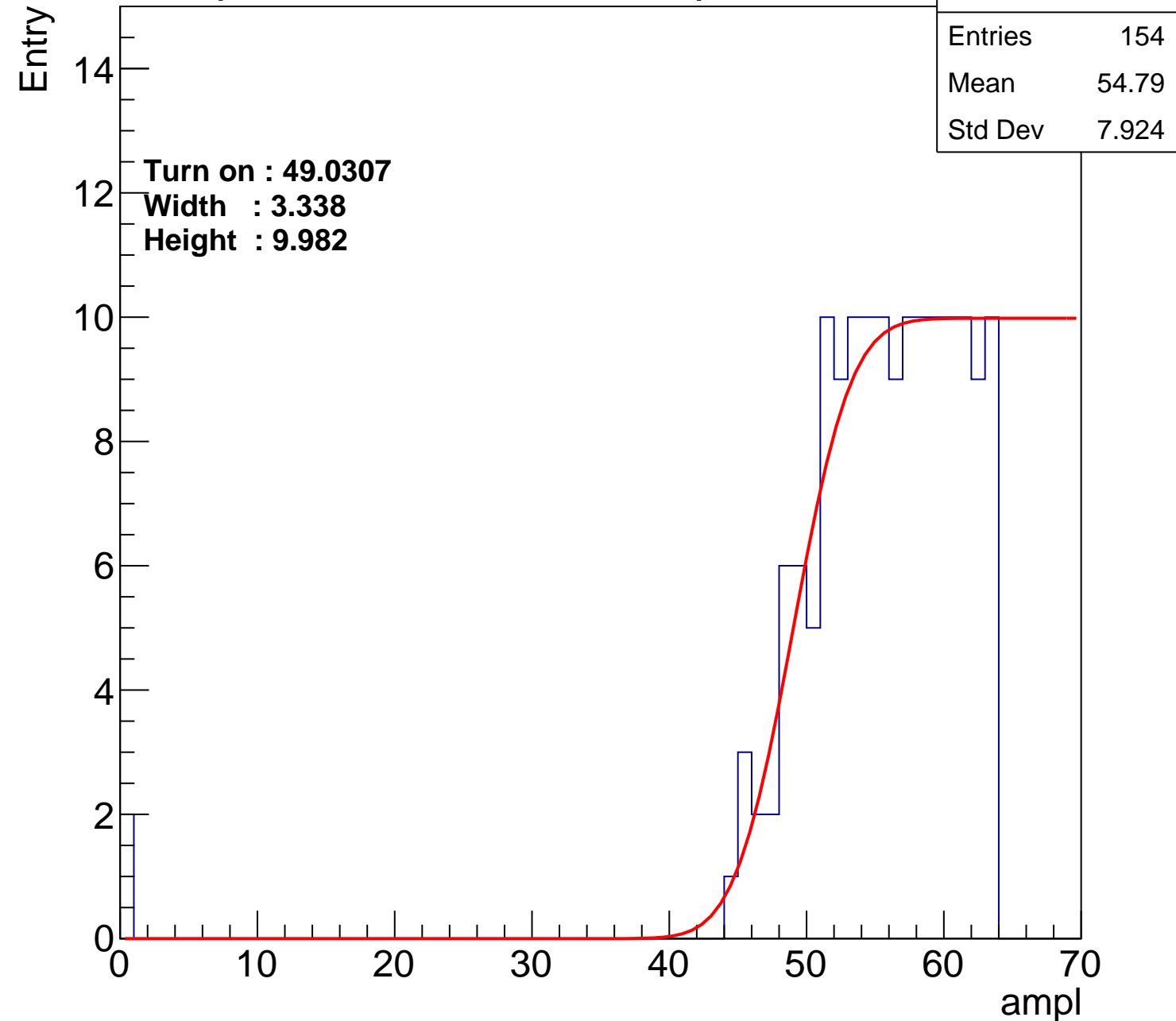
Width : 3.338

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

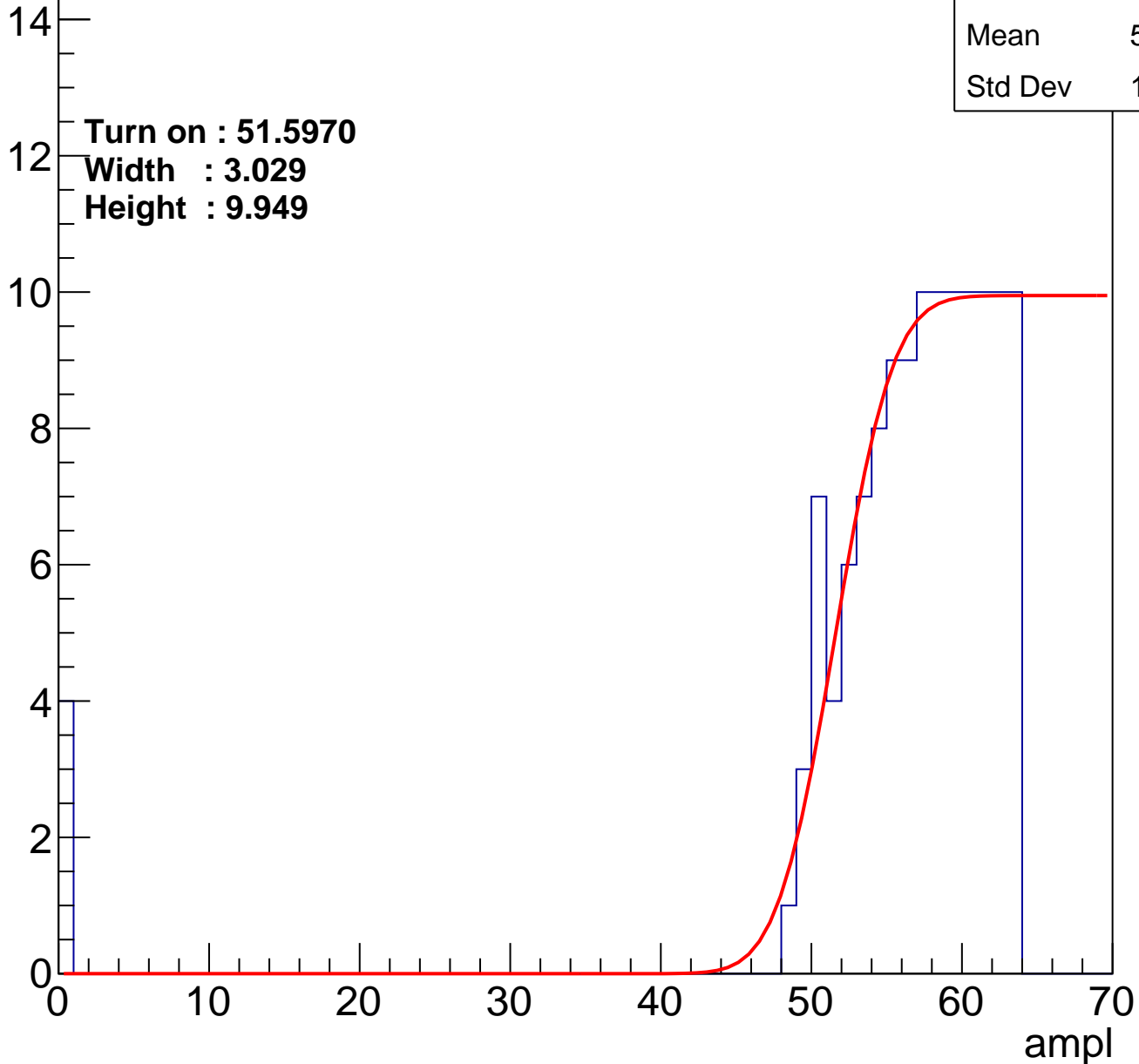
Entries	128
Mean	55.18
Std Dev	10.69

Turn on : 51.5970

Width : 3.029

Height : 9.949

Entry



# B0L103S, U9-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.94
Std Dev	9.174

Turn on : 50.0477

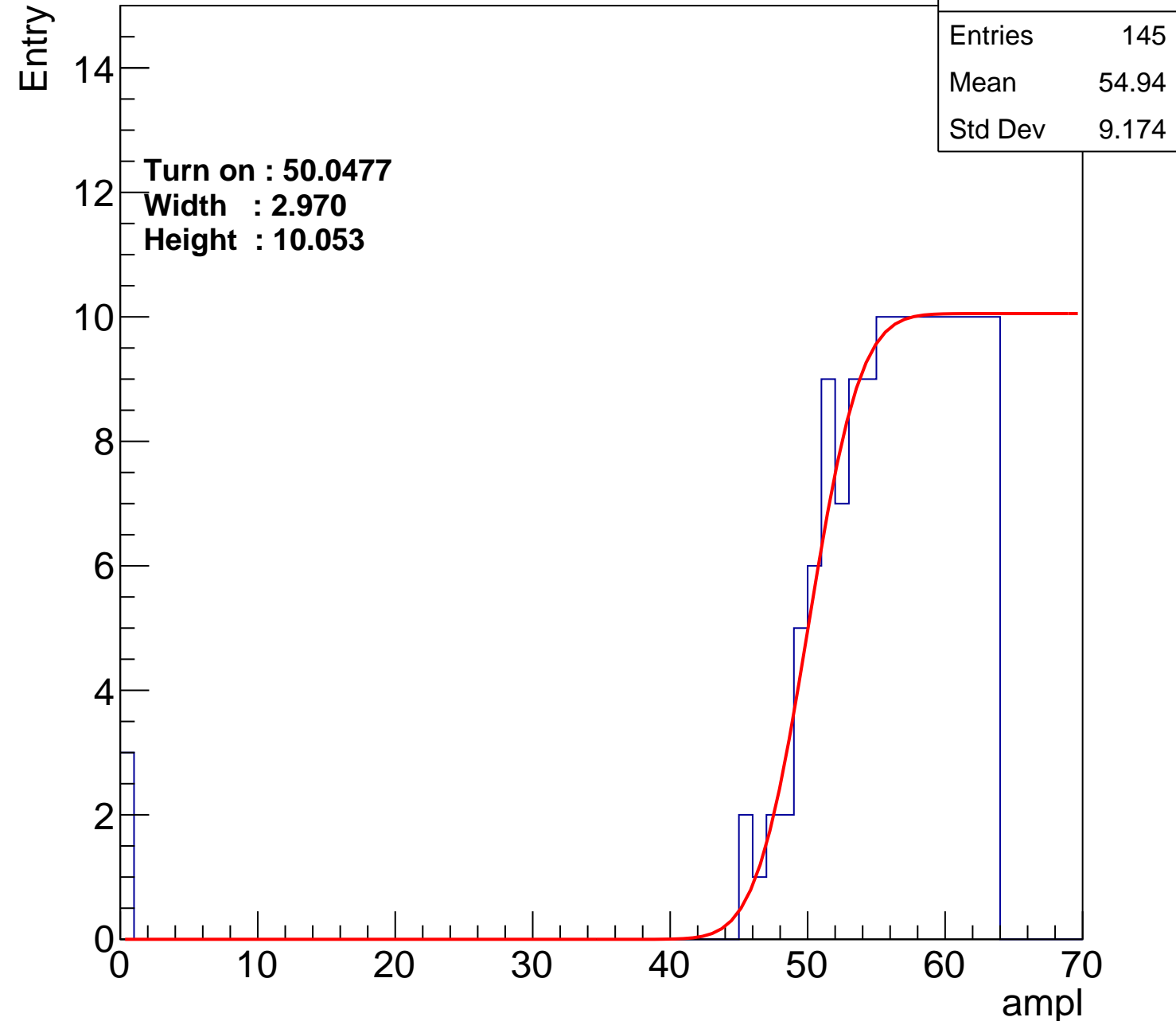
Width : 2.970

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch60

calib\_packv5\_040323\_1717.root, FC#2, port C3

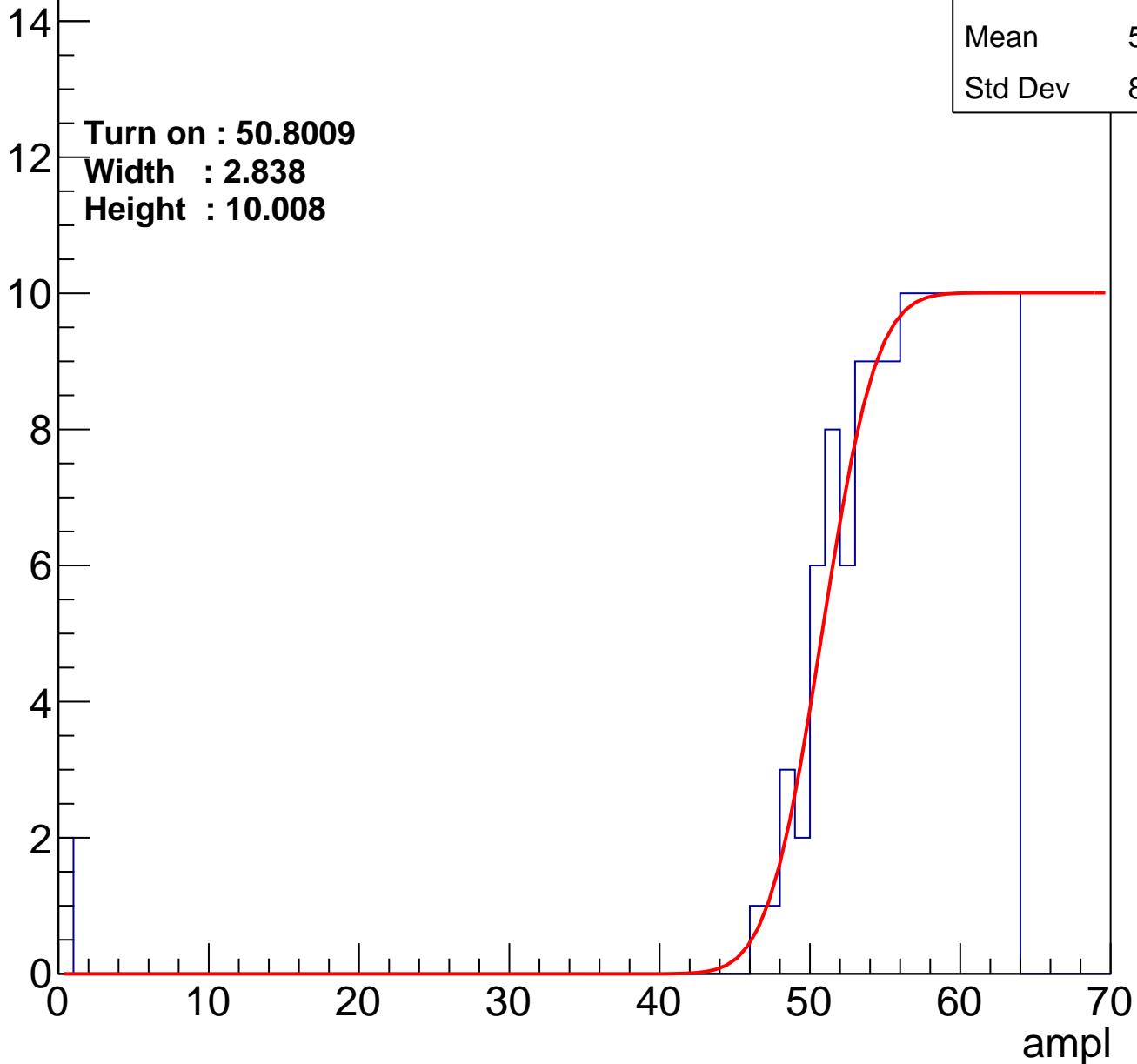
Entry

Entries	136
Mean	55.68
Std Dev	8.029

Turn on : 50.8009

Width : 2.838

Height : 10.008



# B0L103S, U9-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.78
Std Dev	9.686

Turn on : 52.6012

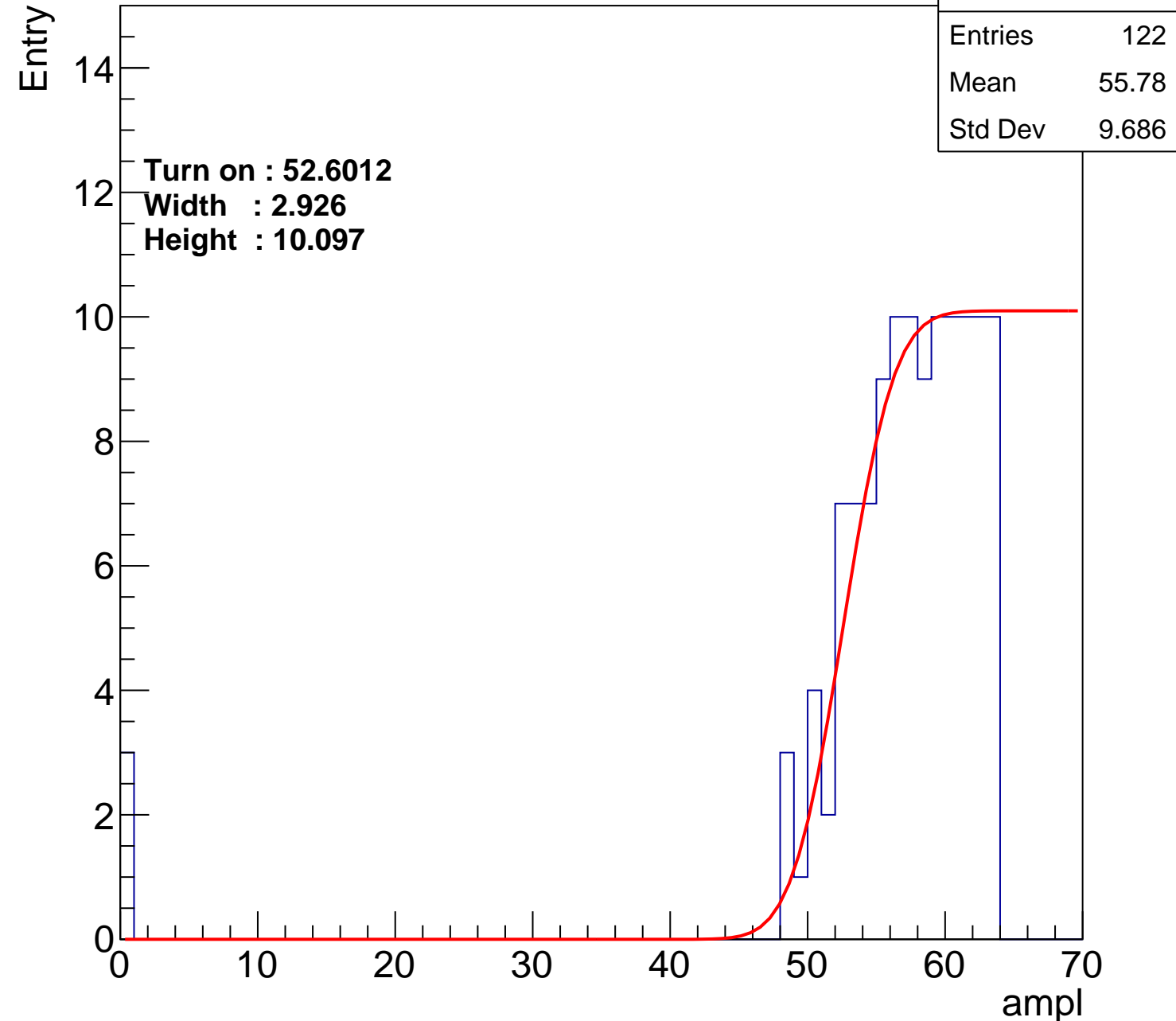
Width : 2.926

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

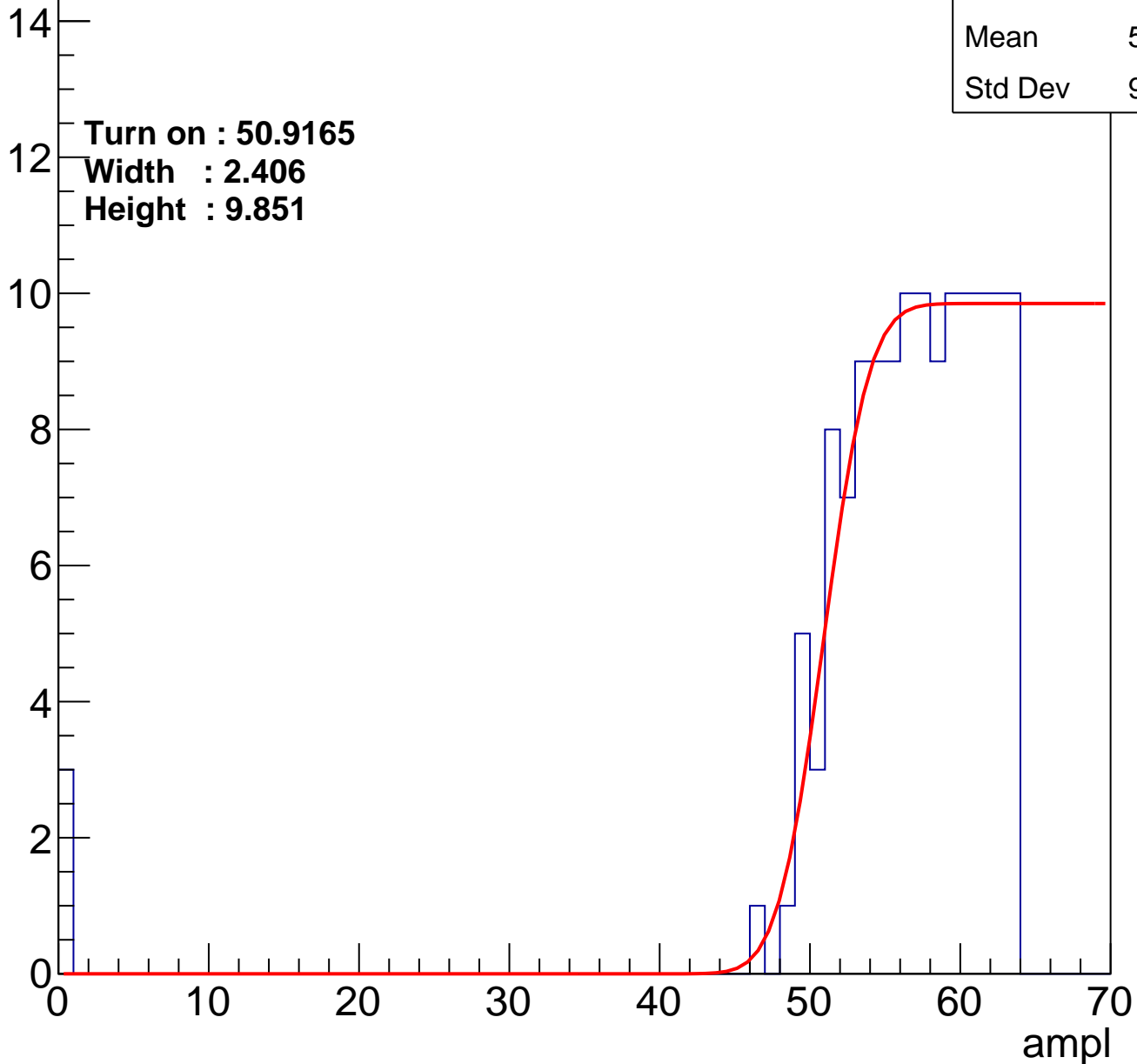
Entry

Entries	134
Mean	55.38
Std Dev	9.347

Turn on : 50.9165

Width : 2.406

Height : 9.851





# B0L103S, U9-ch63

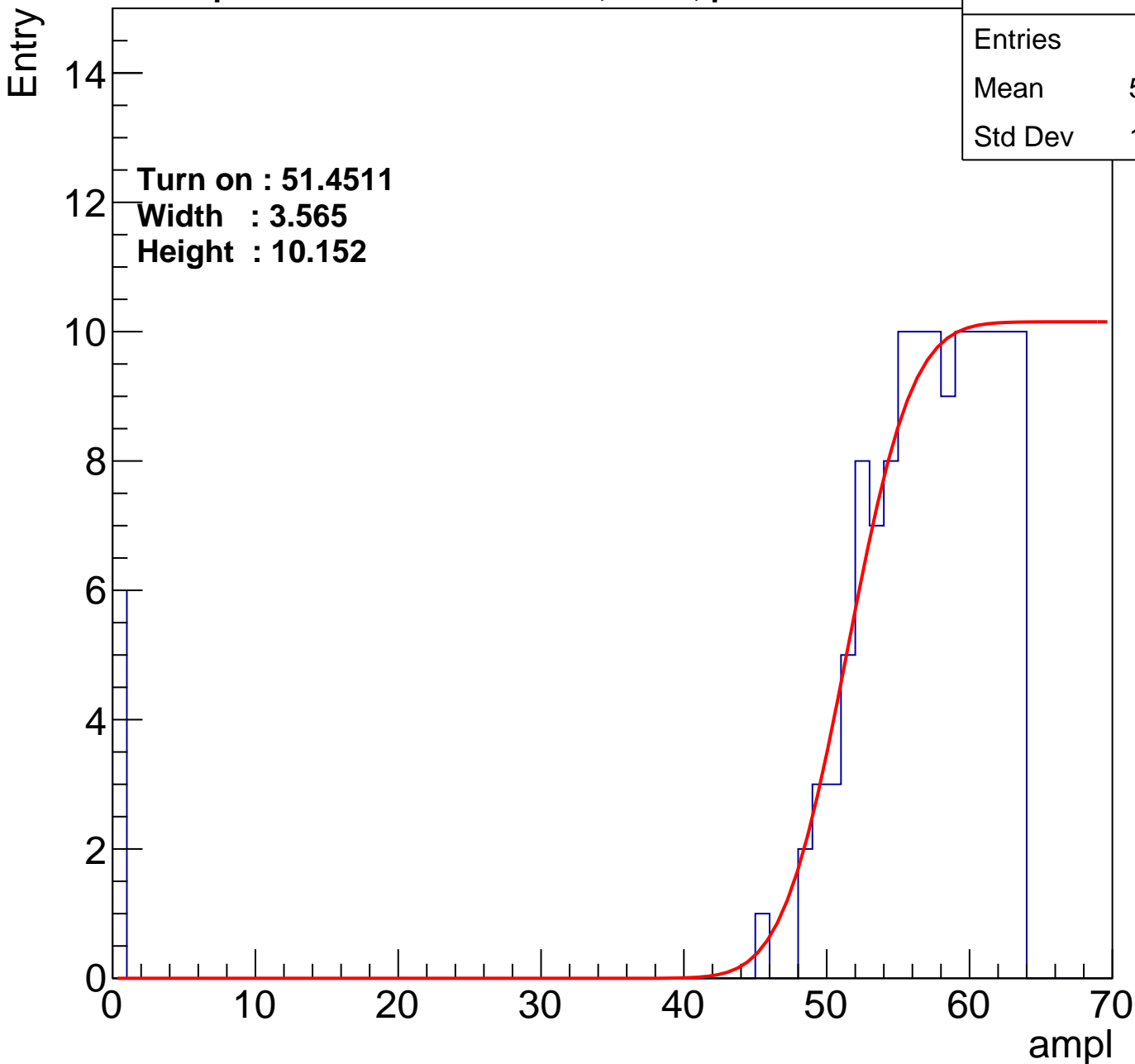
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	132
Mean	54.27
Std Dev	12.52

**Turn on : 51.4511**

**Width : 3.565**

**Height : 10.152**



# B0L103S, U9-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.09
Std Dev	9.284

Turn on : 50.4586

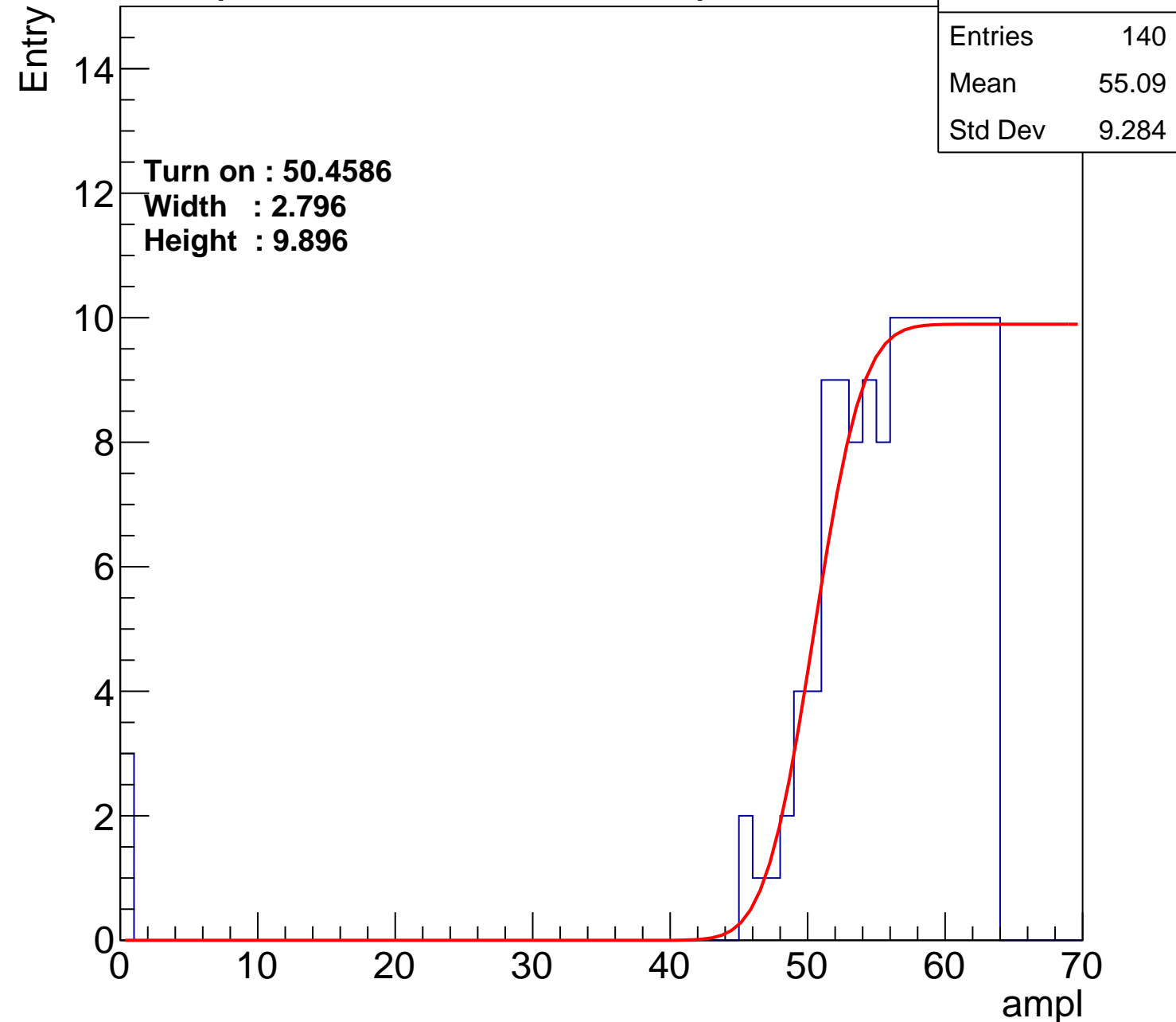
Width : 2.796

Height : 9.896

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	55.64
Std Dev	7.941

Turn on : 50.3004

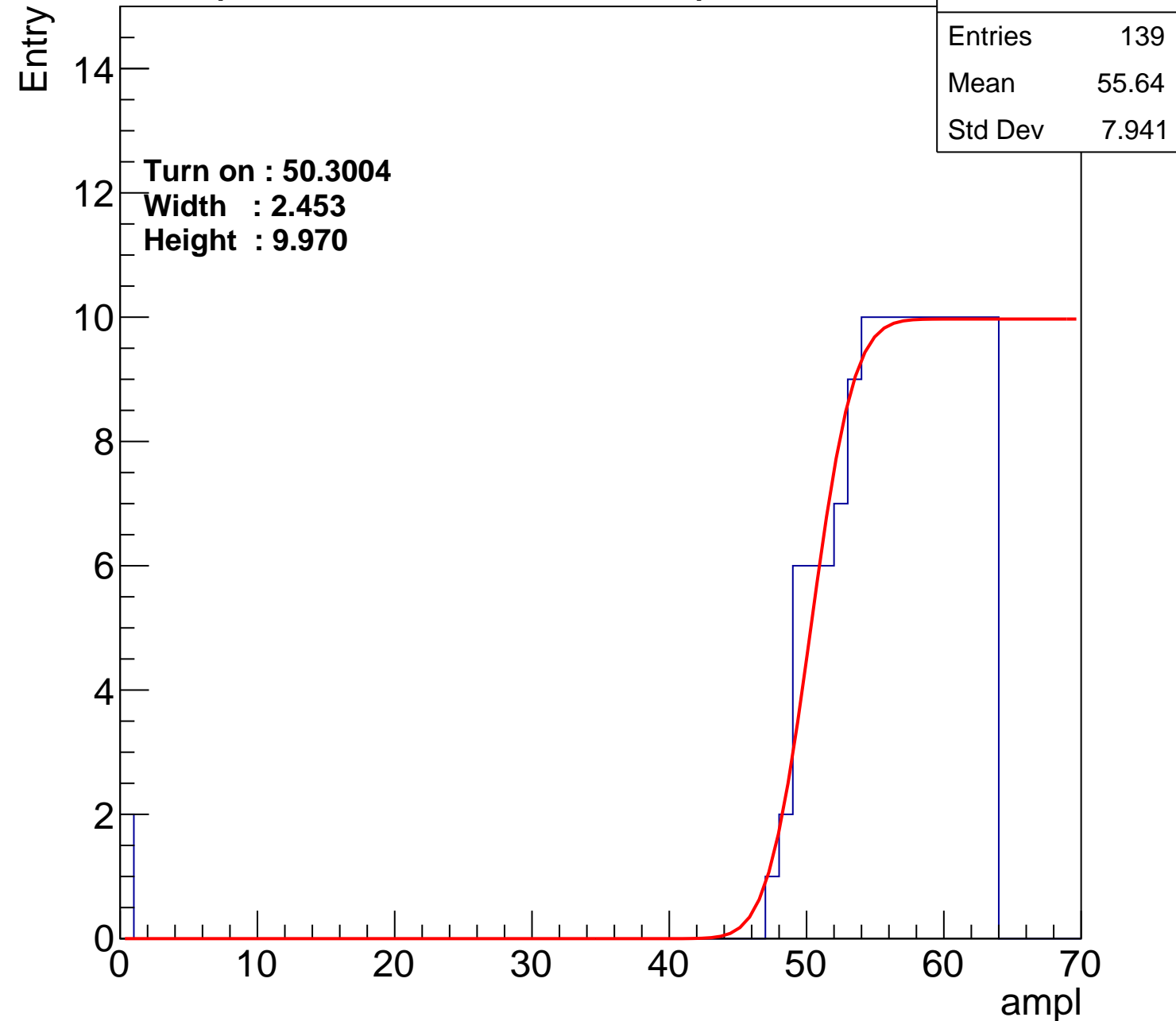
Width : 2.453

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.4
Std Dev	9.339

Turn on : 51.2181

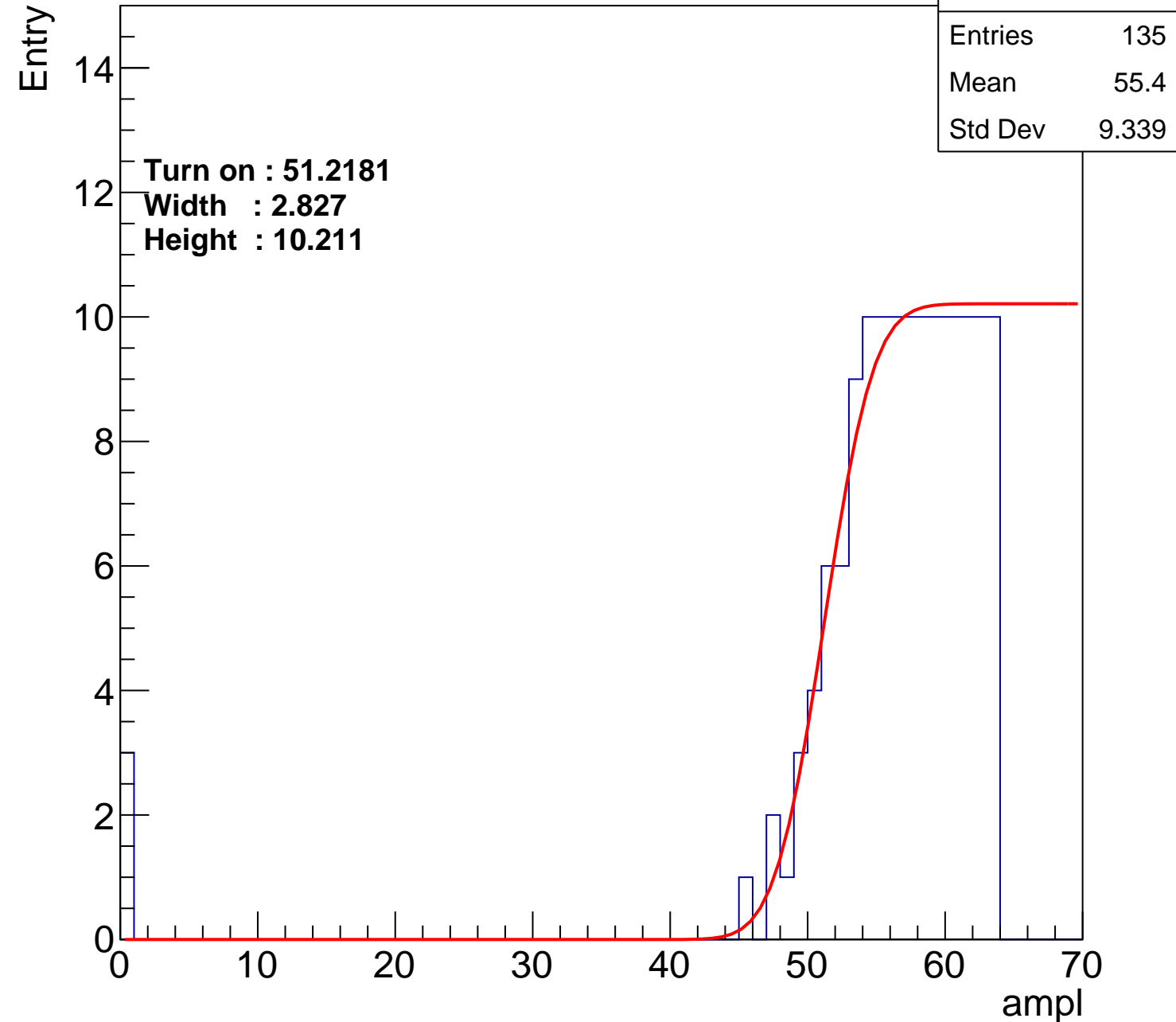
Width : 2.827

Height : 10.211

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

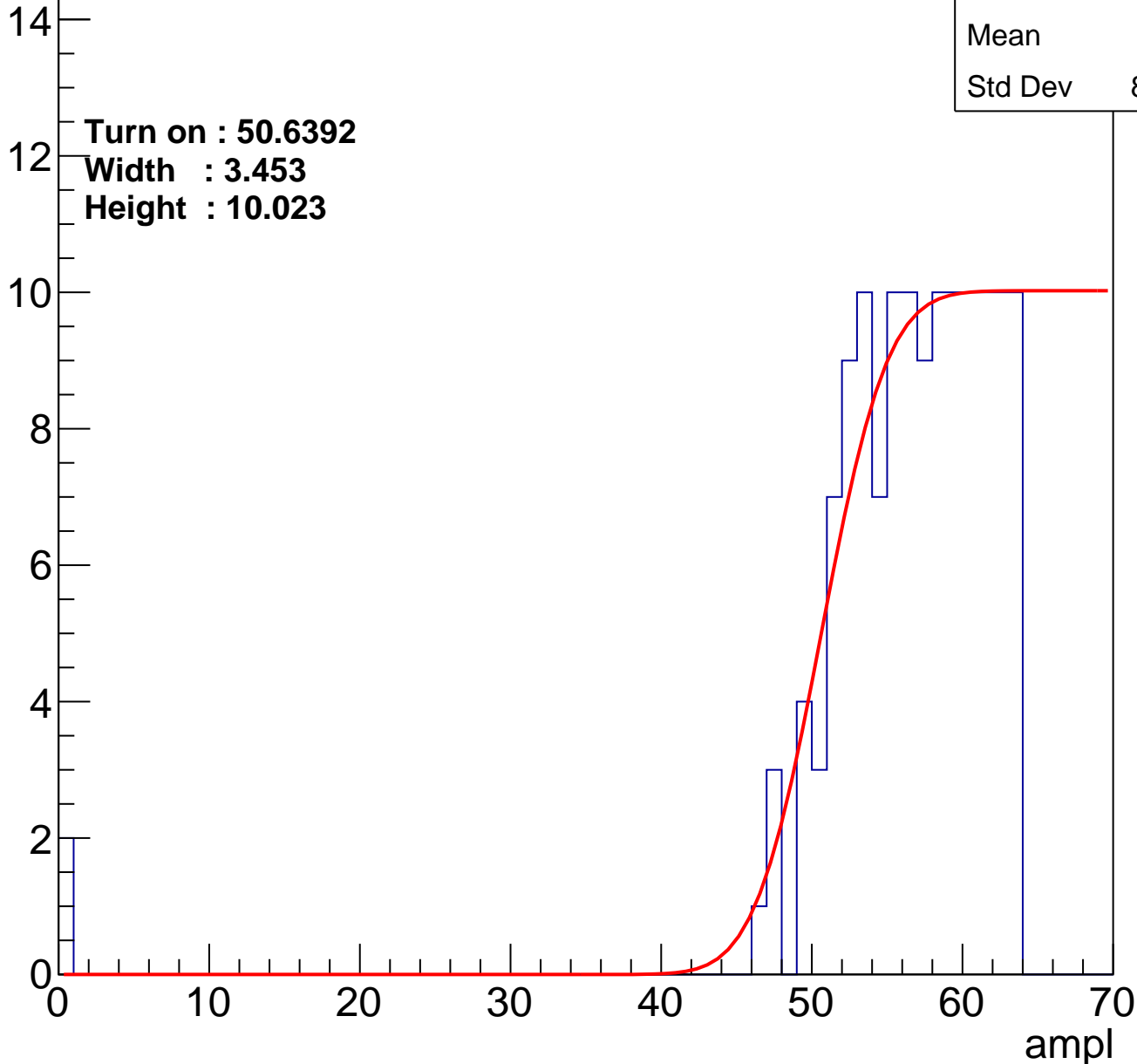
Entry

Entries	135
Mean	55.7
Std Dev	8.052

Turn on : 50.6392

Width : 3.453

Height : 10.023



# B0L103S, U9-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	55.65
Std Dev	9.554

Turn on : 51.6898

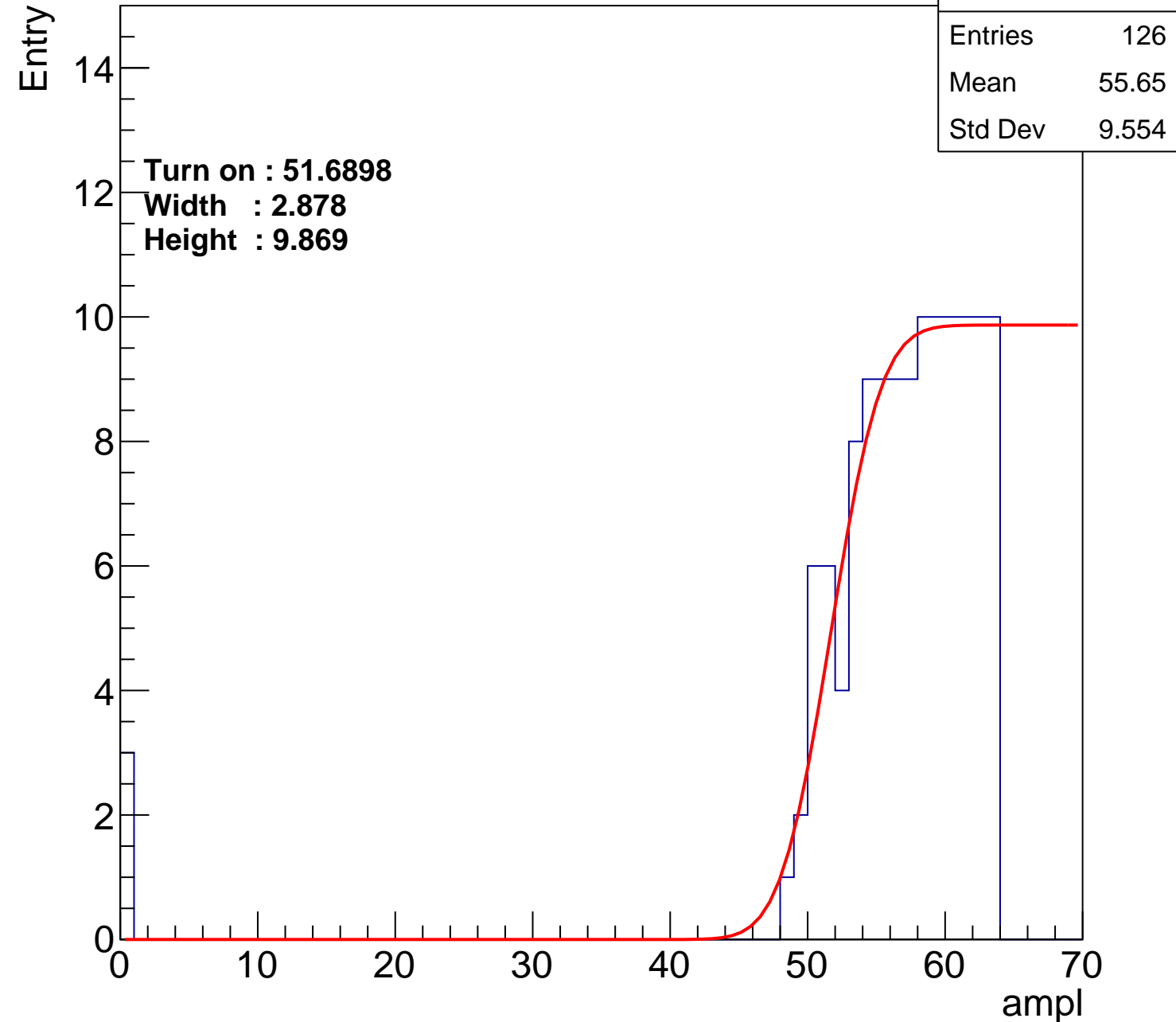
Width : 2.878

Height : 9.869

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.19
Std Dev	10.87

Turn on : 52.2539

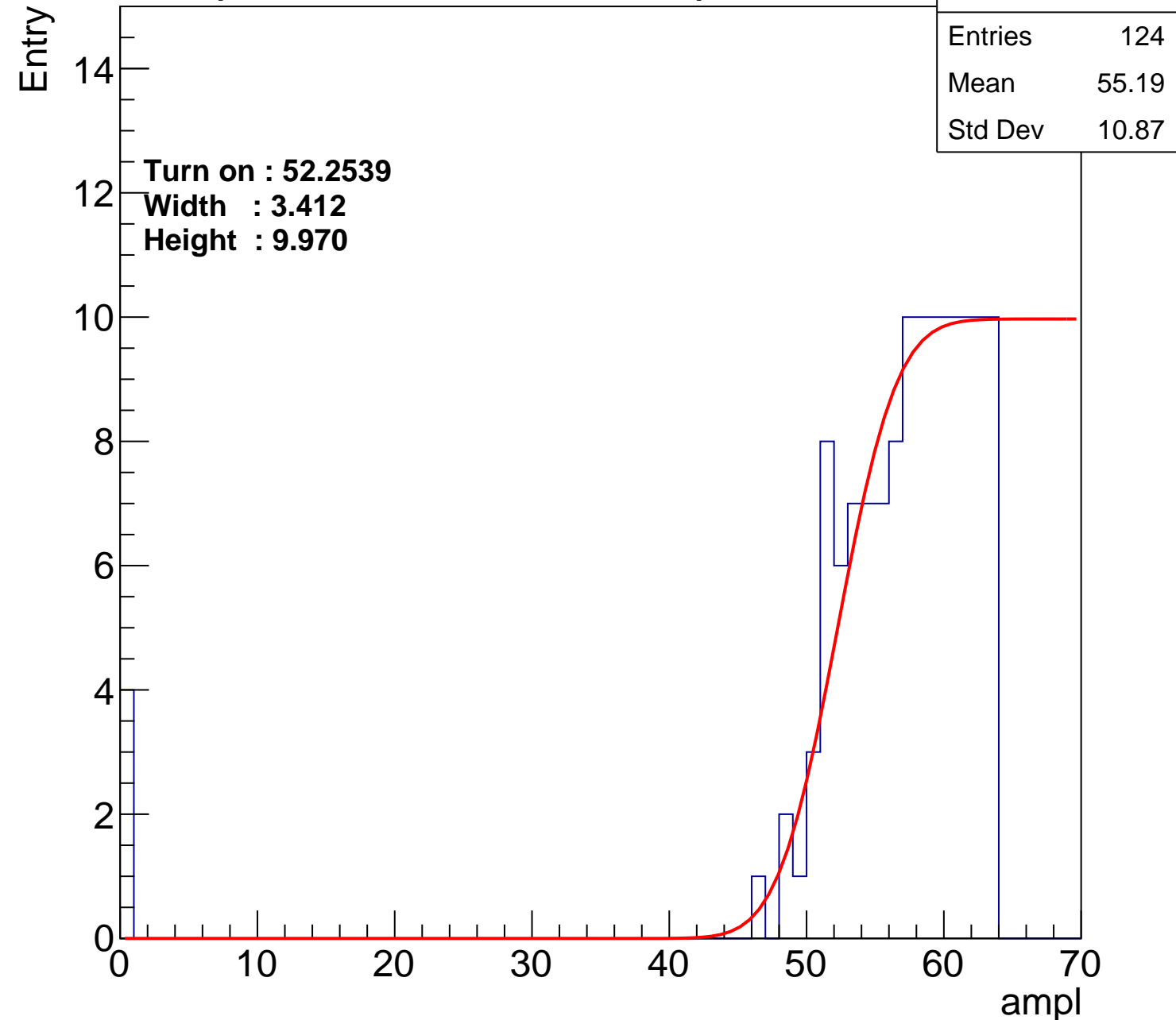
Width : 3.412

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

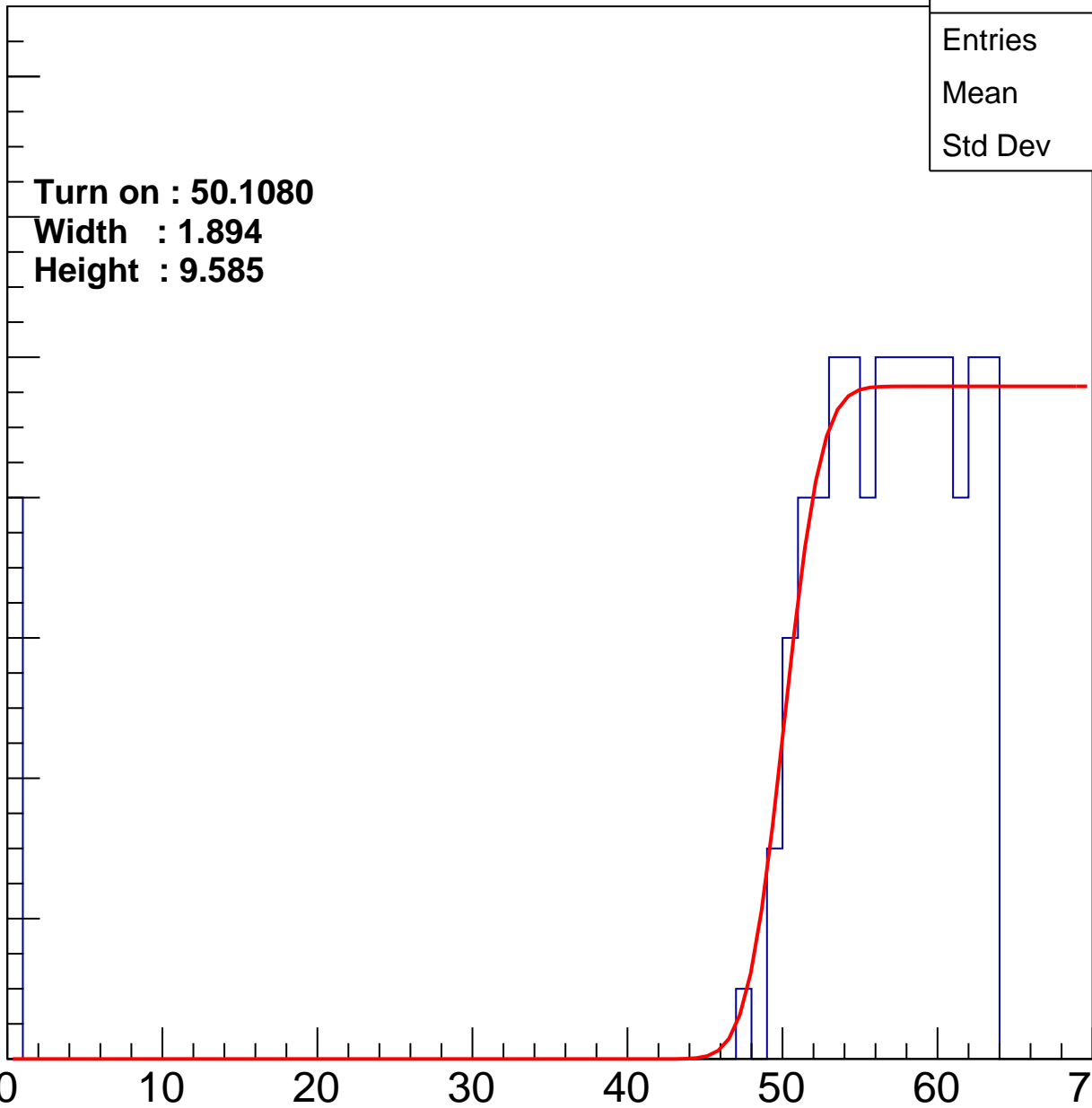
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.1080  
Width : 1.894  
Height : 9.585

Entries	140
Mean	53.33
Std Dev	13.72

ampl





# B0L103S, U9-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	153
Mean	52.91
Std Dev	13.29

Turn on : 50.0466

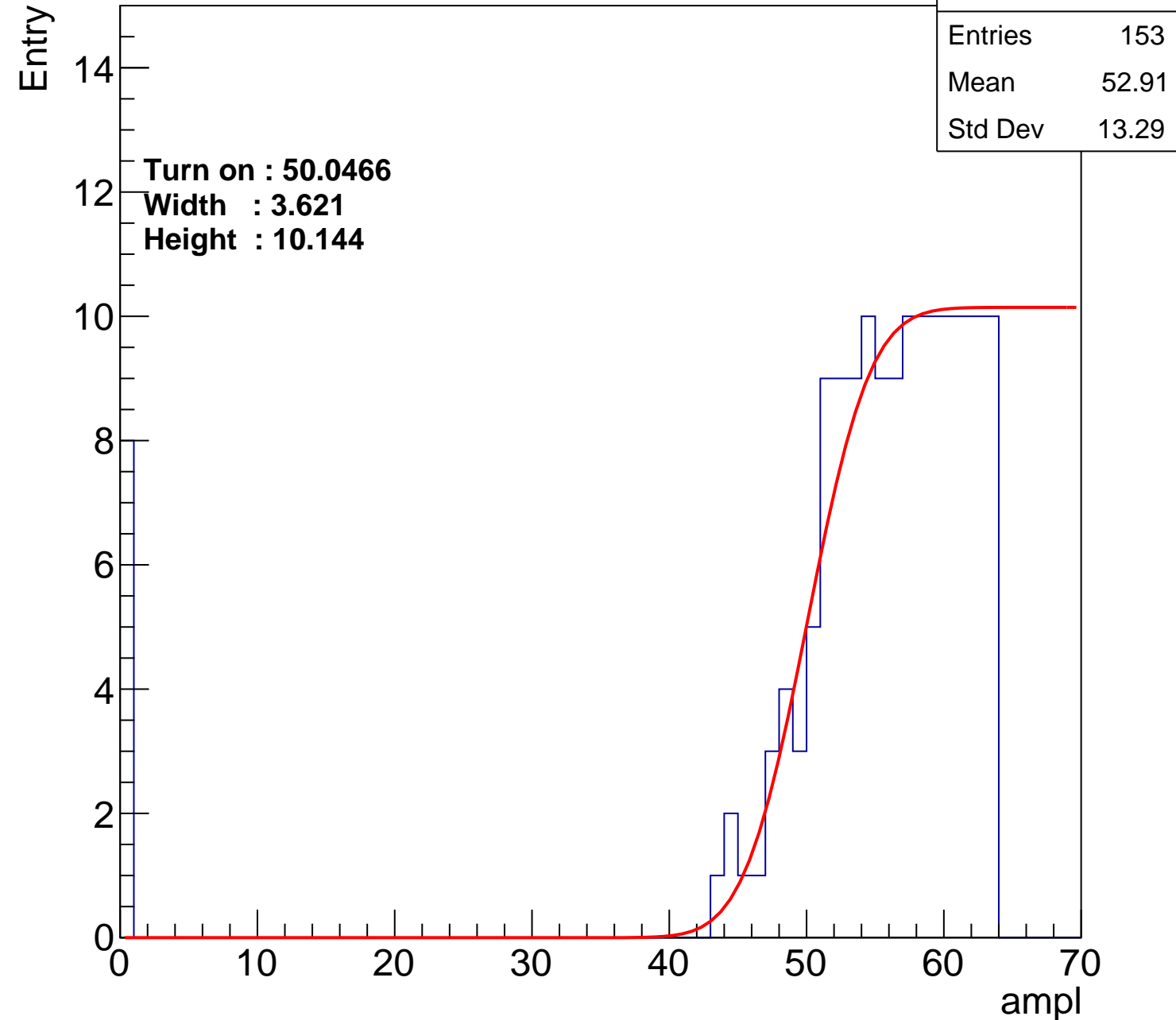
Width : 3.621

Height : 10.144

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

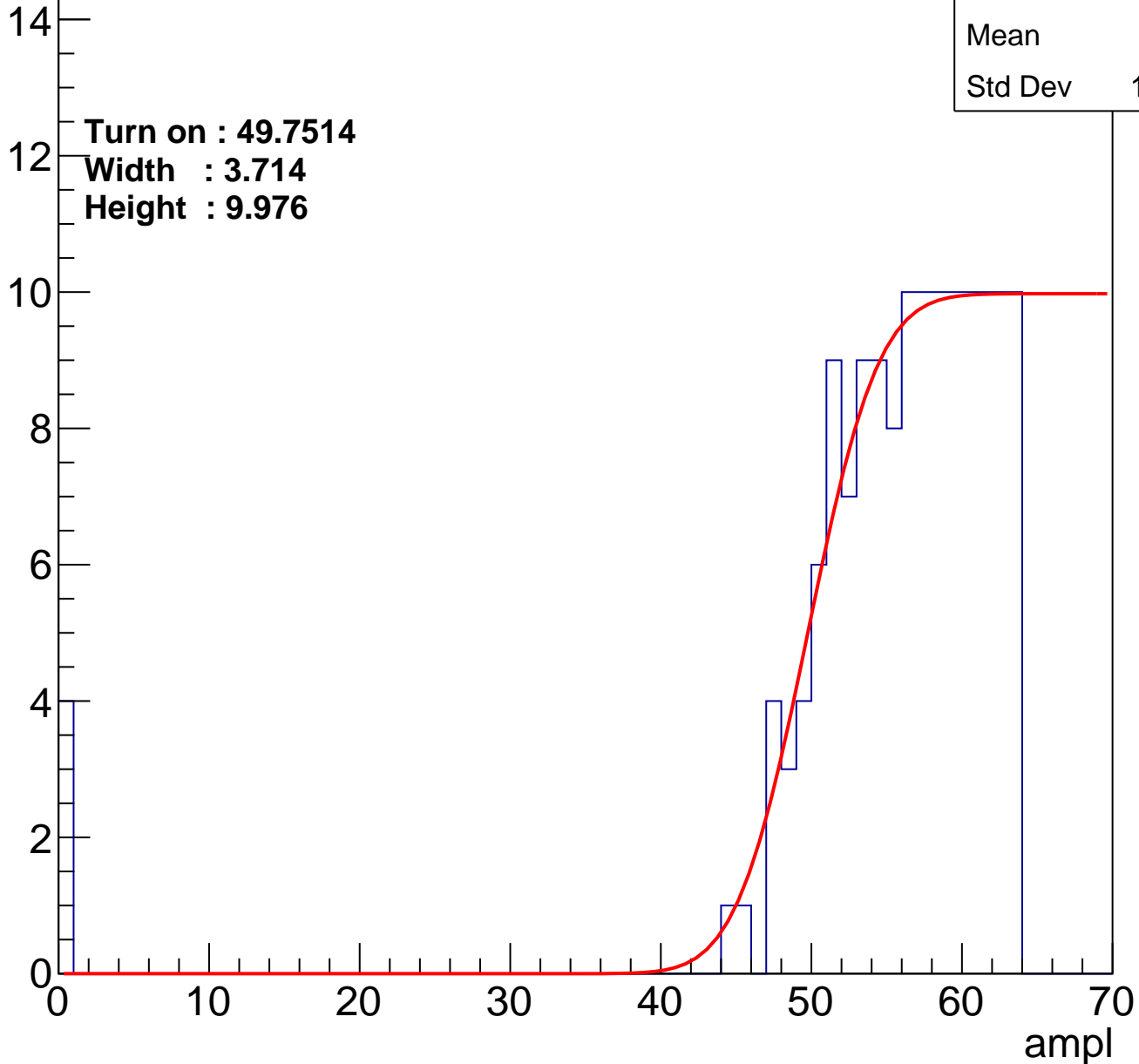
Entries	145
Mean	54.5
Std Dev	10.26

Turn on : 49.7514

Width : 3.714

Height : 9.976

Entry



# B0L103S, U9-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.68
Std Dev	8.024

Turn on : 50.7547

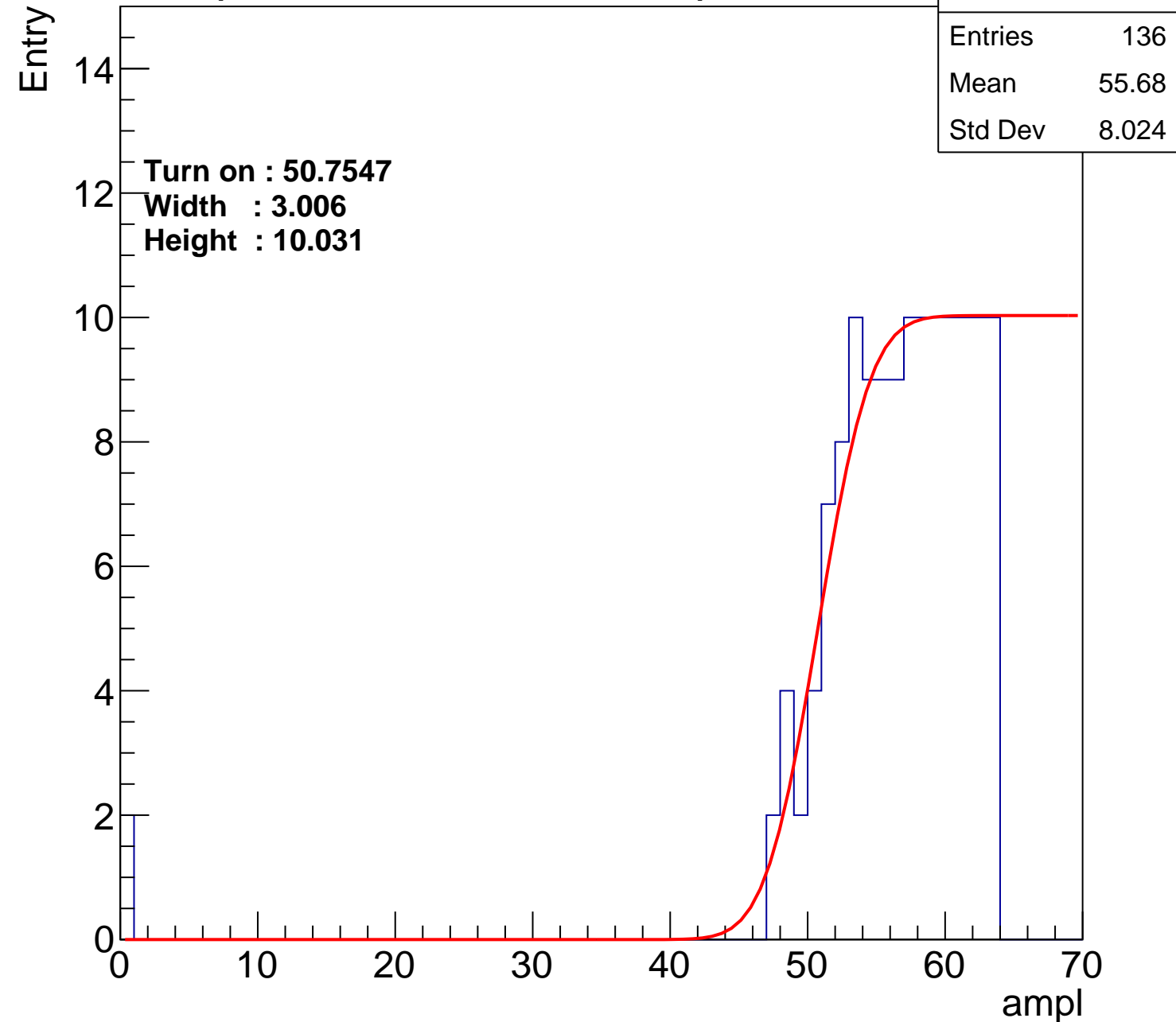
Width : 3.006

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	54.76
Std Dev	11.77

Turn on : 51.7172

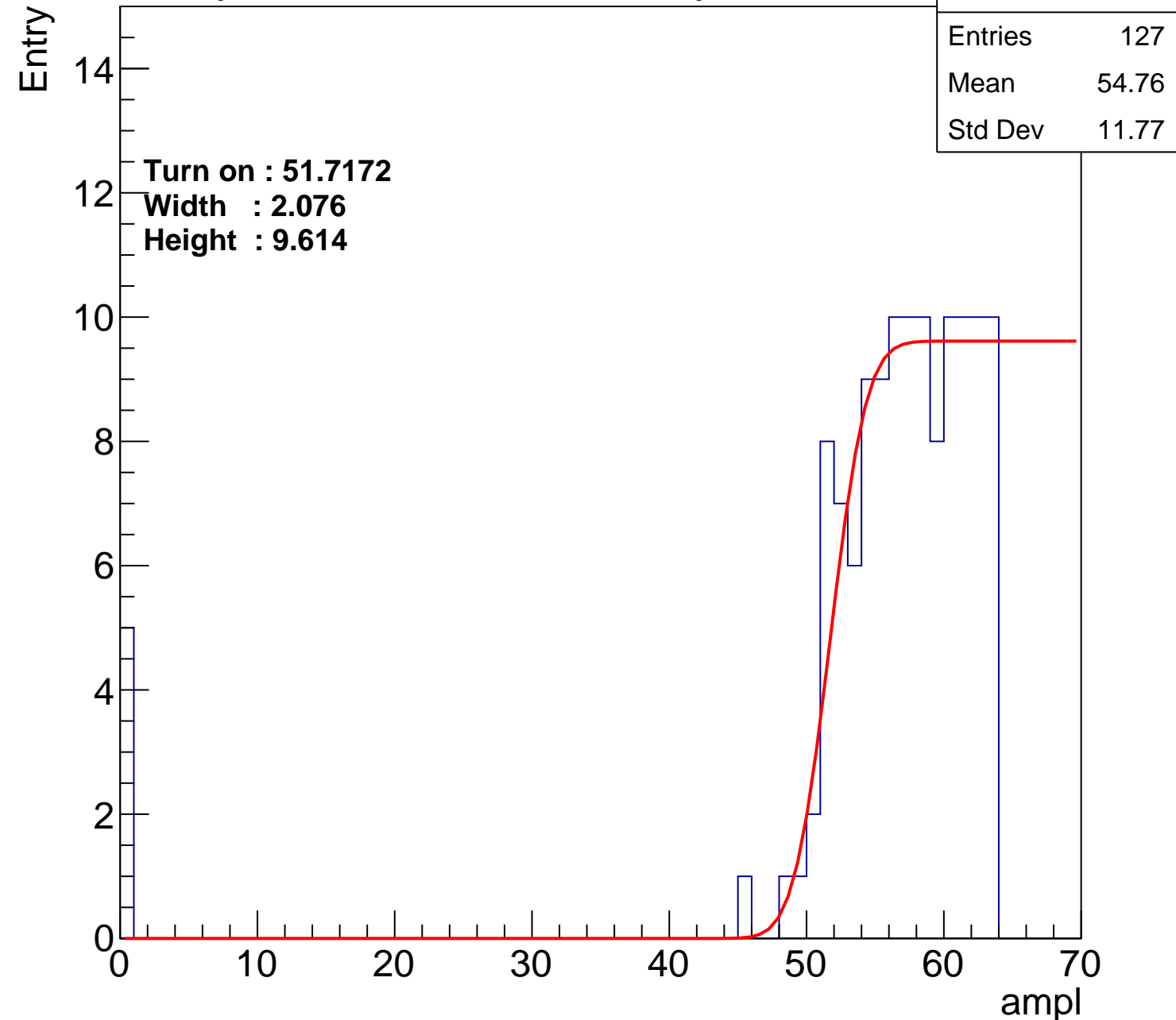
Width : 2.076

Height : 9.614

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	55.23
Std Dev	8.022

Turn on : 50.1150

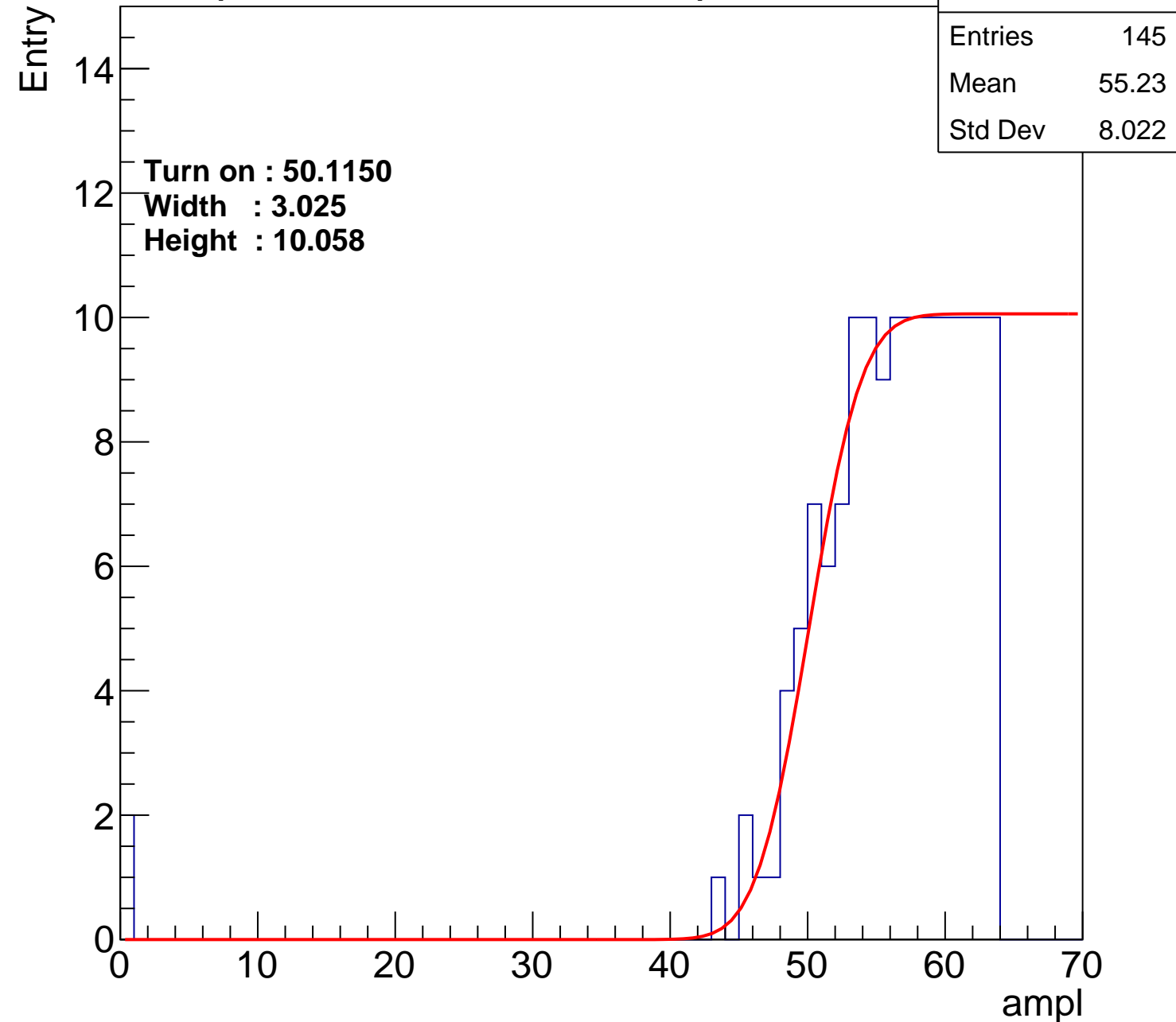
Width : 3.025

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

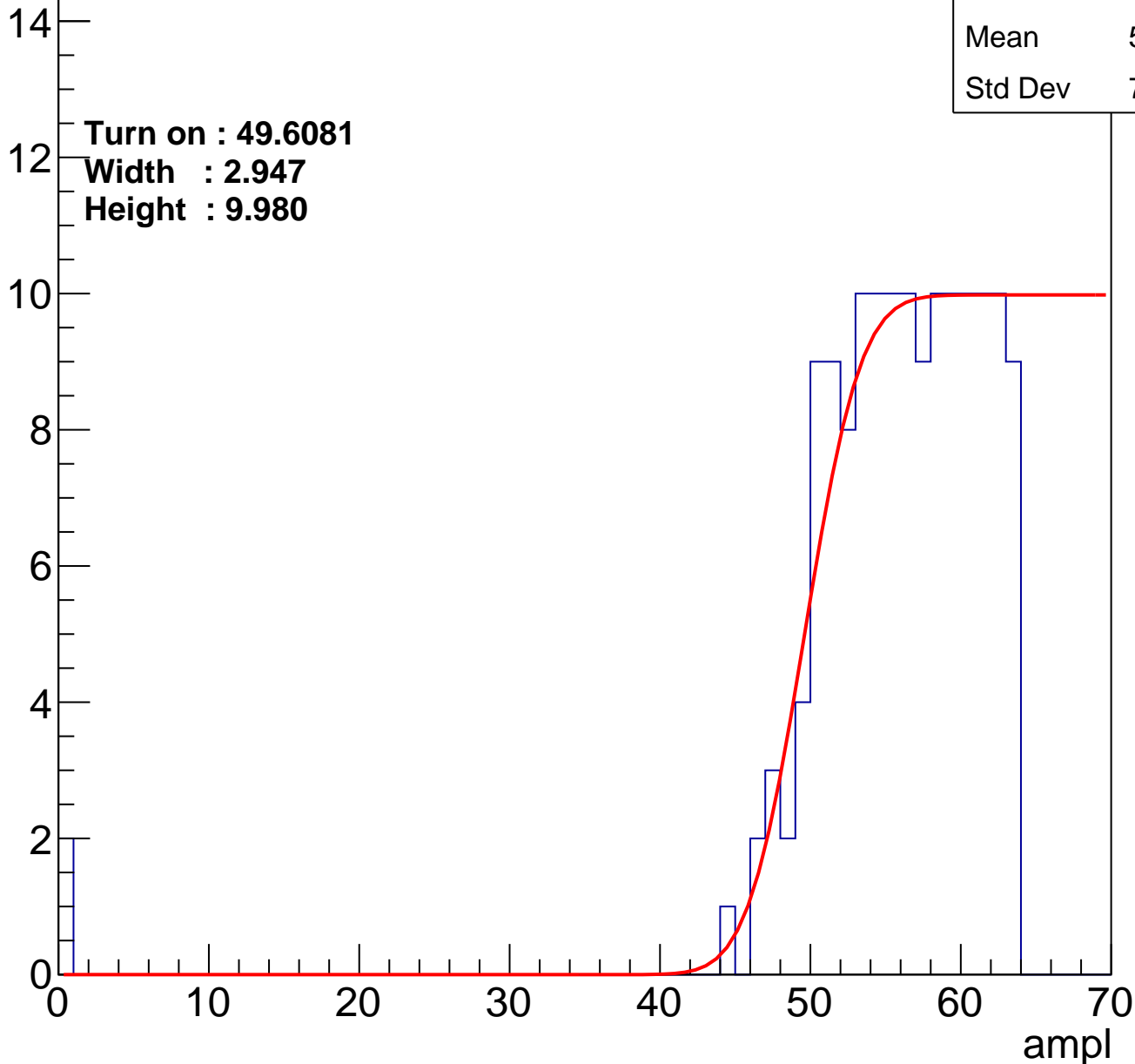
Entry

Entries	148
Mean	55.09
Std Dev	7.897

Turn on : 49.6081

Width : 2.947

Height : 9.980



# B0L103S, U9-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

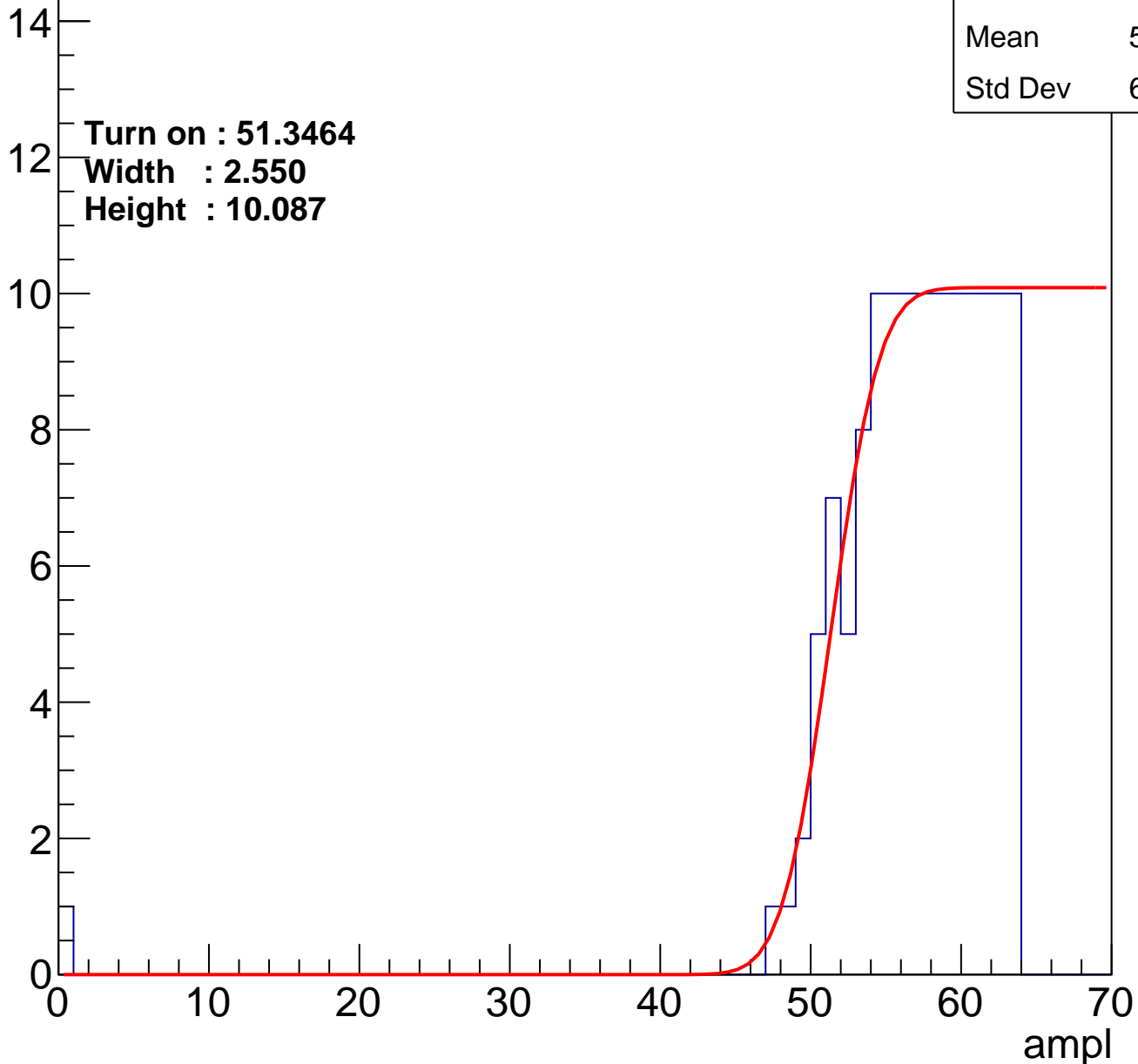
Entry

Entries	130
Mean	56.42
Std Dev	6.393

Turn on : 51.3464

Width : 2.550

Height : 10.087



# B0L103S, U9-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.34
Std Dev	11.51

Turn on : 50.7041

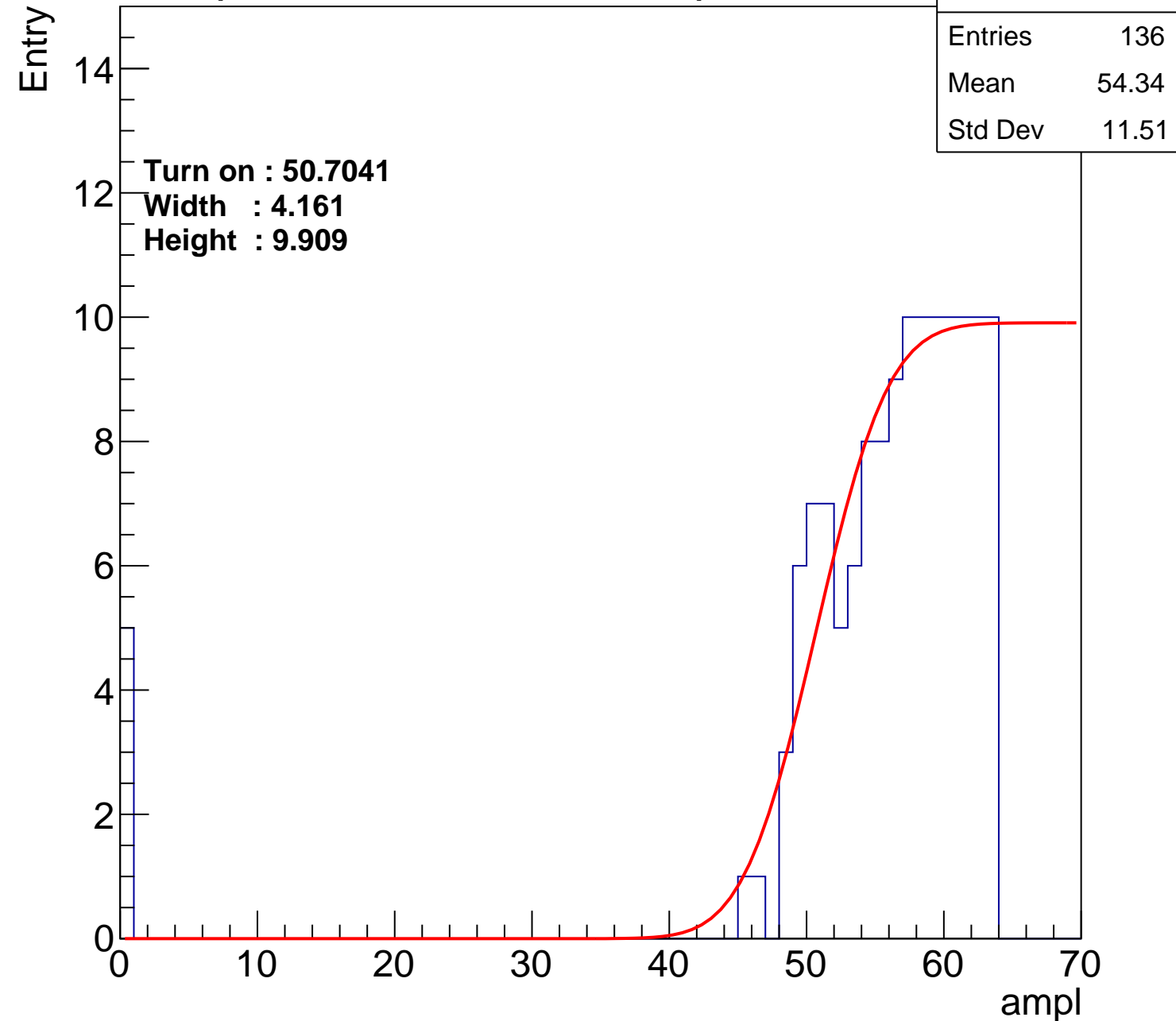
Width : 4.161

Height : 9.909

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

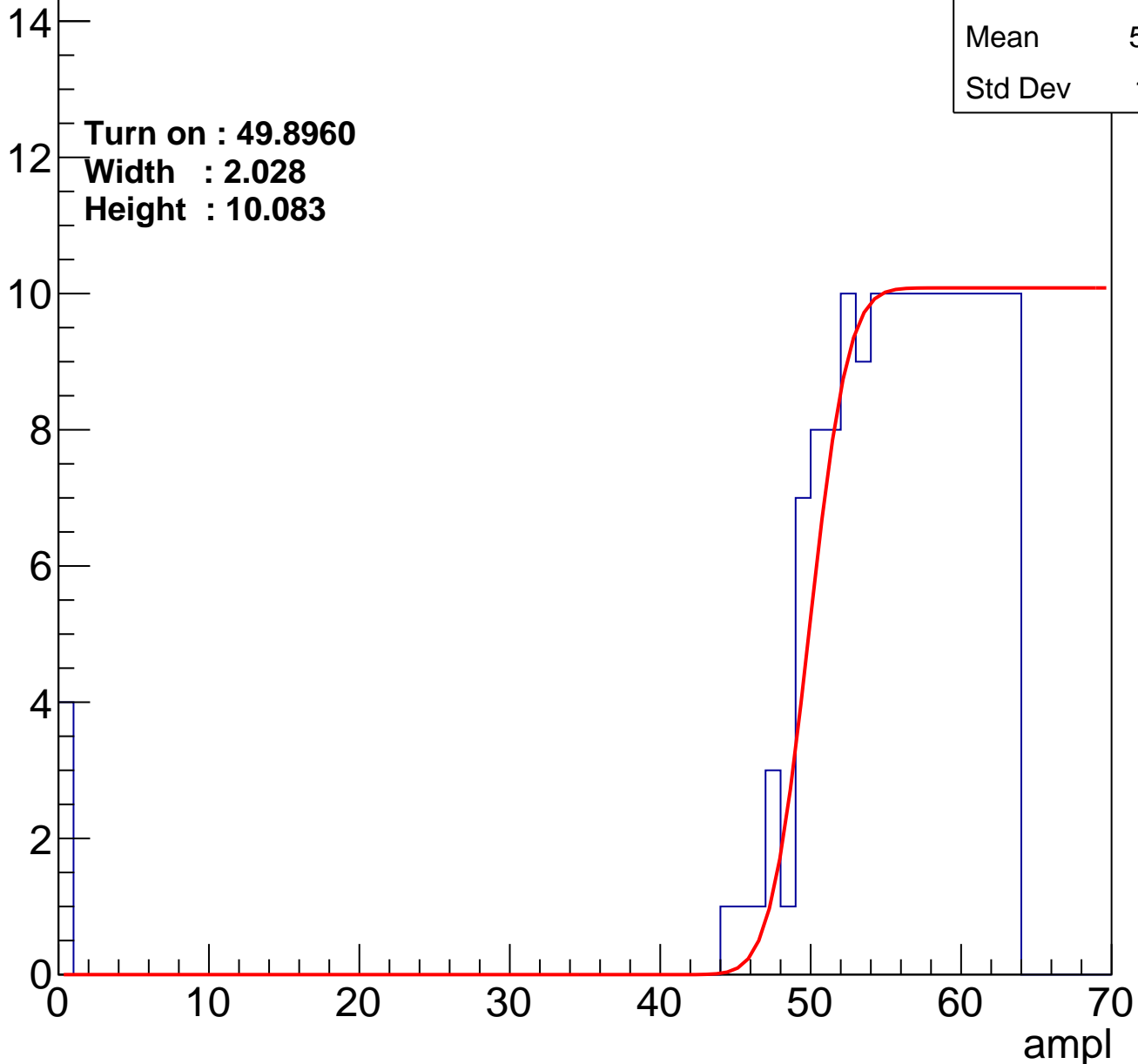
Entry

Entries	153
Mean	54.39
Std Dev	10.01

Turn on : 49.8960

Width : 2.028

Height : 10.083



# B0L103S, U9-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.8
Std Dev	9.652

Turn on : 52.4534

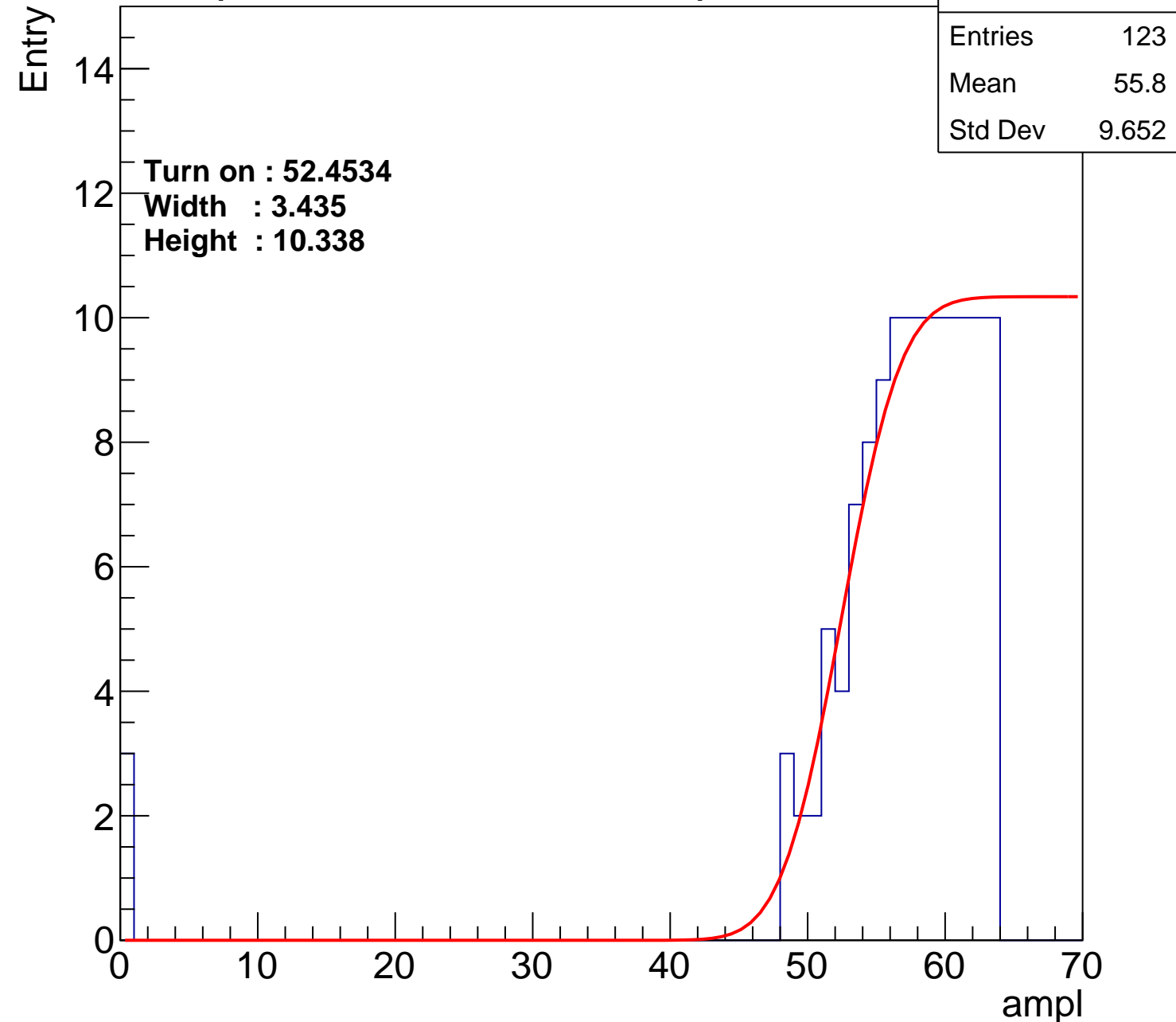
Width : 3.435

Height : 10.338

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch81

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.32
Std Dev	10.8

Turn on : 52.1238

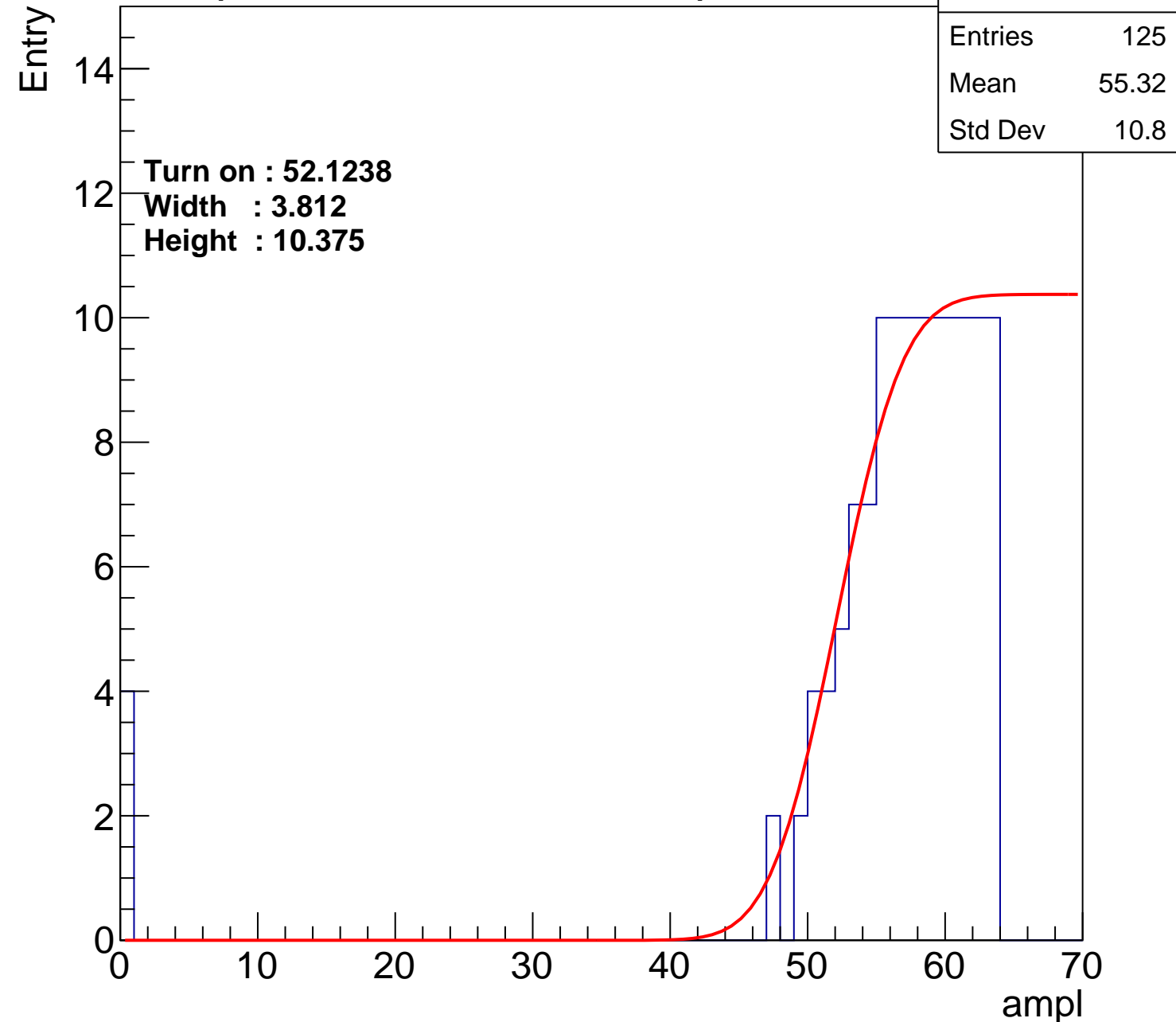
Width : 3.812

Height : 10.375

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.06
Std Dev	10.5

Turn on : 51.6506

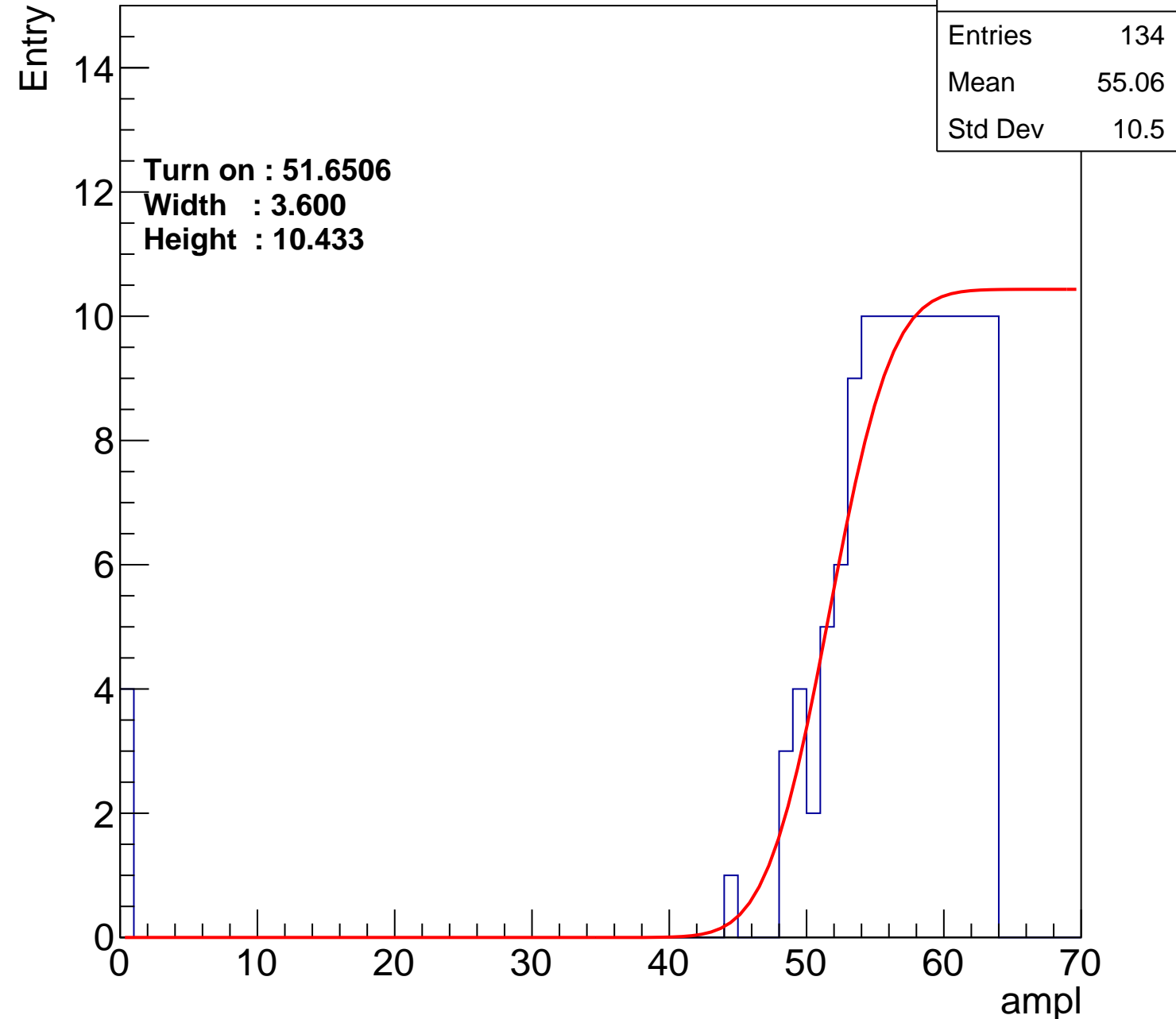
Width : 3.600

Height : 10.433

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.56
Std Dev	10.94

Turn on : 52.1899

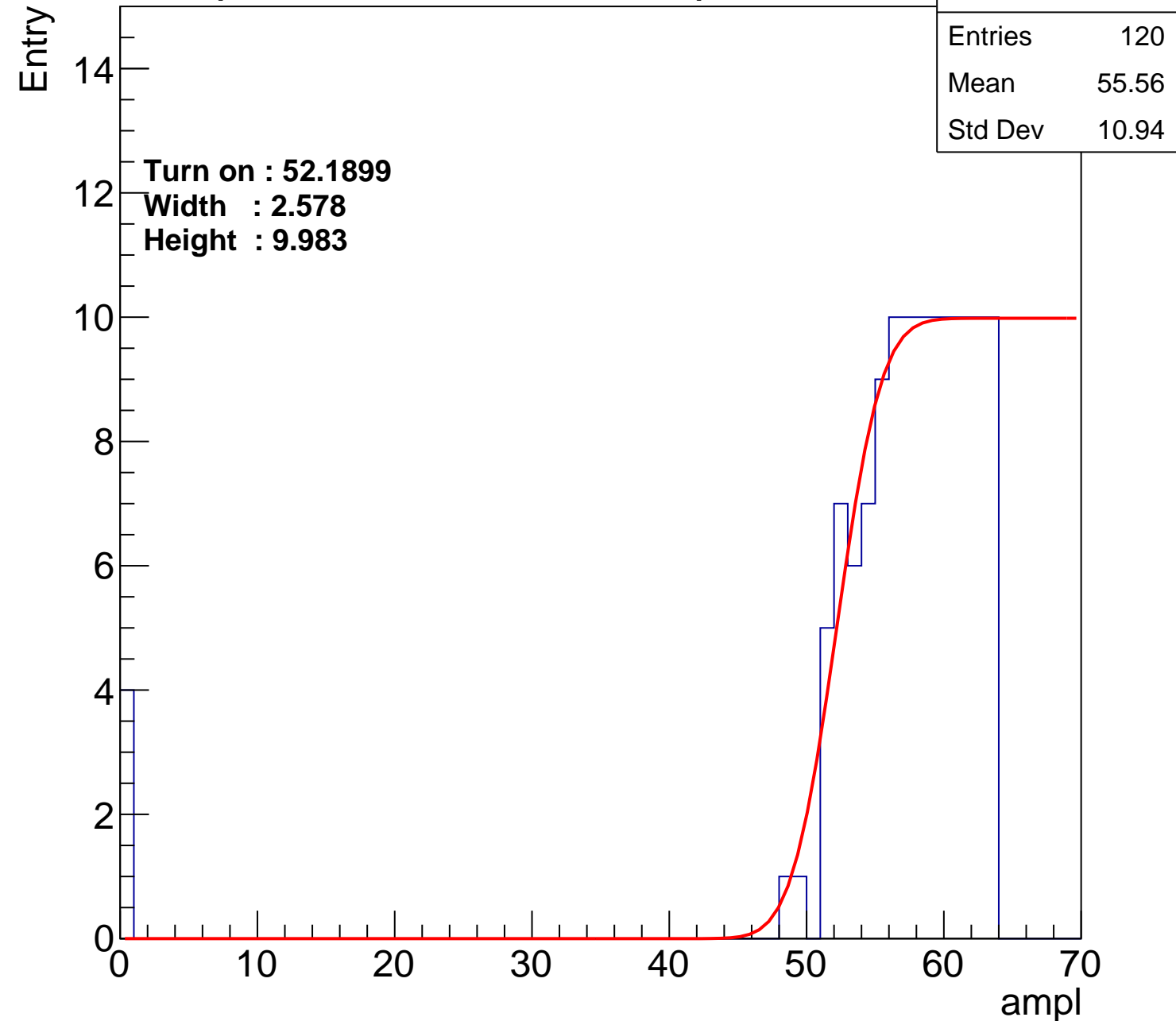
Width : 2.578

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	54.03
Std Dev	10.03

Turn on : 49.0917

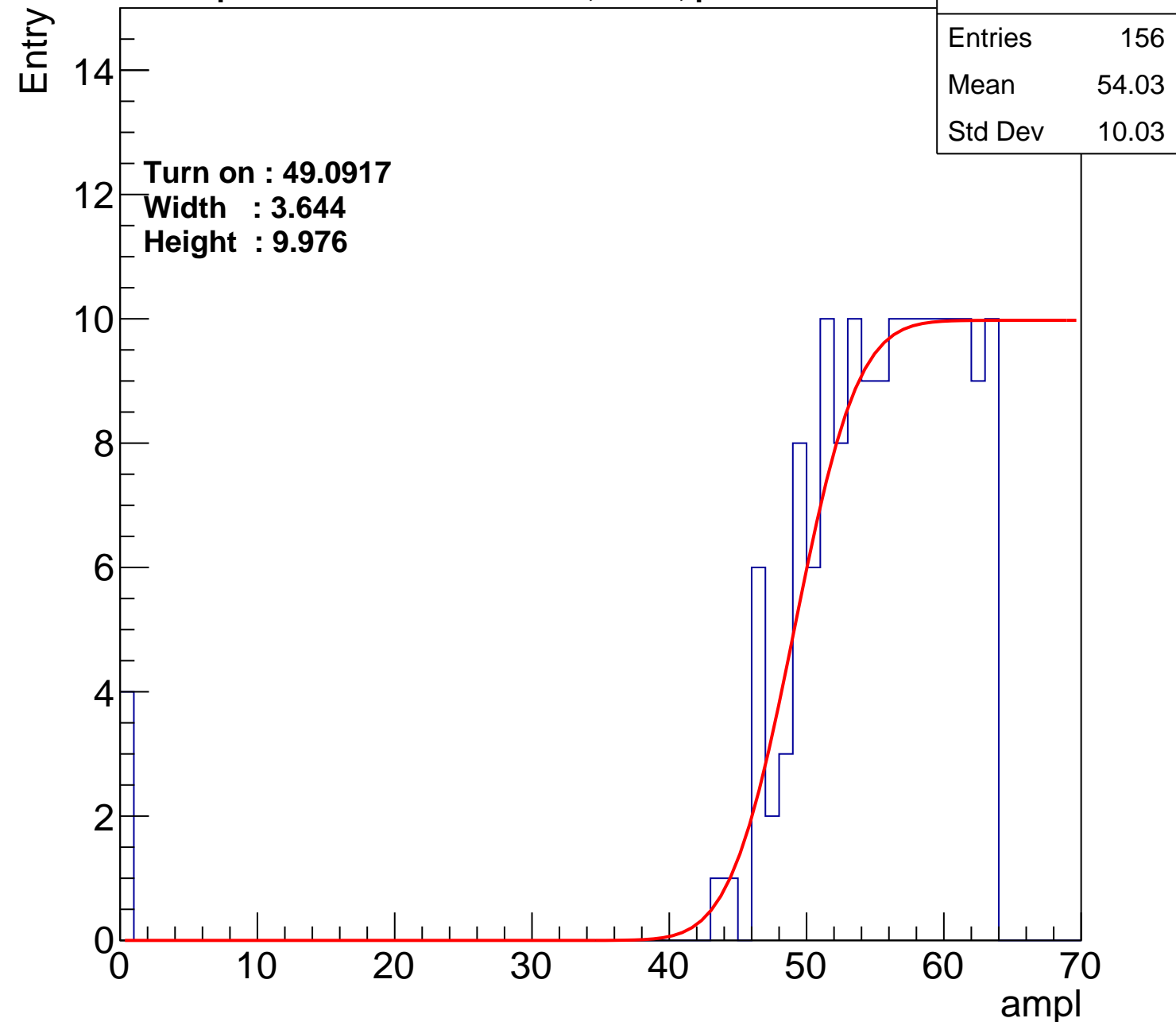
Width : 3.644

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.52
Std Dev	10.24

Turn on : 50.0405

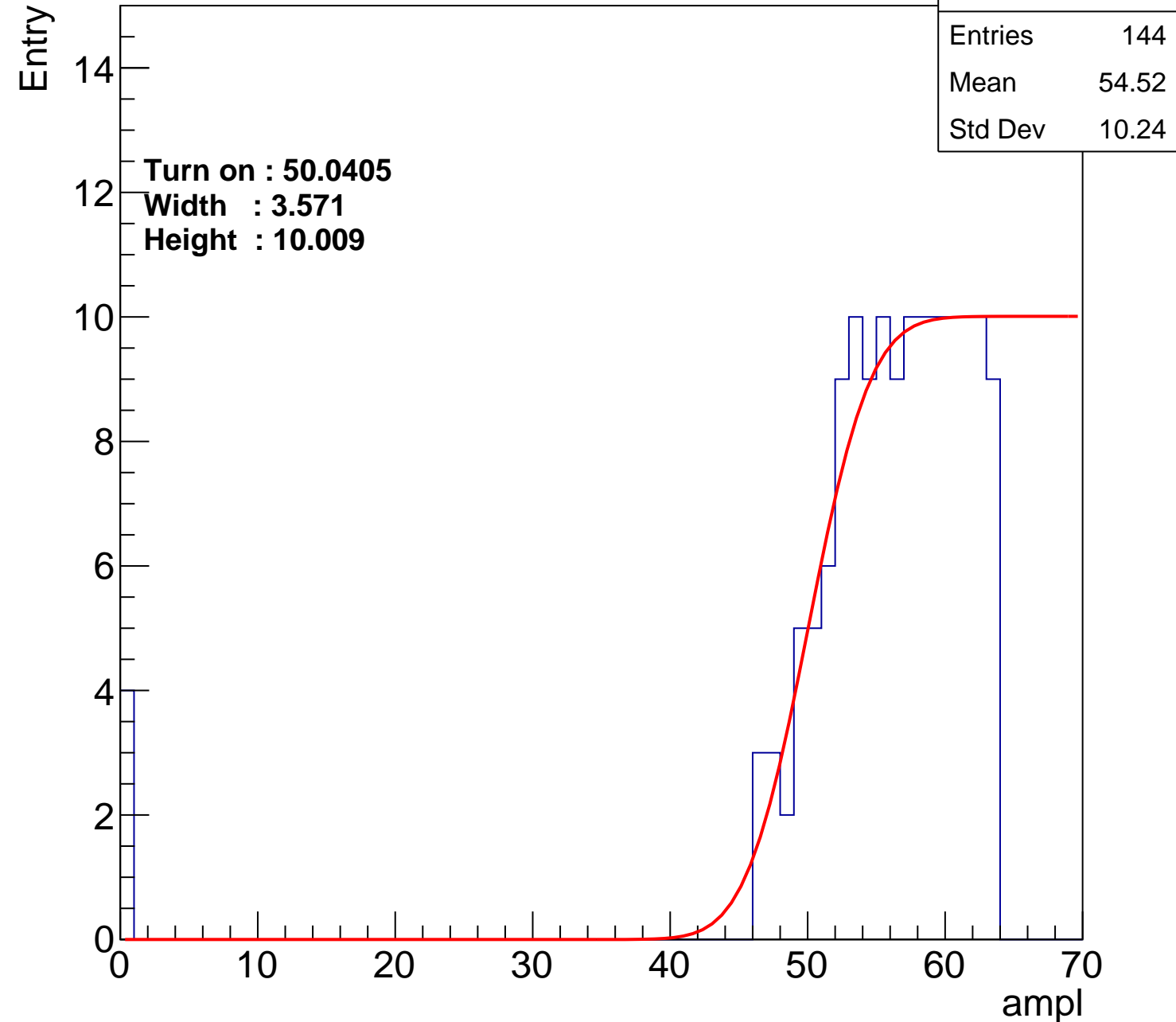
Width : 3.571

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.07
Std Dev	8.354

Turn on : 52.4858

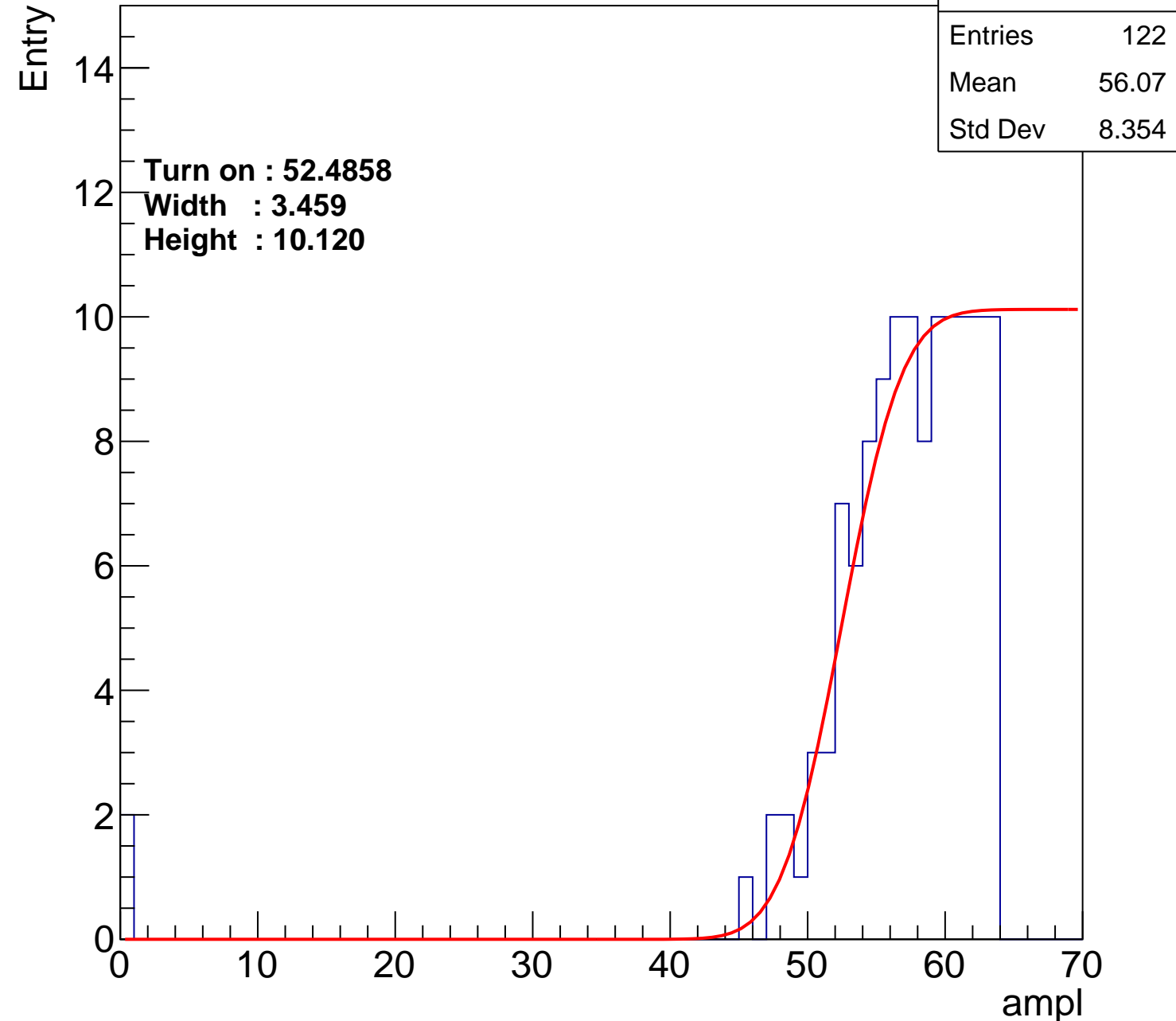
Width : 3.459

Height : 10.120

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	56.32
Std Dev	8.429

Turn on : 53.1491

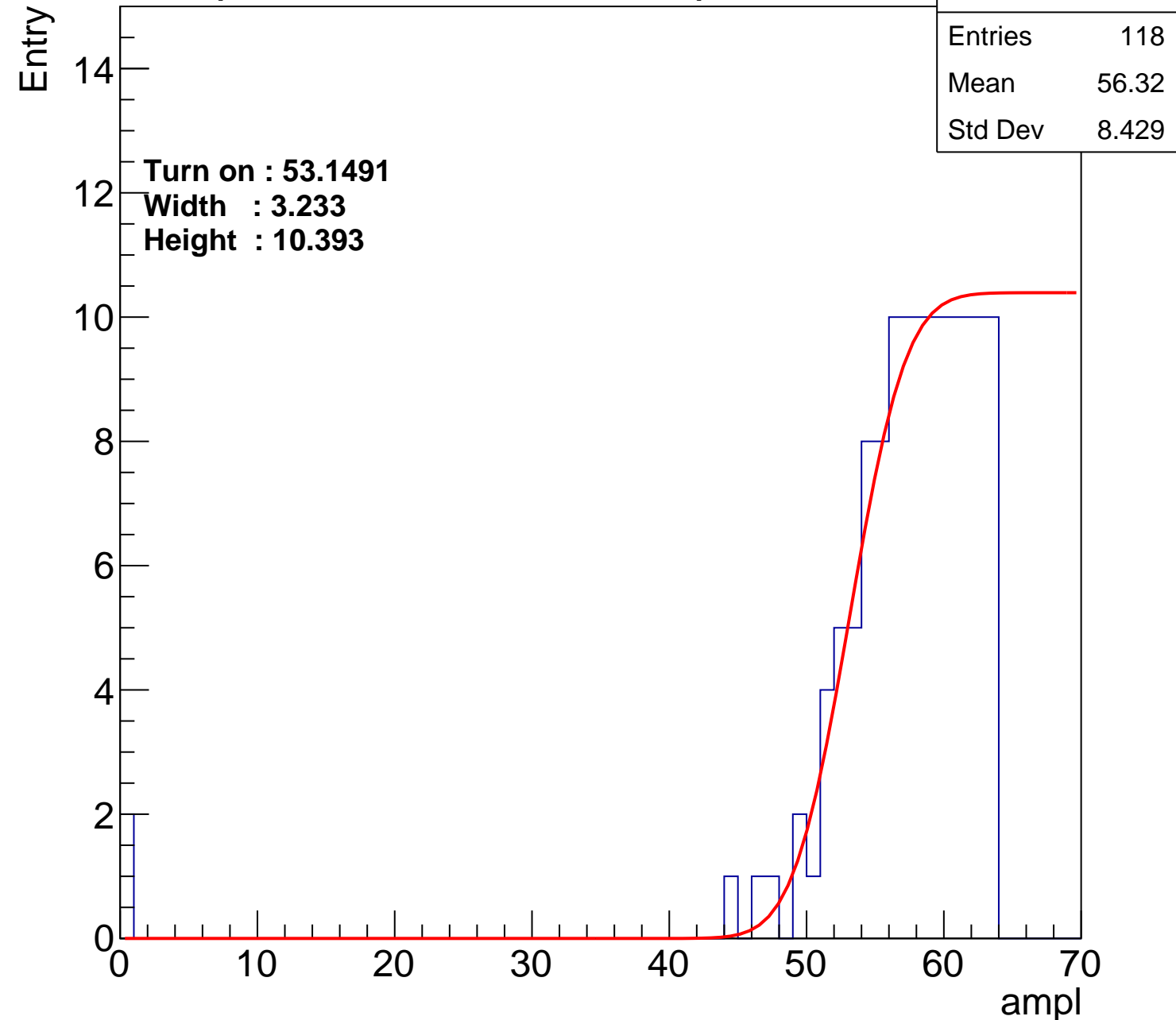
Width : 3.233

Height : 10.393

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

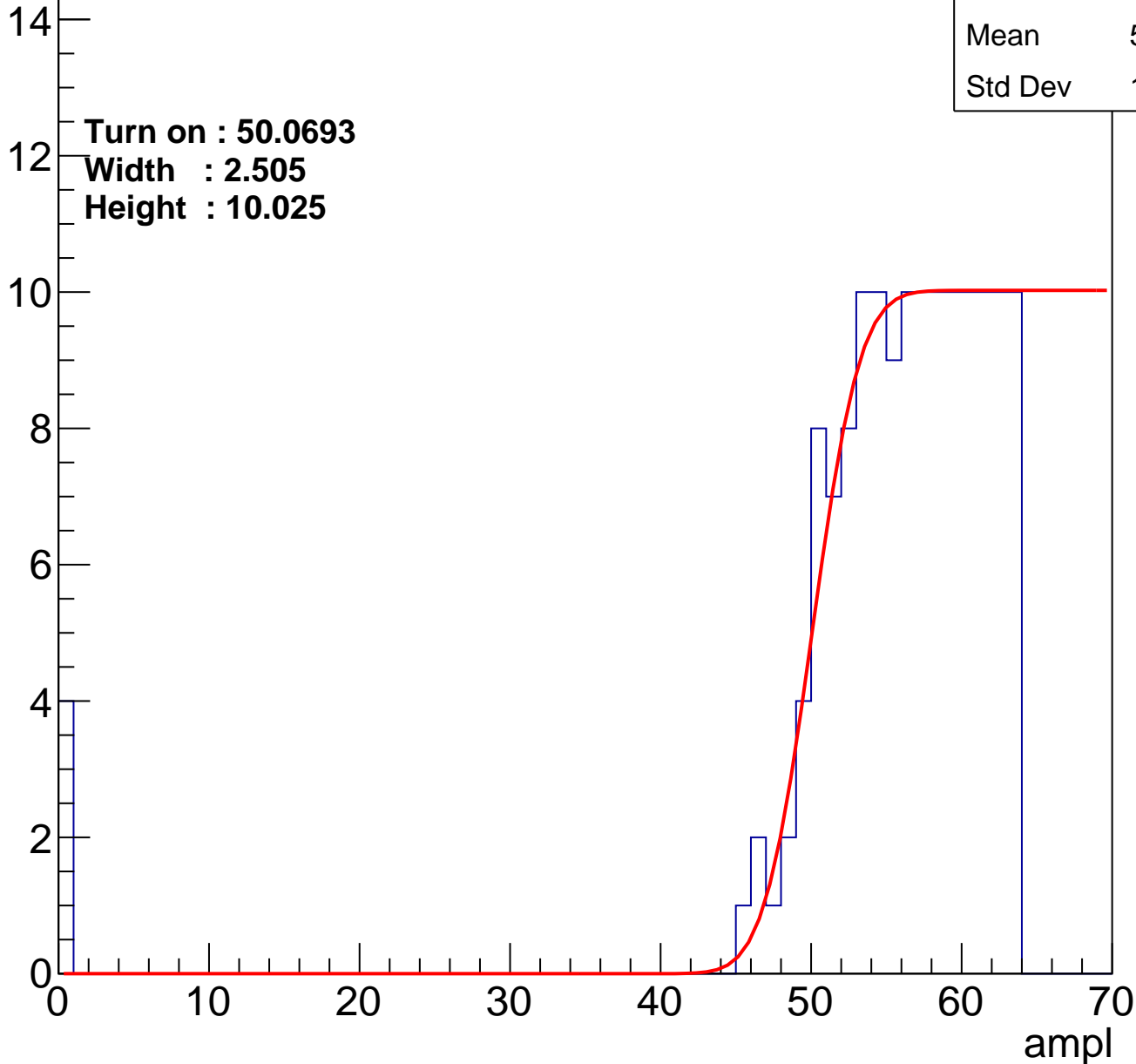
Entry

Entries	146
Mean	54.62
Std Dev	10.18

Turn on : 50.0693

Width : 2.505

Height : 10.025



# B0L103S, U9-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.46
Std Dev	11.53

Turn on : 51.4372

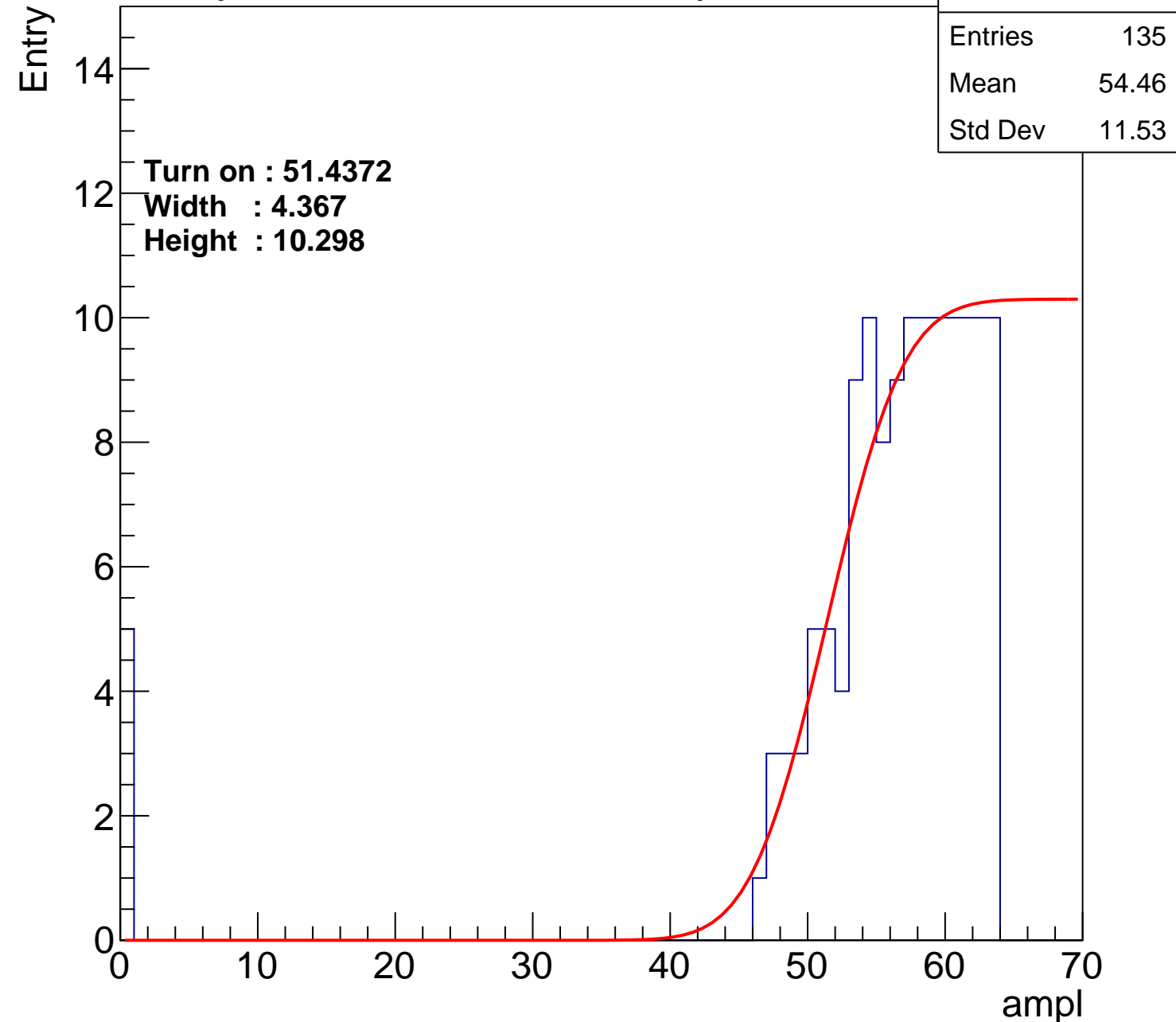
Width : 4.367

Height : 10.298

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.97
Std Dev	9.146

Turn on : 49.6807

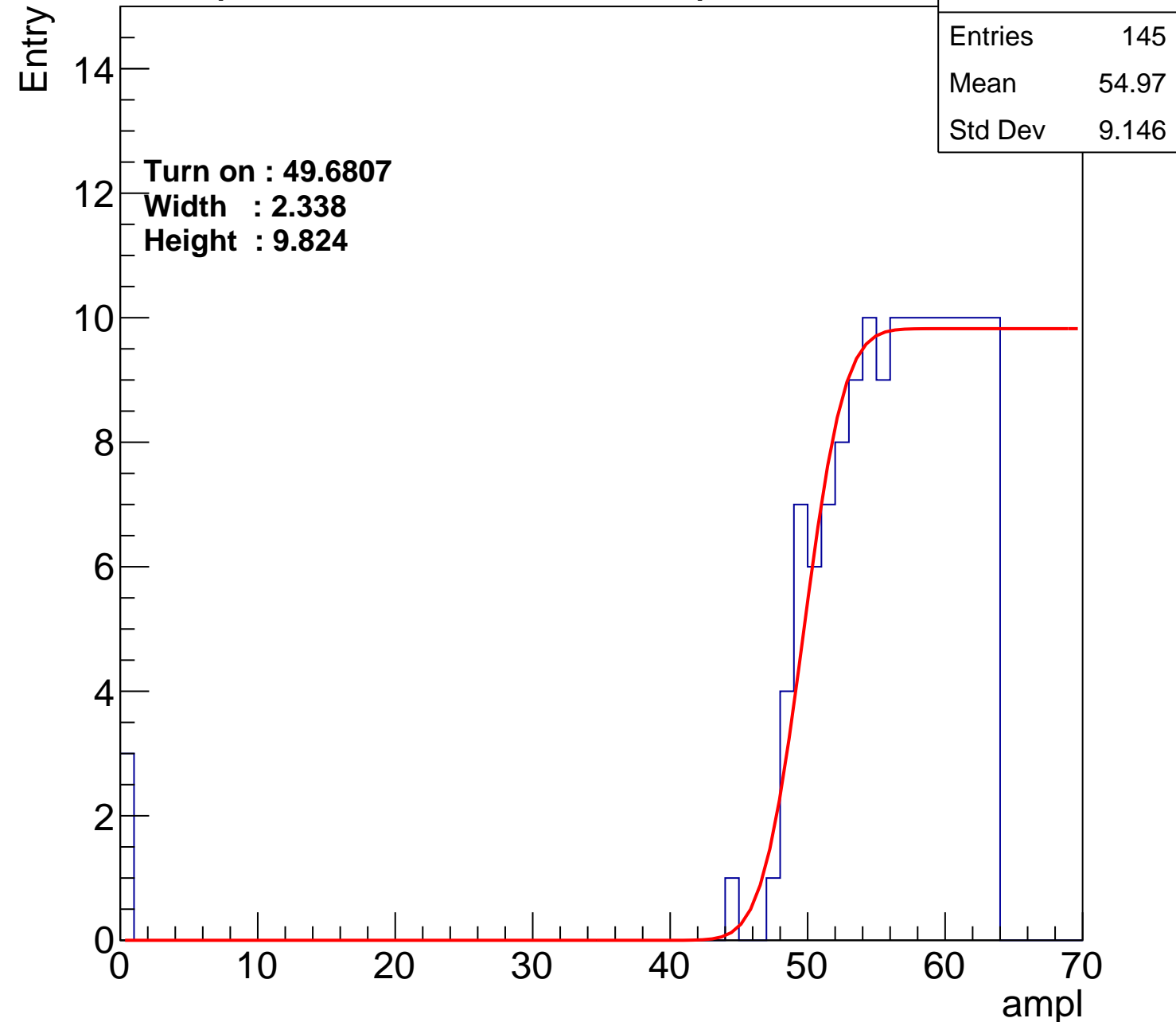
Width : 2.338

Height : 9.824

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

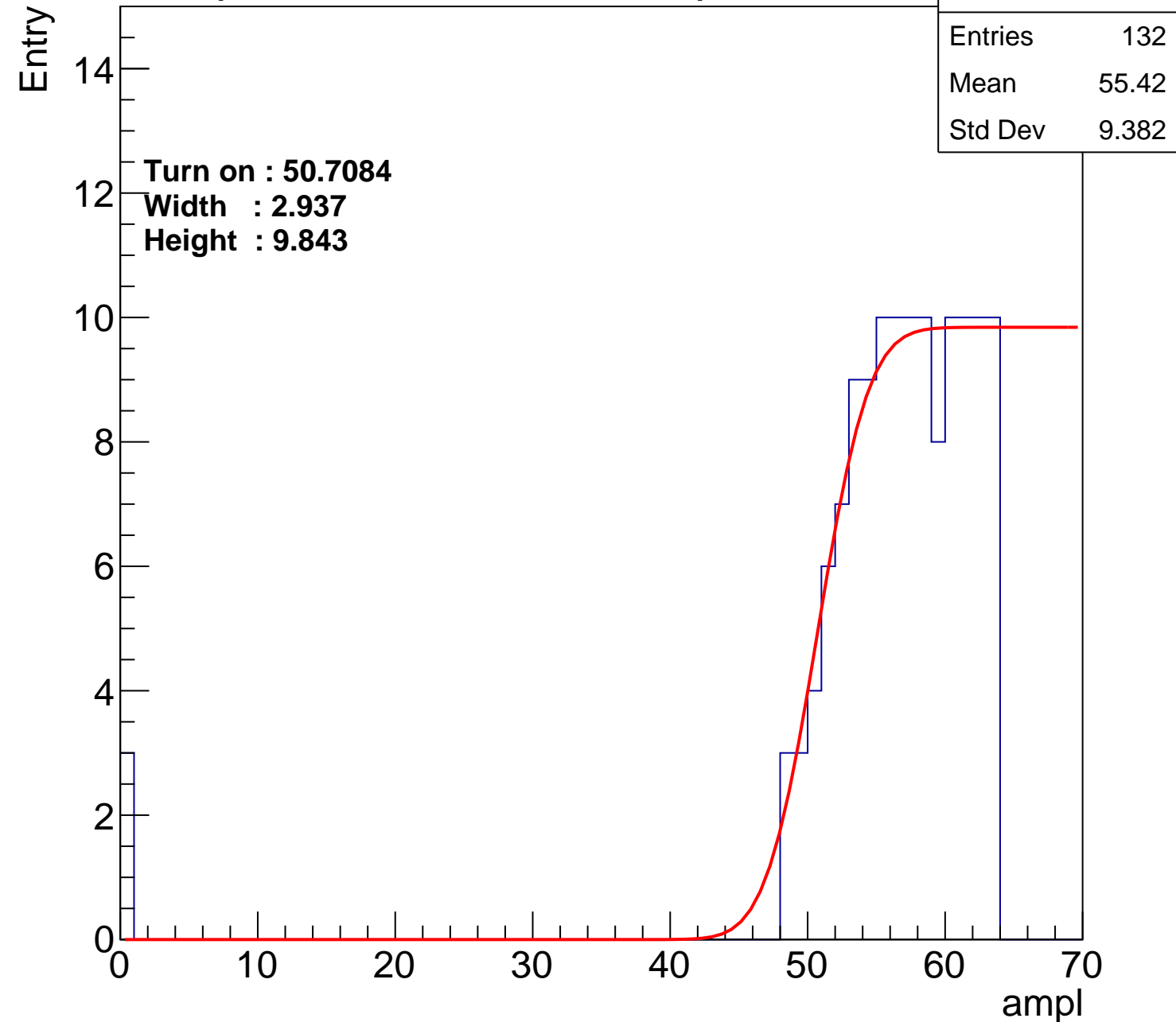
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.7084  
Width : 2.937  
Height : 9.843

Entries	132
Mean	55.42
Std Dev	9.382

ampl



# B0L103S, U9-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

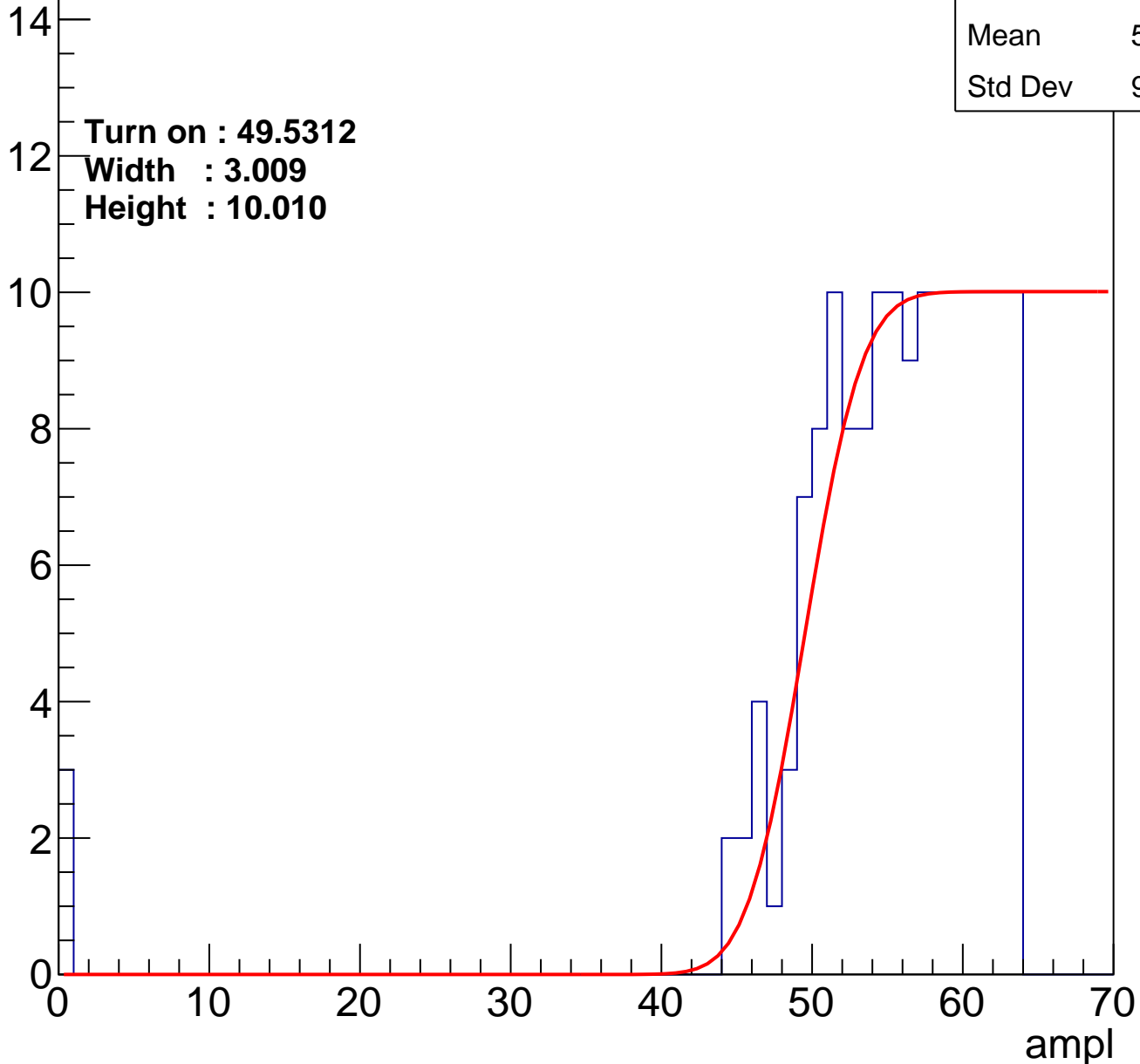
Entry

Entries	155
Mean	54.45
Std Dev	9.077

Turn on : 49.5312

Width : 3.009

Height : 10.010



# B0L103S, U9-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.52
Std Dev	6.666

Turn on : 52.3605

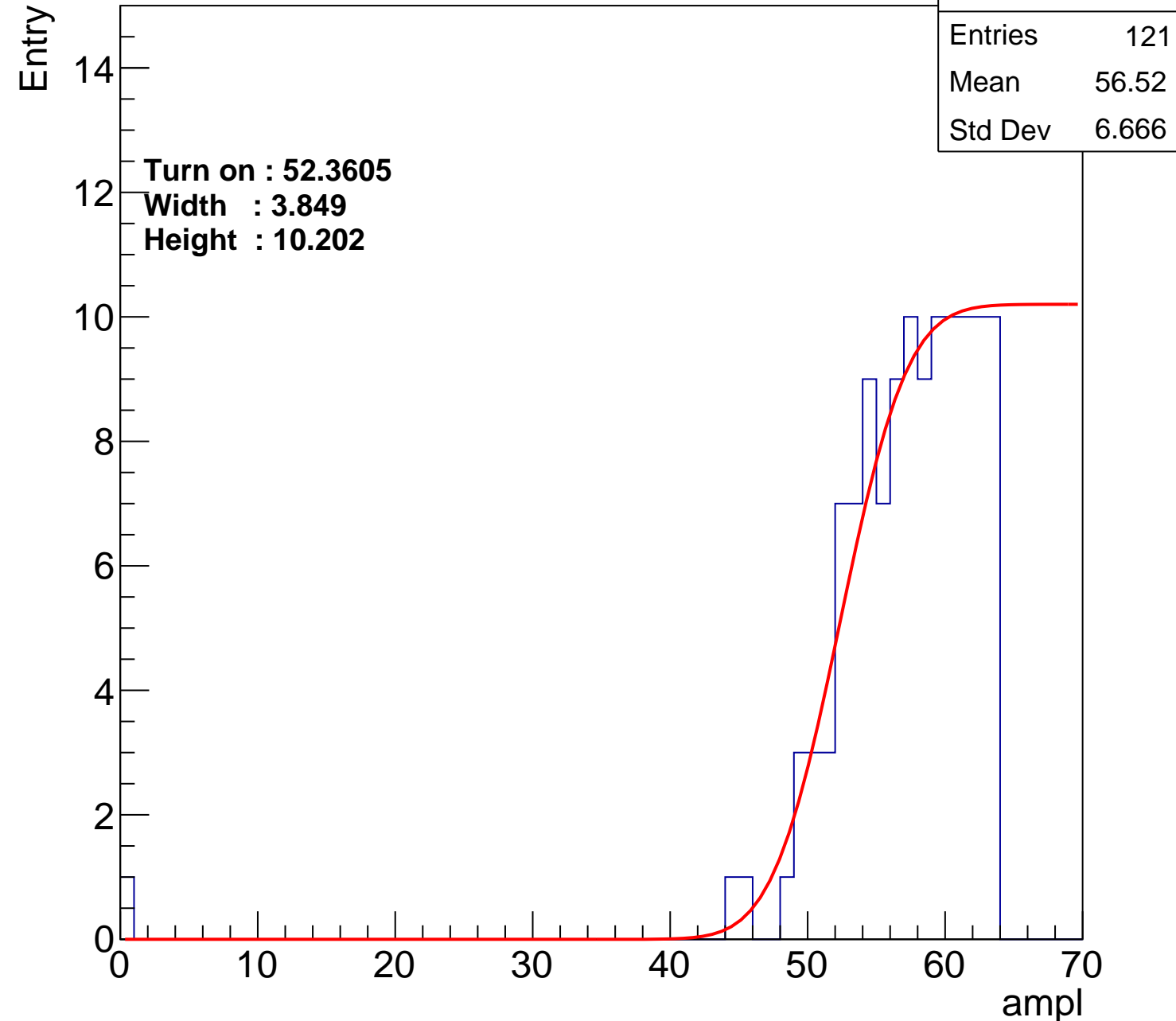
Width : 3.849

Height : 10.202

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.7
Std Dev	8.053

Turn on : 51.2071

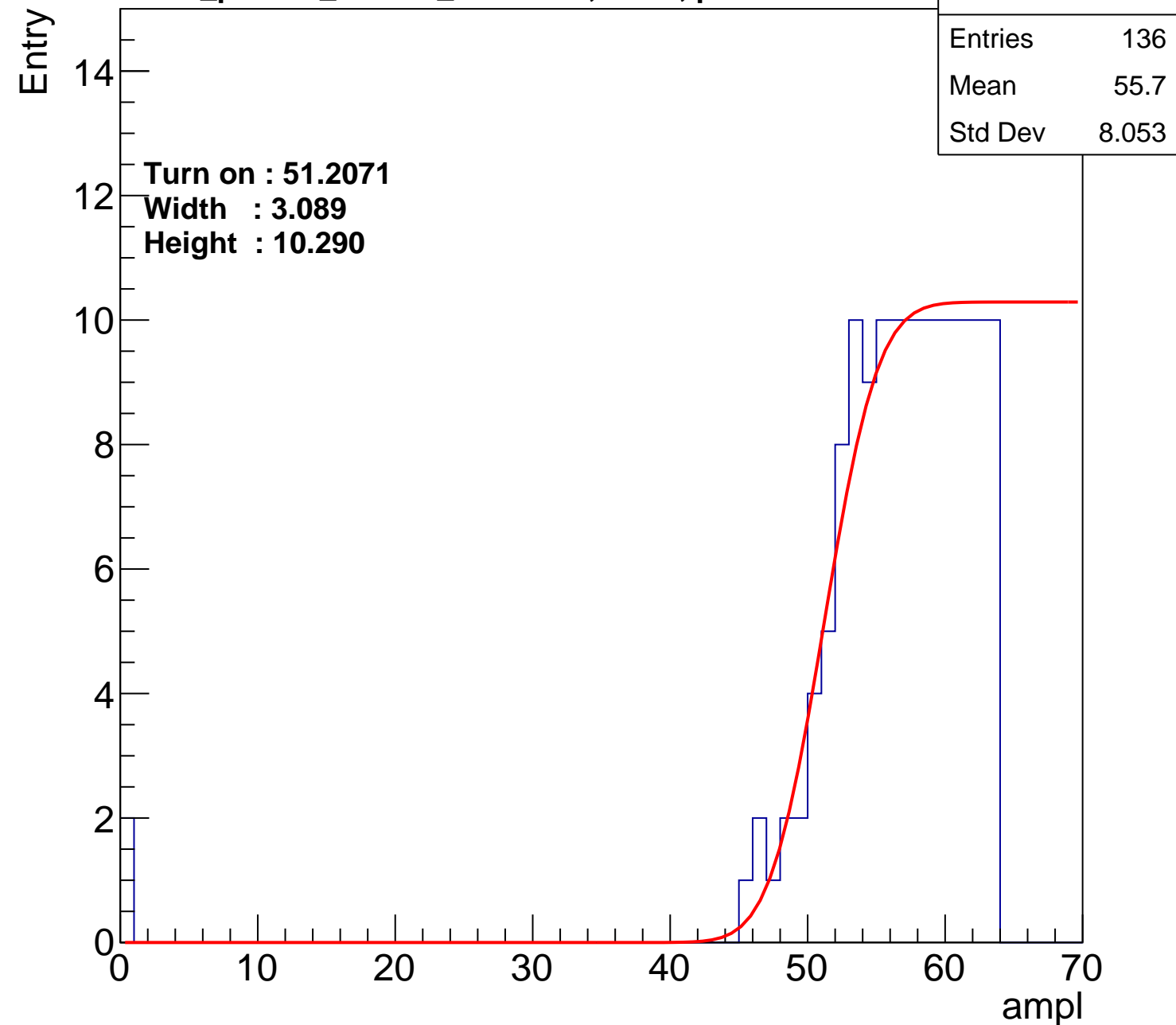
Width : 3.089

Height : 10.290

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.81
Std Dev	8.156

Turn on : 51.2403

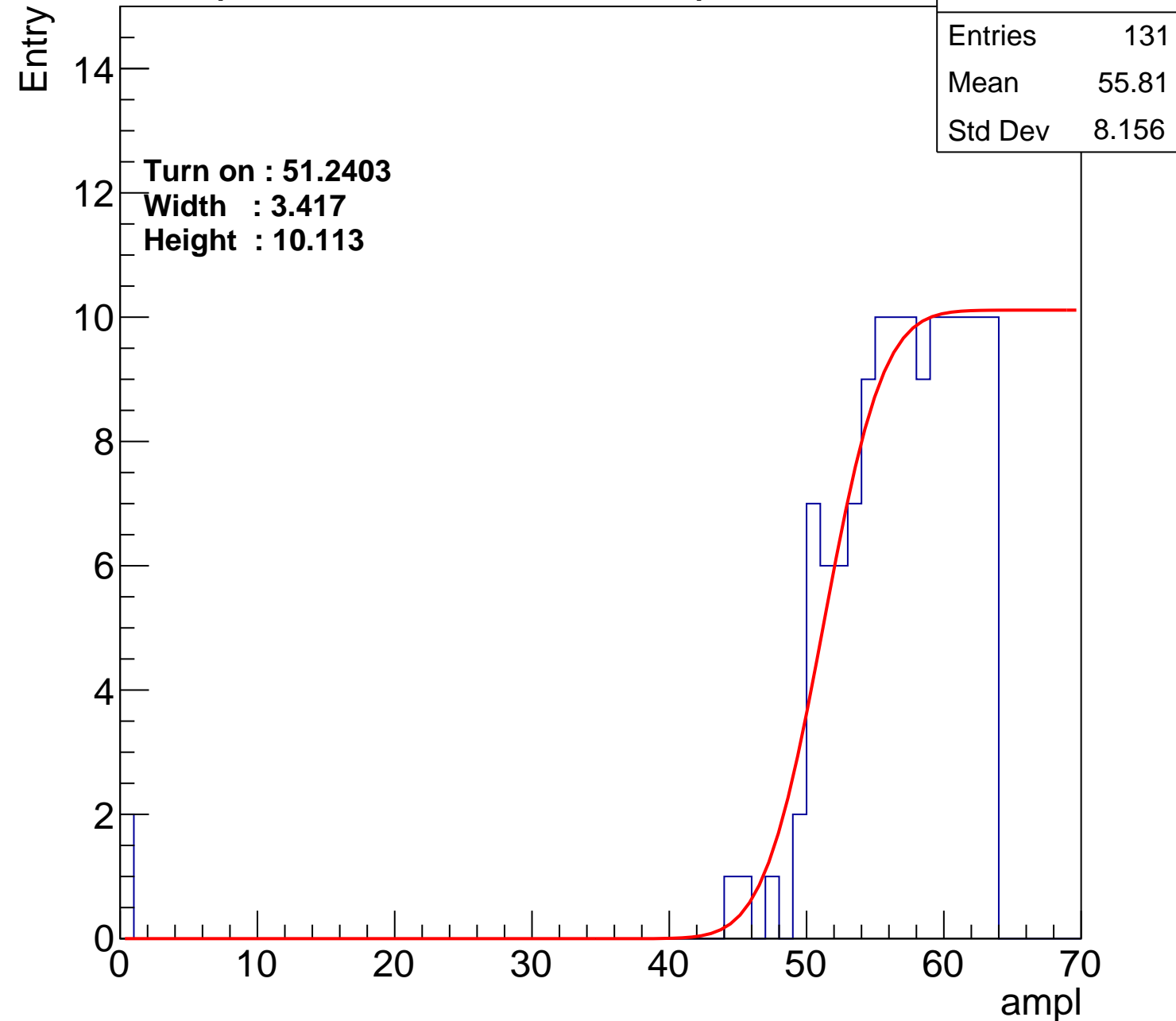
Width : 3.417

Height : 10.113

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

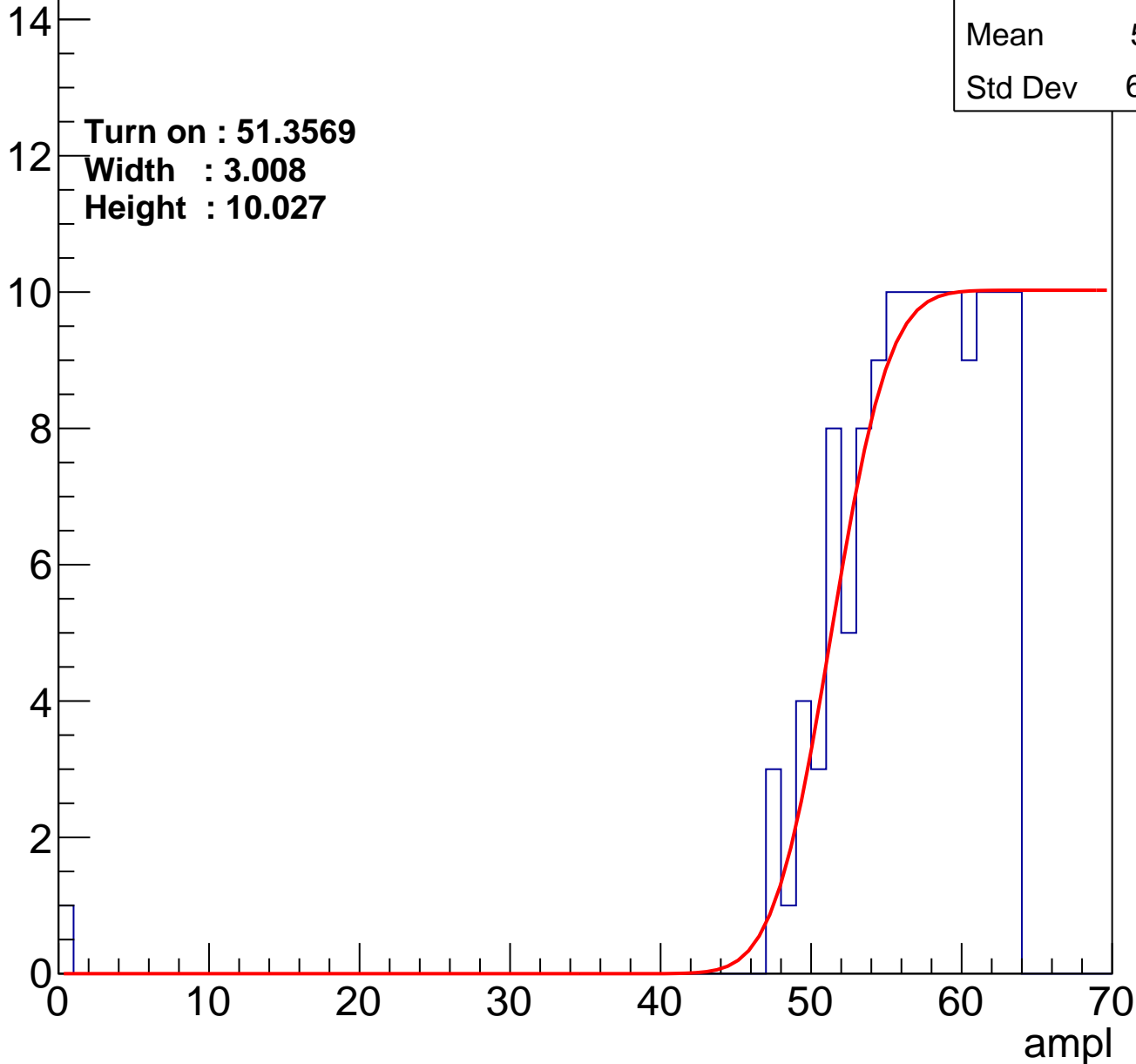
Entry

Entries	131
Mean	56.21
Std Dev	6.493

Turn on : 51.3569

Width : 3.008

Height : 10.027



# B0L103S, U9-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.02
Std Dev	8.265

Turn on : 52.0863

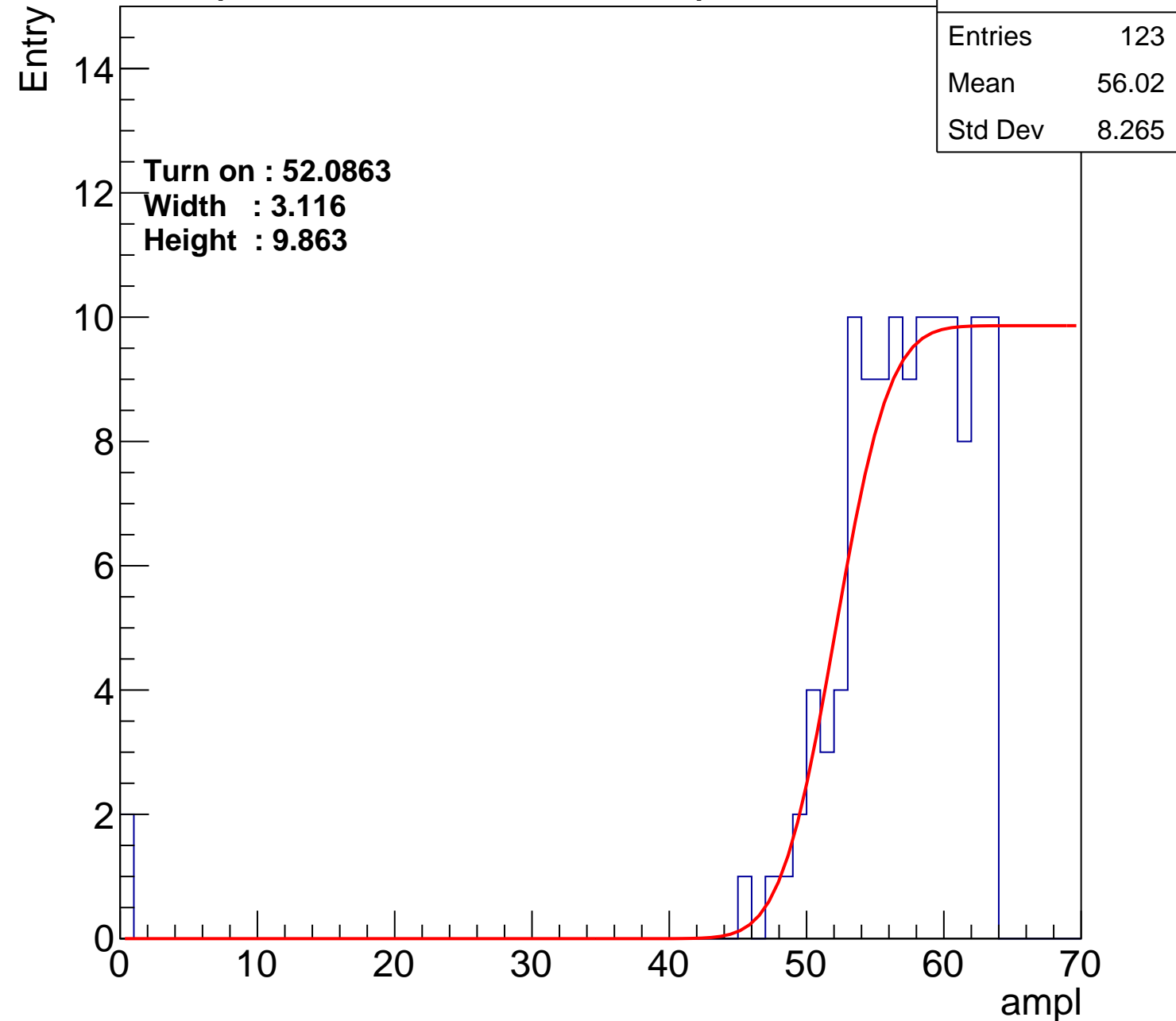
Width : 3.116

Height : 9.863

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

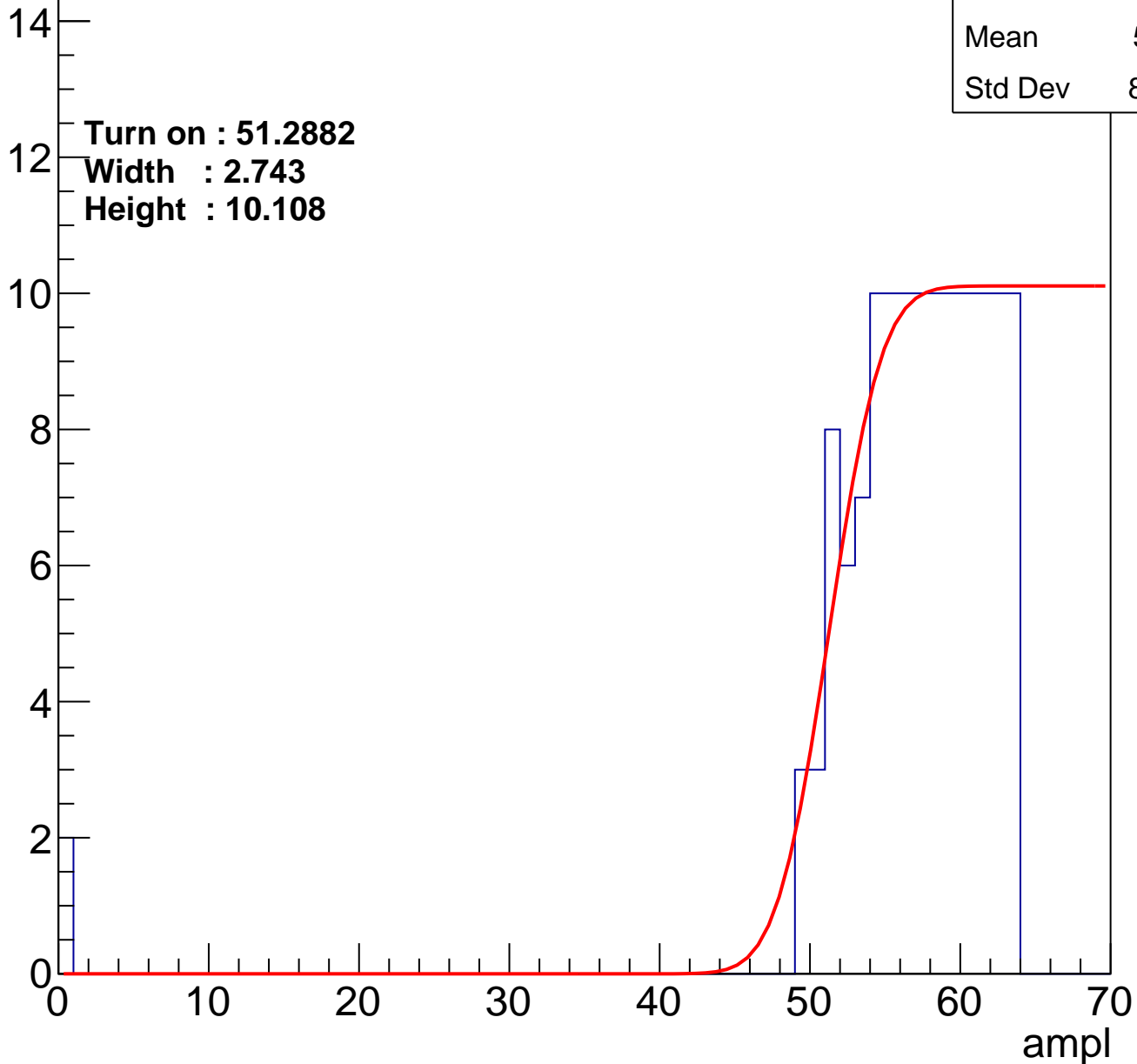
Entry

Entries	129
Mean	56.11
Std Dev	8.038

Turn on : 51.2882

Width : 2.743

Height : 10.108



# B0L103S, U9-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.5
Std Dev	8.512

**Turn on : 53.2598**

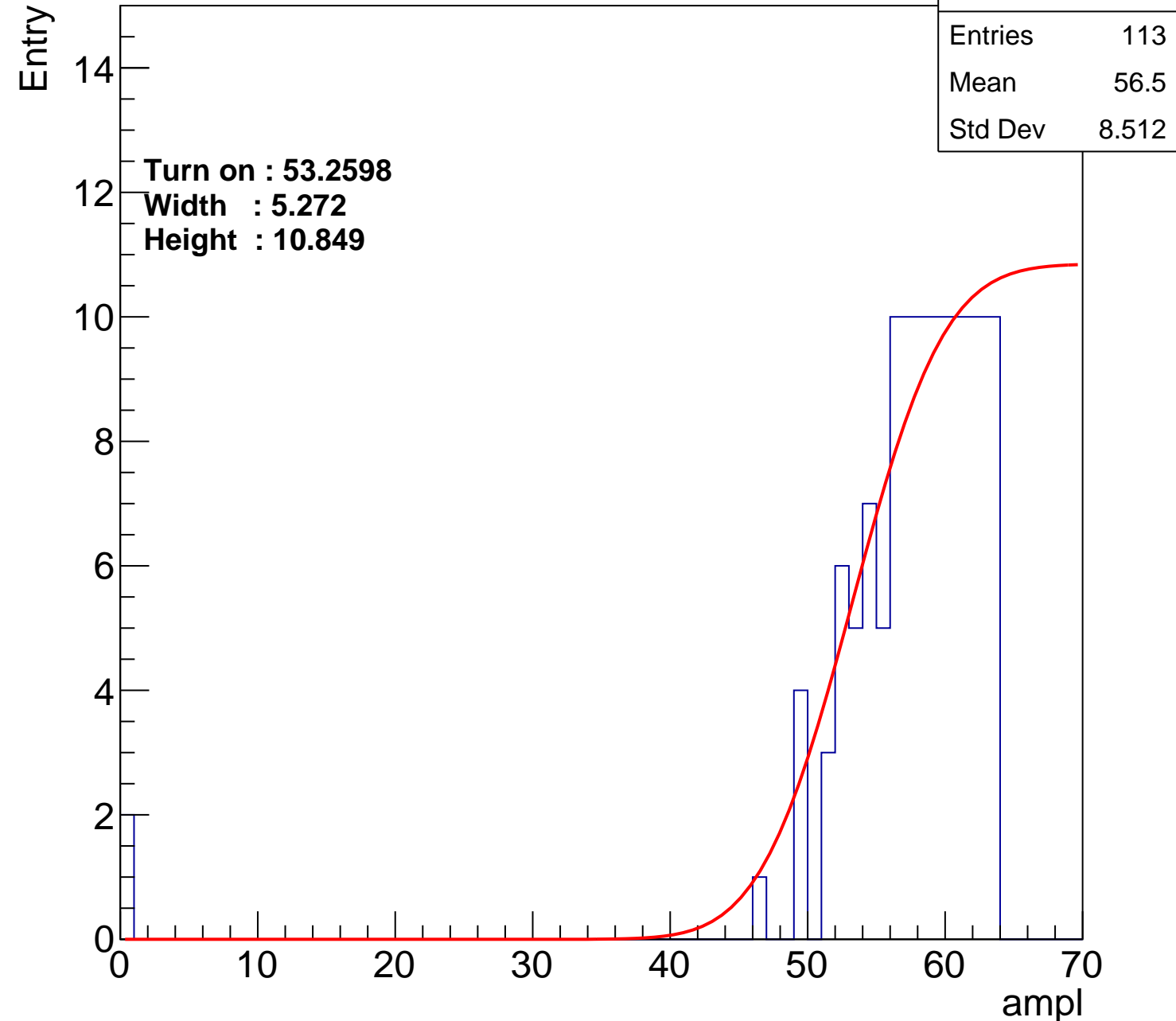
**Width : 5.272**

**Height : 10.849**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.25
Std Dev	8.323

Turn on : 52.2558

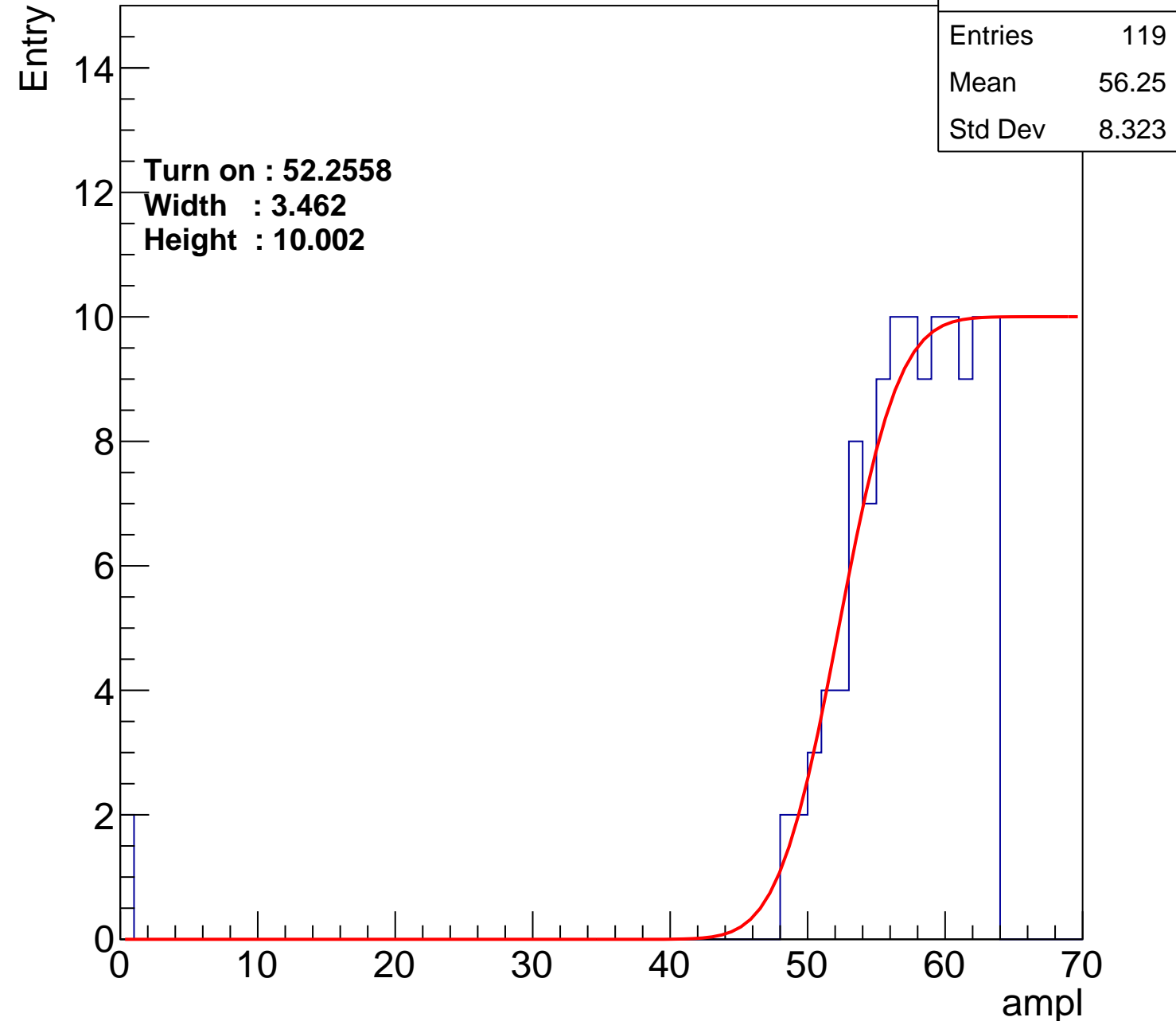
Width : 3.462

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.31
Std Dev	9.408

Turn on : 50.5353

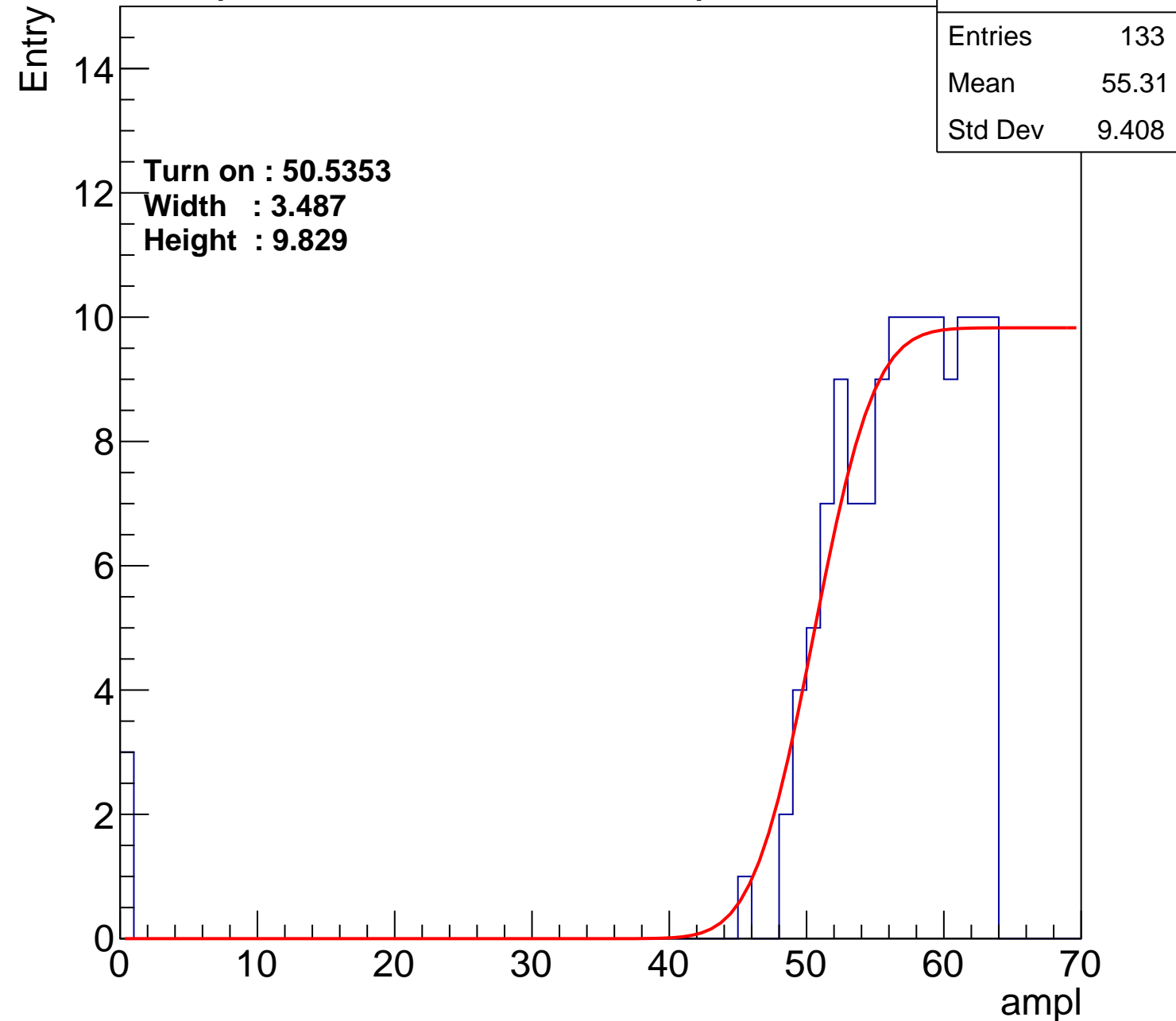
Width : 3.487

Height : 9.829

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

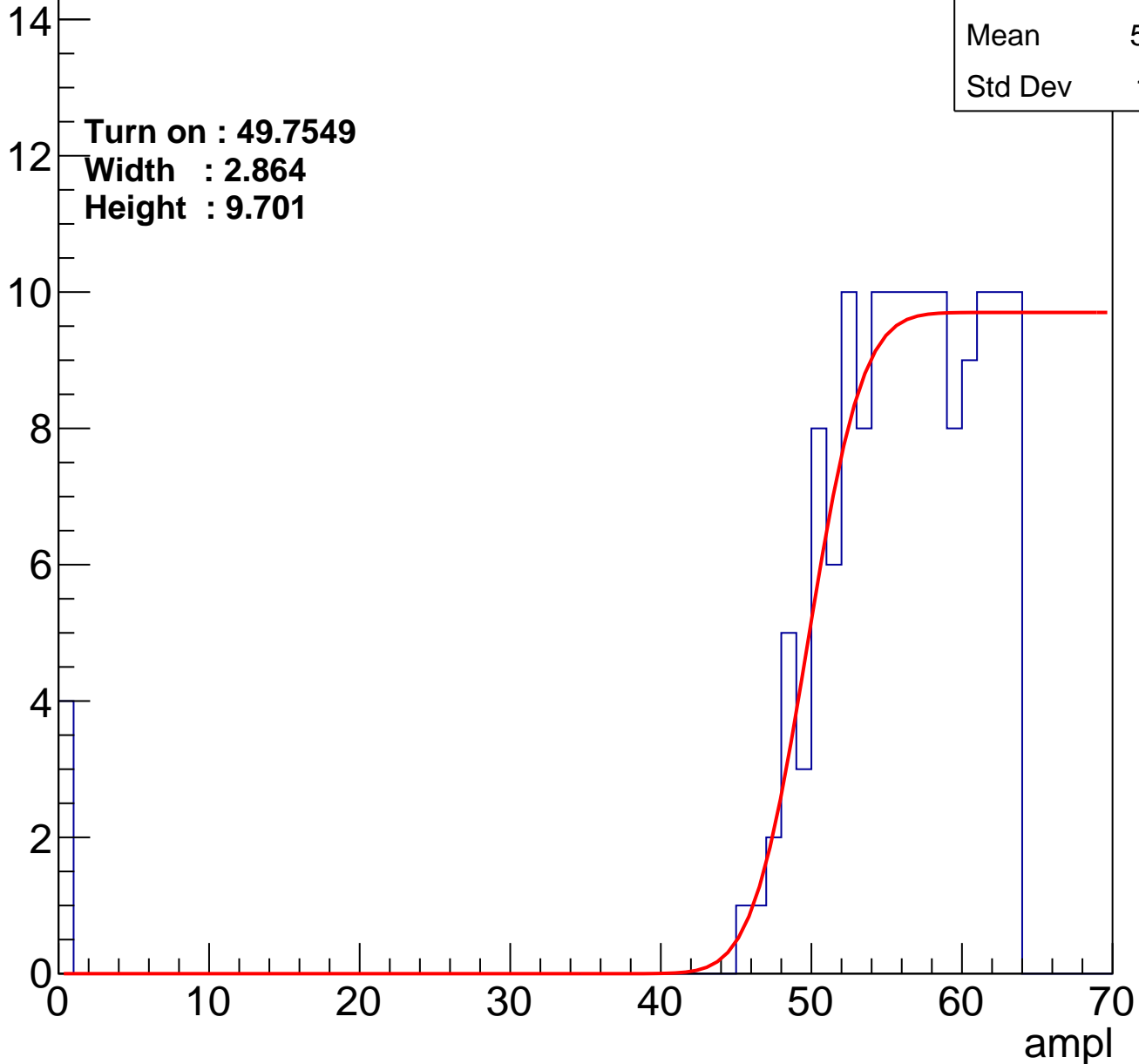
Entries	145
Mean	54.44
Std Dev	10.21

**Turn on : 49.7549**

**Width : 2.864**

**Height : 9.701**

Entry





# B0L103S, U9-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

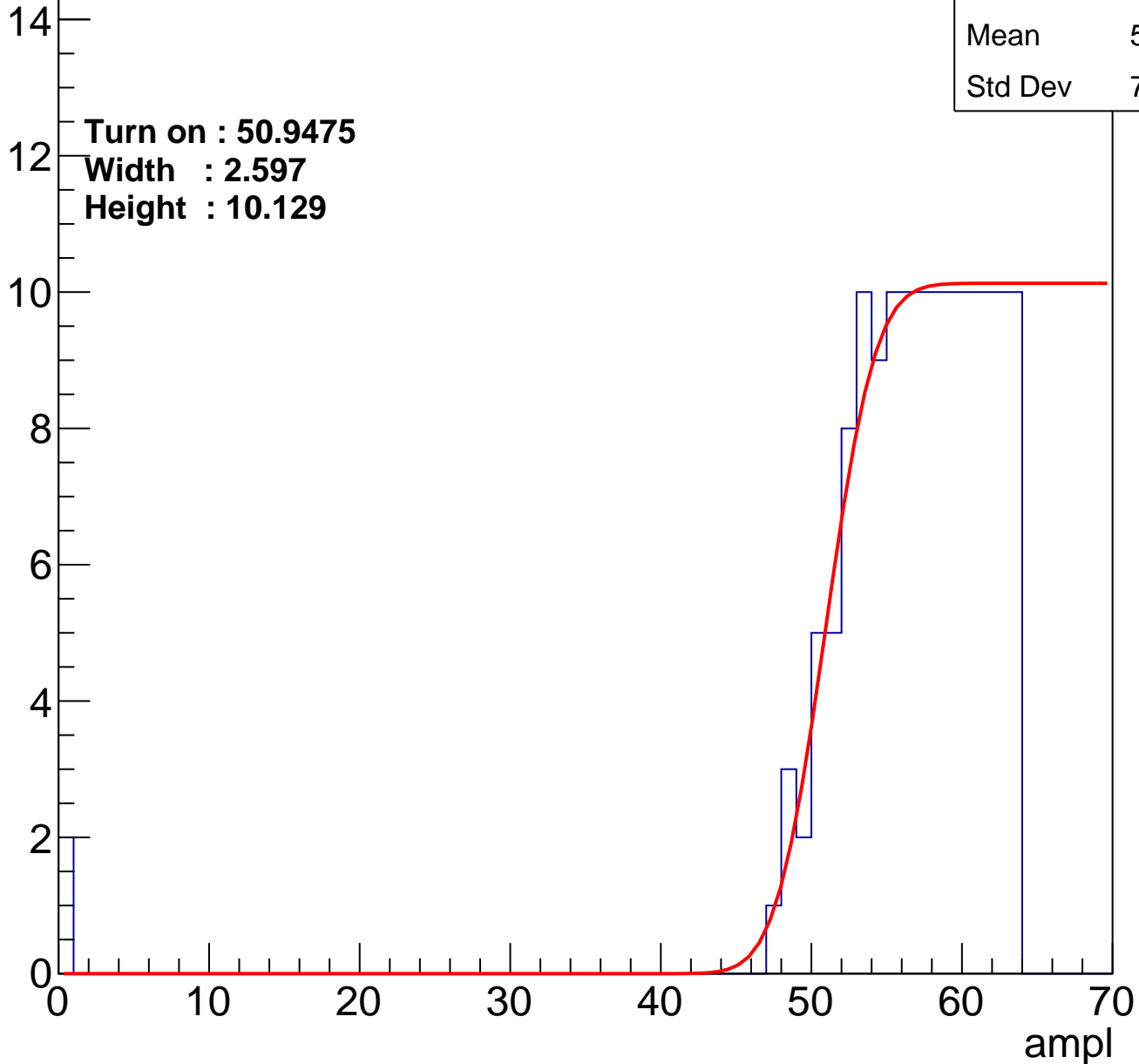
Entries	135
Mean	55.82
Std Dev	7.985

Turn on : 50.9475

Width : 2.597

Height : 10.129

Entry



# B0L103S, U9-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

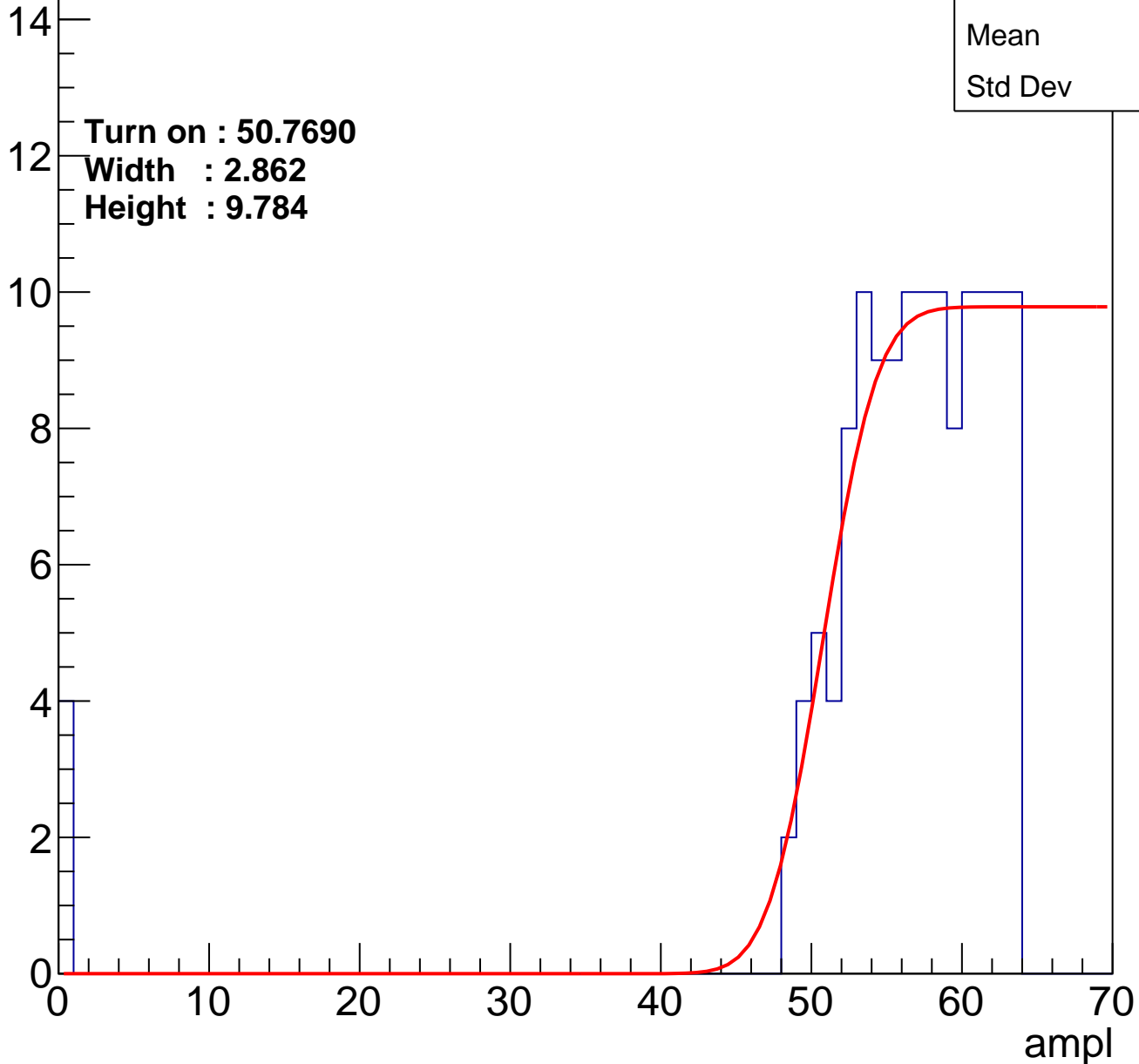
Entries	133
Mean	55
Std Dev	10.5

Turn on : 50.7690

Width : 2.862

Height : 9.784

Entry



# B0L103S, U9-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	55.96
Std Dev	9.891

Turn on : 53.2574

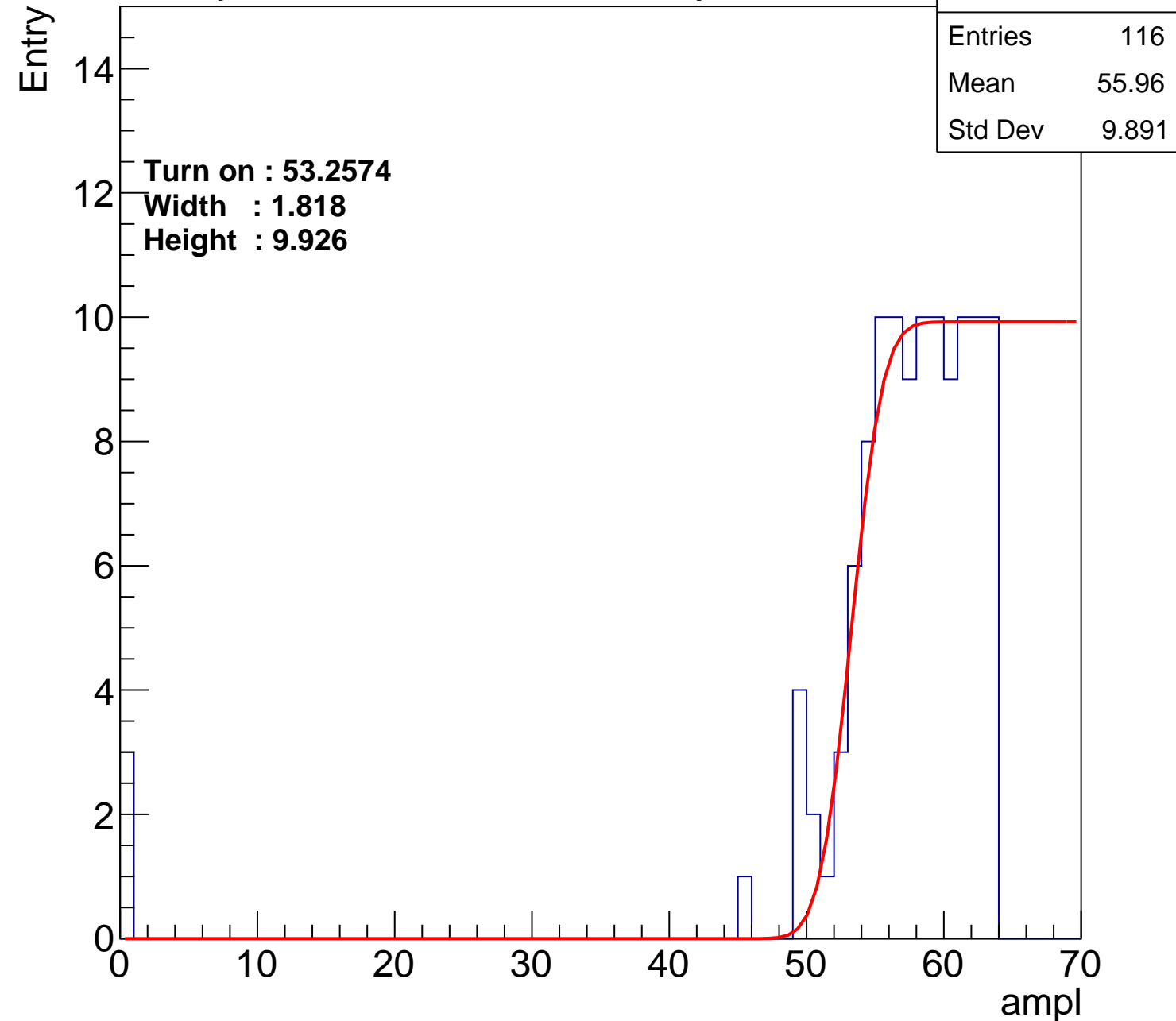
Width : 1.818

Height : 9.926

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	147
Mean	55.2
Std Dev	7.926

Turn on : 49.3692

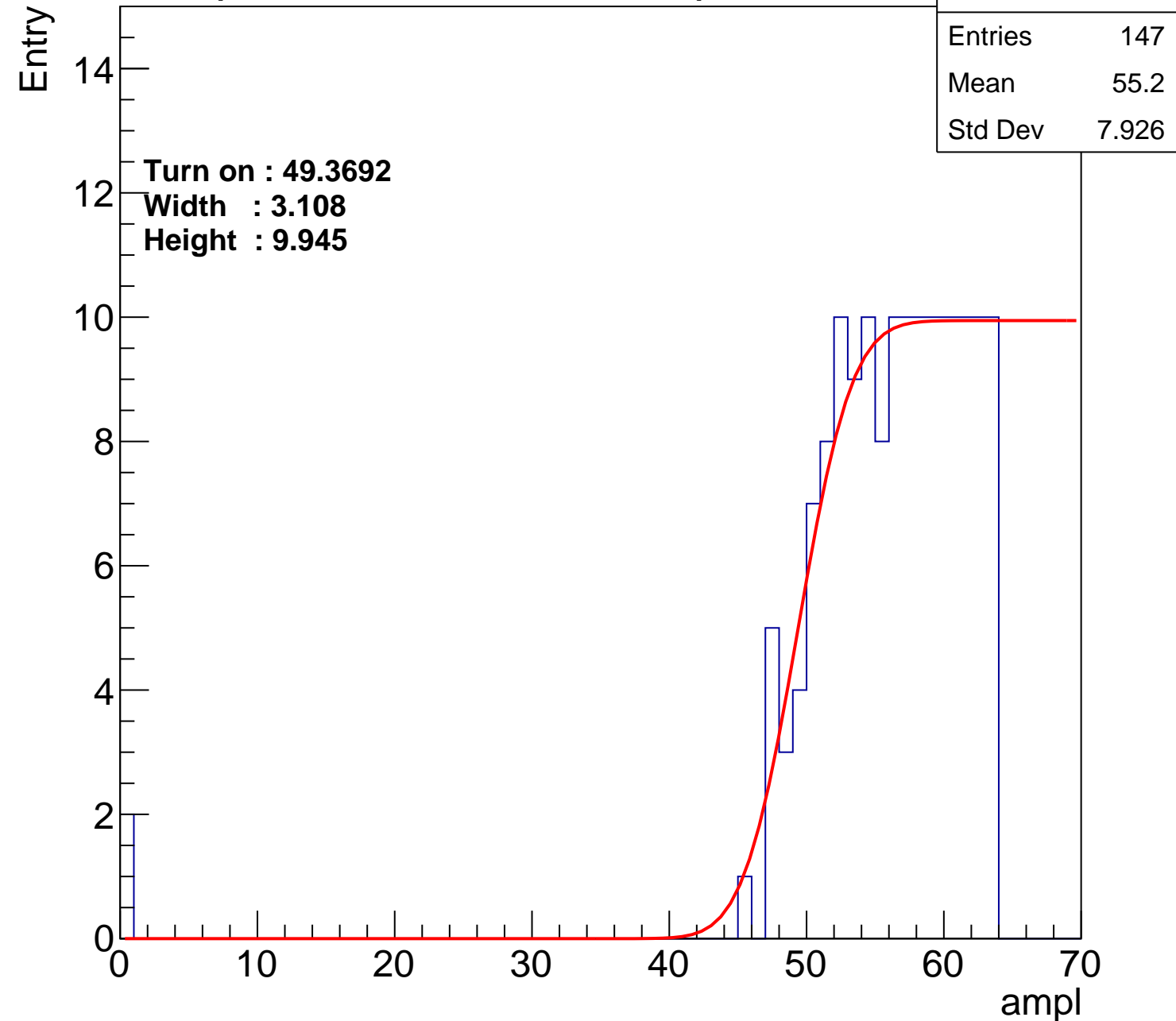
Width : 3.108

Height : 9.945

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	55.1
Std Dev	9.333

Turn on : 50.8140

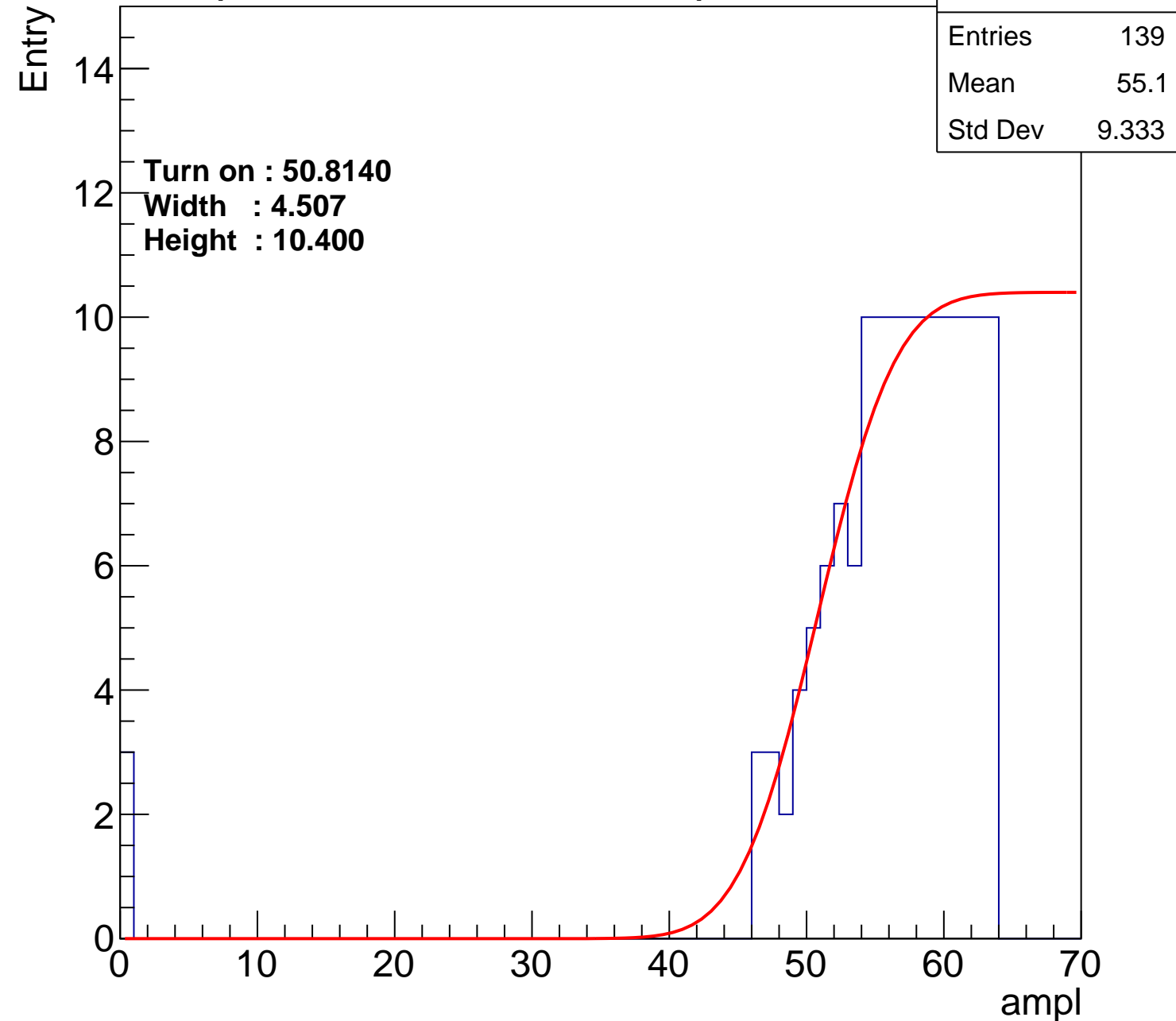
Width : 4.507

Height : 10.400

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch108

**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	133
---------	-----

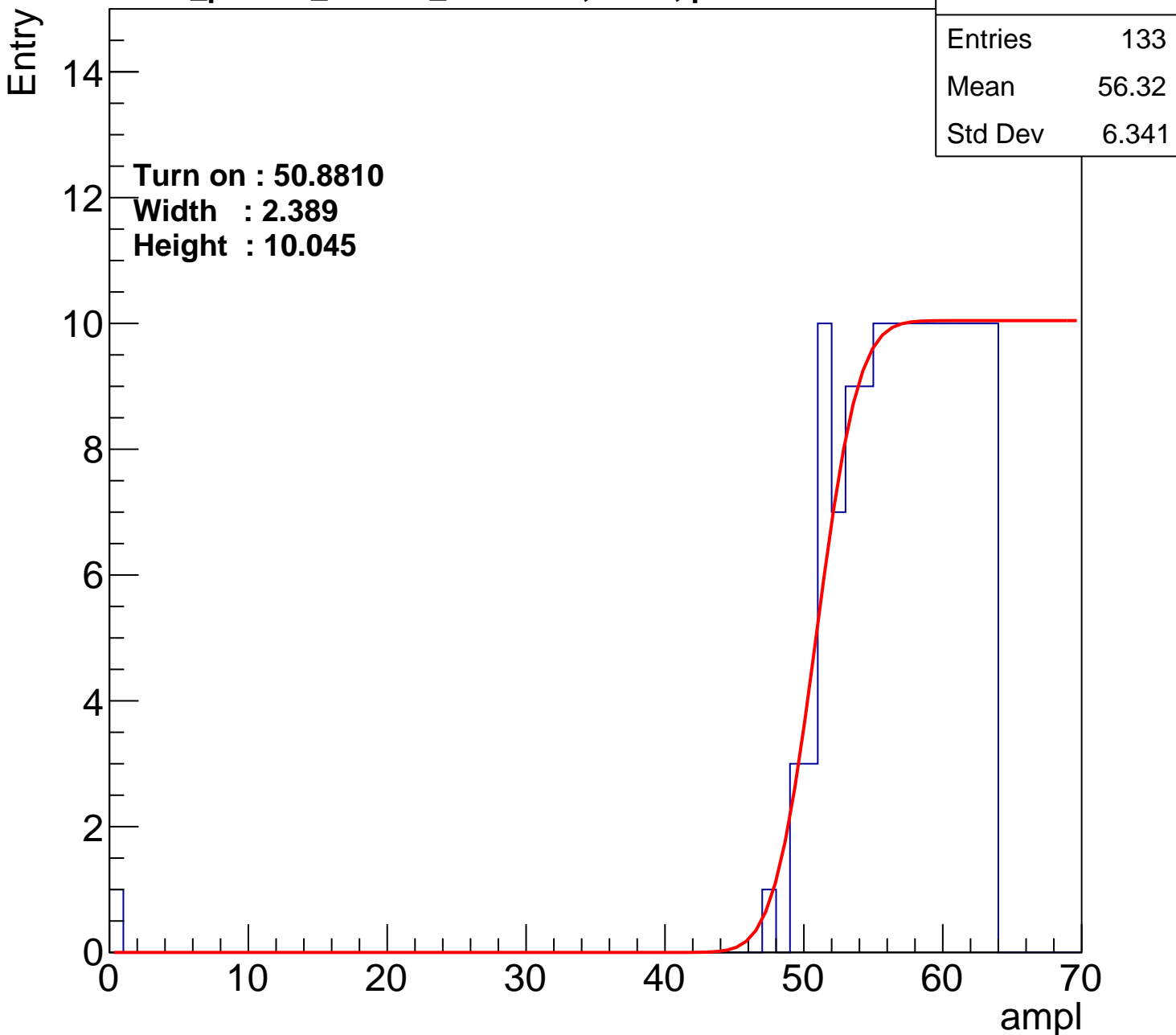
Mean	56.32
------	-------

Std Dev	6.341
---------	-------

**Turn on : 50.8810**

**Width : 2.389**

**Height : 10.045**



# B0L103S, U9-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.31
Std Dev	9.426

Turn on : 51.7817

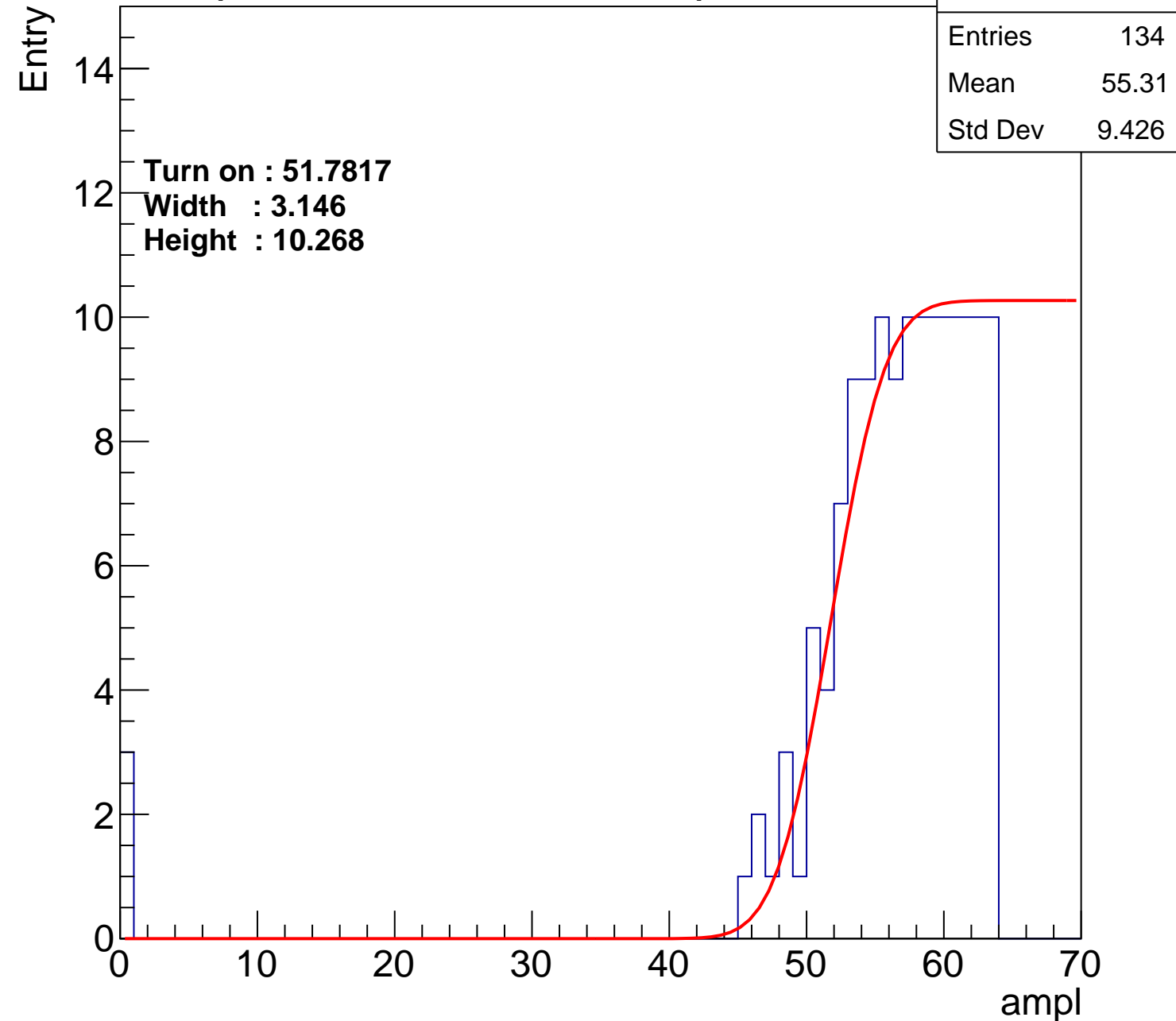
Width : 3.146

Height : 10.268

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

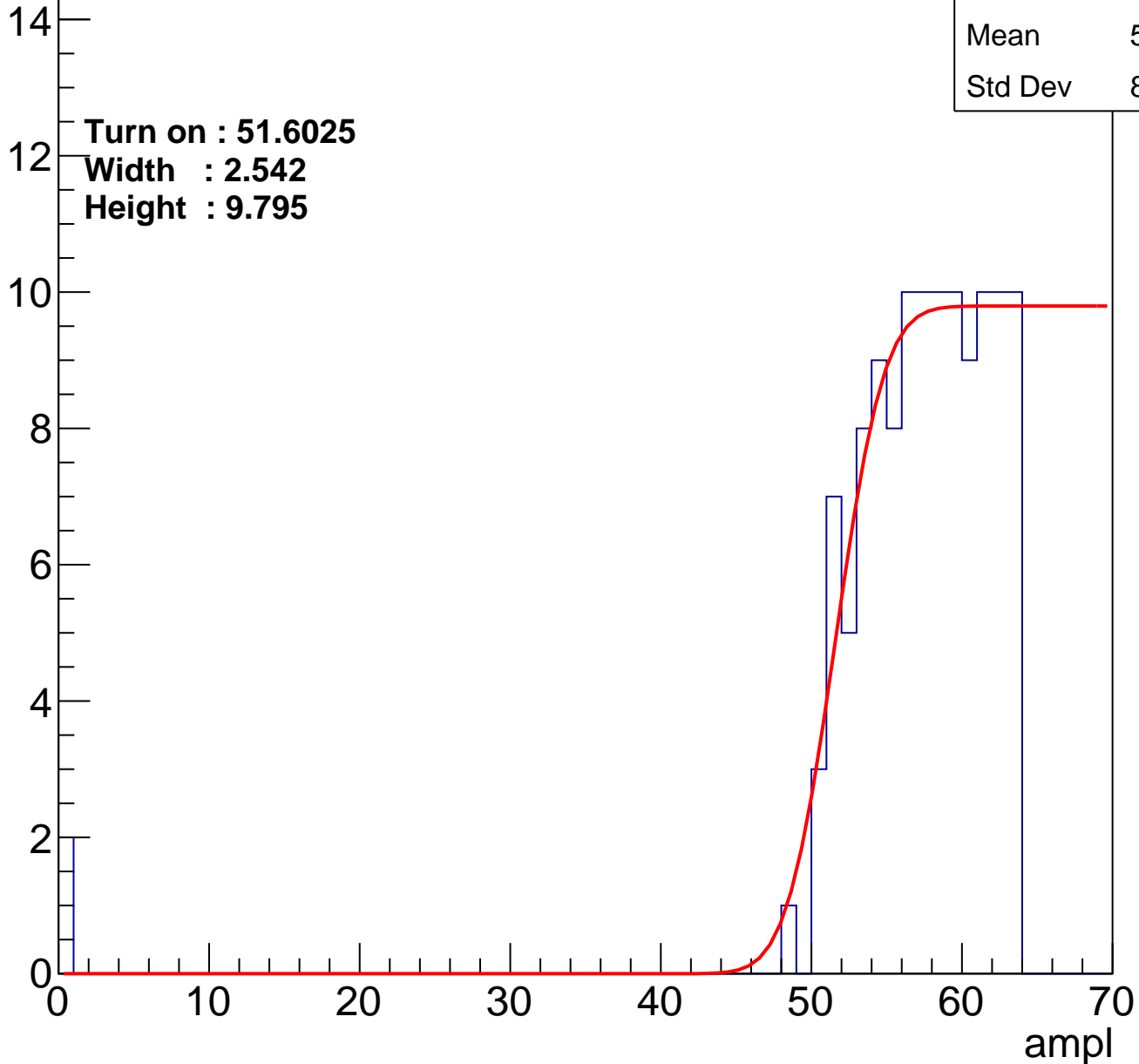
Entries	122
Mean	56.27
Std Dev	8.193

Turn on : 51.6025

Width : 2.542

Height : 9.795

Entry





# B0L103S, U9-ch111

calib\_packv5\_040323\_1717.root, FC#2, port C3

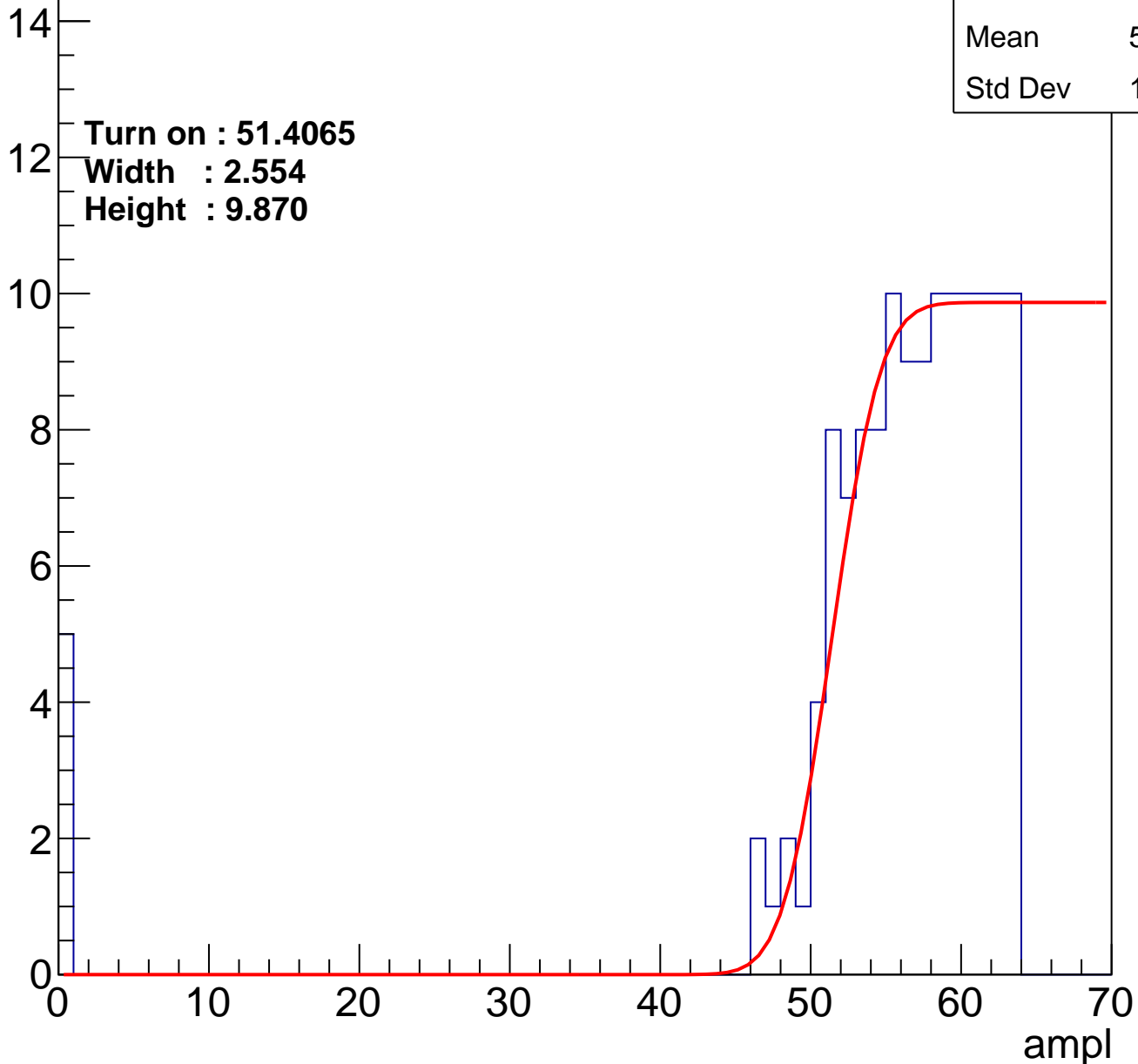
Entries	134
Mean	54.54
Std Dev	11.53

Turn on : 51.4065

Width : 2.554

Height : 9.870

Entry



# B0L103S, U9-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.37
Std Dev	9.433

Turn on : 51.7172

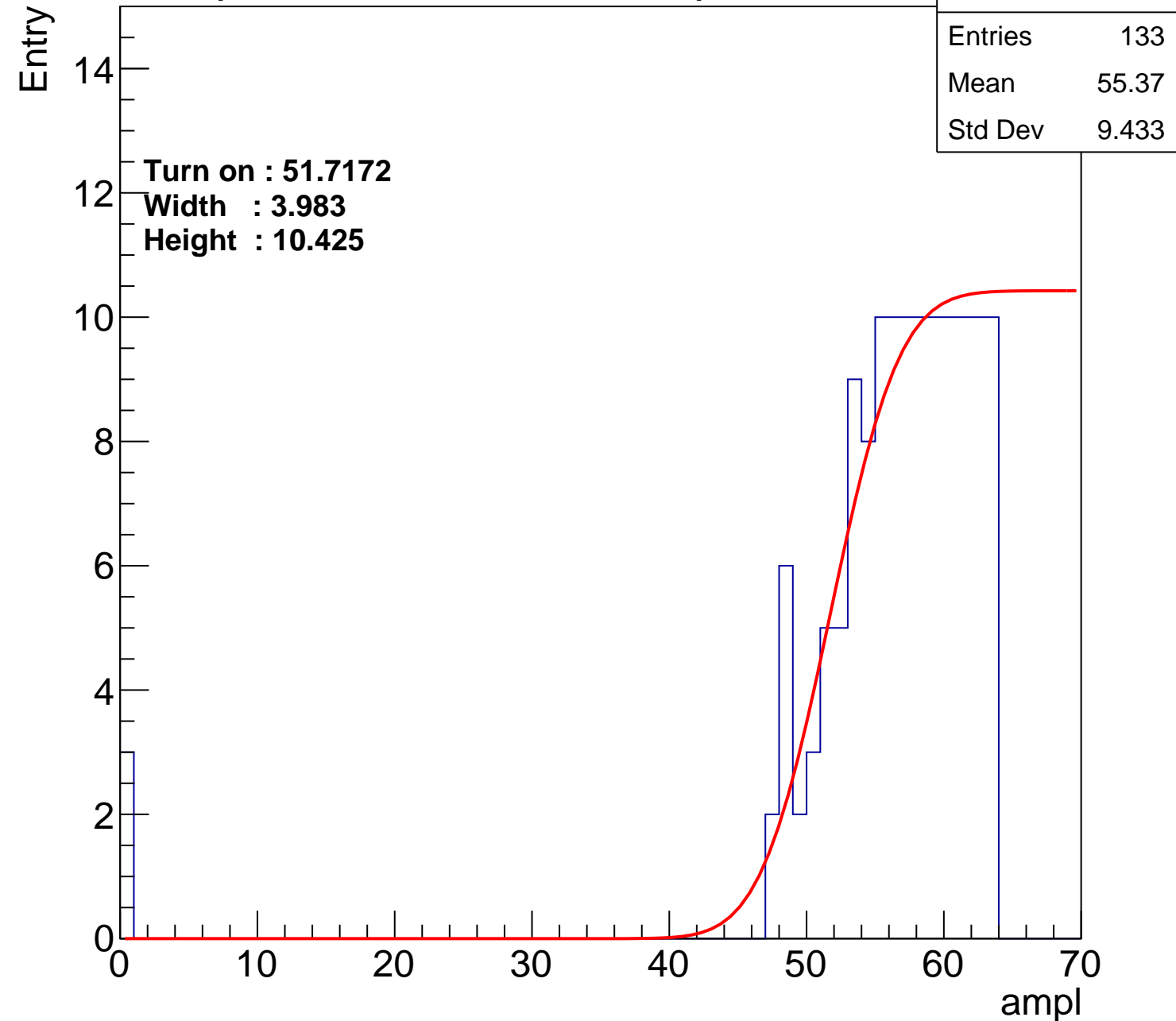
Width : 3.983

Height : 10.425

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

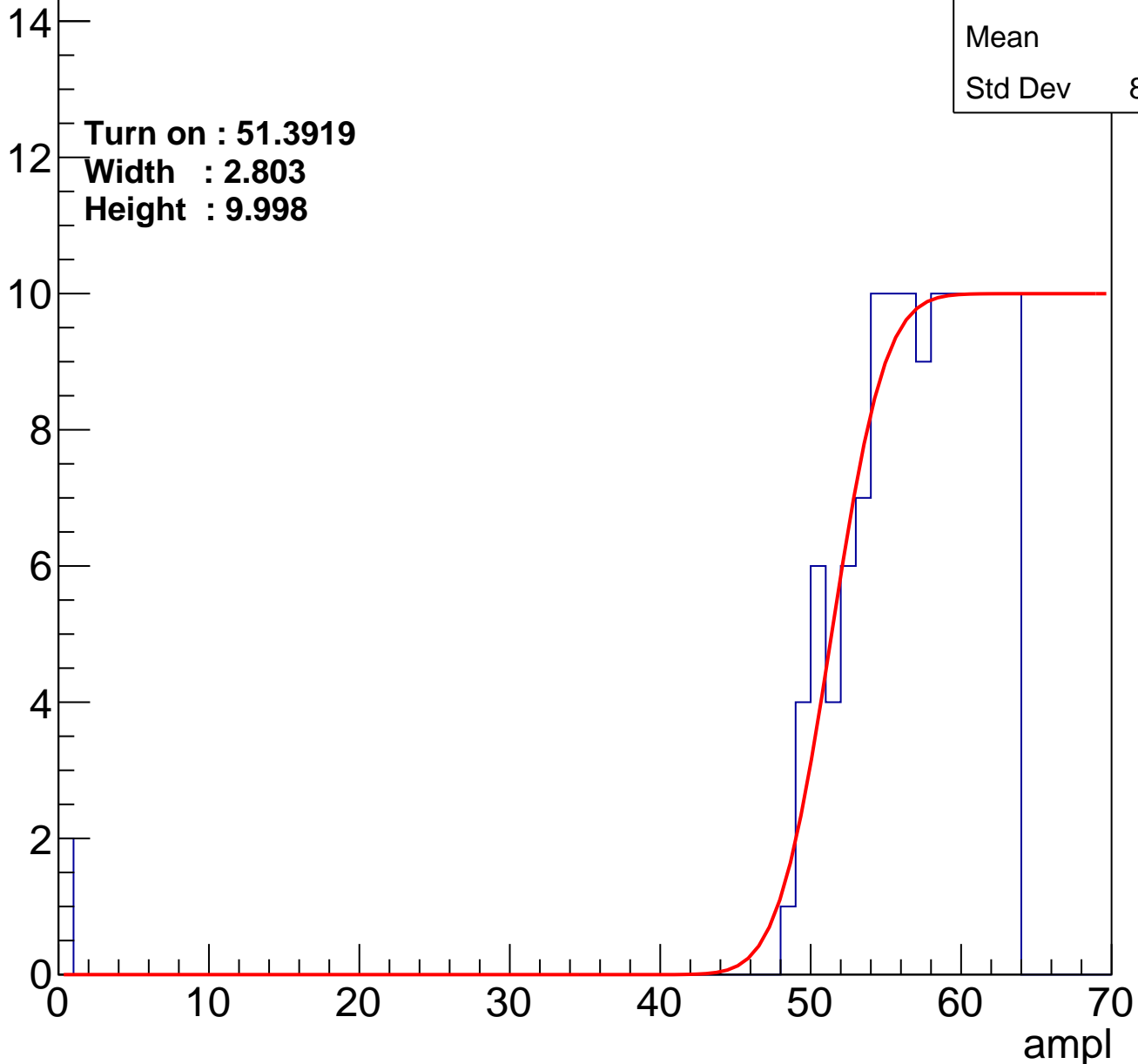
Entries	129
Mean	56
Std Dev	8.096

Turn on : 51.3919

Width : 2.803

Height : 9.998

Entry



# B0L103S, U9-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	54.89
Std Dev	12.22

Turn on : 52.5610

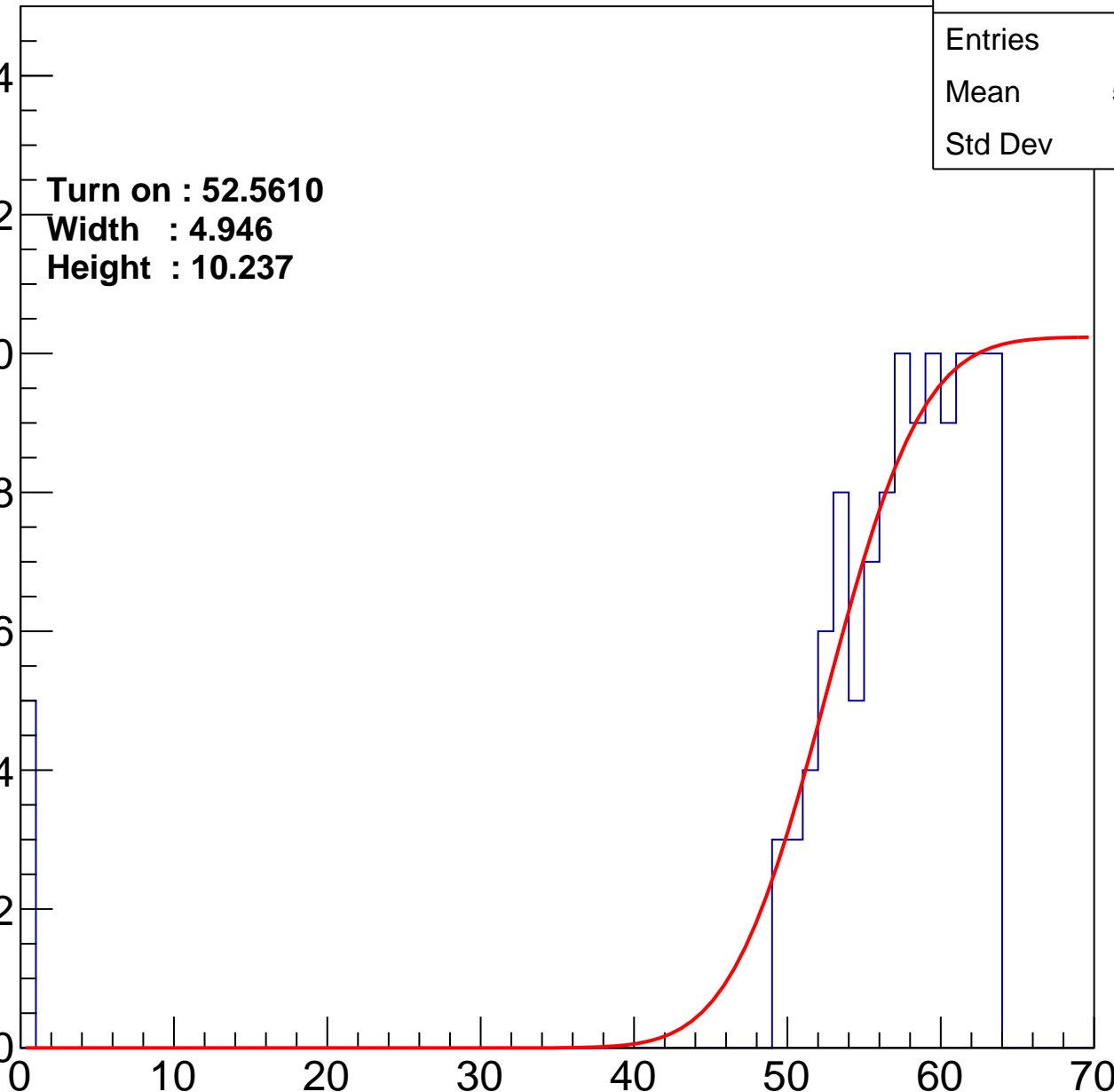
Width : 4.946

Height : 10.237

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.34
Std Dev	8.359

Turn on : 53.4936

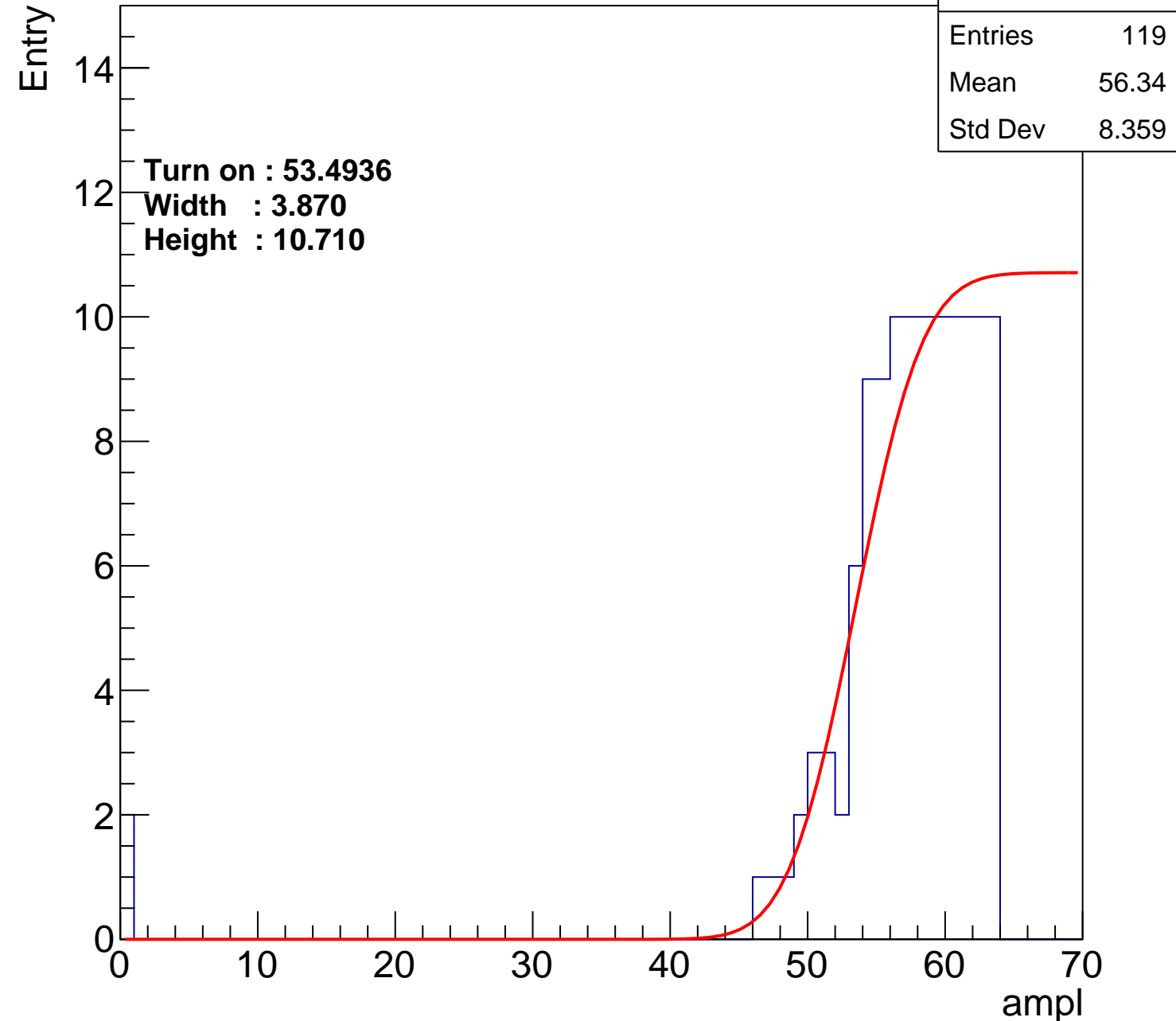
Width : 3.870

Height : 10.710

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

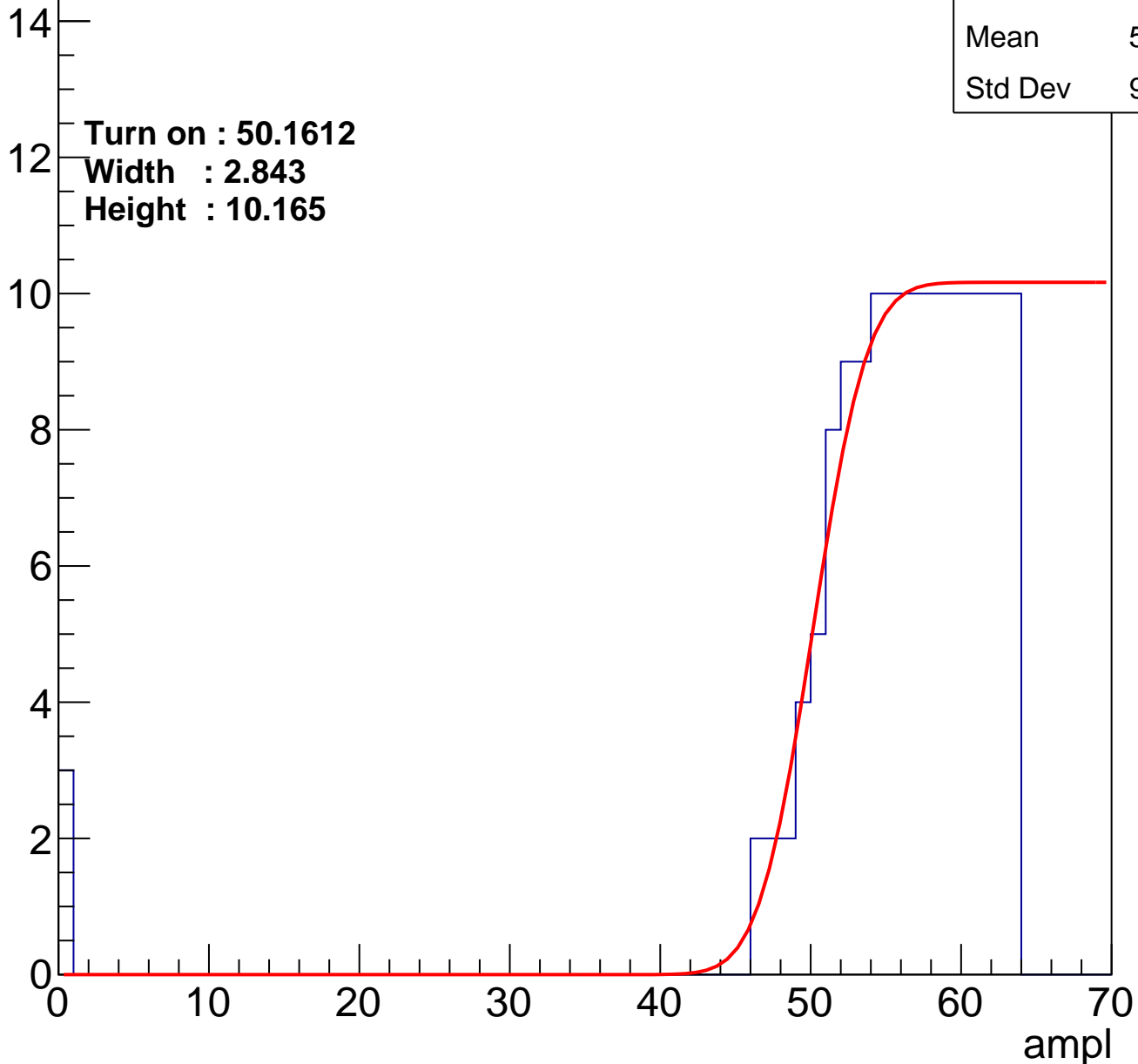
Entries	144
Mean	55.08
Std Dev	9.139

Turn on : 50.1612

Width : 2.843

Height : 10.165

Entry



# B0L103S, U9-ch117

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.77
Std Dev	9.82

Turn on : 52.9824

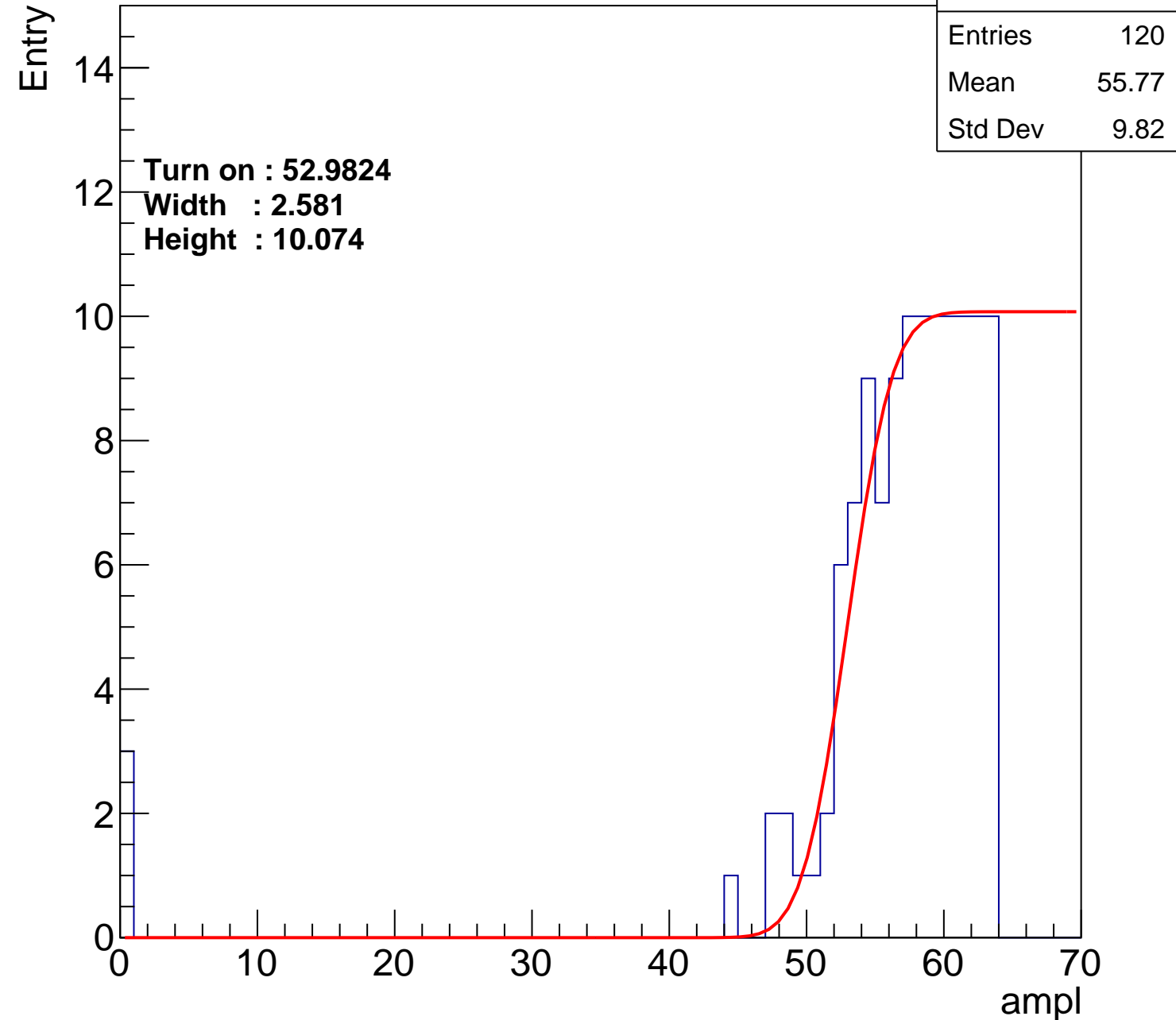
Width : 2.581

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	55.6
Std Dev	7.963

Turn on : 50.2814

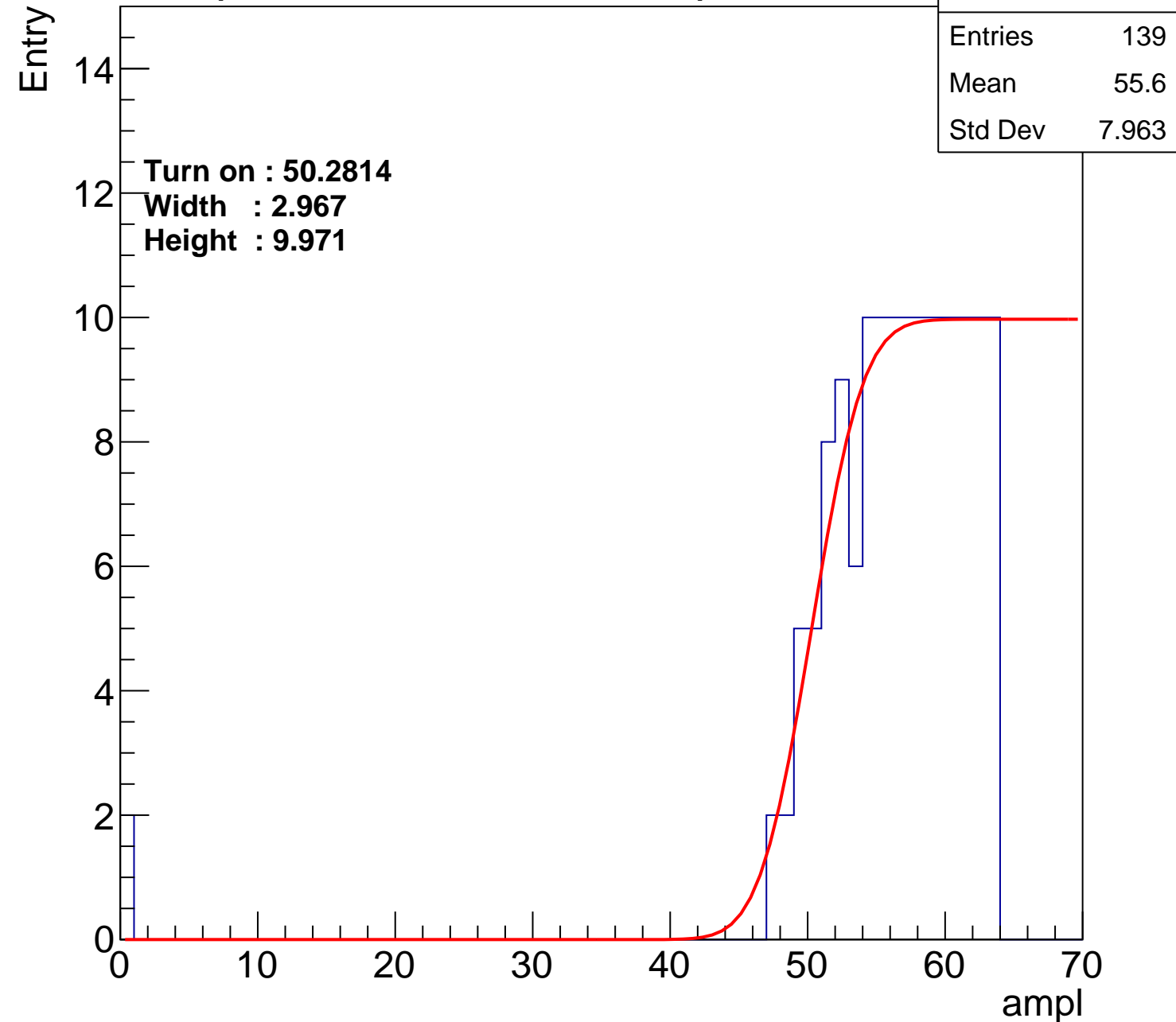
Width : 2.967

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U9-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

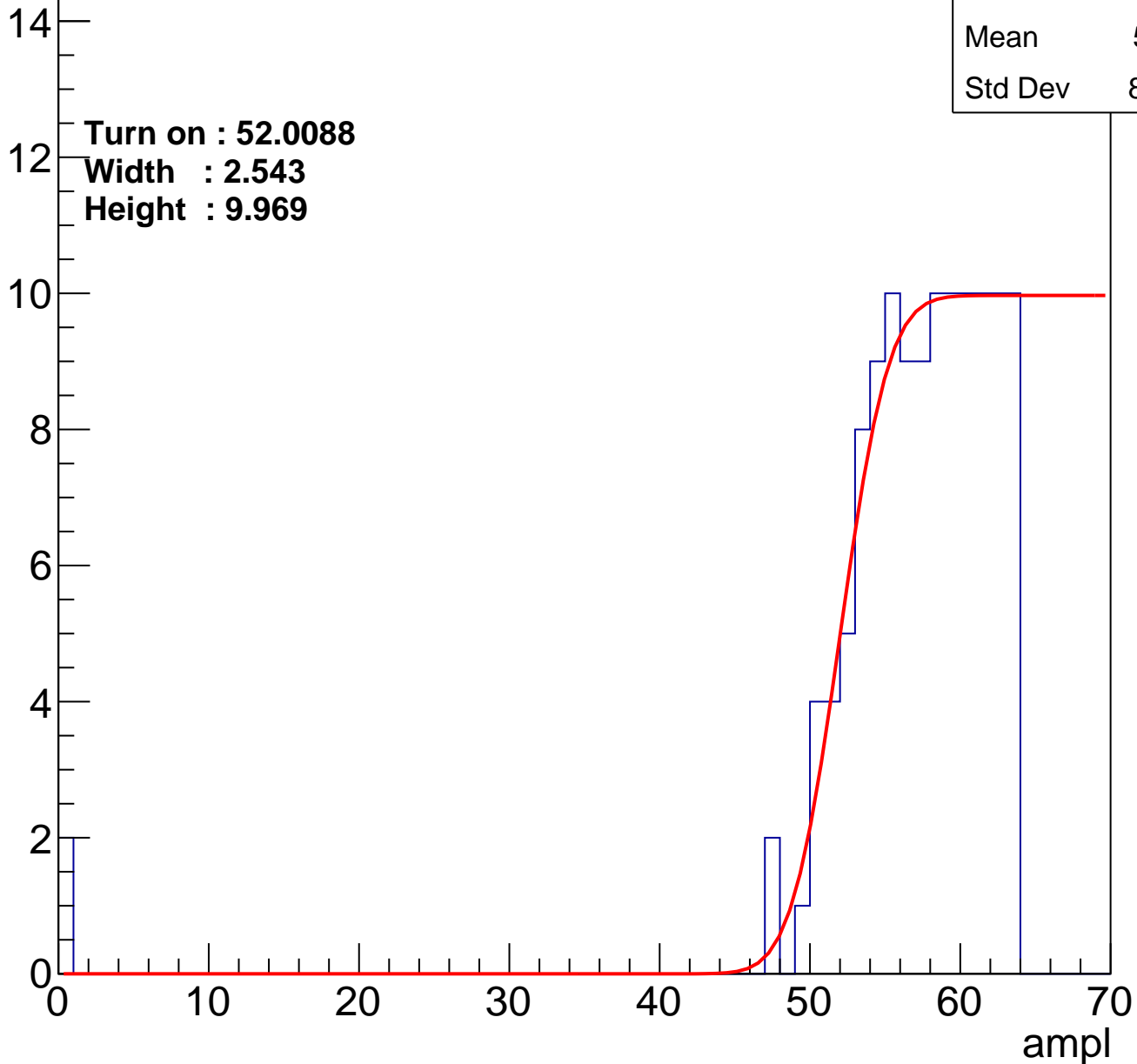
Entries	123
Mean	56.21
Std Dev	8.224

Turn on : 52.0088

Width : 2.543

Height : 9.969

Entry



# B0L103S, U9-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

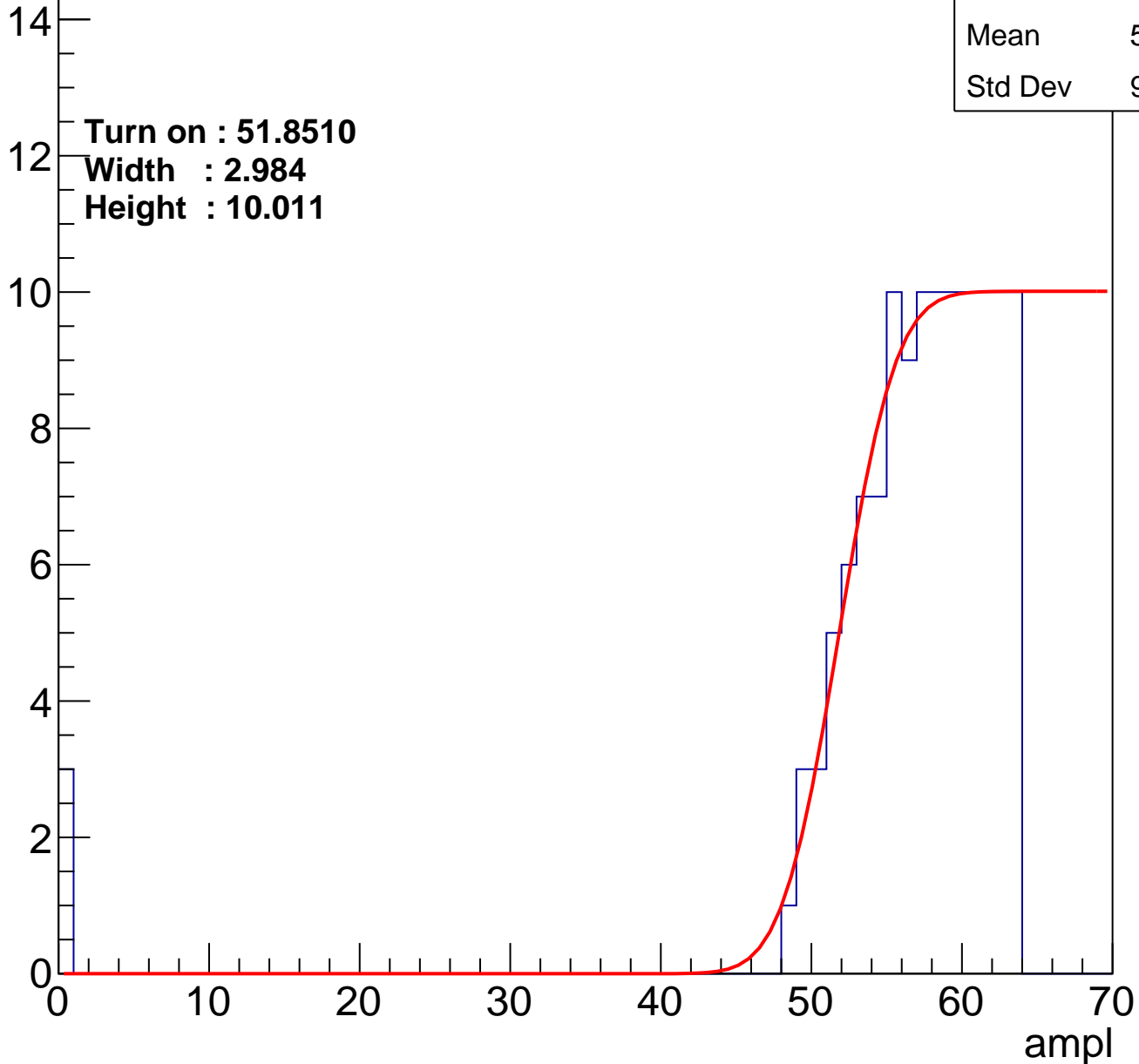
Entries	124
Mean	55.77
Std Dev	9.606

Turn on : 51.8510

Width : 2.984

Height : 10.011

Entry



# B0L103S, U9-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	53.57
Std Dev	14.84

**Turn on : 52.4803**

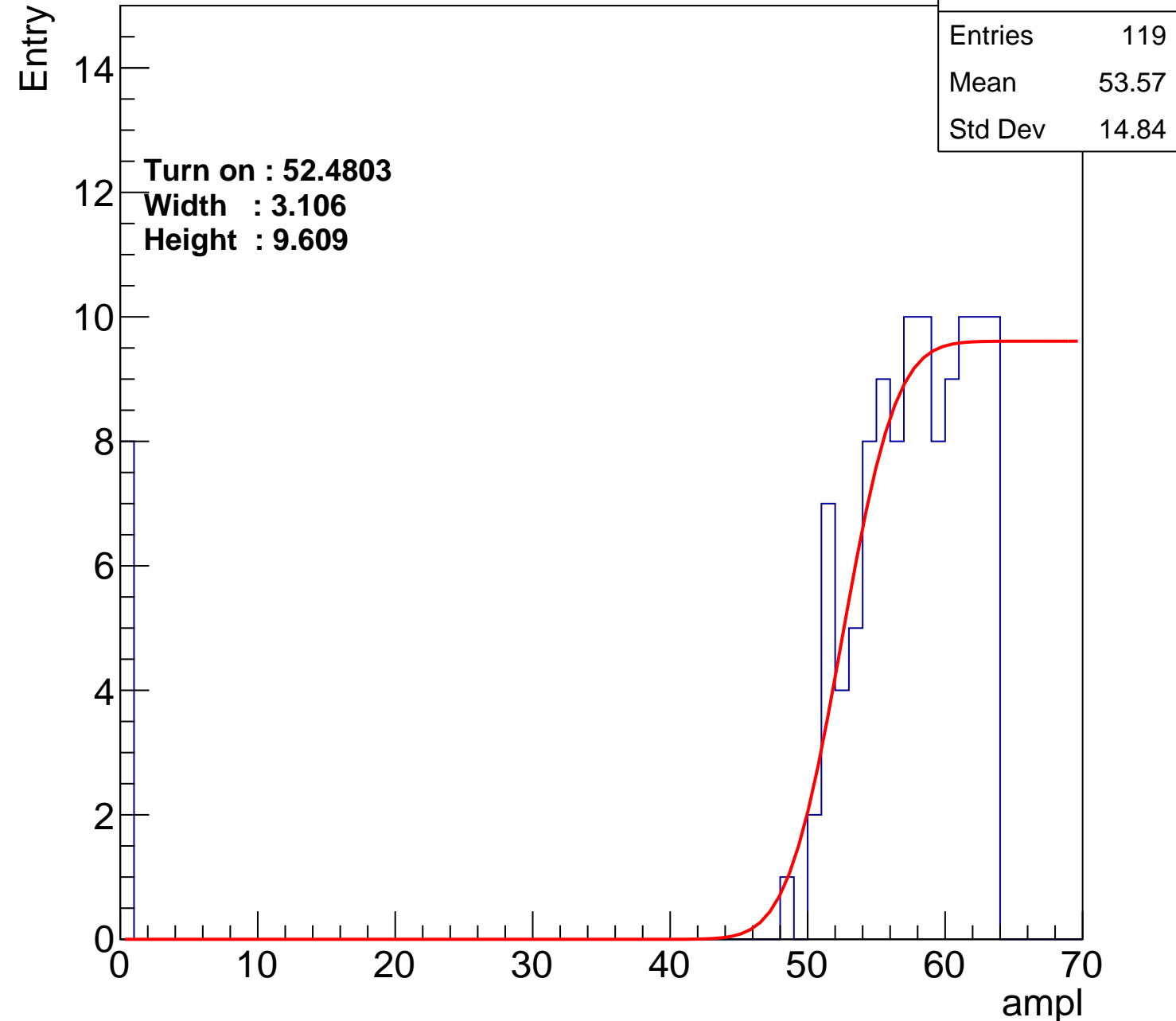
**Width : 3.106**

**Height : 9.609**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

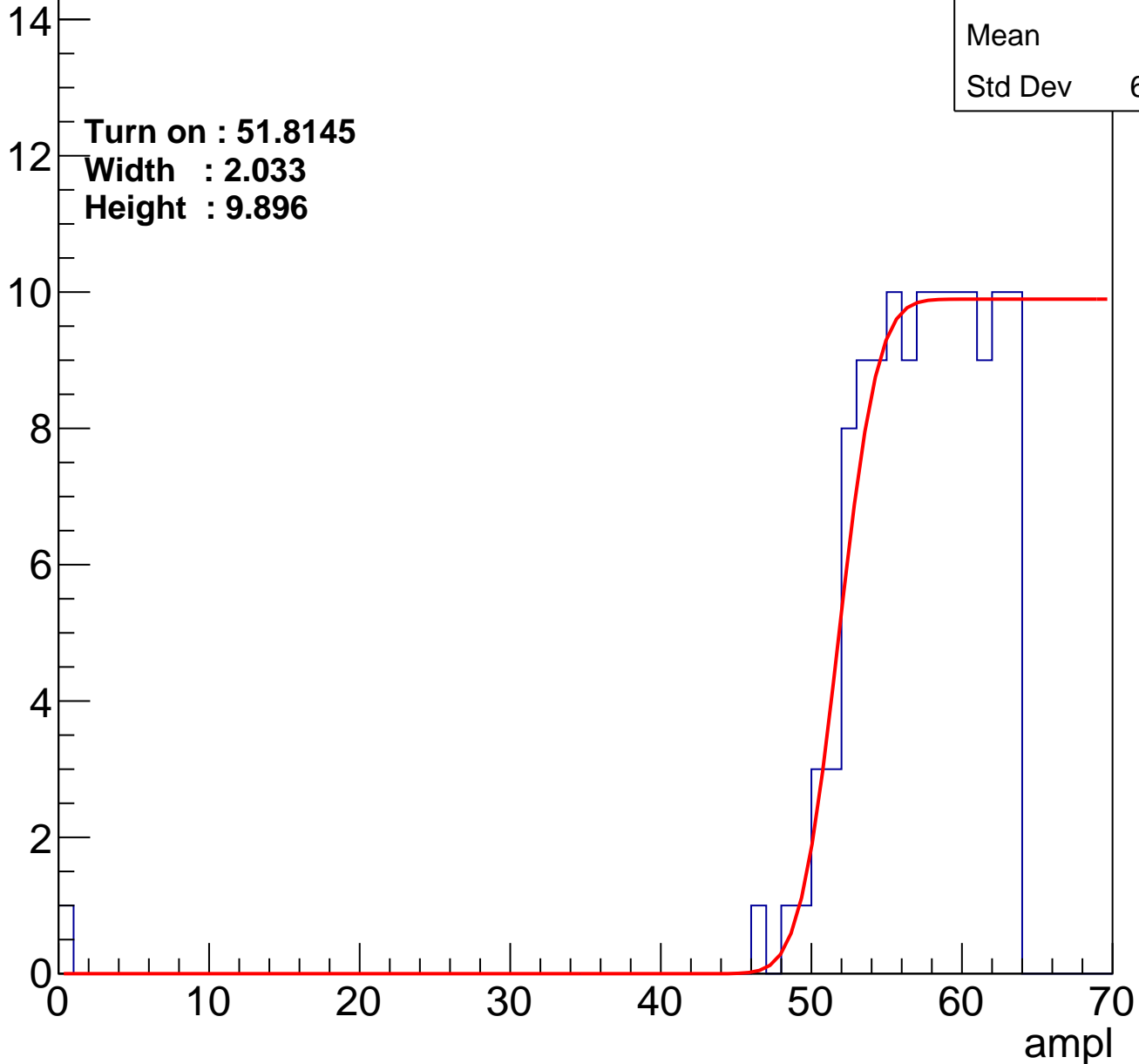
Entries	124
Mean	56.6
Std Dev	6.424

Turn on : 51.8145

Width : 2.033

Height : 9.896

Entry



# B0L103S, U9-ch123

calib\_packv5\_040323\_1717.root, FC#2, port C3

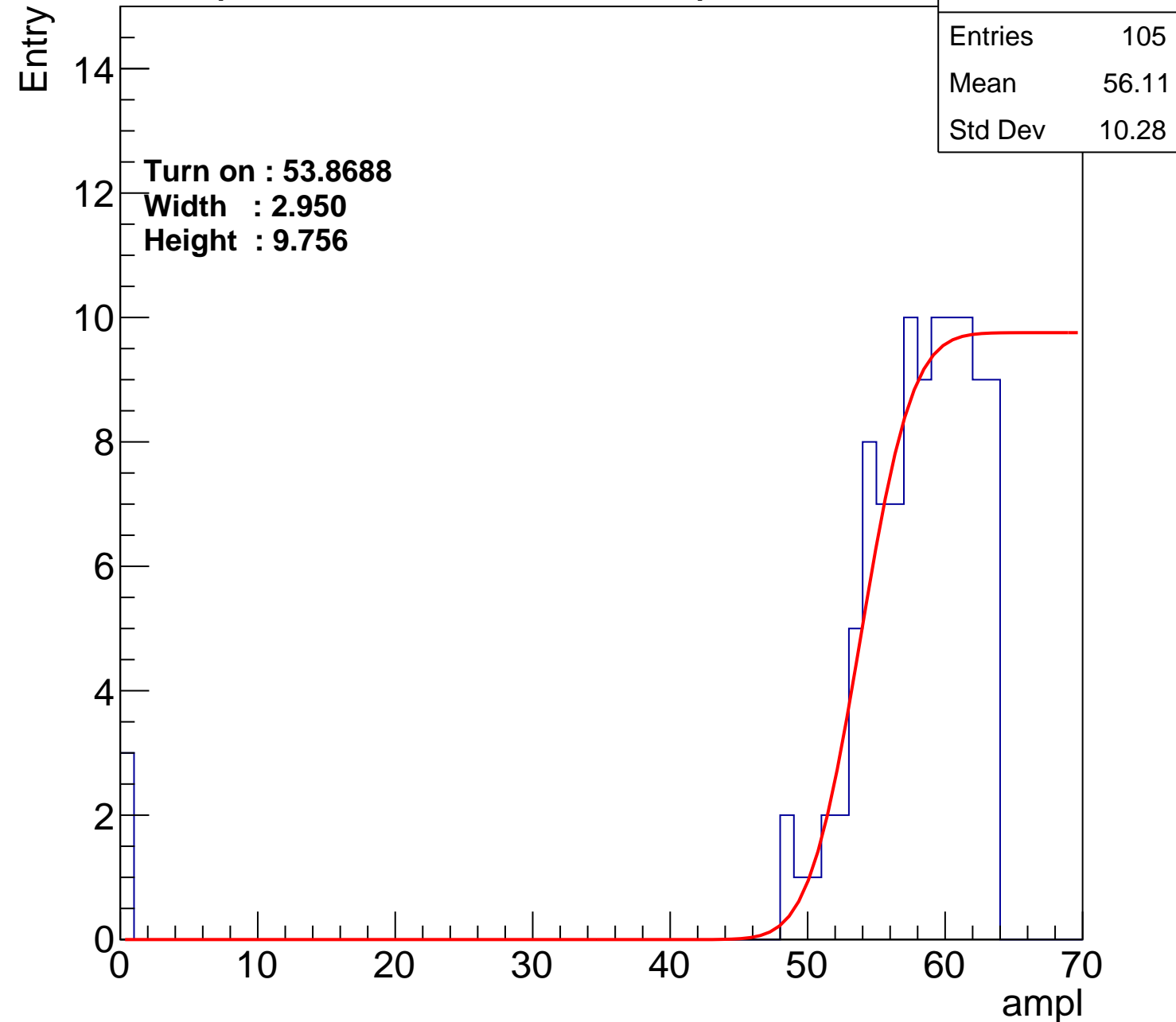
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 53.8688  
Width : 2.950  
Height : 9.756

Entries	105
Mean	56.11
Std Dev	10.28

ampl



# B0L103S, U9-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.65
Std Dev	10.22

Turn on : 51.0508

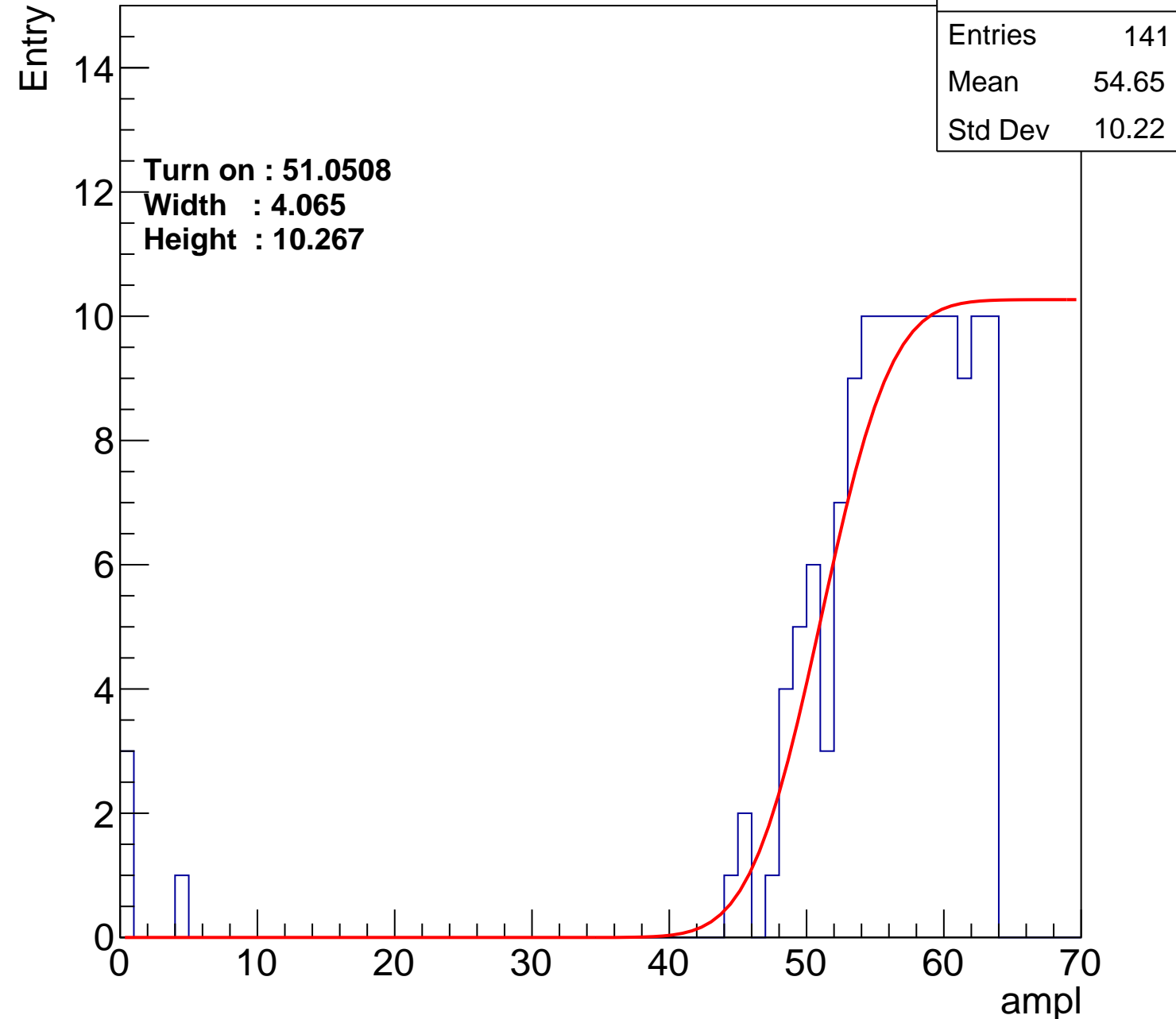
Width : 4.065

Height : 10.267

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch125

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.24
Std Dev	11.08

Turn on : 51.8324

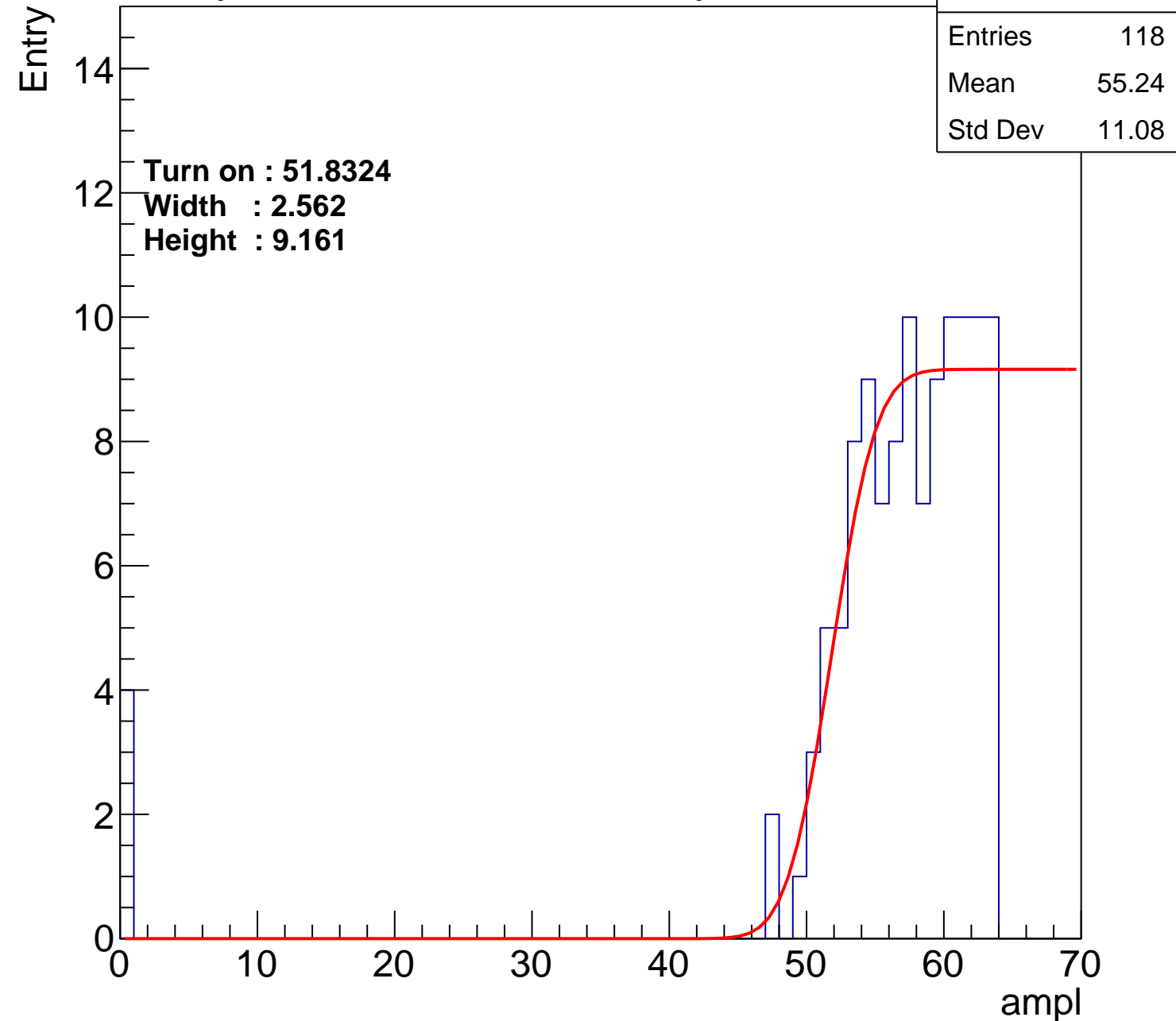
Width : 2.562

Height : 9.161

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

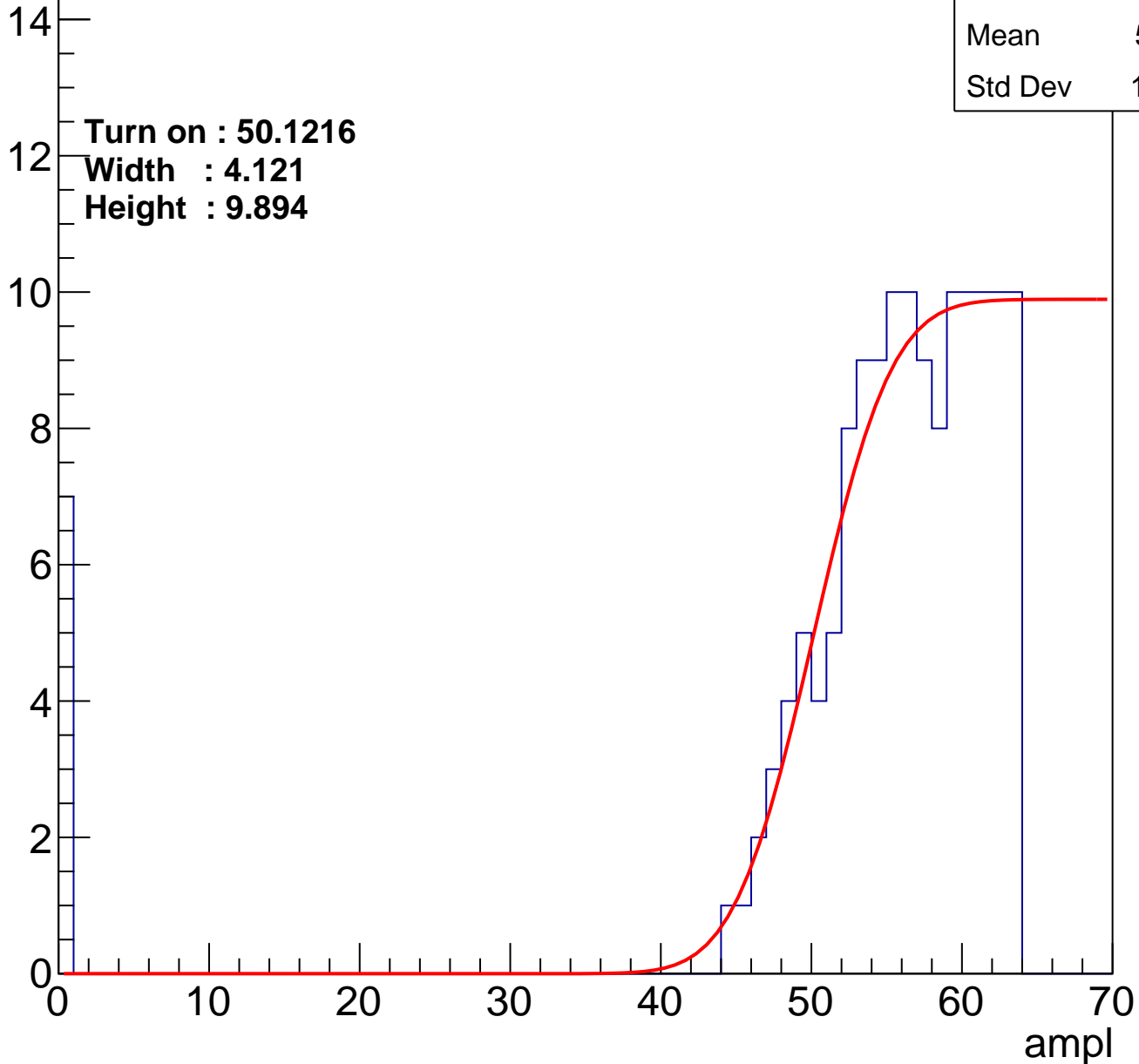
Entries	145
Mean	53.31
Std Dev	12.87

Turn on : 50.1216

Width : 4.121

Height : 9.894

Entry





# B0L103S, U9-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	56.01
Std Dev	8.152

**Turn on : 51.6100**

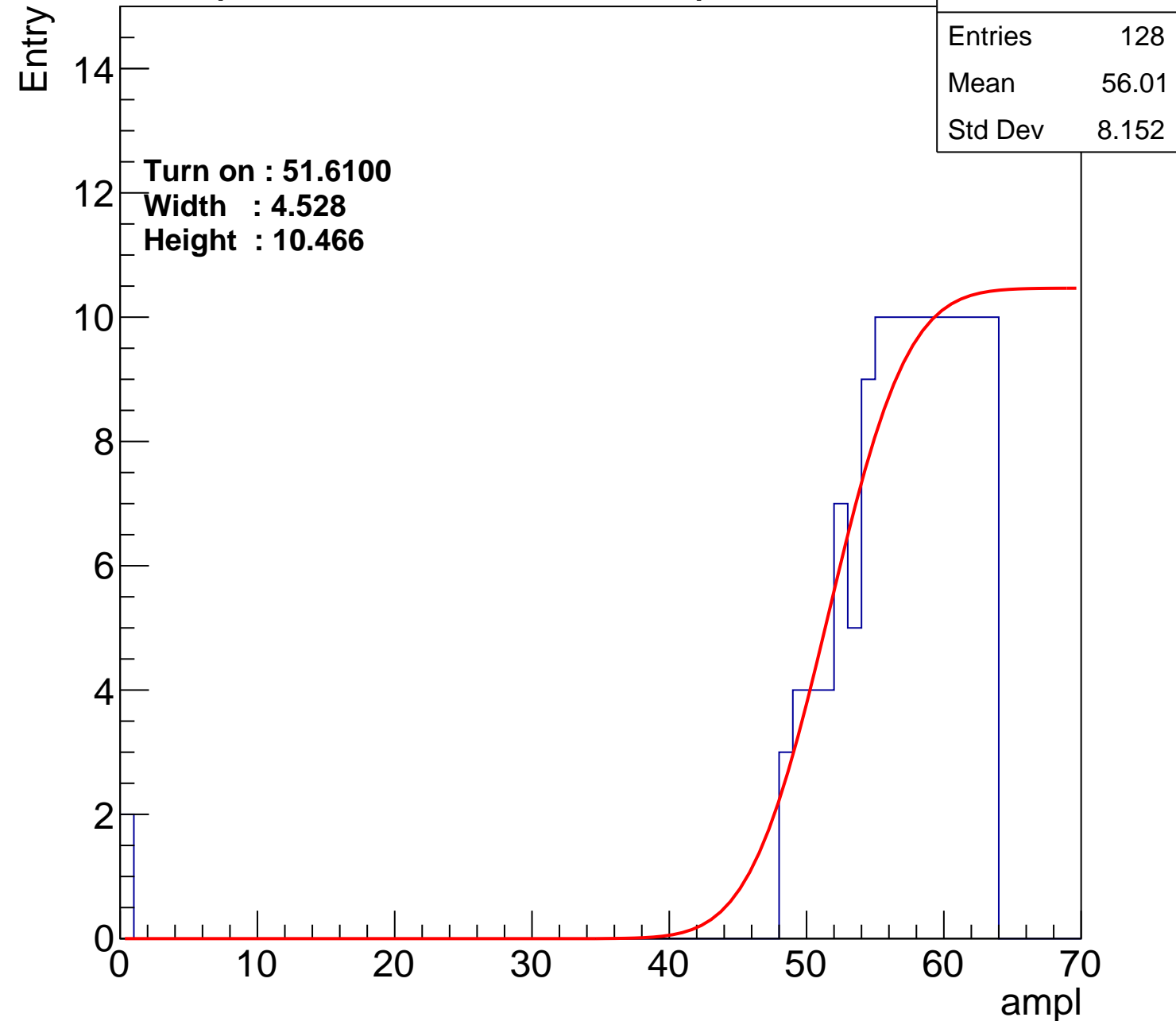
**Width : 4.528**

**Height : 10.466**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U9-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	56.01
Std Dev	8.152

**Turn on : 51.6100**

**Width : 4.528**

**Height : 10.466**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

