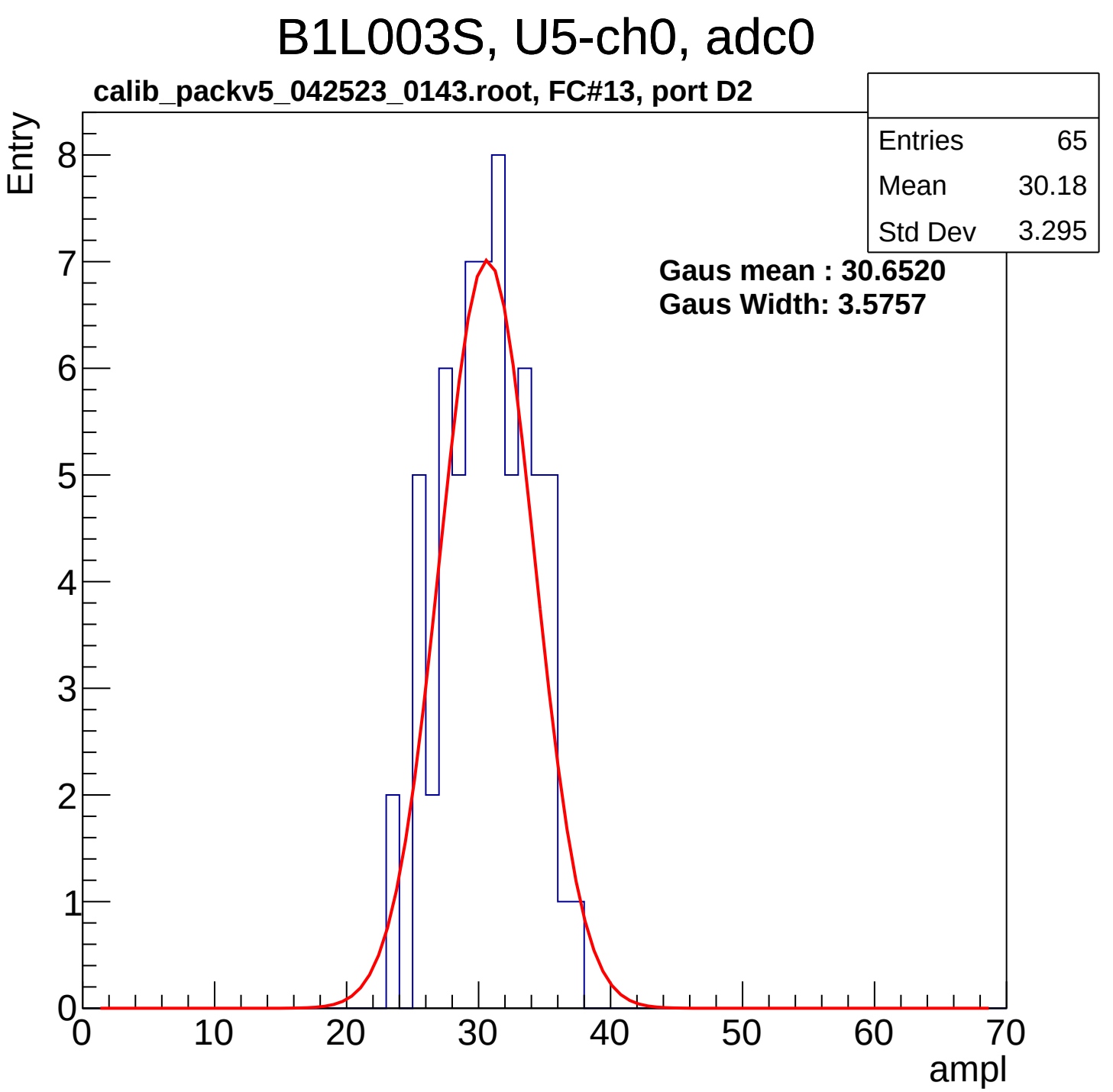
B1L003S, U5-ch0, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.18
Std Dev 3.295
Gaus mean : 30.6520
Gaus Width: 3.5757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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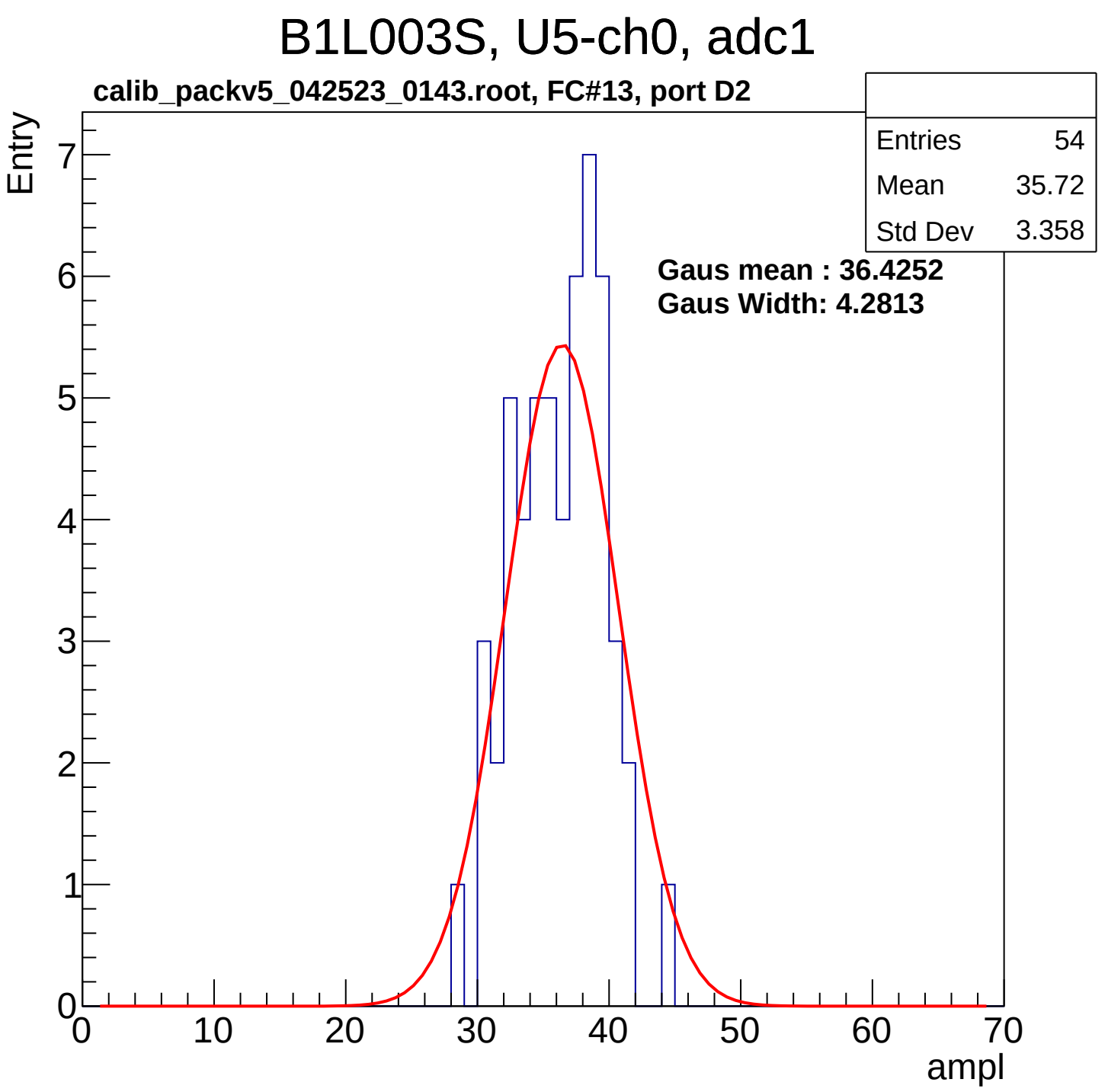
B1L003S, U5-ch0, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.72
Std Dev 3.358
Gaus mean : 36.4252
Gaus Width: 4.2813
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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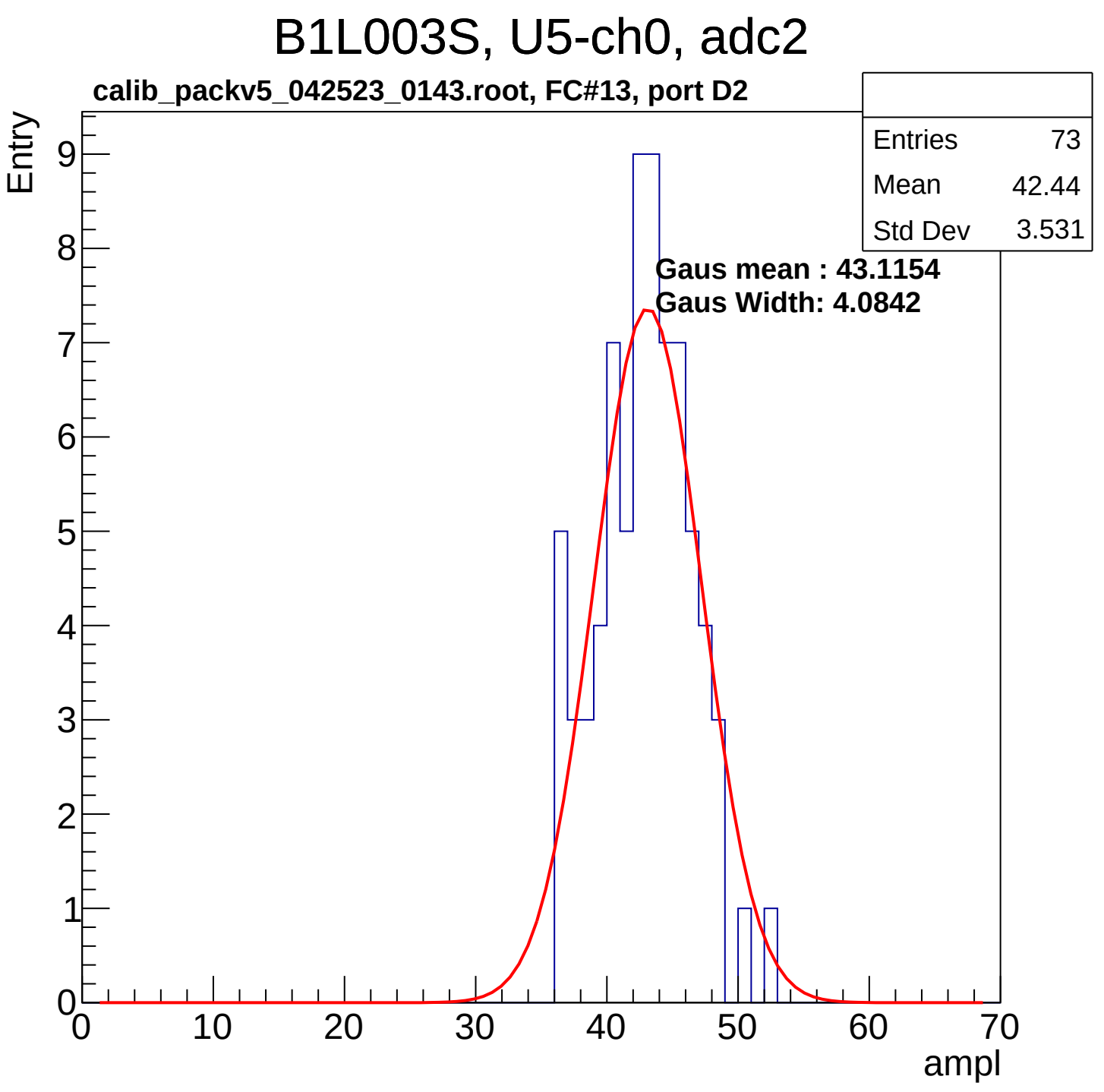
B1L003S, U5-ch0, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.44
Std Dev 3.531
Gaus mean : 43.1154
Gaus Width: 4.0842
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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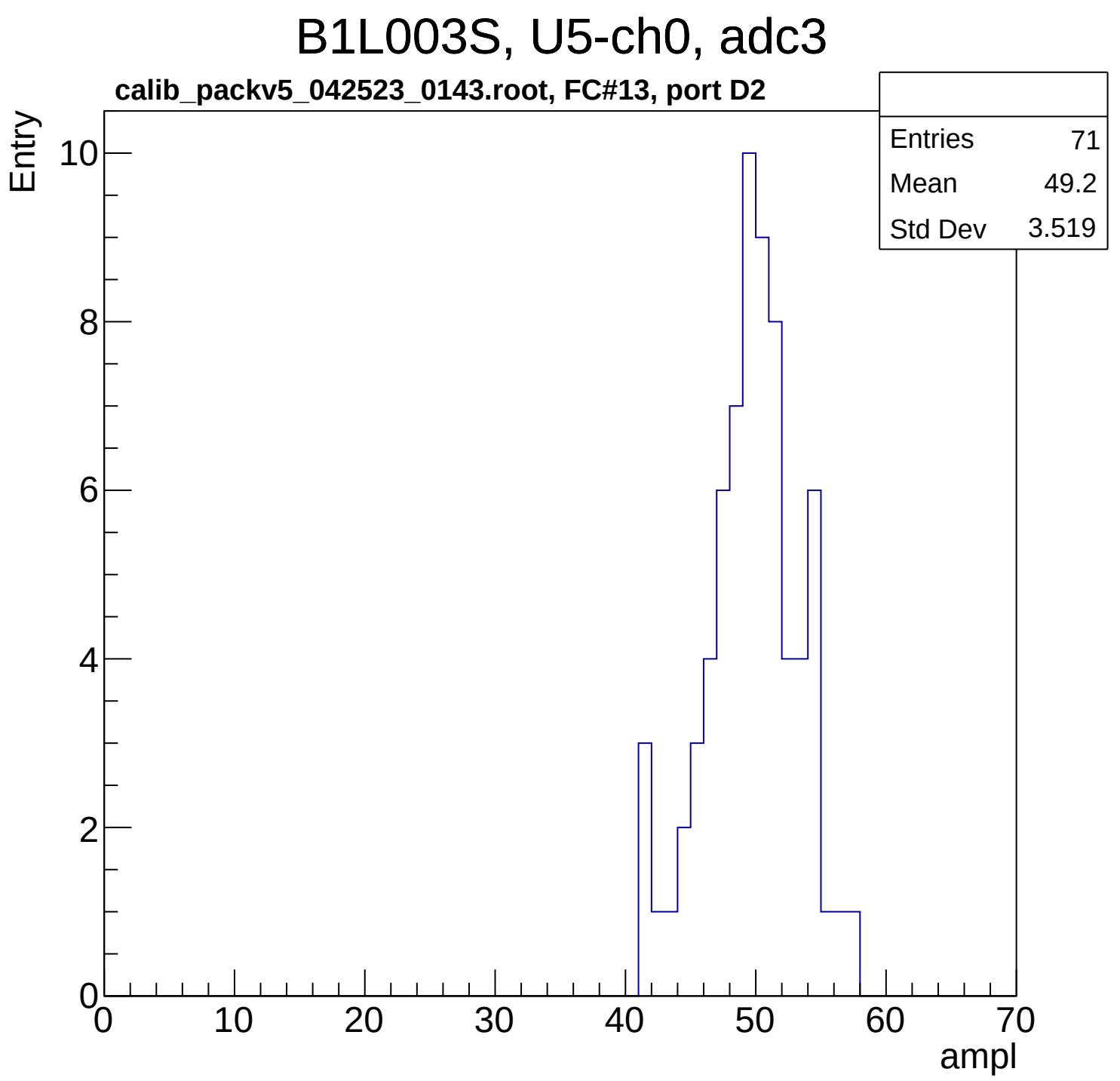
B1L003S, U5-ch0, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.2
Std Dev 3.519
Entry
0
2
4
6
8
10
0
10
20
30
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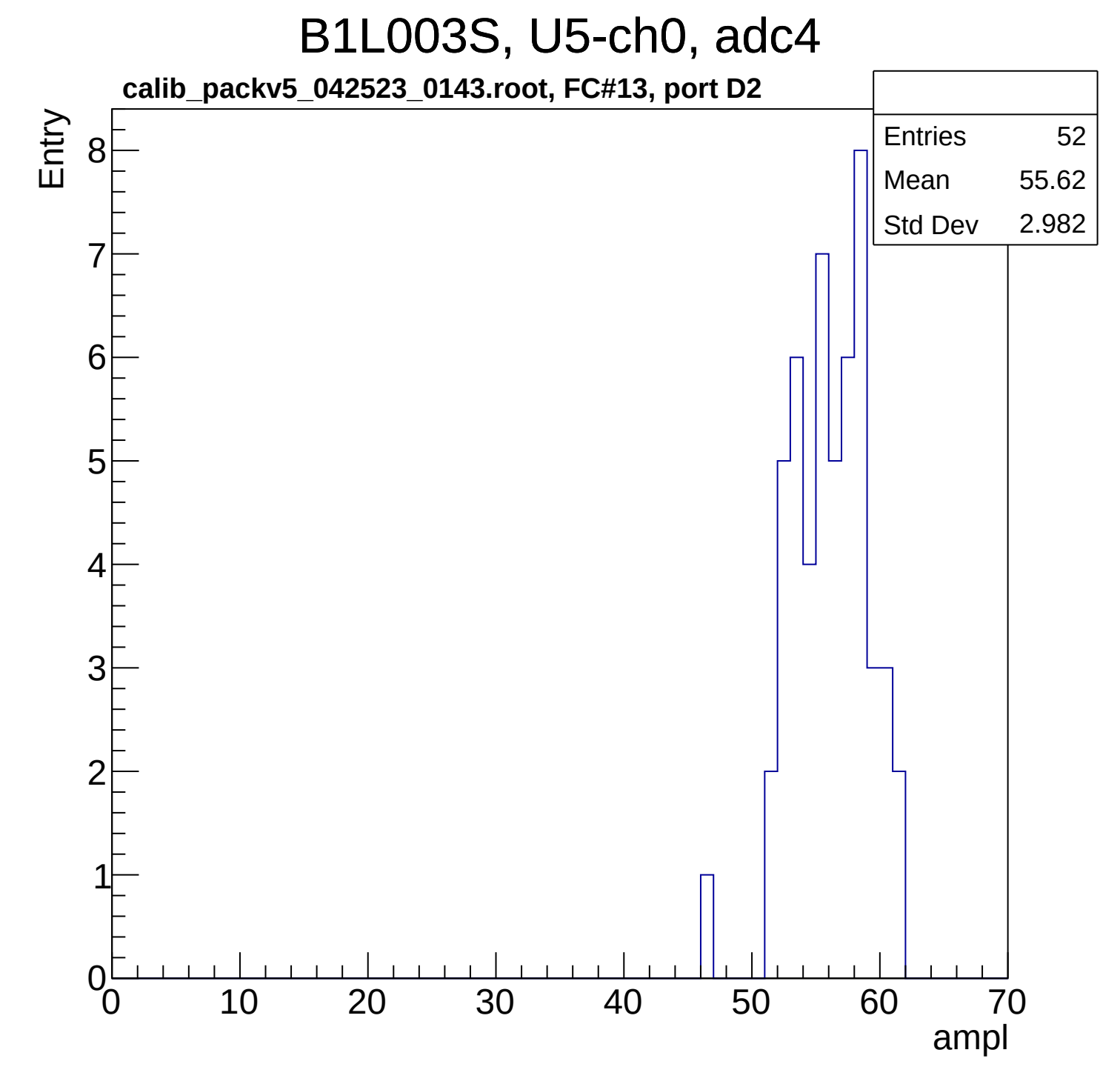
50
60
70
ampl


B1L003S, U5-ch0, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.62
Std Dev 2.982
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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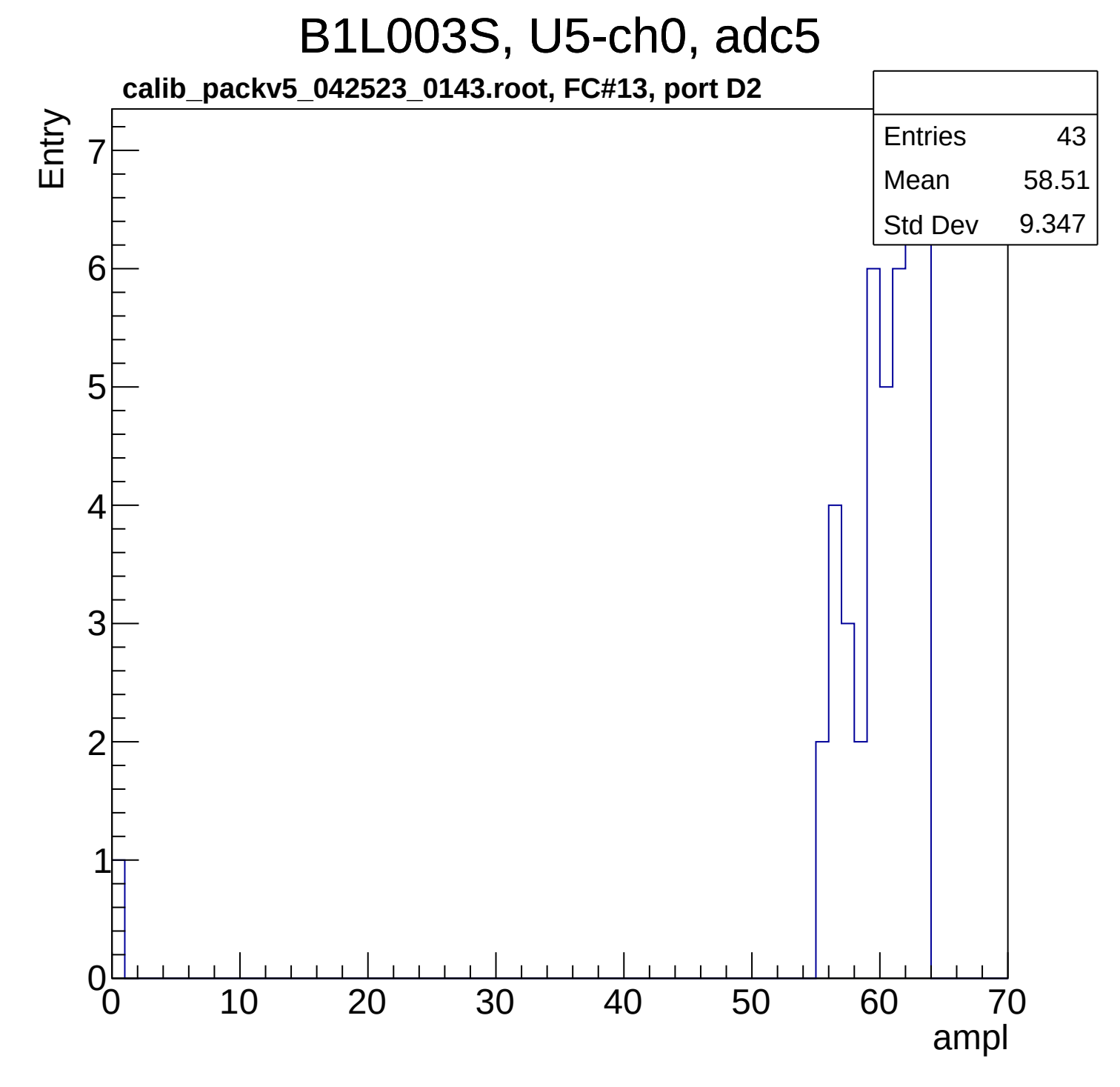
B1L003S, U5-ch0, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.51
Std Dev 9.347
Entry
ampl
0
1
2
3
4
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0
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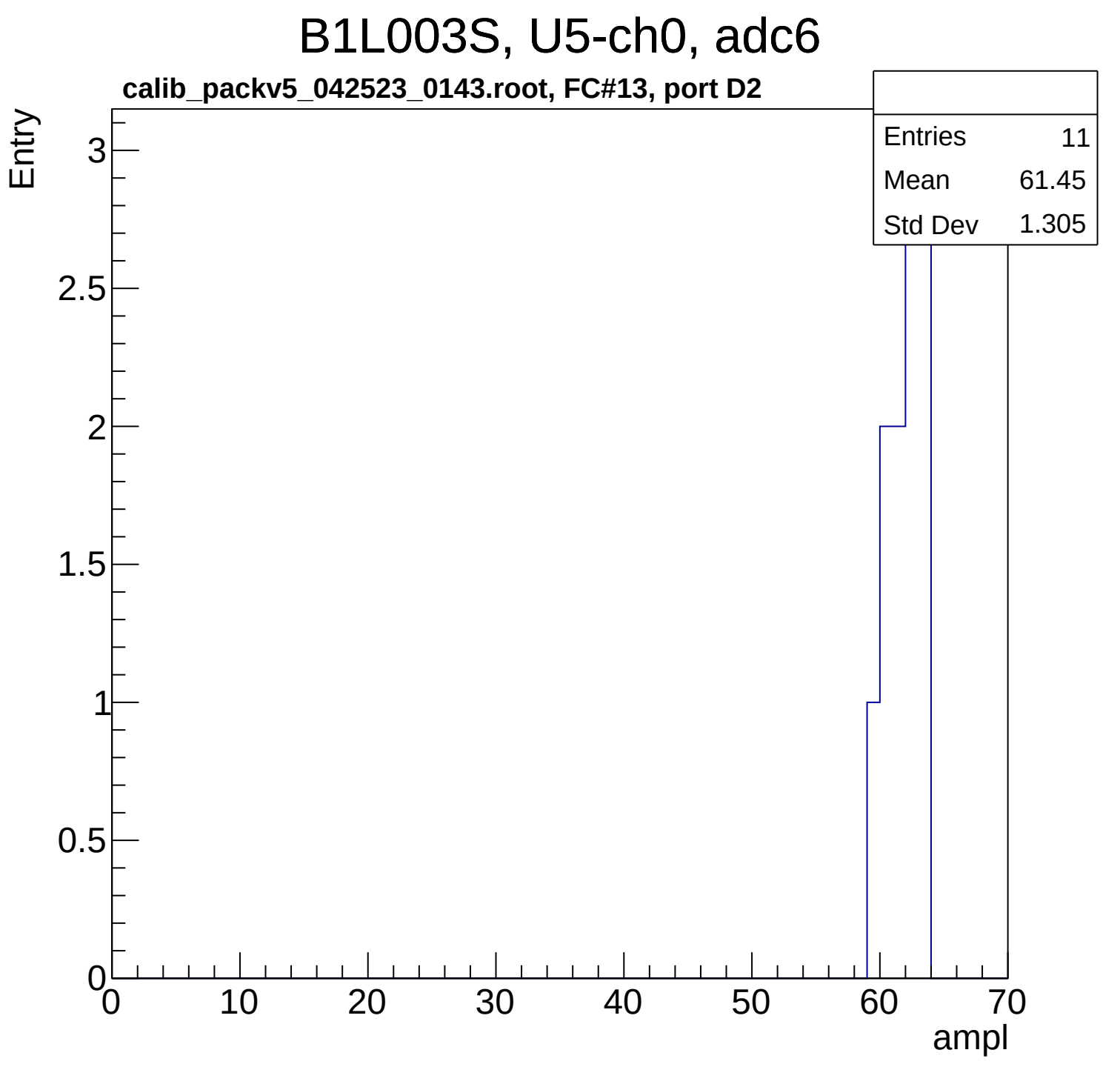
B1L003S, U5-ch0, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.45
Std Dev 1.305
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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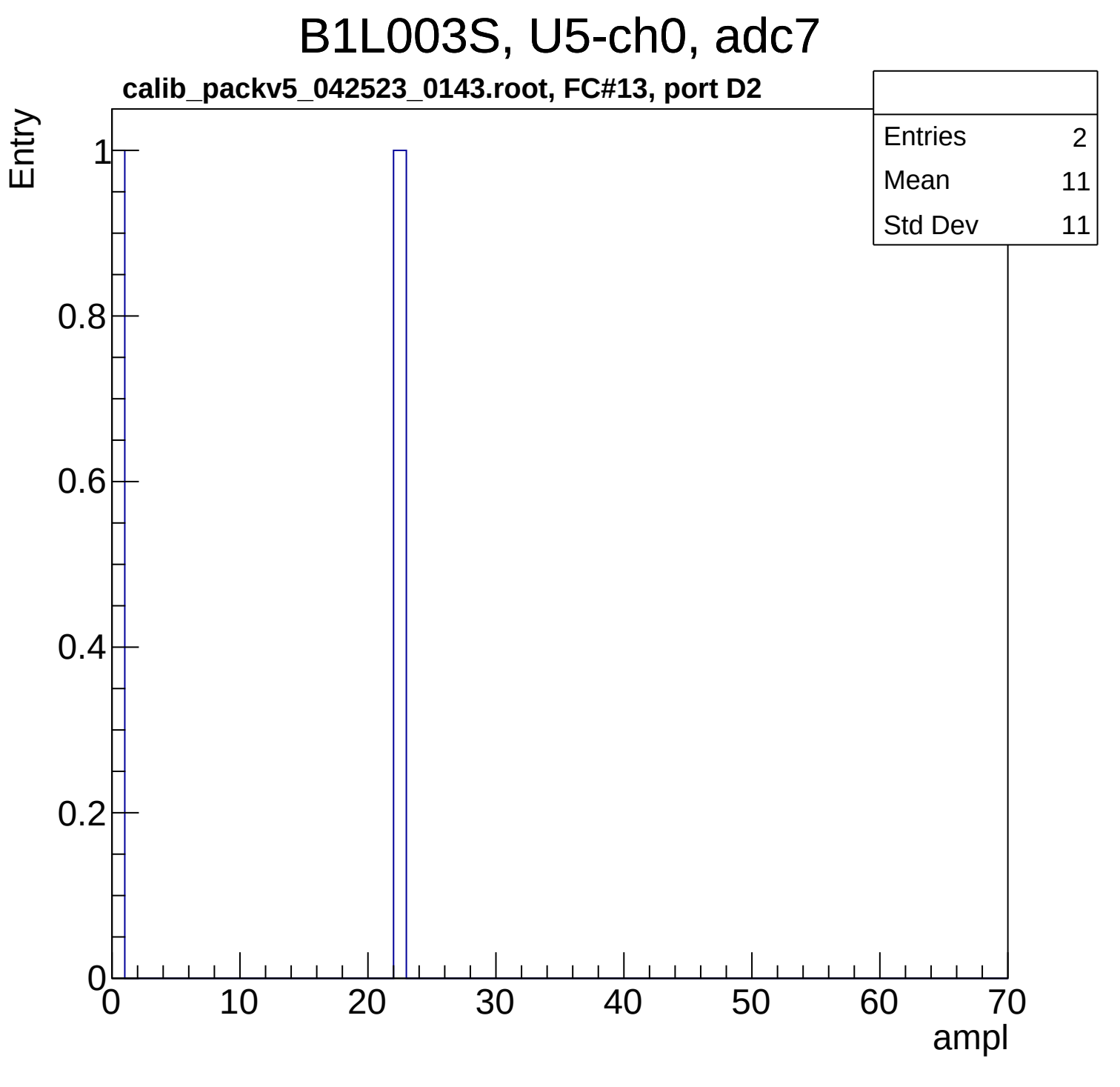
B1L003S, U5-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
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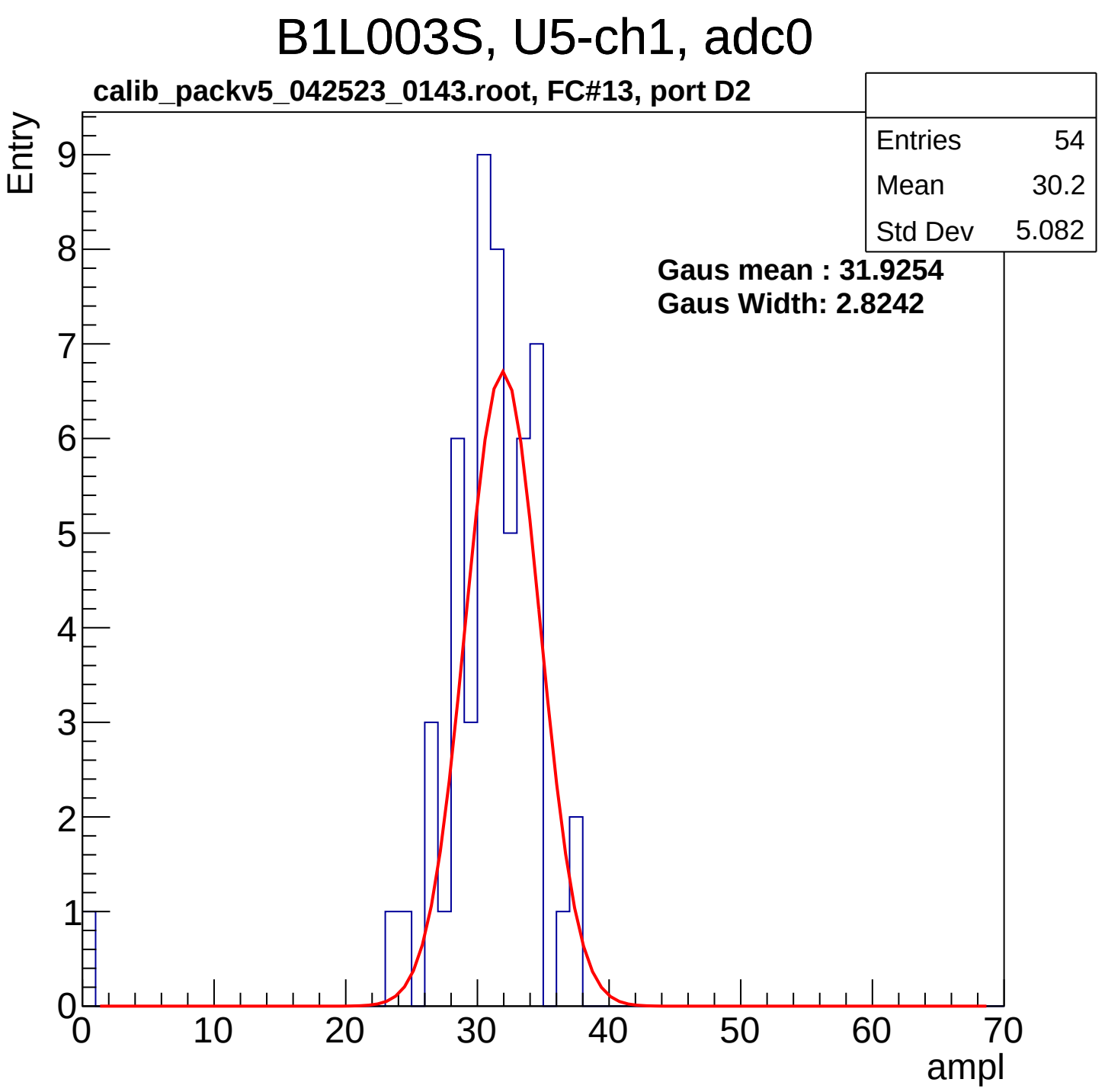
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch1, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 30.2
Std Dev 5.082
Gaus mean : 31.9254
Gaus Width: 2.8242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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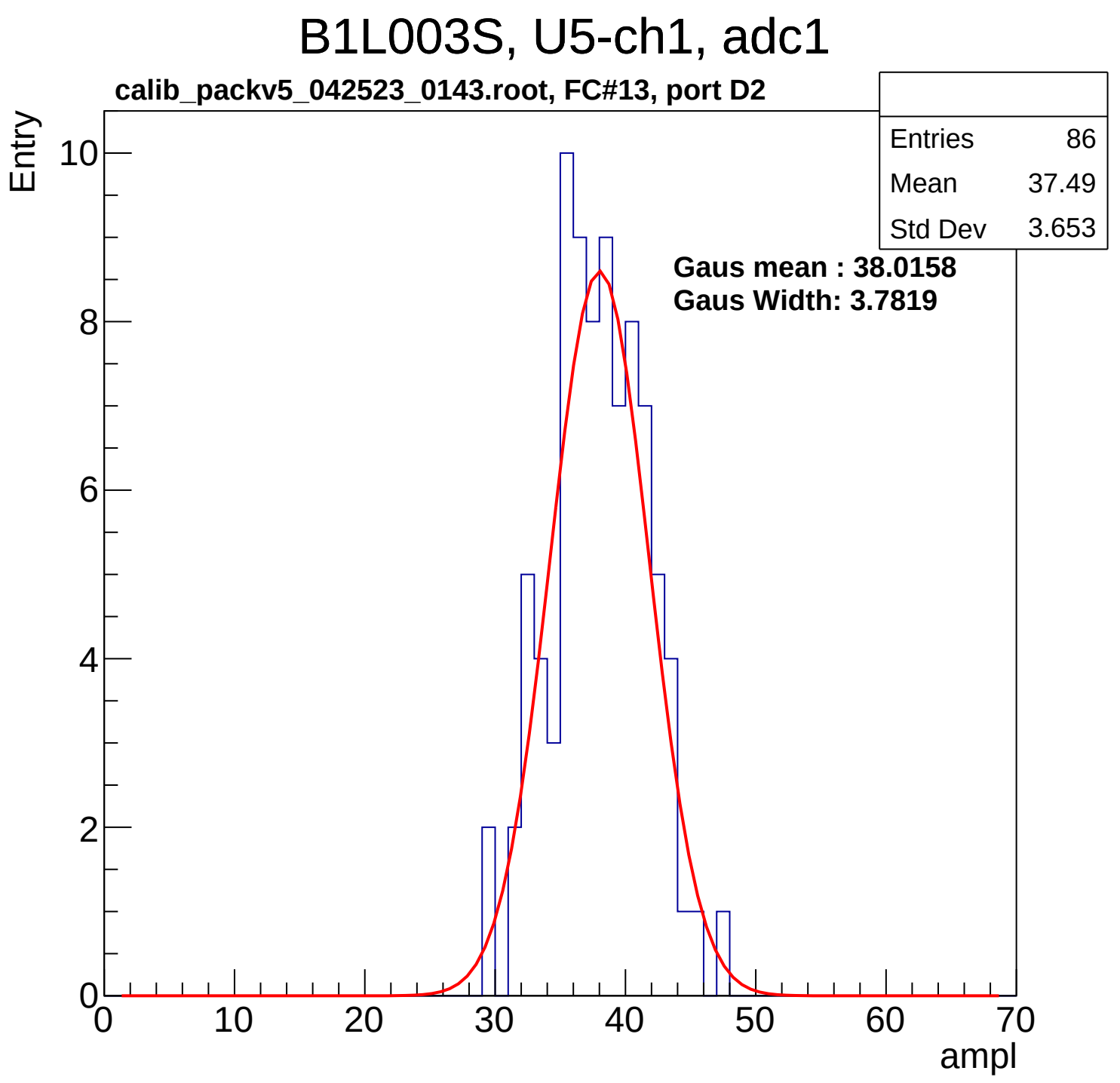
B1L003S, U5-ch1, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 37.49
Std Dev 3.653
Gaus mean : 38.0158
Gaus Width: 3.7819
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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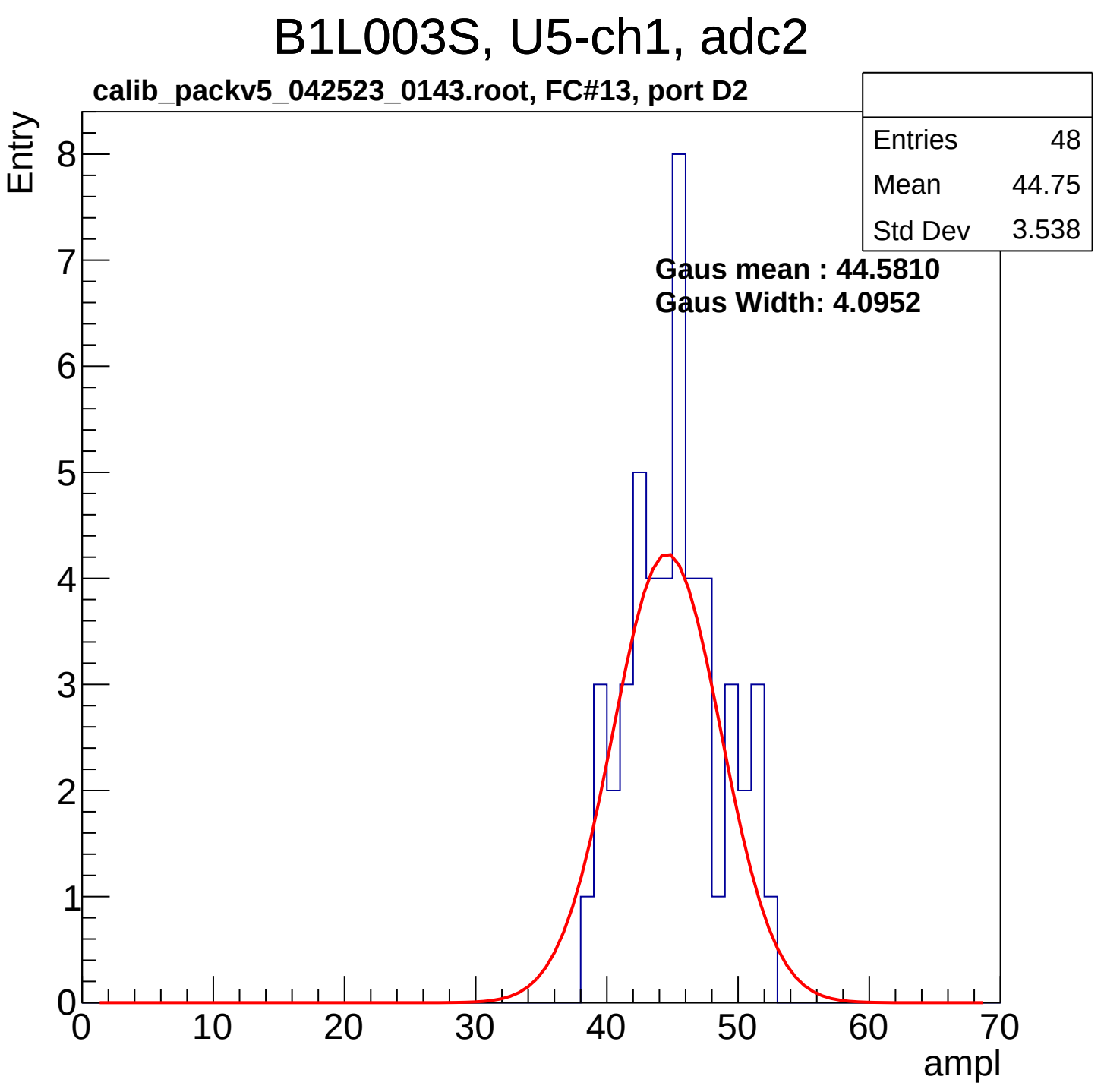
B1L003S, U5-ch1, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 44.75
Std Dev 3.538
Gaus mean : 44.5810
Gaus Width: 4.0952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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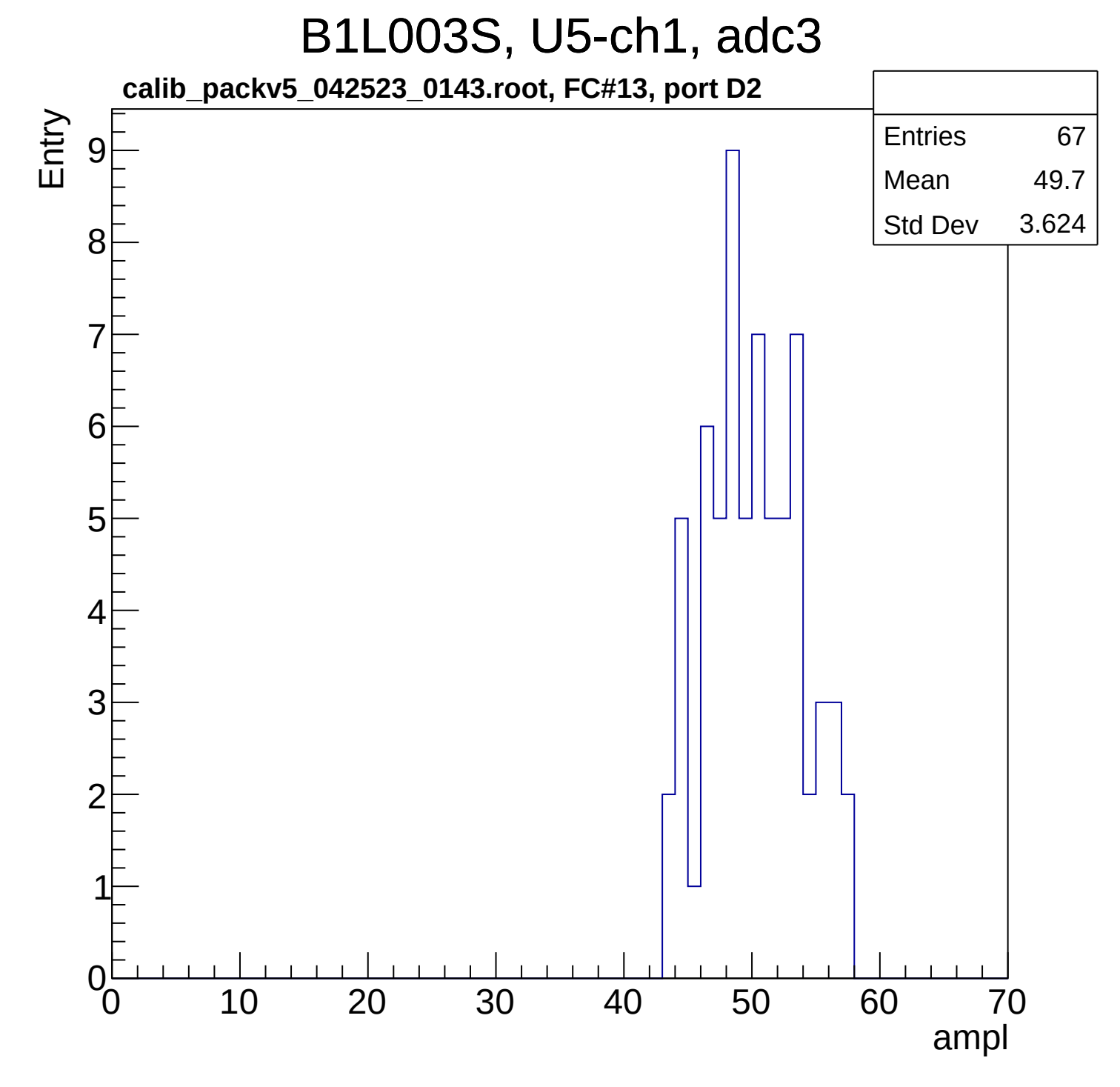
B1L003S, U5-ch1, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.7
Std Dev 3.624
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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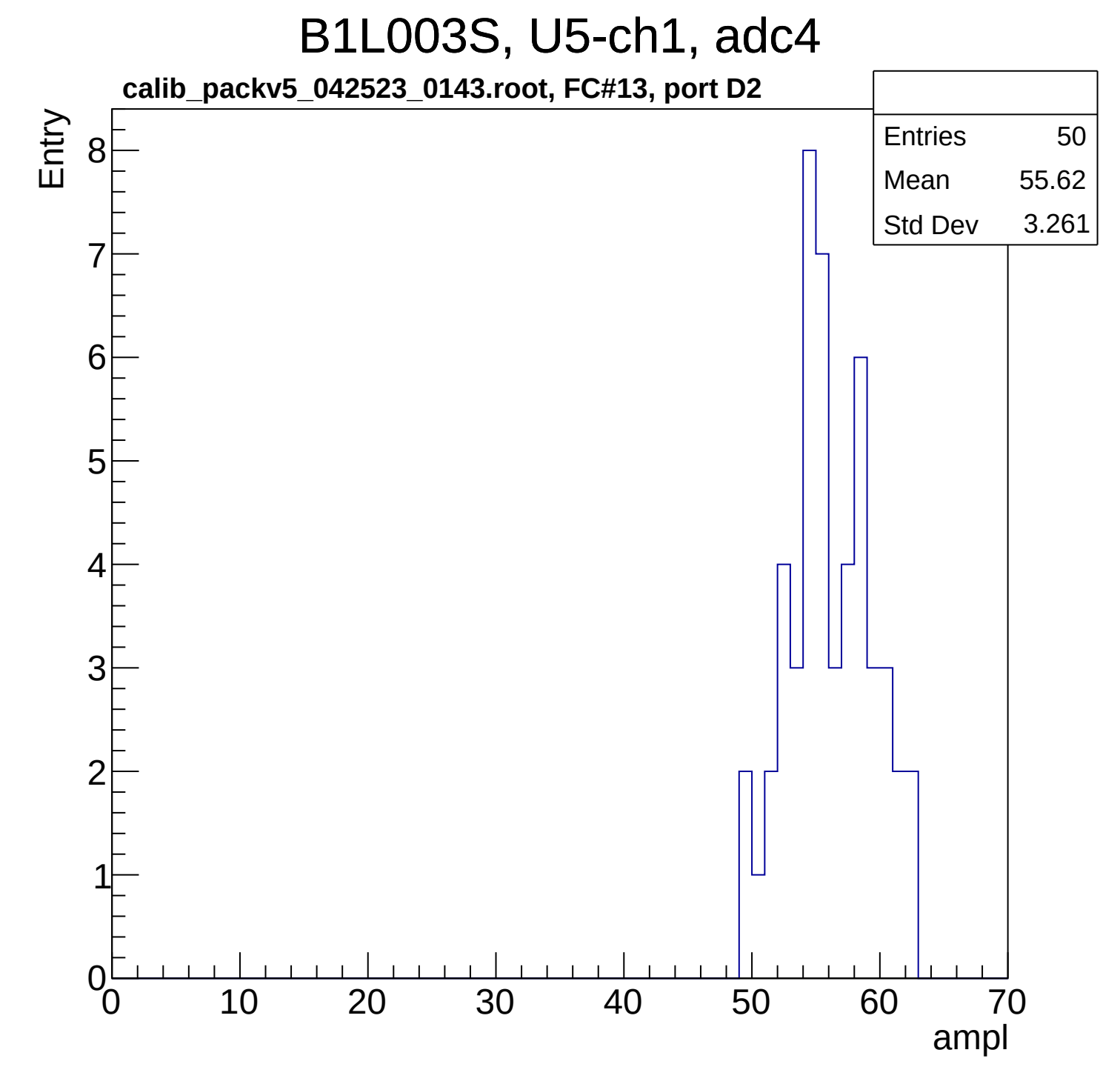
B1L003S, U5-ch1, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.62
Std Dev 3.261
Entry
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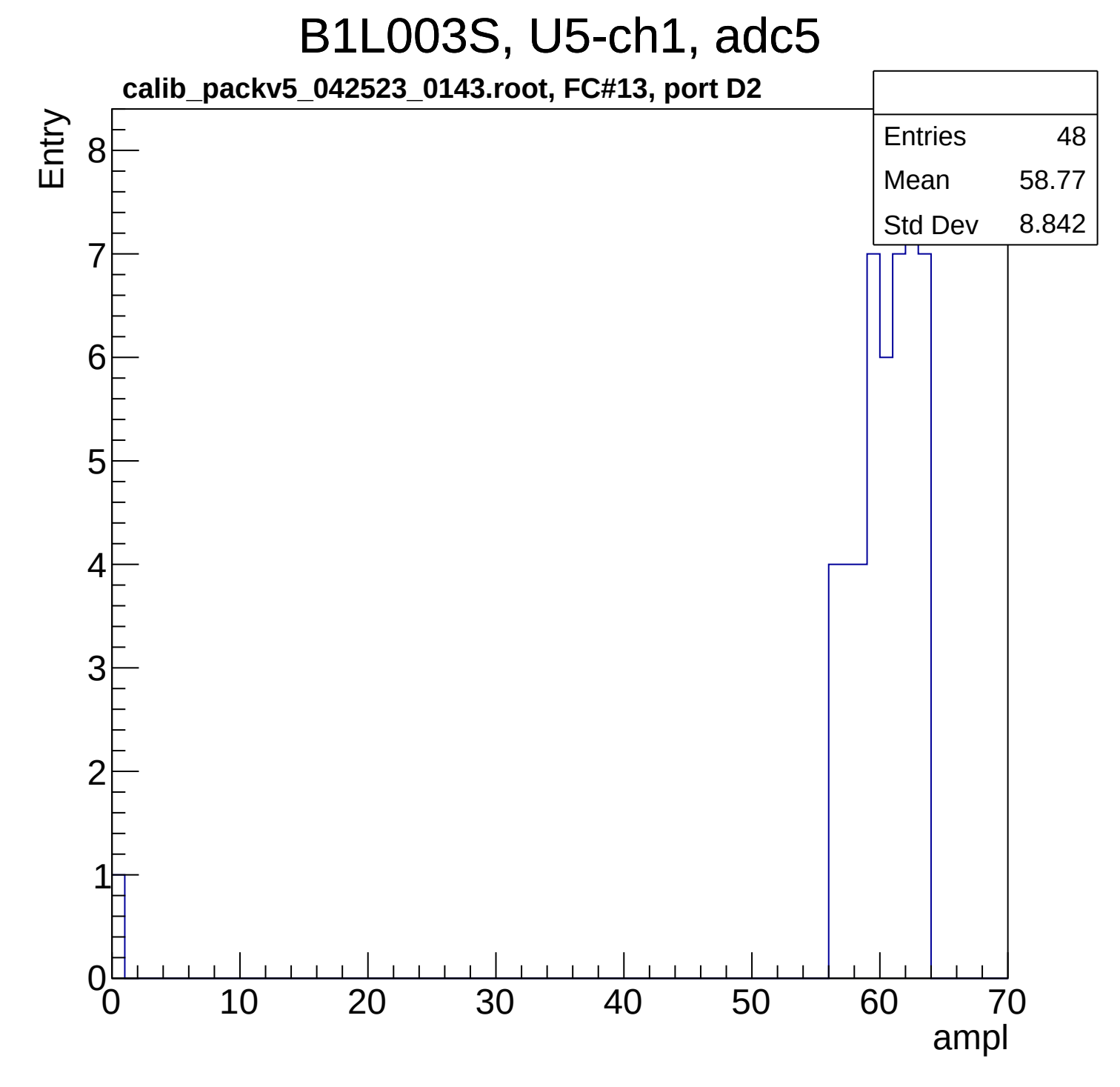
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch1, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.77
Std Dev 8.842
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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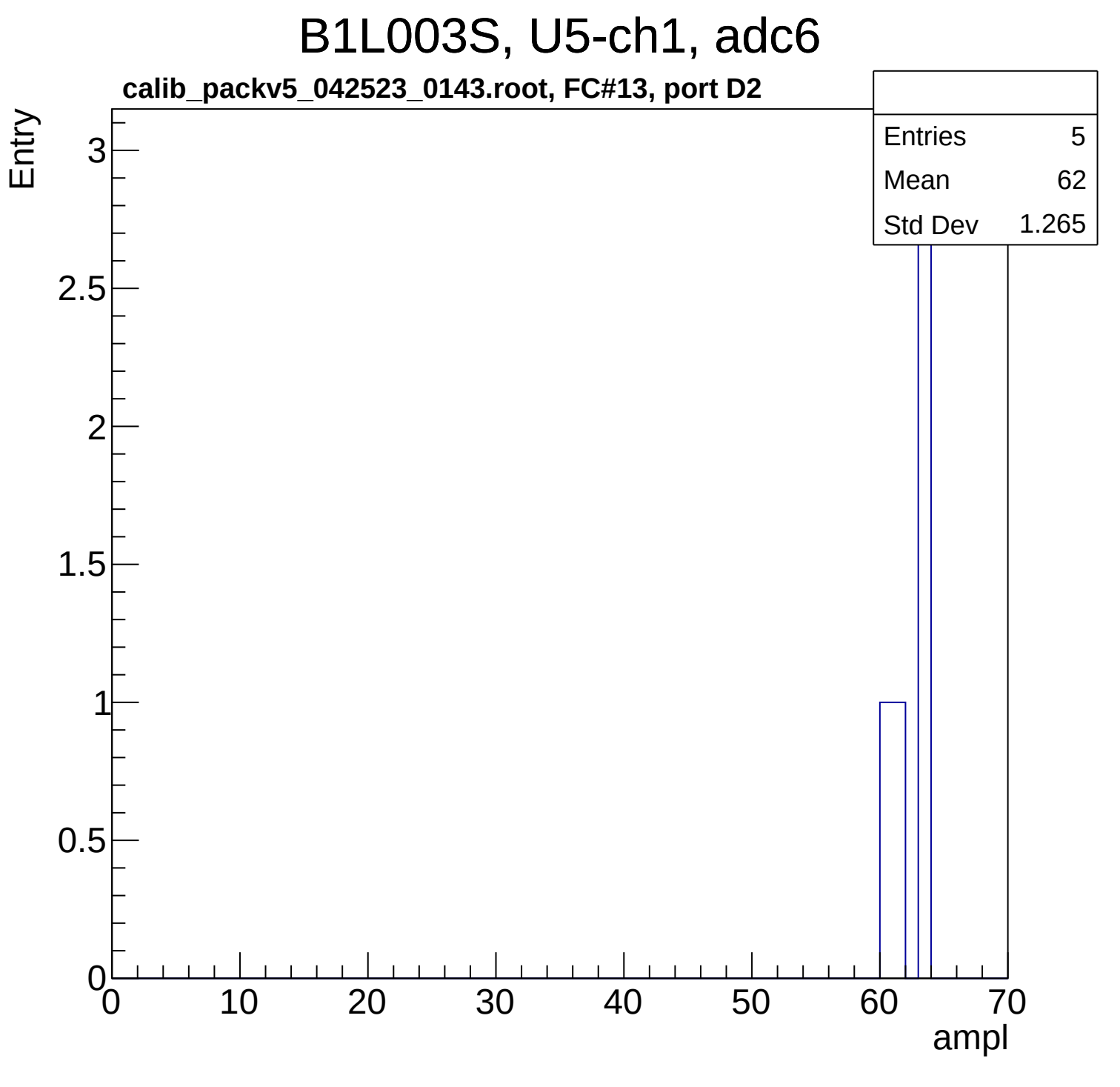
B1L003S, U5-ch1, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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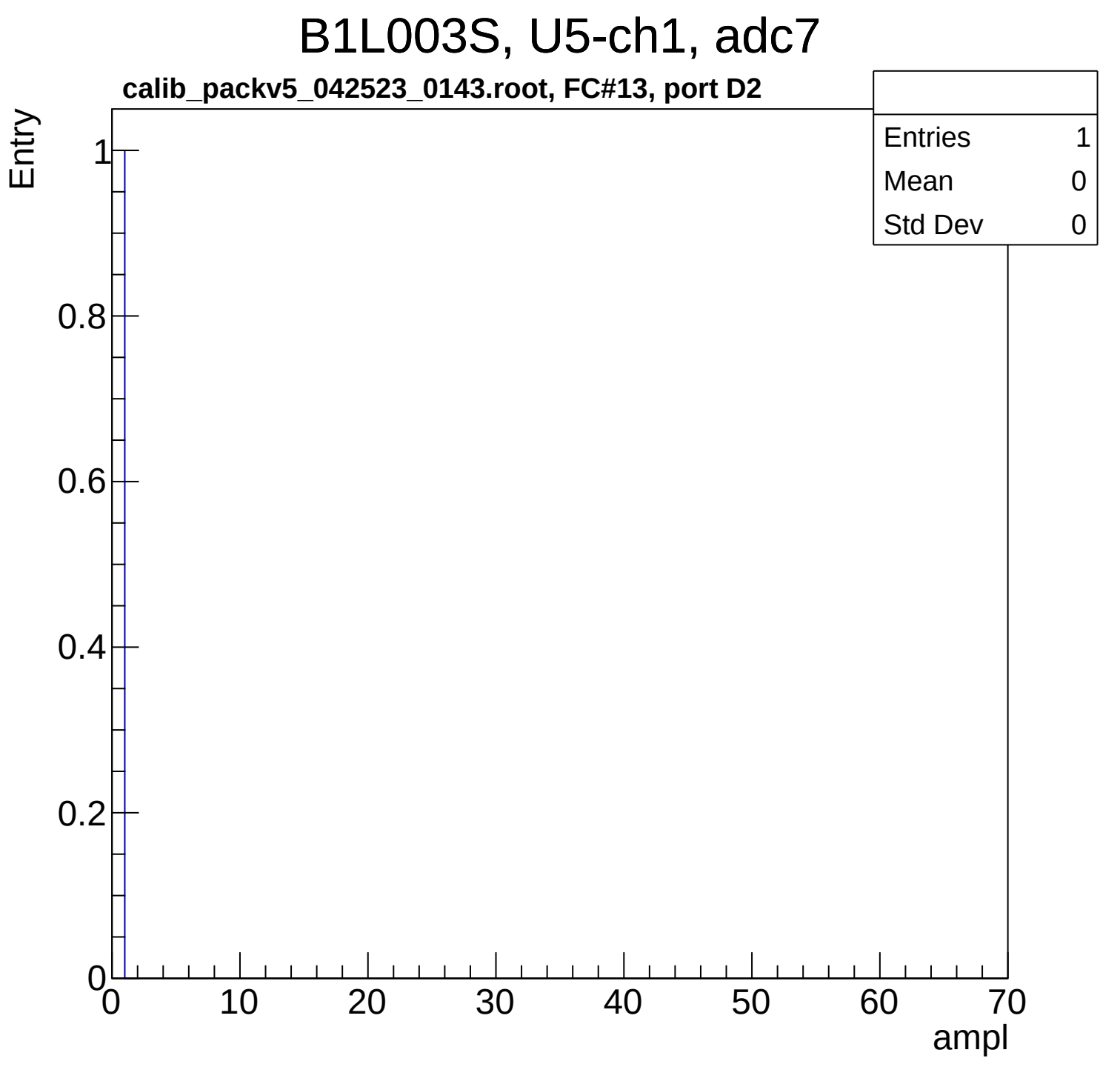
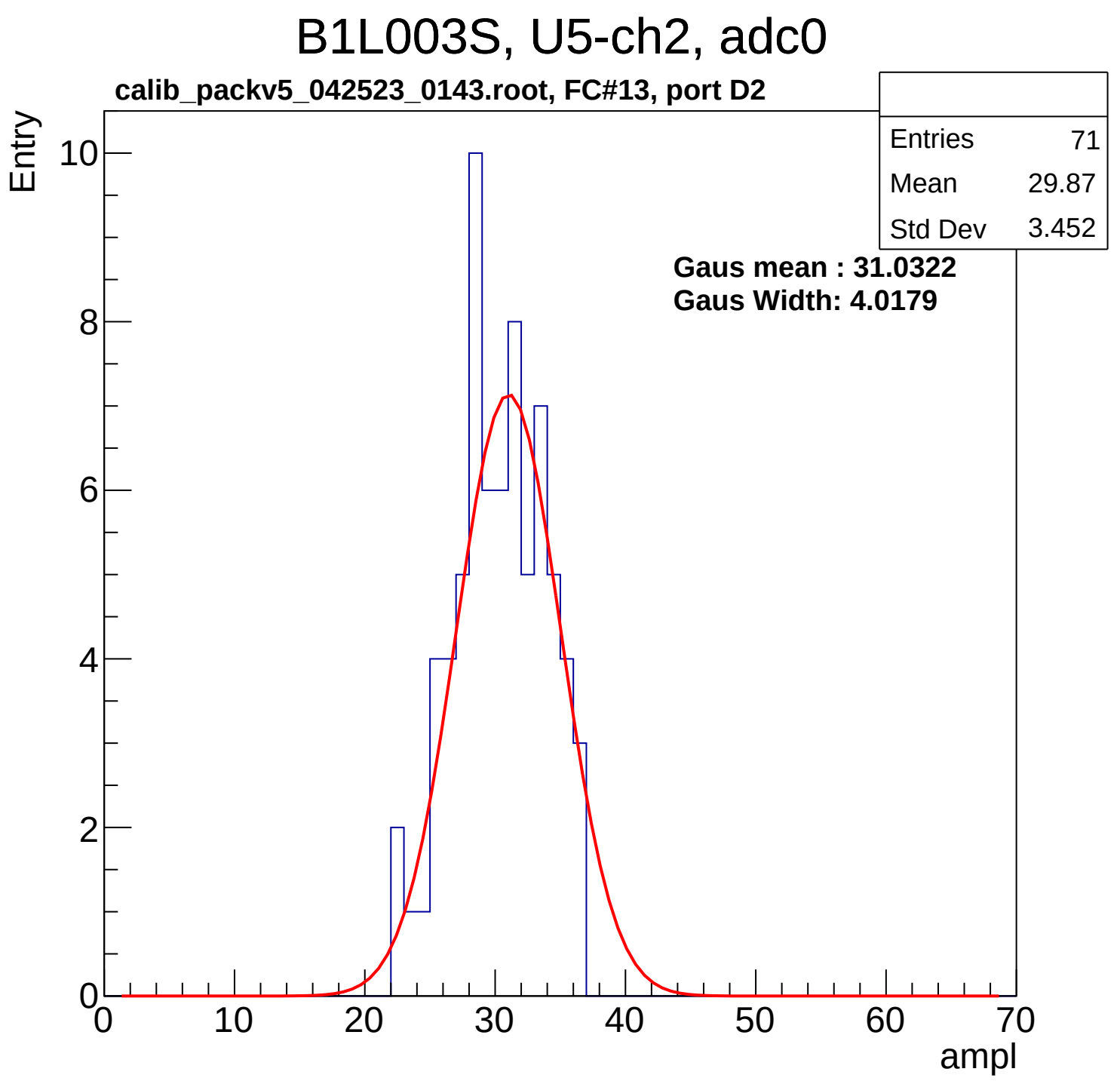

B1L003S, U5-ch1, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch2, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.87
Std Dev 3.452
Gaus mean : 31.0322
Gaus Width: 4.0179
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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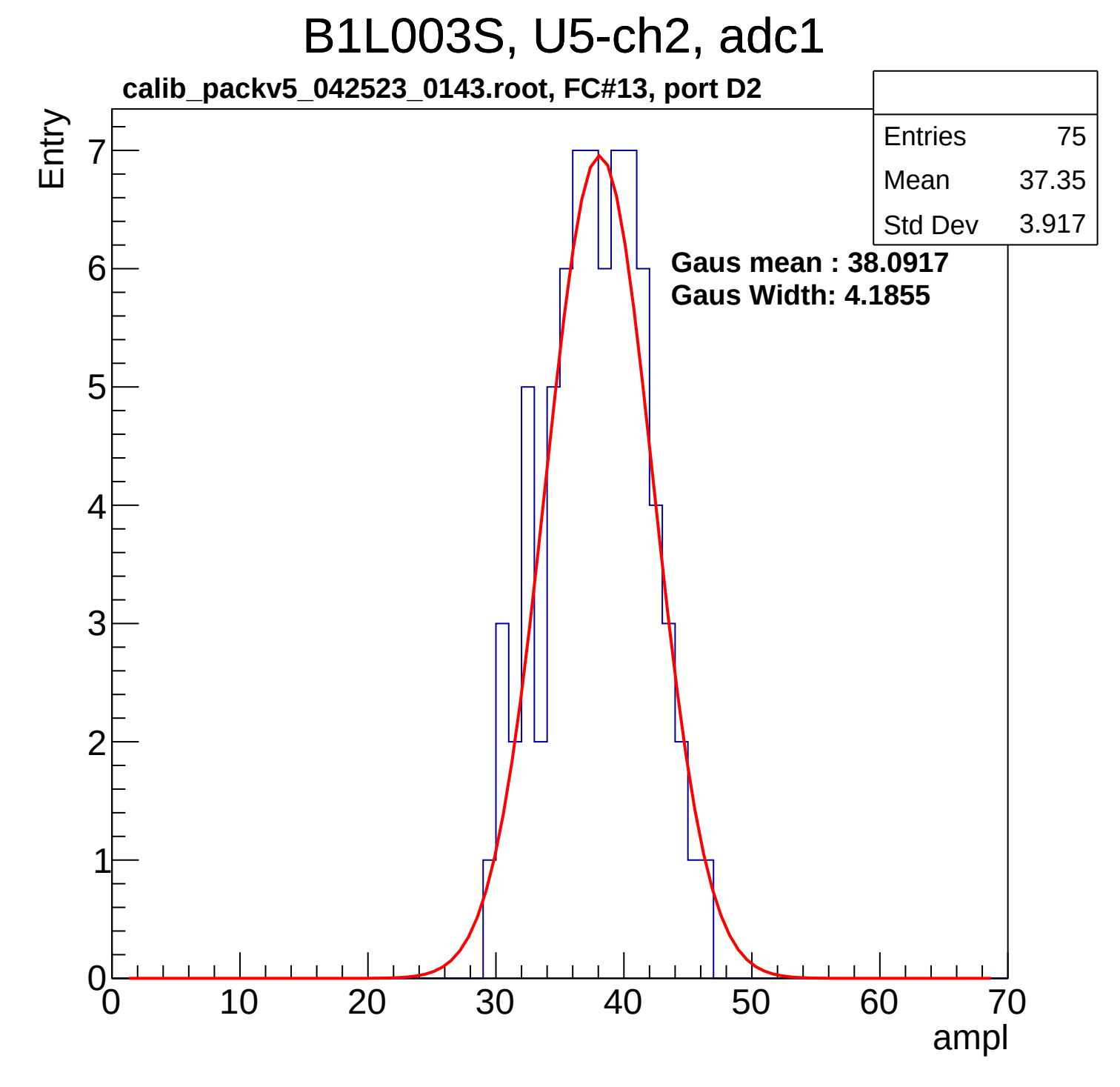
B1L003S, U5-ch2, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.35
Std Dev 3.917
Gaus mean : 38.0917
Gaus Width: 4.1855
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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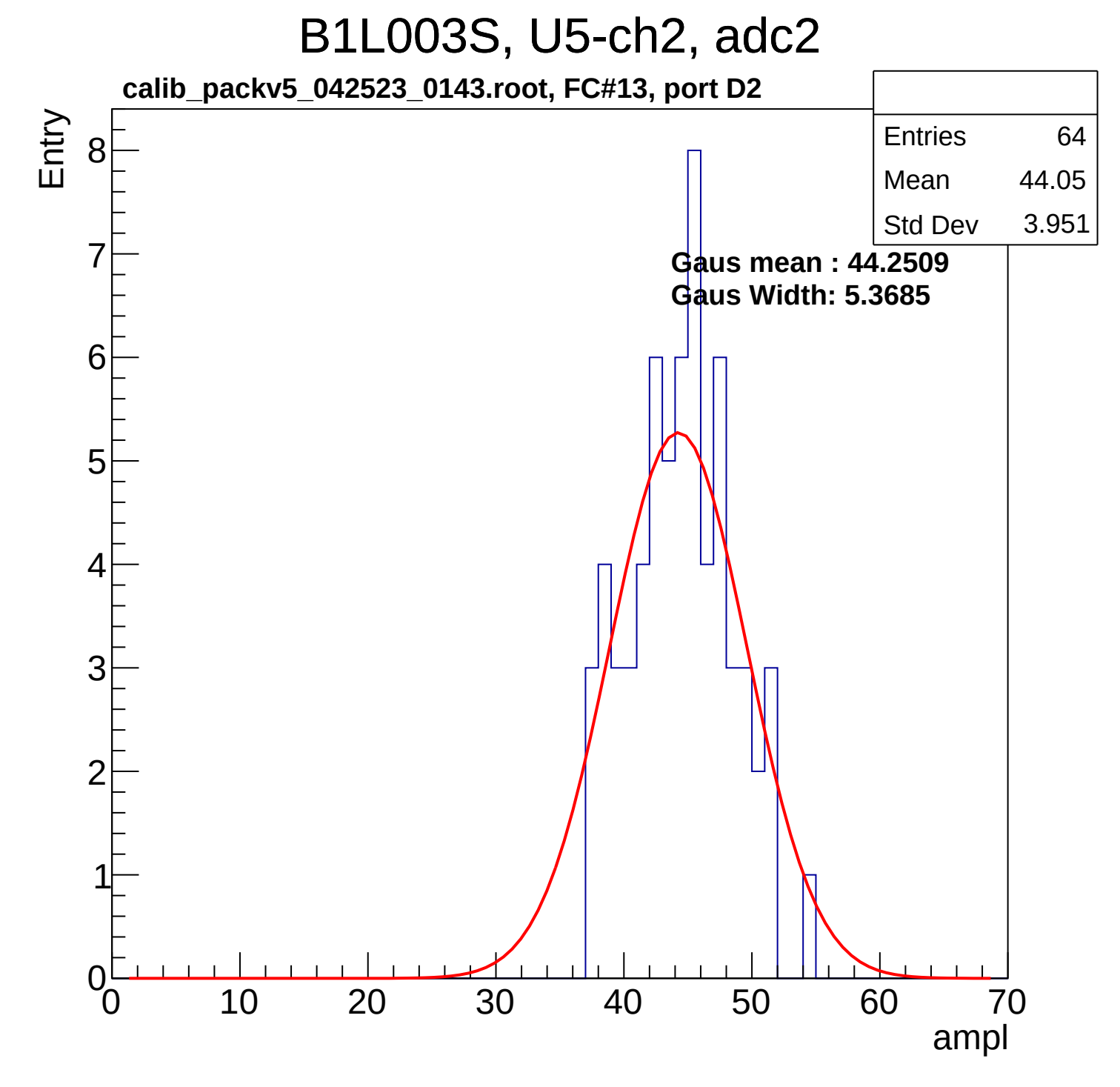
B1L003S, U5-ch2, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.05
Std Dev 3.951
Gaus mean : 44.2509
Gaus Width: 5.3685
Entry
ampl
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2
3
4
5
6
7
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0
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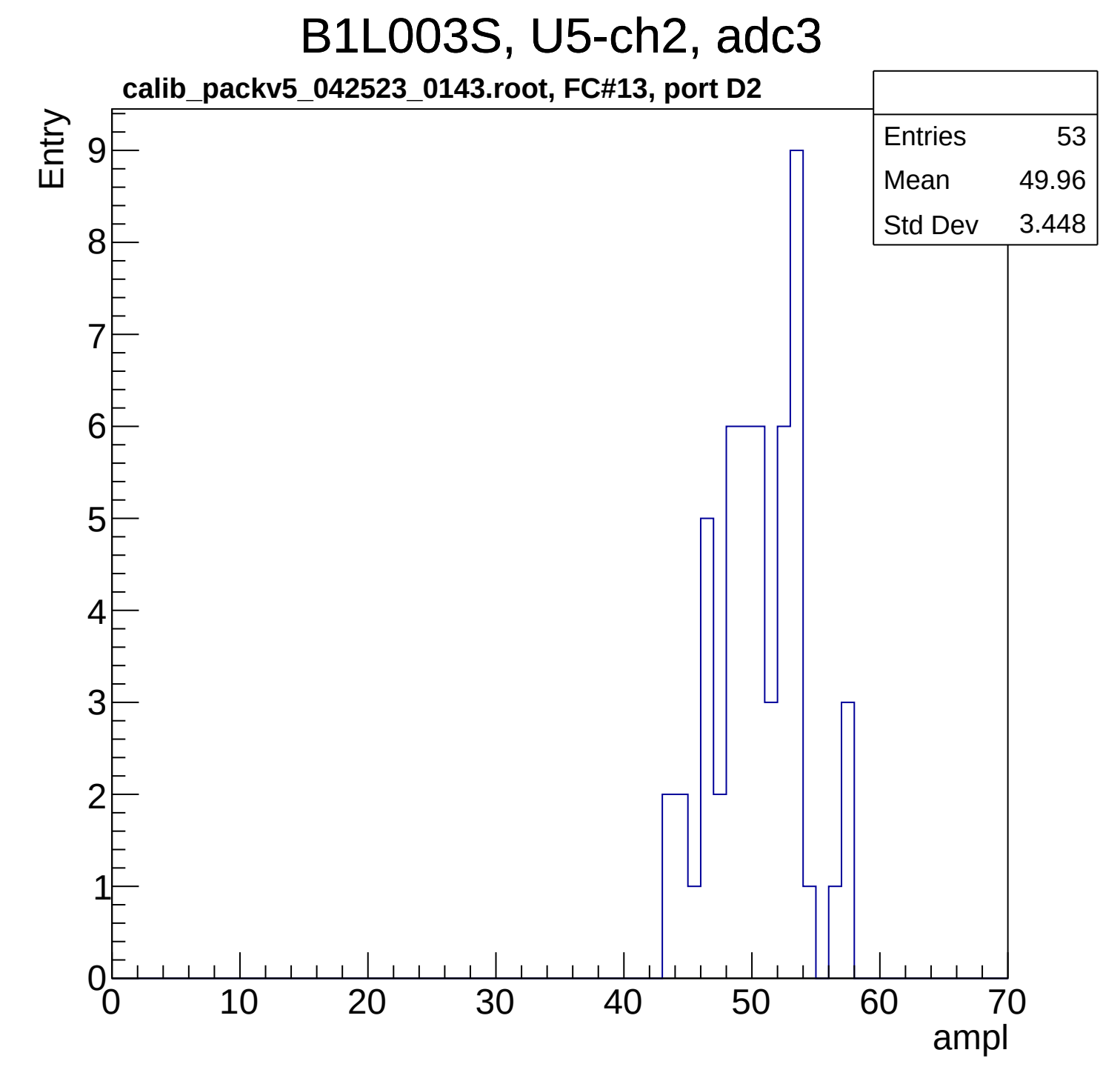
B1L003S, U5-ch2, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.96
Std Dev 3.448
Entry
ampl
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3
4
5
6
7
8
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0
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60
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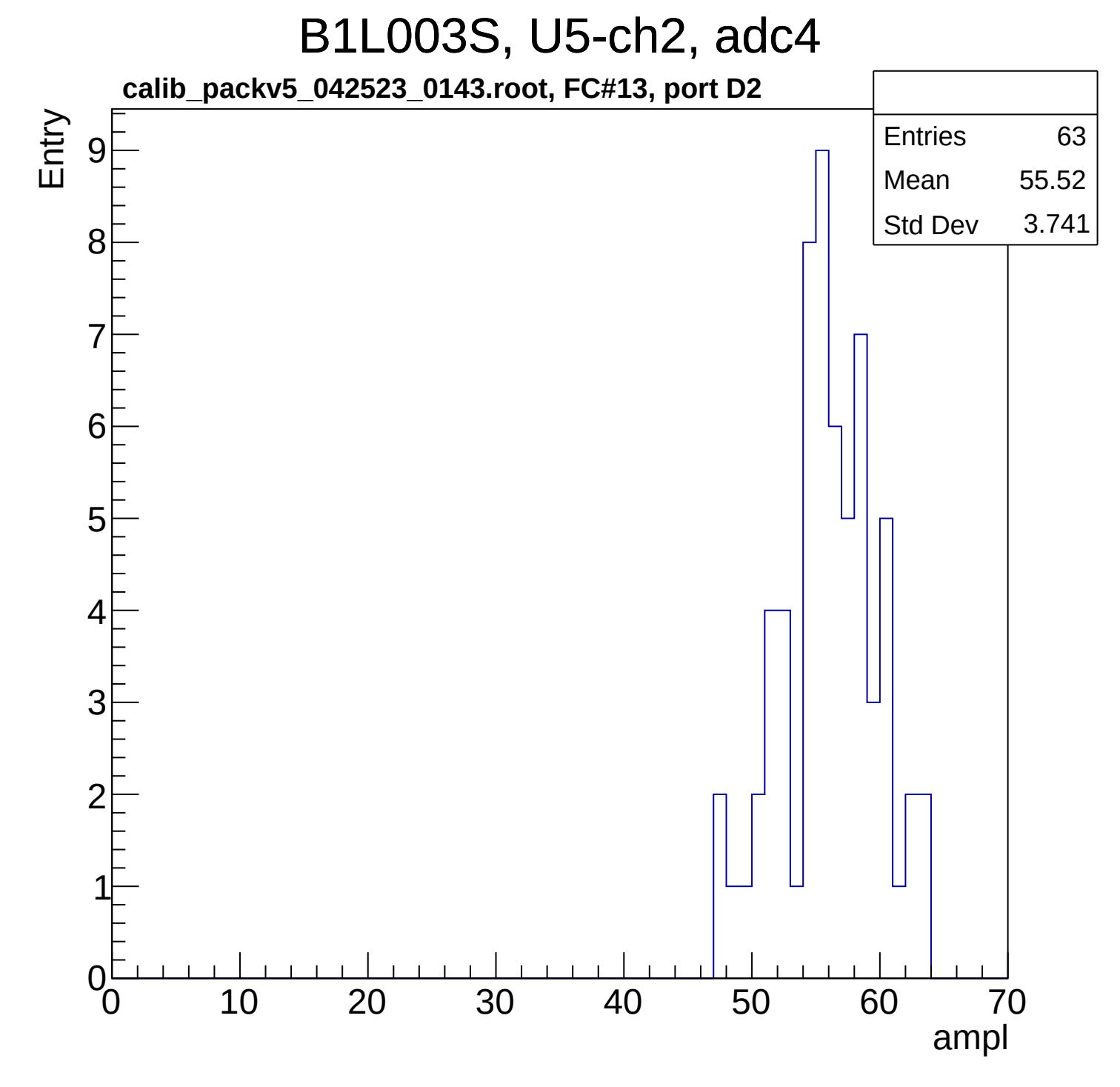
B1L003S, U5-ch2, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.52
Std Dev 3.741
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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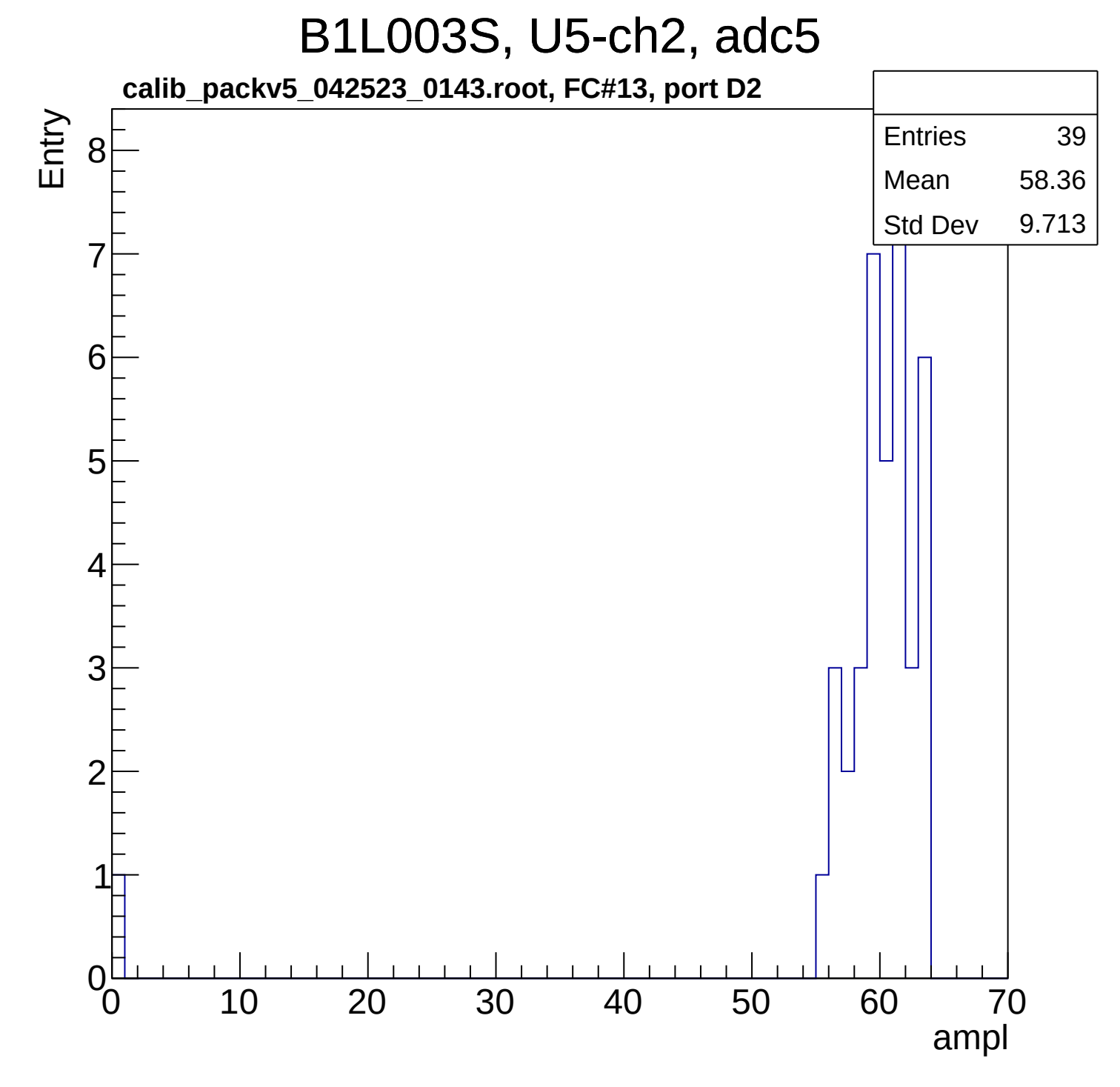
B1L003S, U5-ch2, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.36
Std Dev 9.713
Entry
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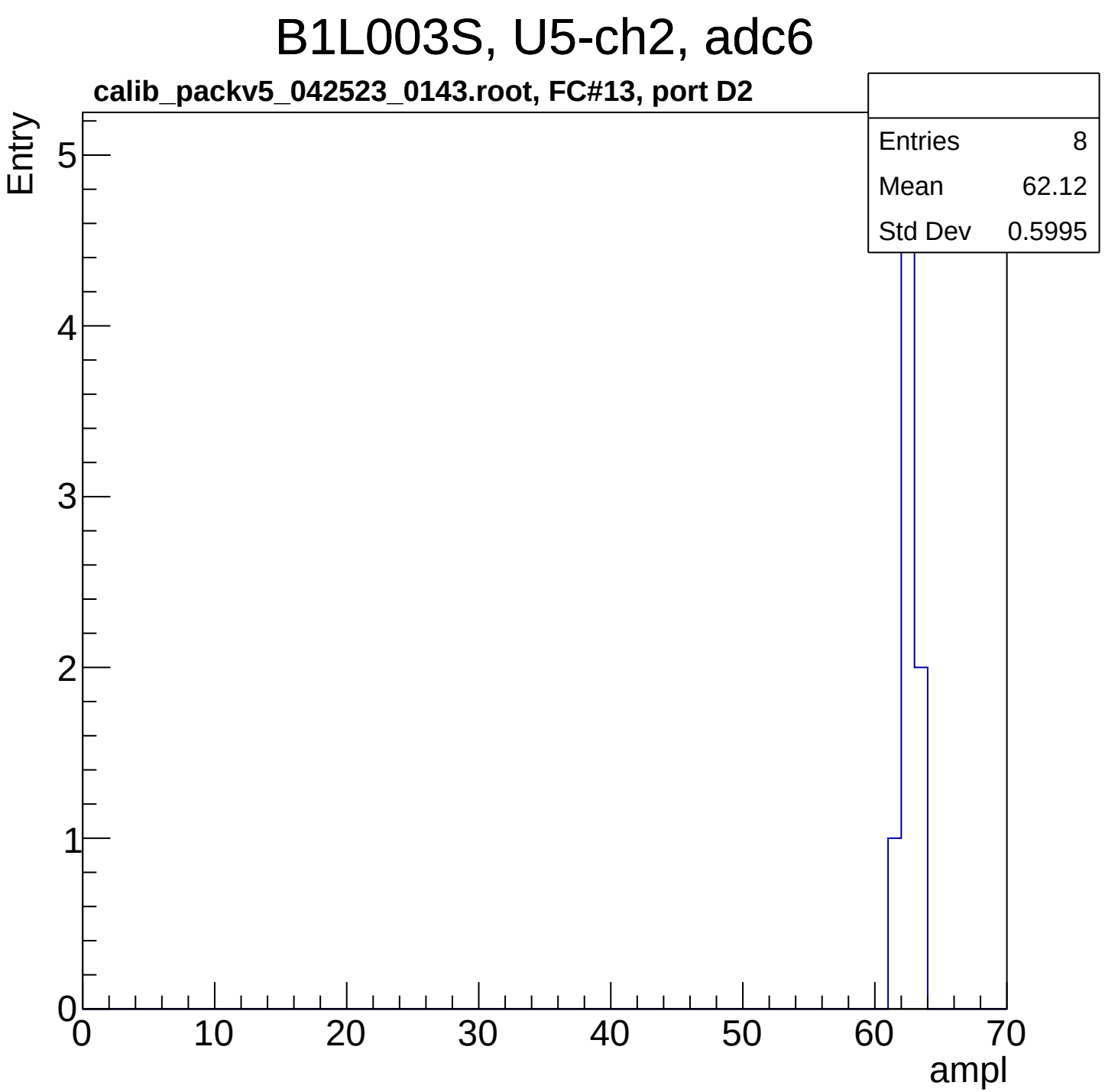
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch2, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.12
Std Dev 0.5995
Entry
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2
3
4
5
0
10
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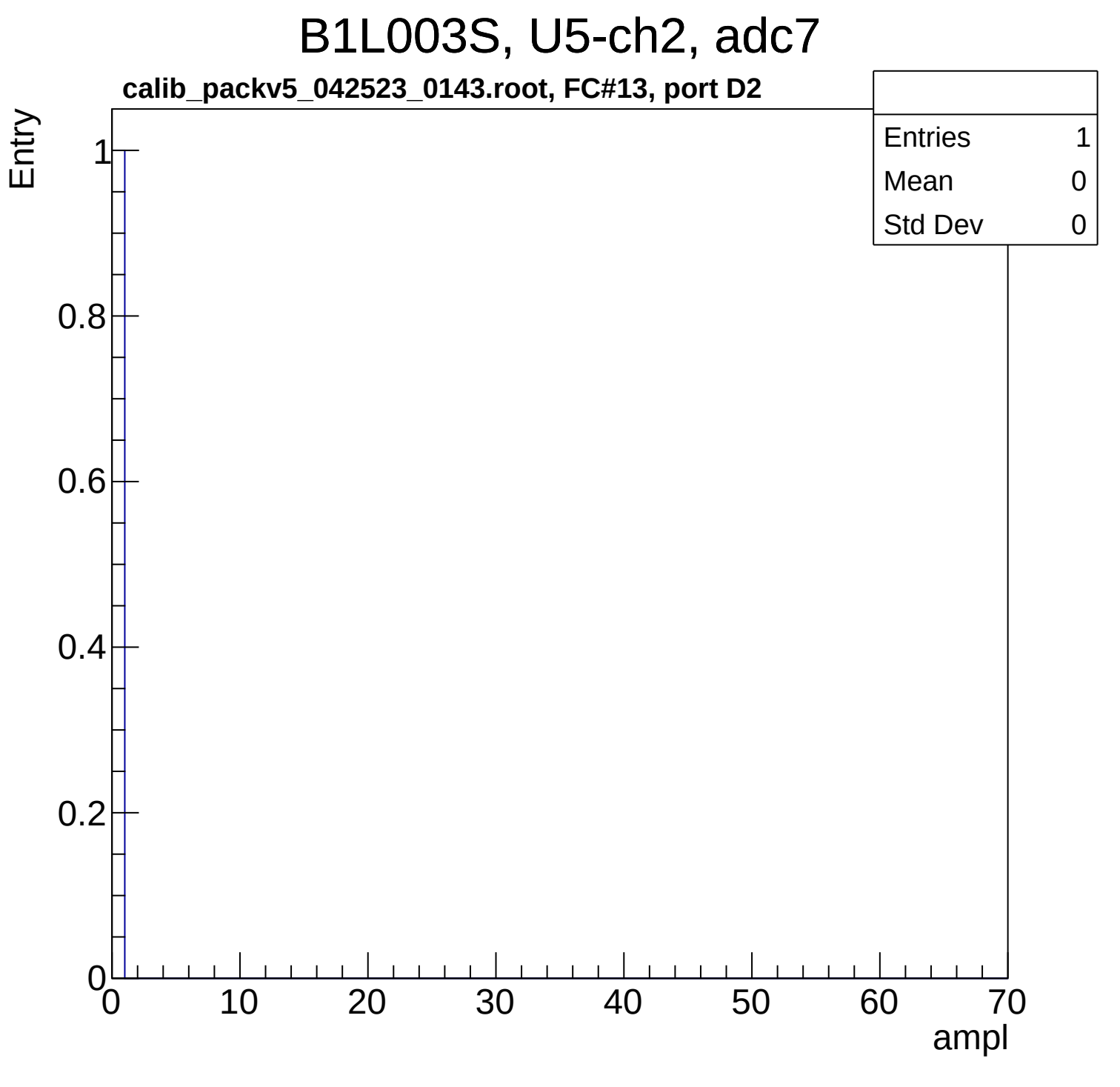
30
40
50
60
70
ampl


B1L003S, U5-ch2, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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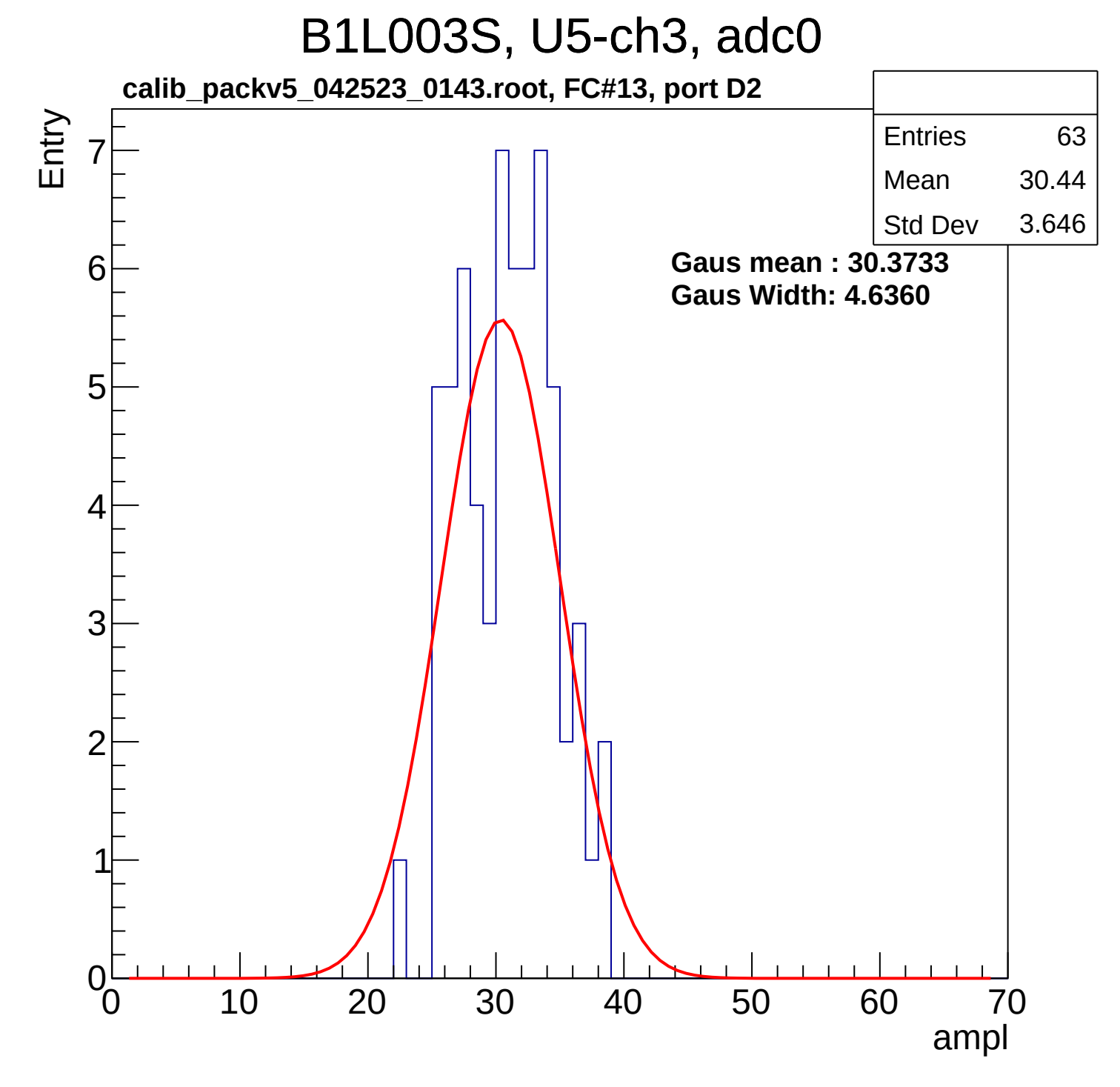
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch3, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30.44
Std Dev 3.646
Gaus mean : 30.3733
Gaus Width: 4.6360
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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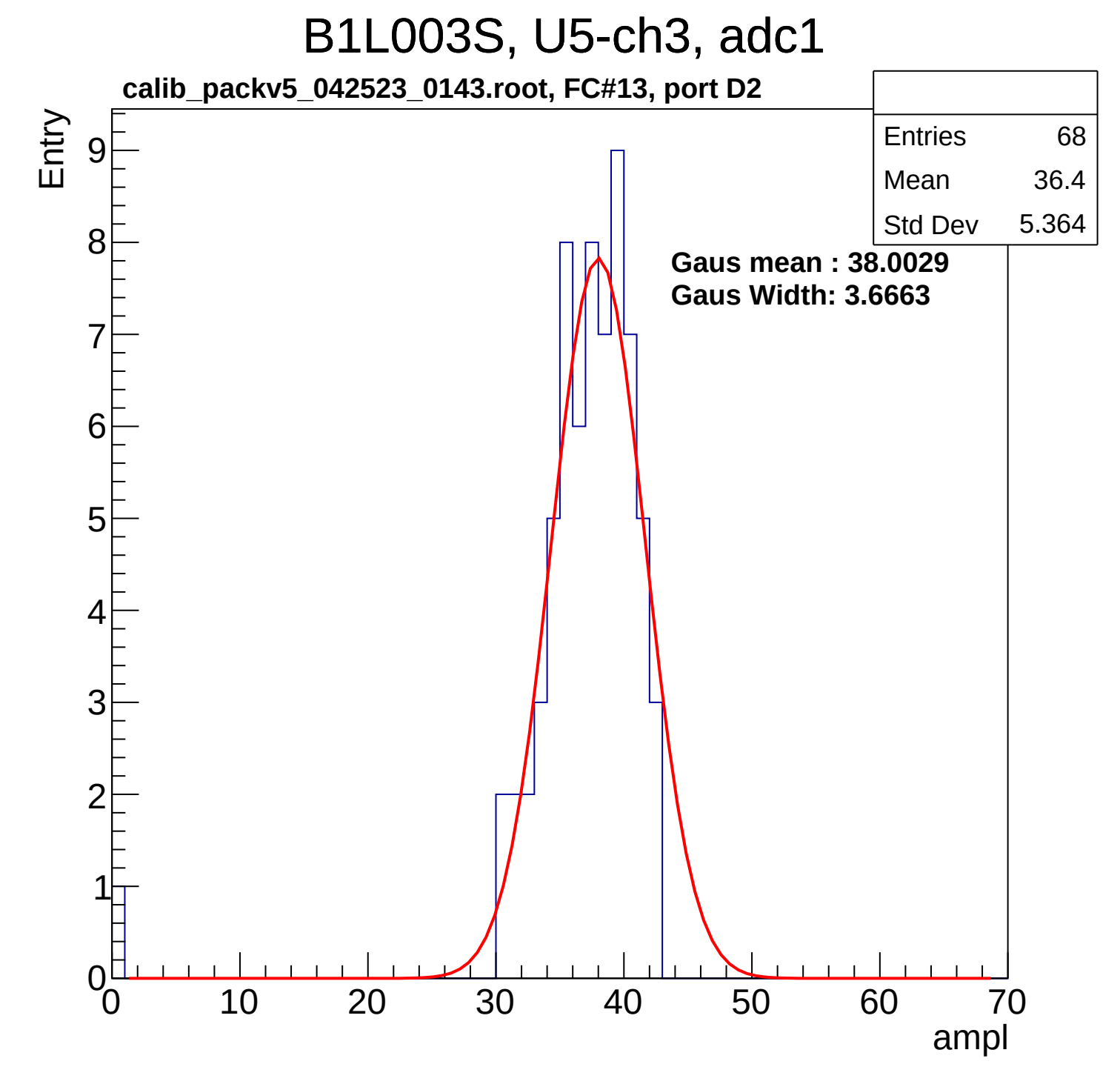
B1L003S, U5-ch3, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.4
Std Dev 5.364
Gaus mean : 38.0029
Gaus Width: 3.6663
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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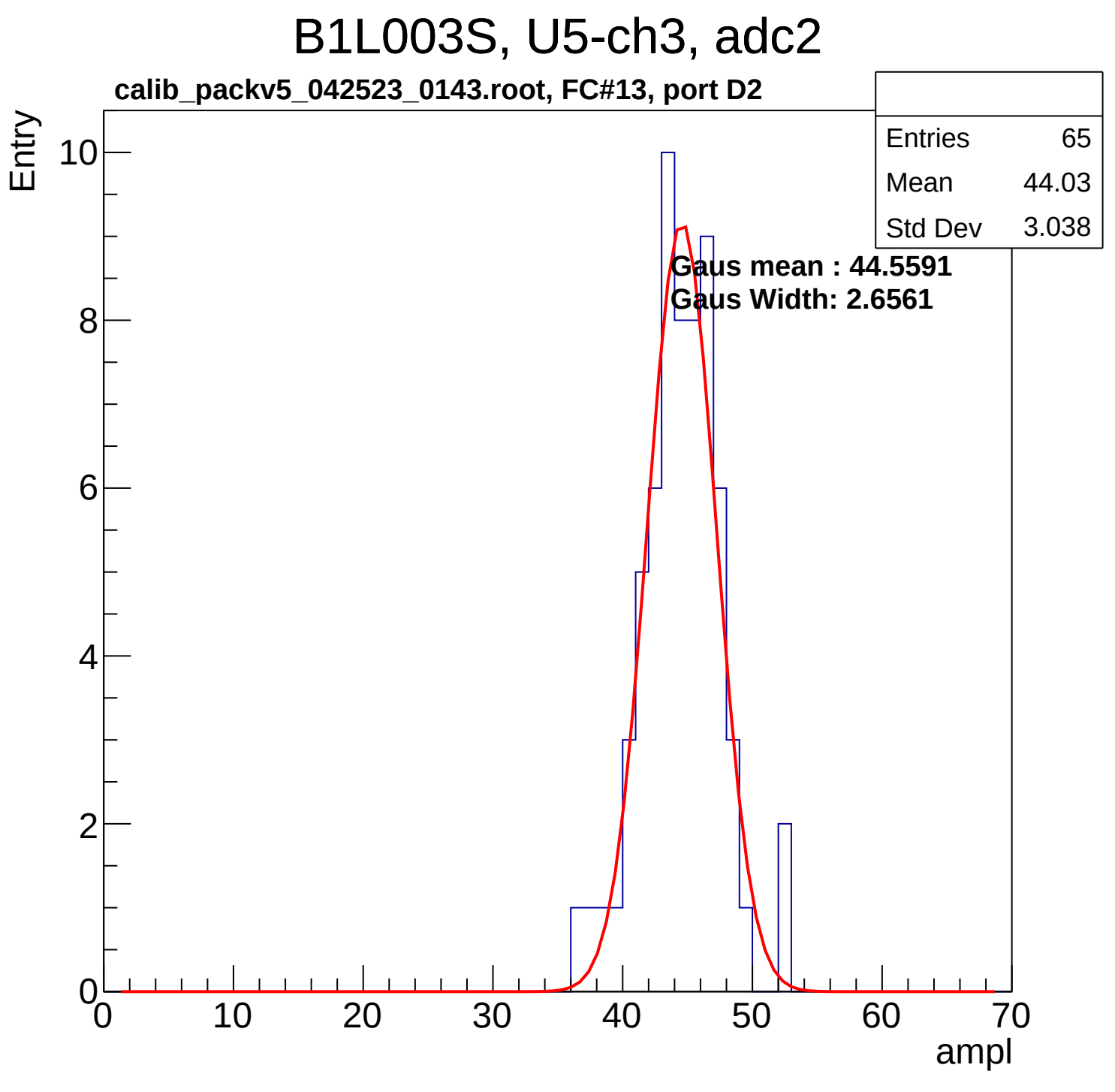
B1L003S, U5-ch3, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 44.03
Std Dev 3.038
Gaus mean : 44.5591
Gaus Width: 2.6561
Entry
ampl
0
2
4
6
8
10
0
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20
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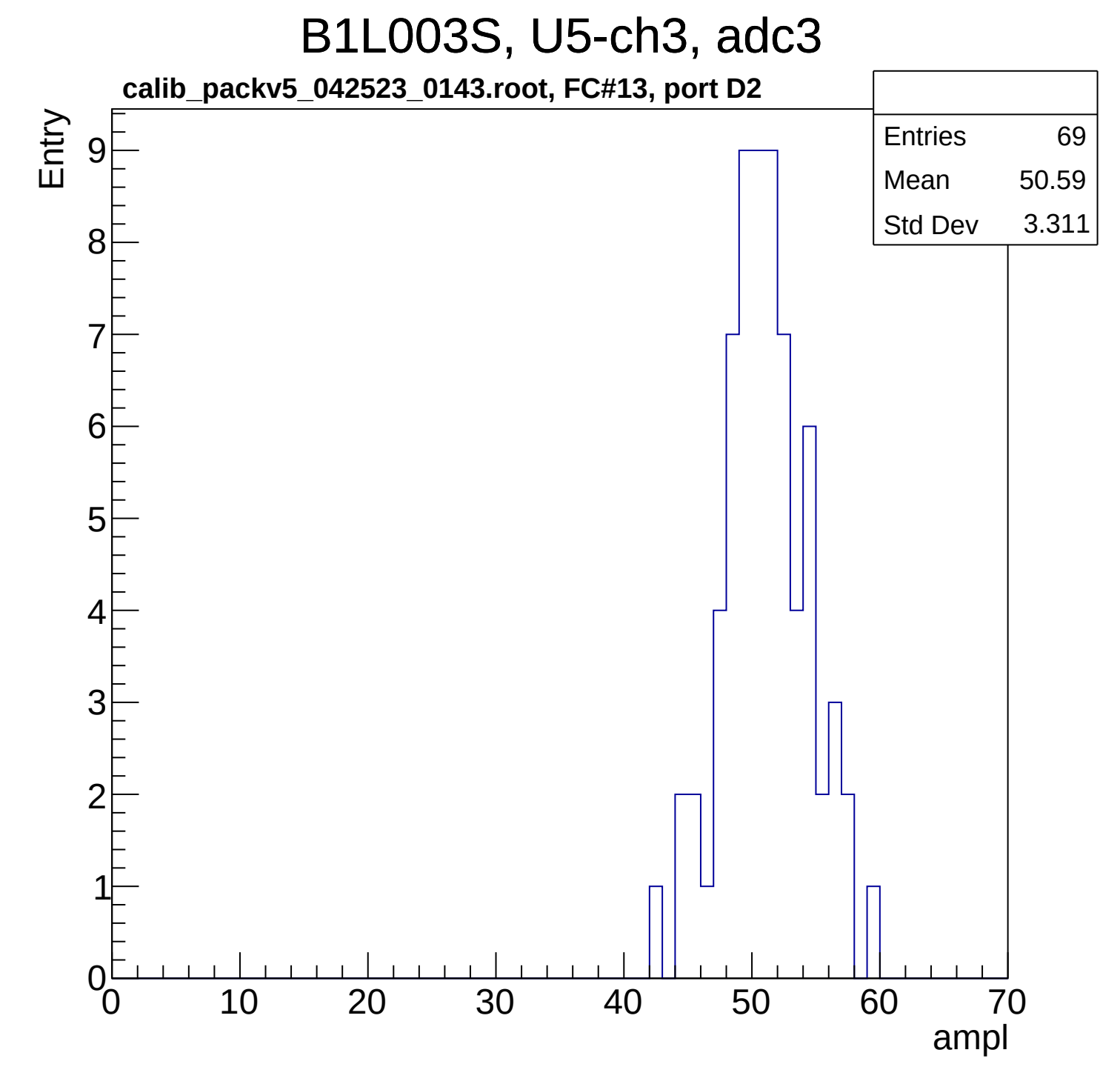
40
50
60
70


B1L003S, U5-ch3, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.59
Std Dev 3.311
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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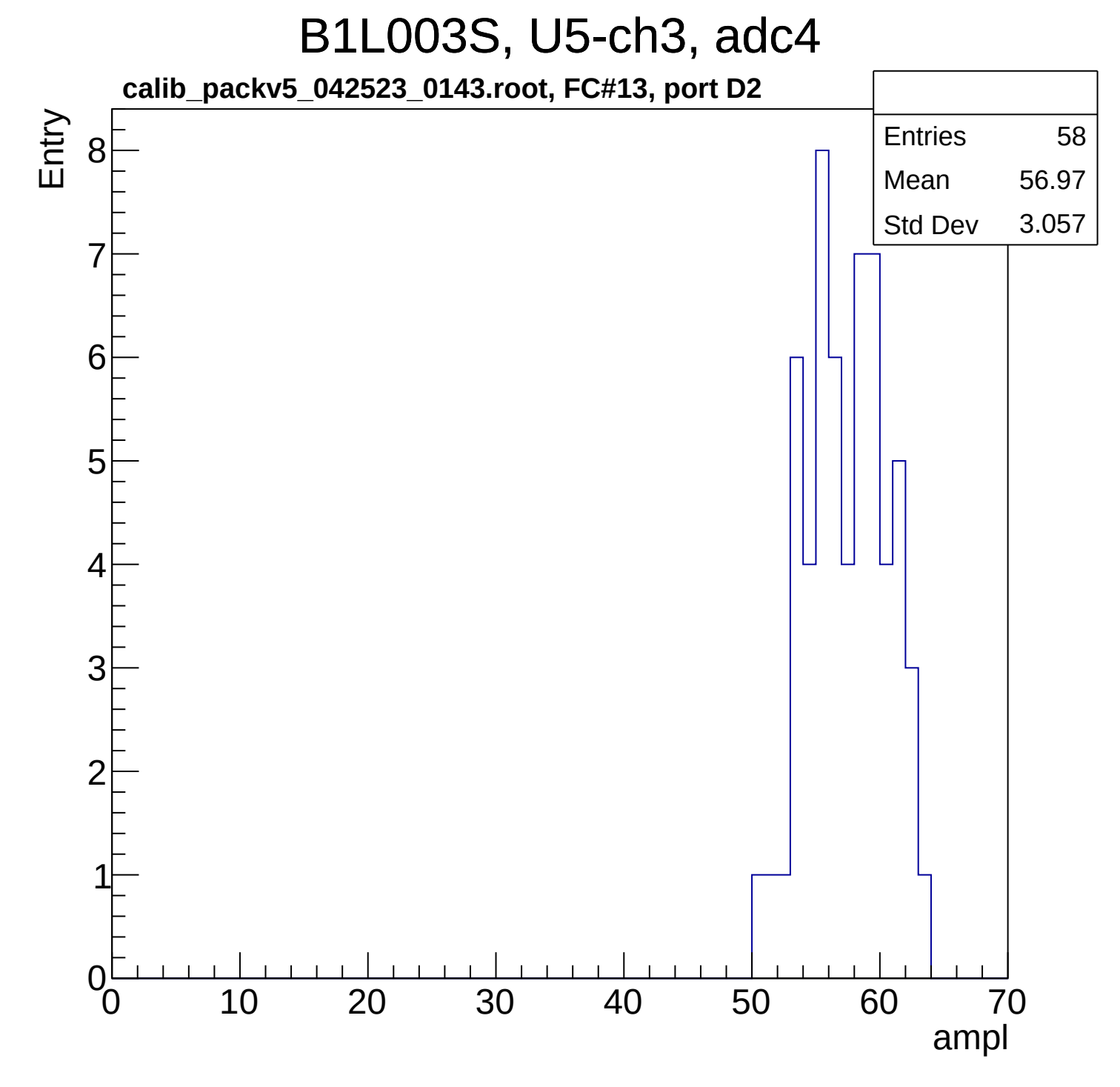
B1L003S, U5-ch3, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 56.97
Std Dev 3.057
Entry
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3
4
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6
7
8
0
10
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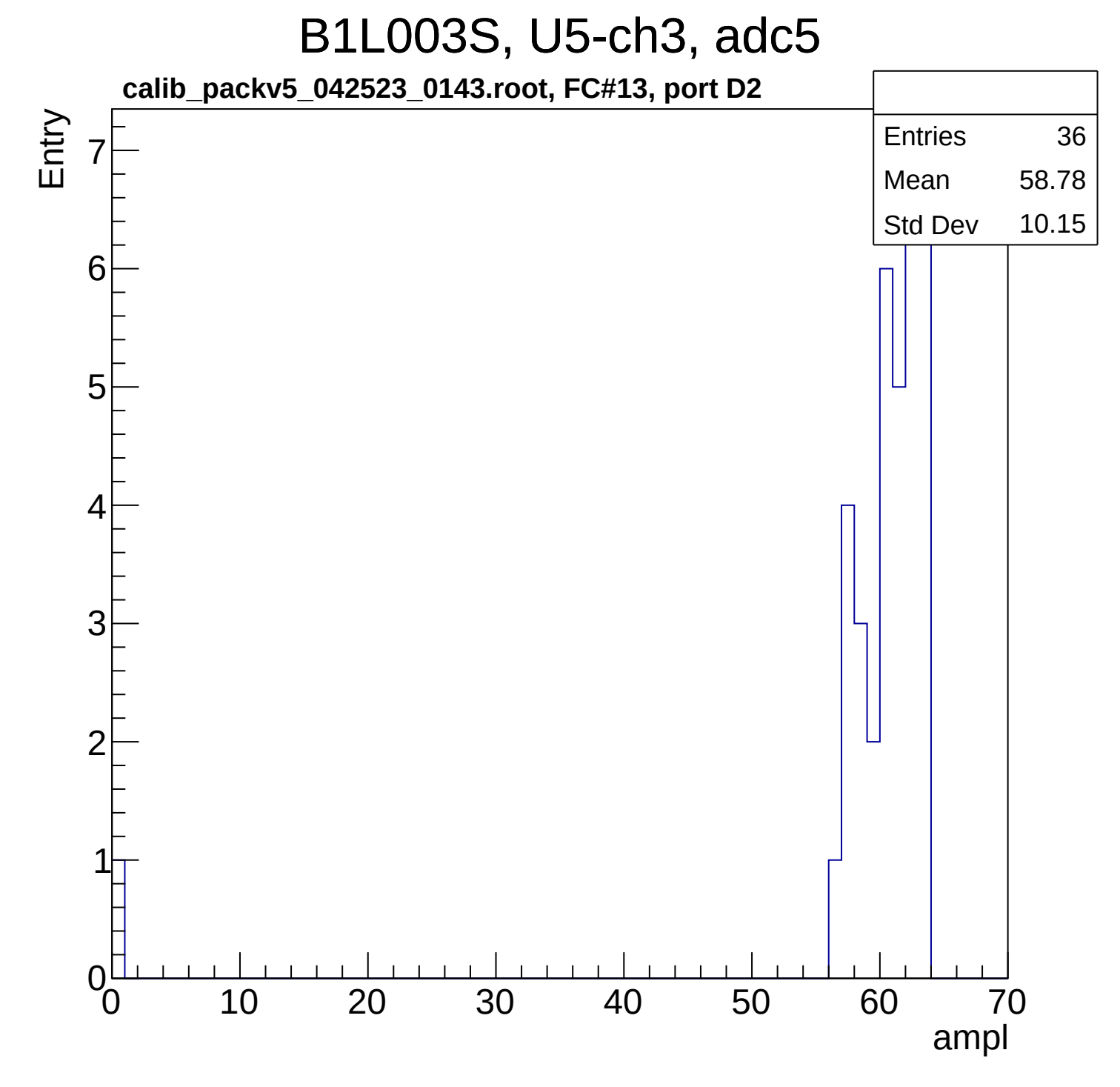
50
60
70
ampl


B1L003S, U5-ch3, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.78
Std Dev 10.15
Entry
ampl
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3
4
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0
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40
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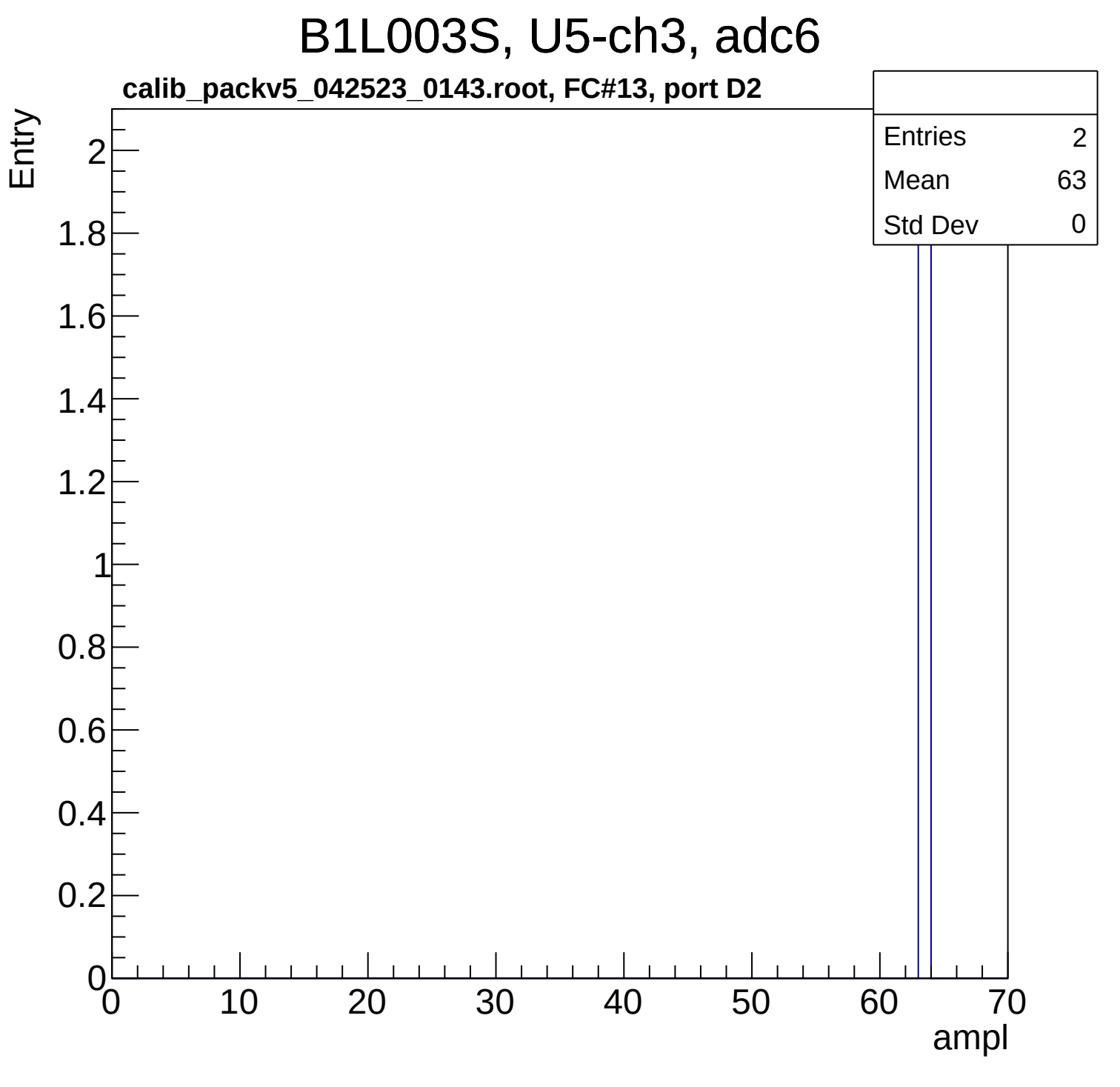
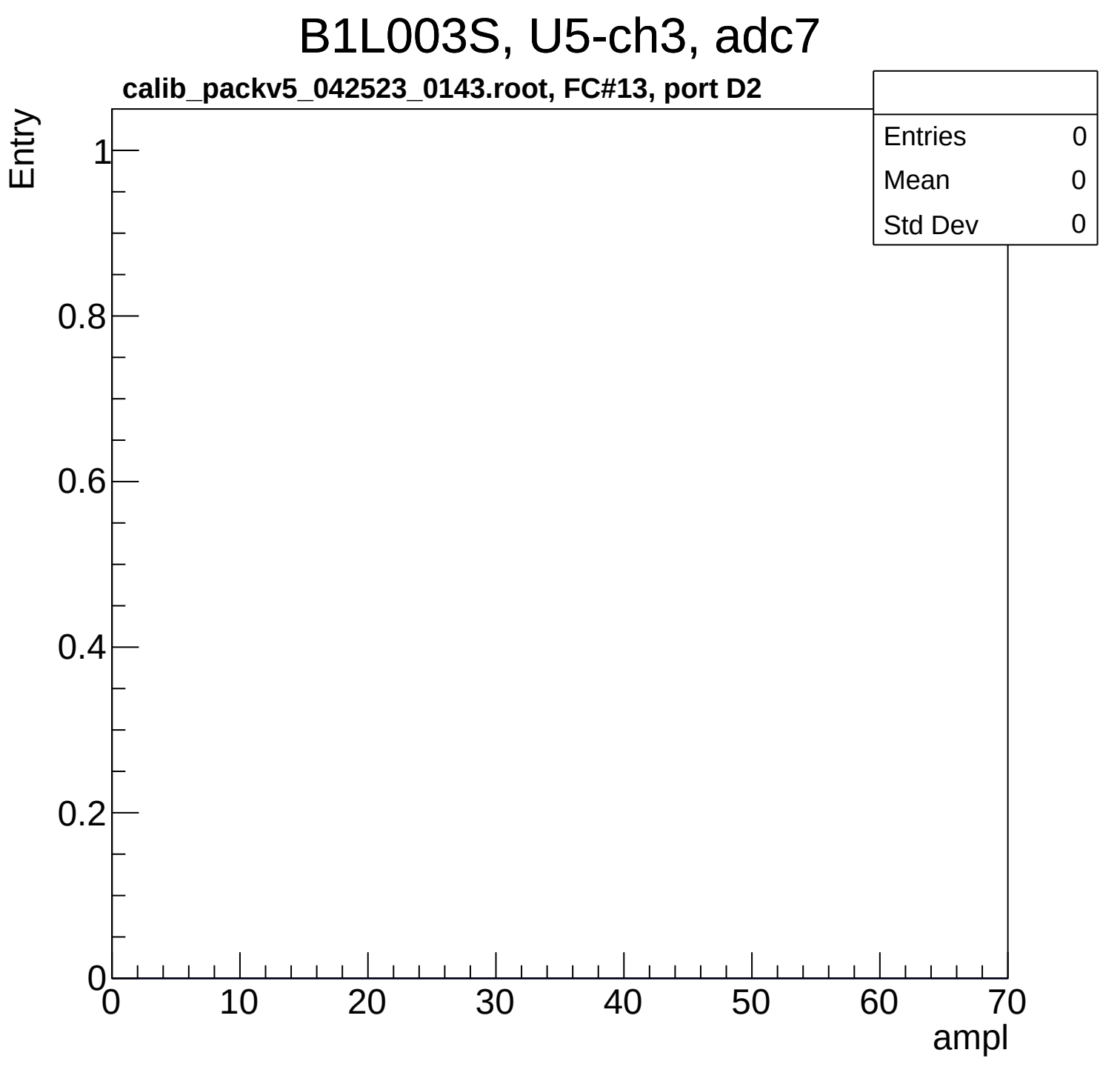

B1L003S, U5-ch3, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch3, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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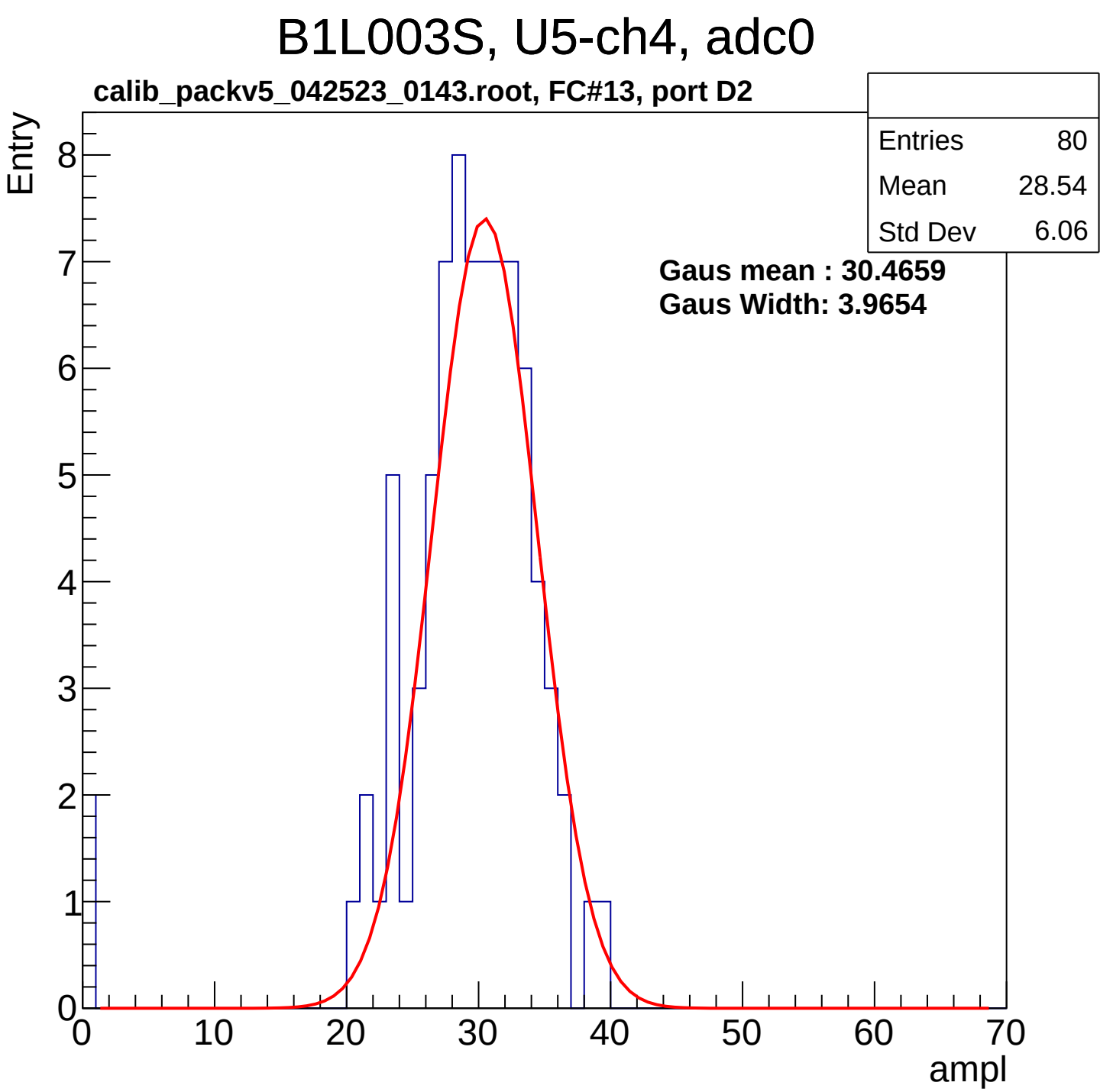
40
50
60
70
ampl


B1L003S, U5-ch4, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 28.54
Std Dev 6.06
Gaus mean : 30.4659
Gaus Width: 3.9654
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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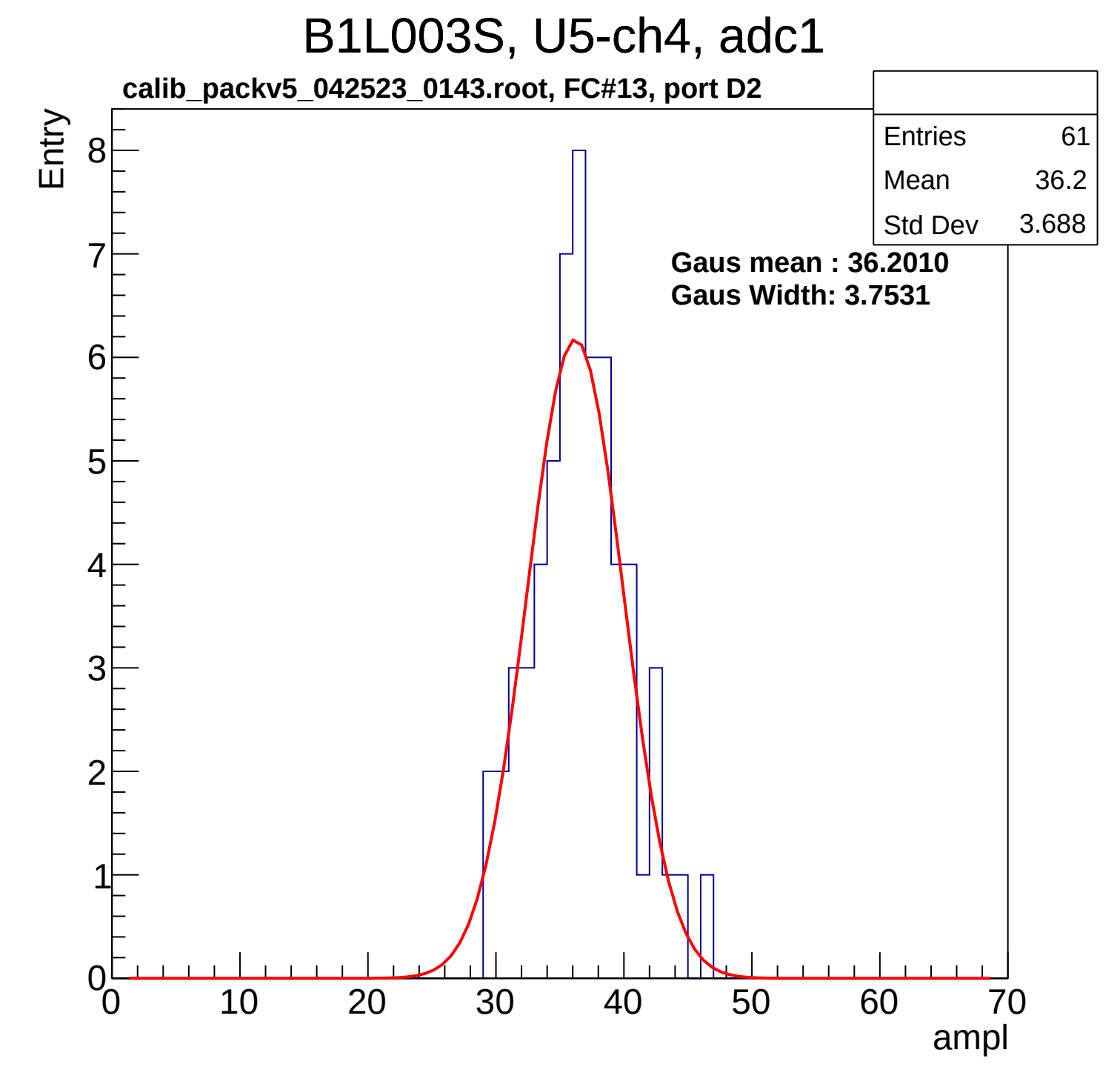
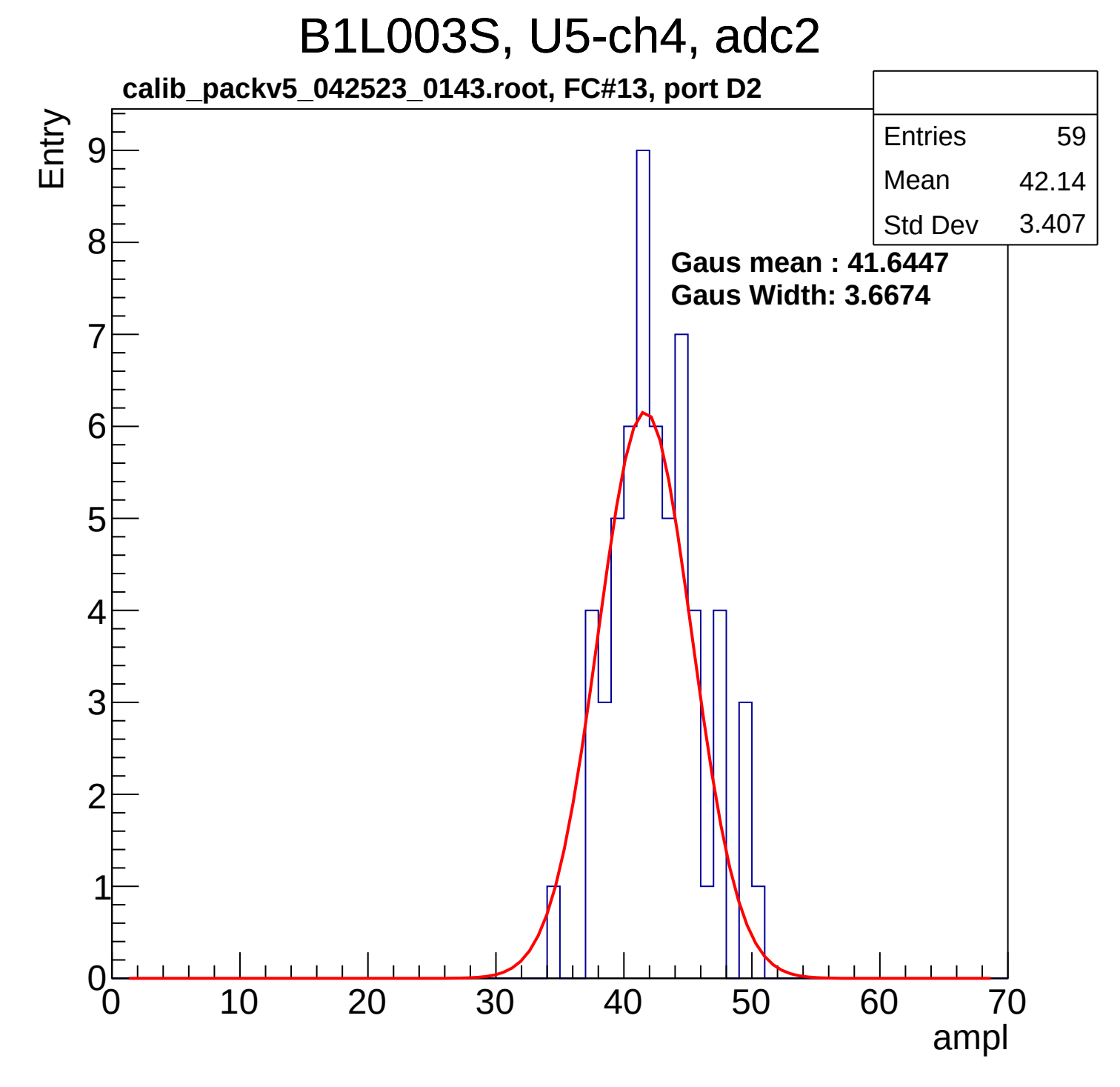
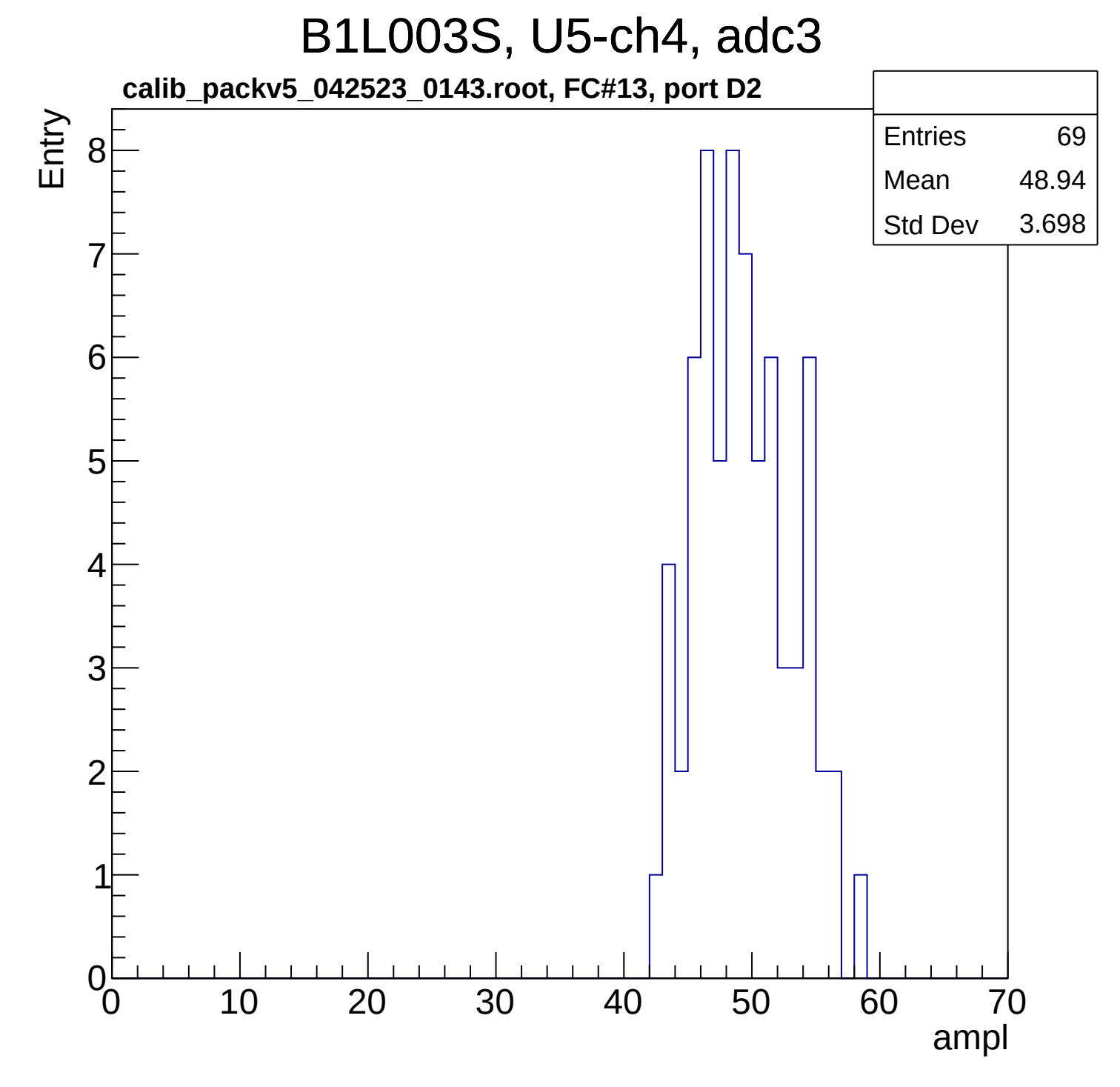

B1L003S, U5-ch4, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.2
Std Dev 3.688
Gaus mean : 36.2010
Gaus Width: 3.7531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch4, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.14
Std Dev 3.407
Gaus mean : 41.6447
Gaus Width: 3.6674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch4, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.94
Std Dev 3.698
Entry
ampl
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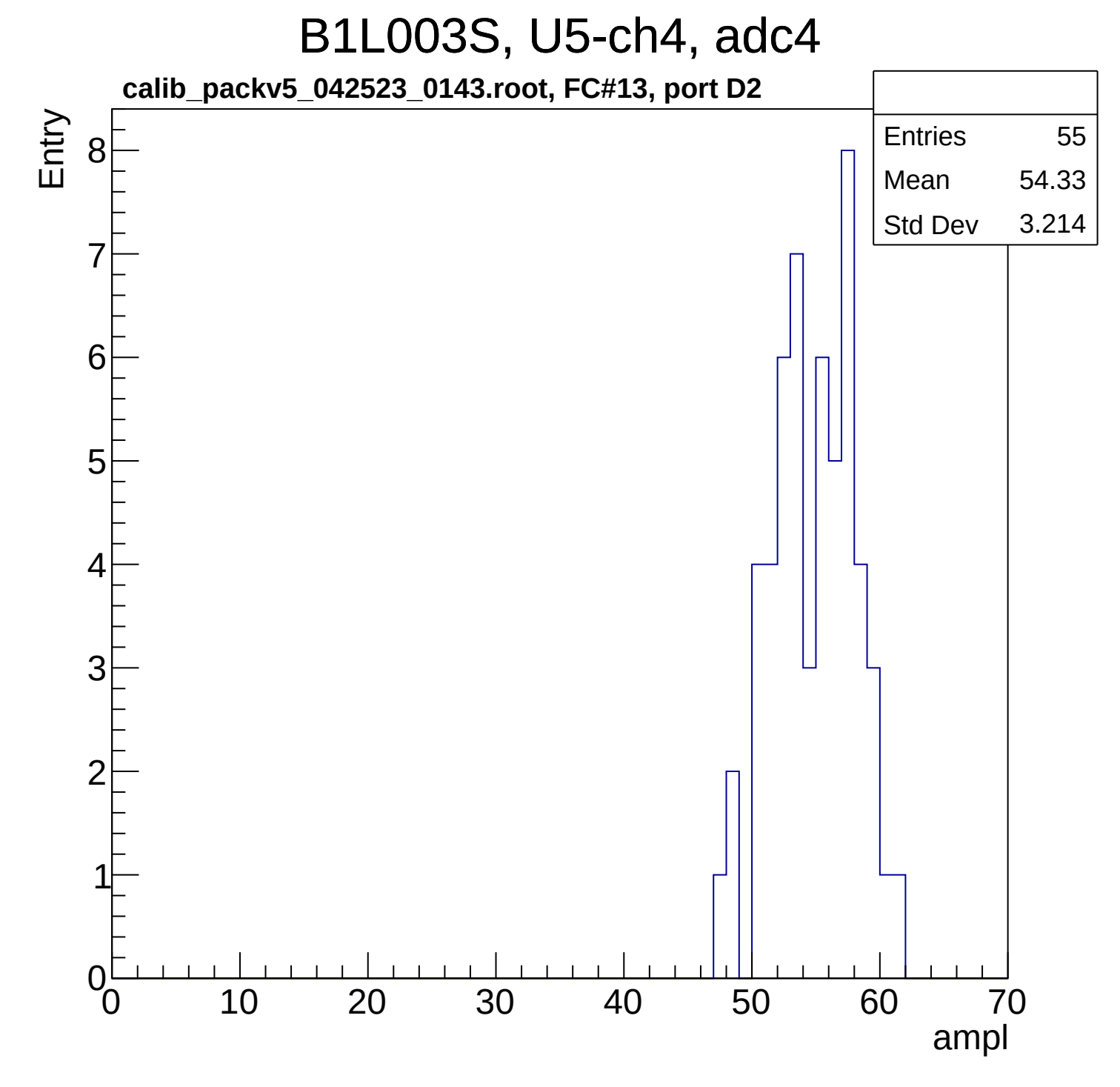
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch4, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.33
Std Dev 3.214
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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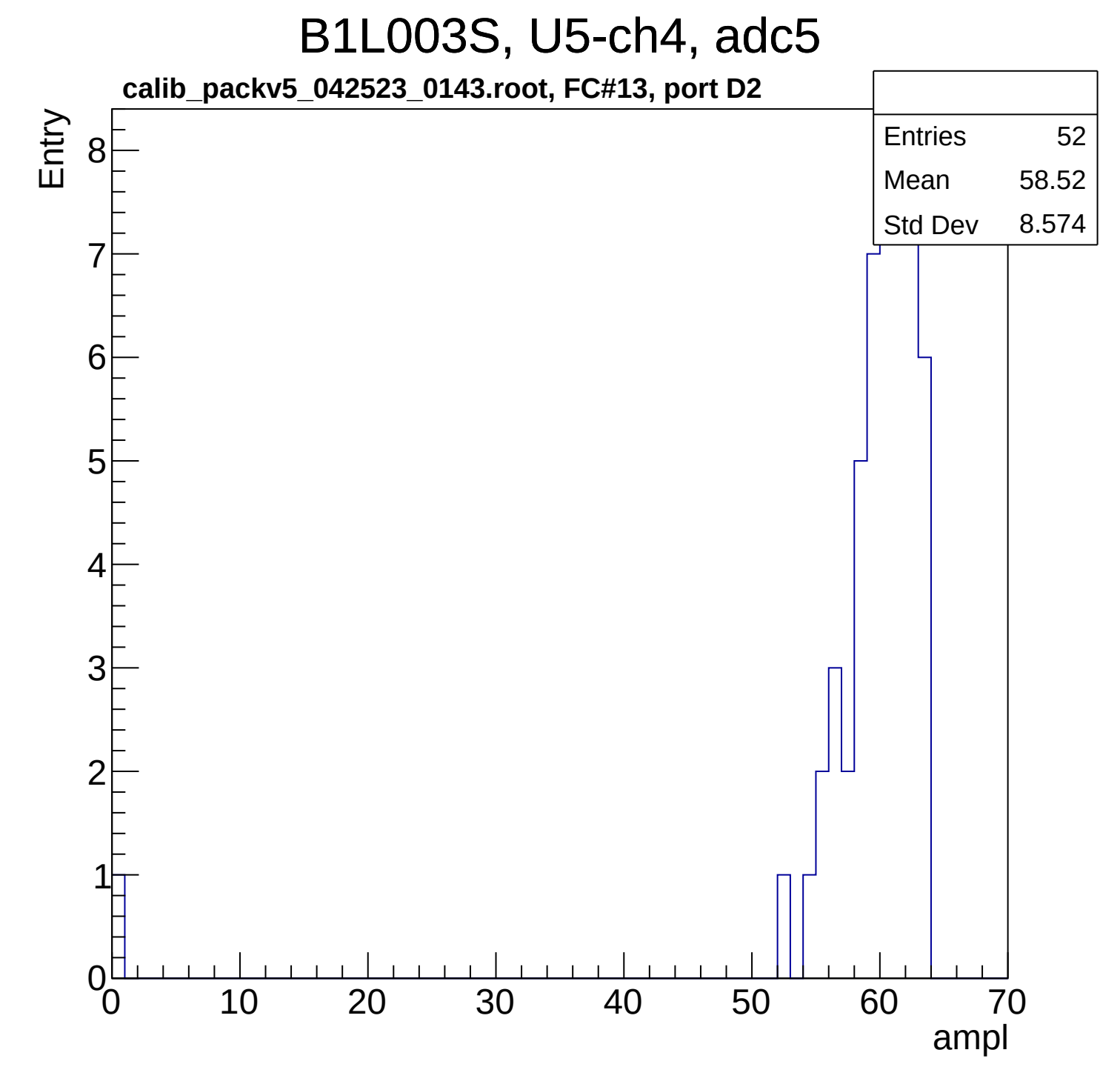
B1L003S, U5-ch4, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.52
Std Dev 8.574
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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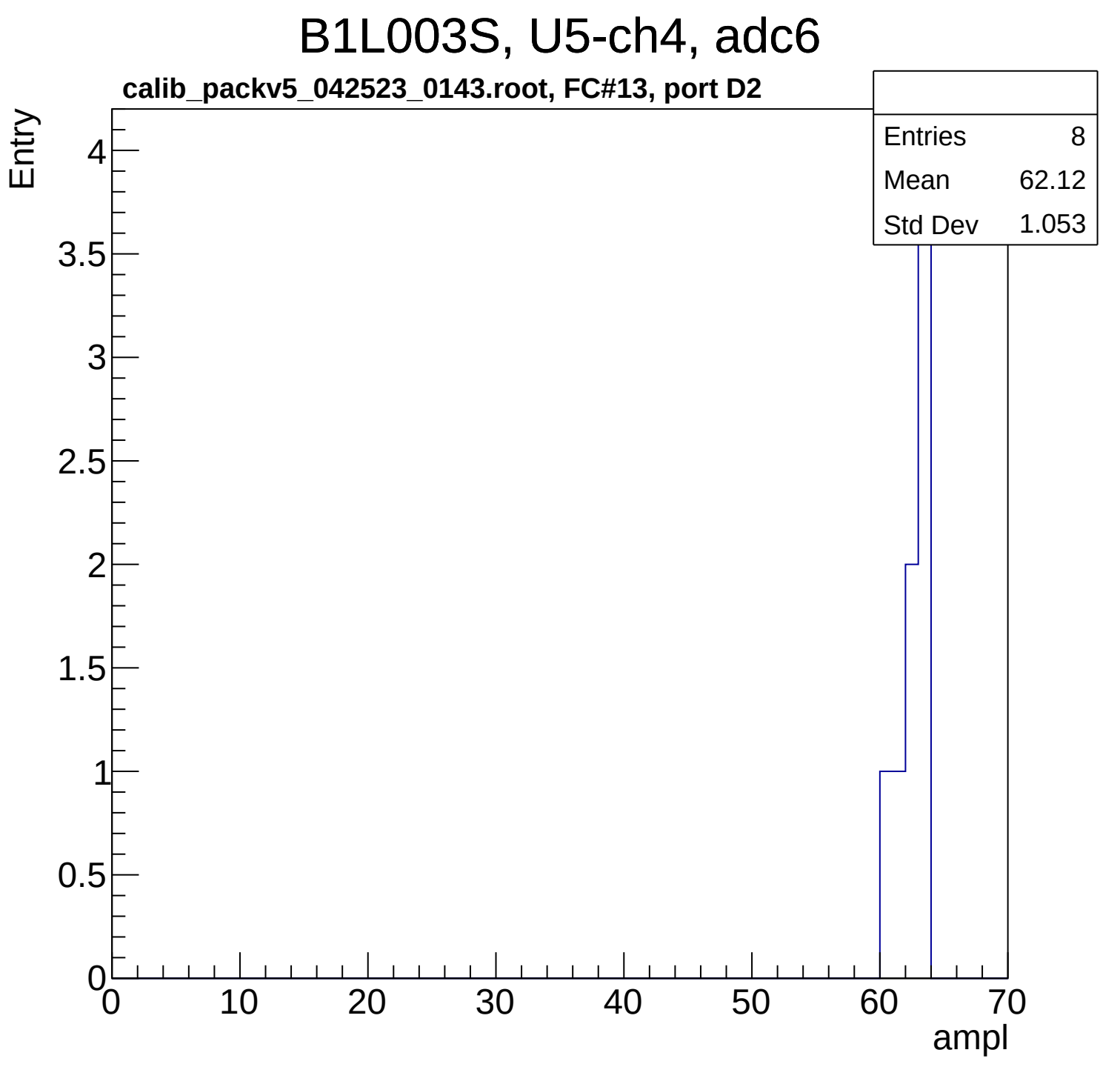
B1L003S, U5-ch4, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
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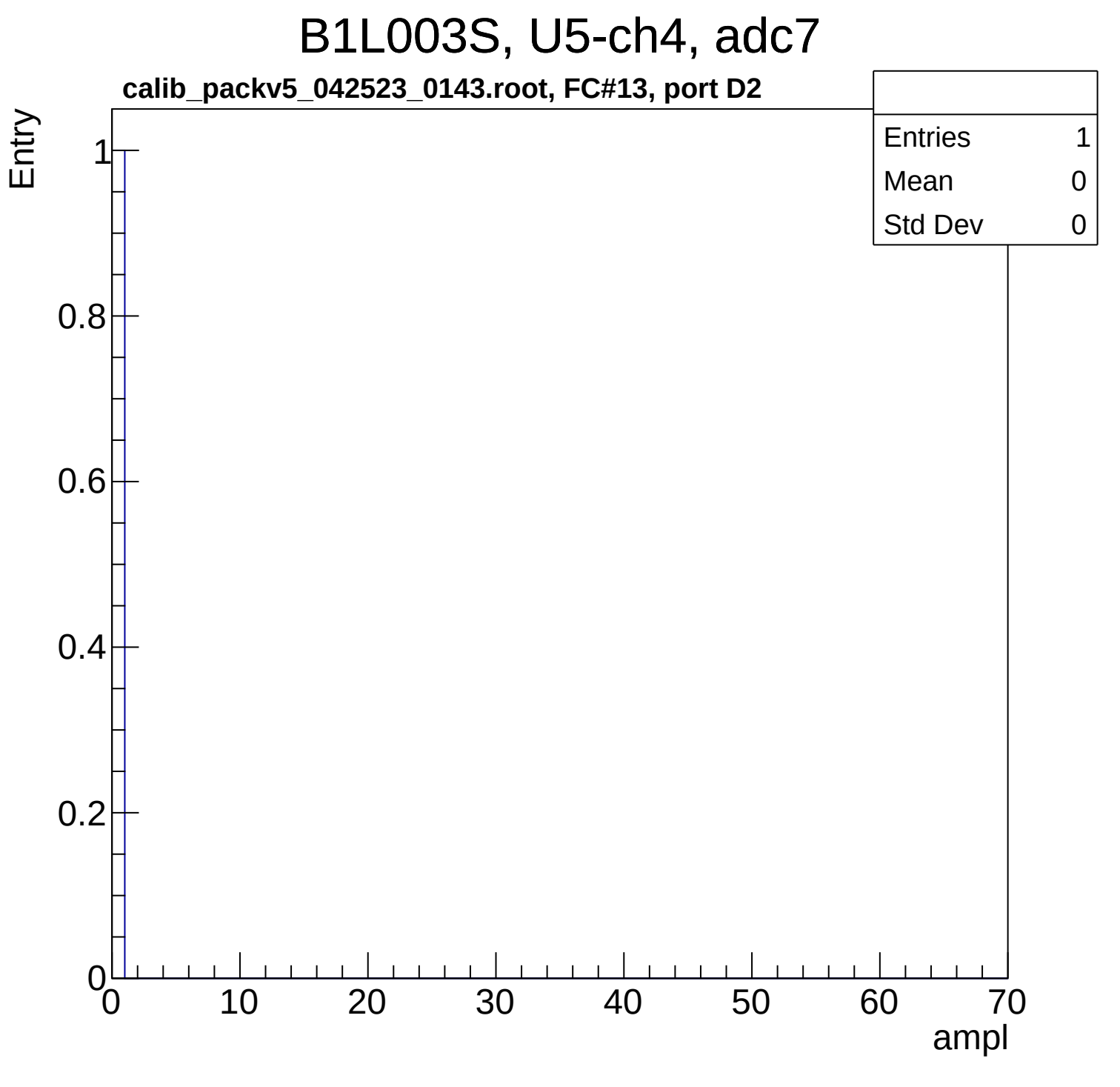
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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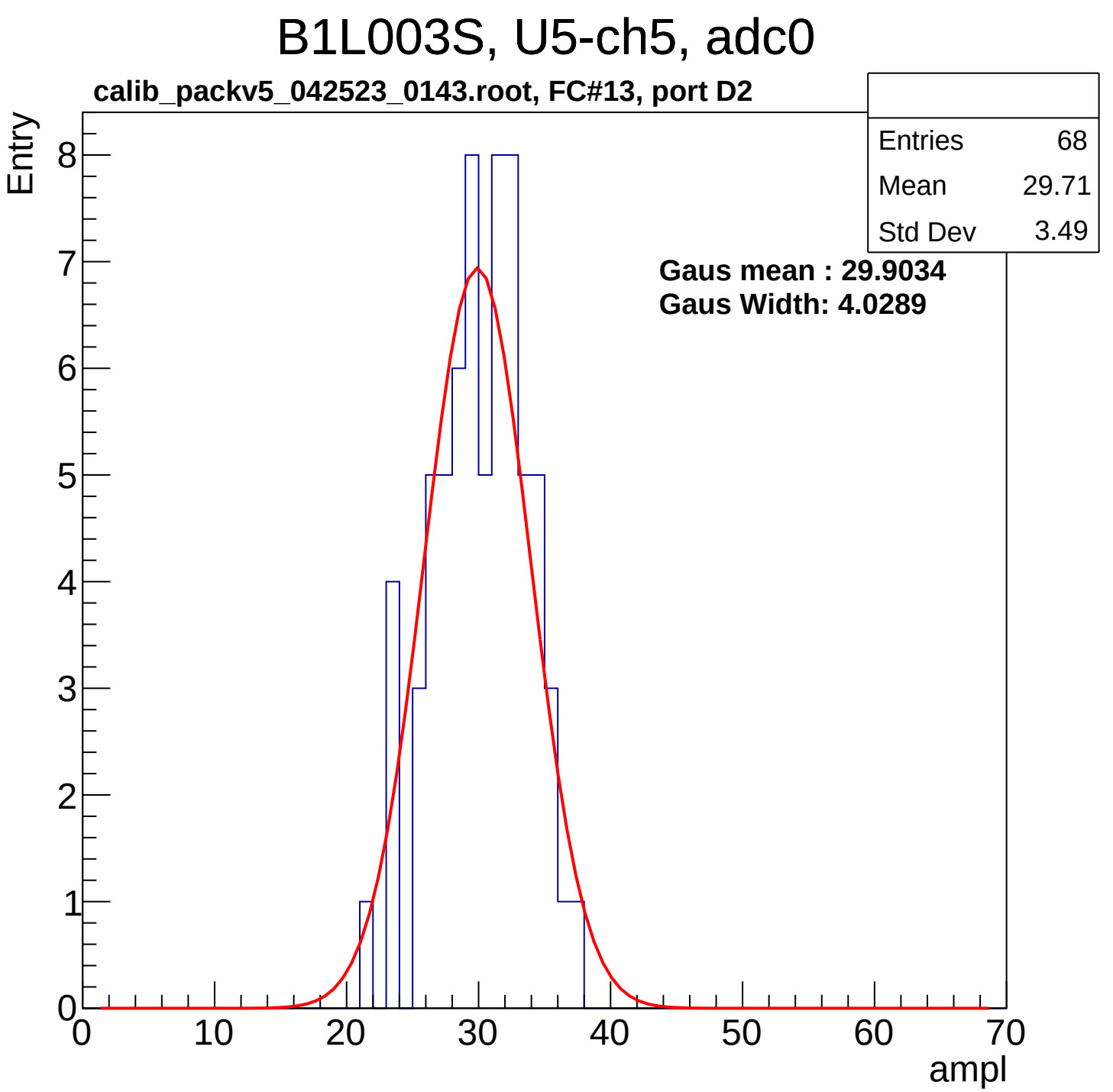
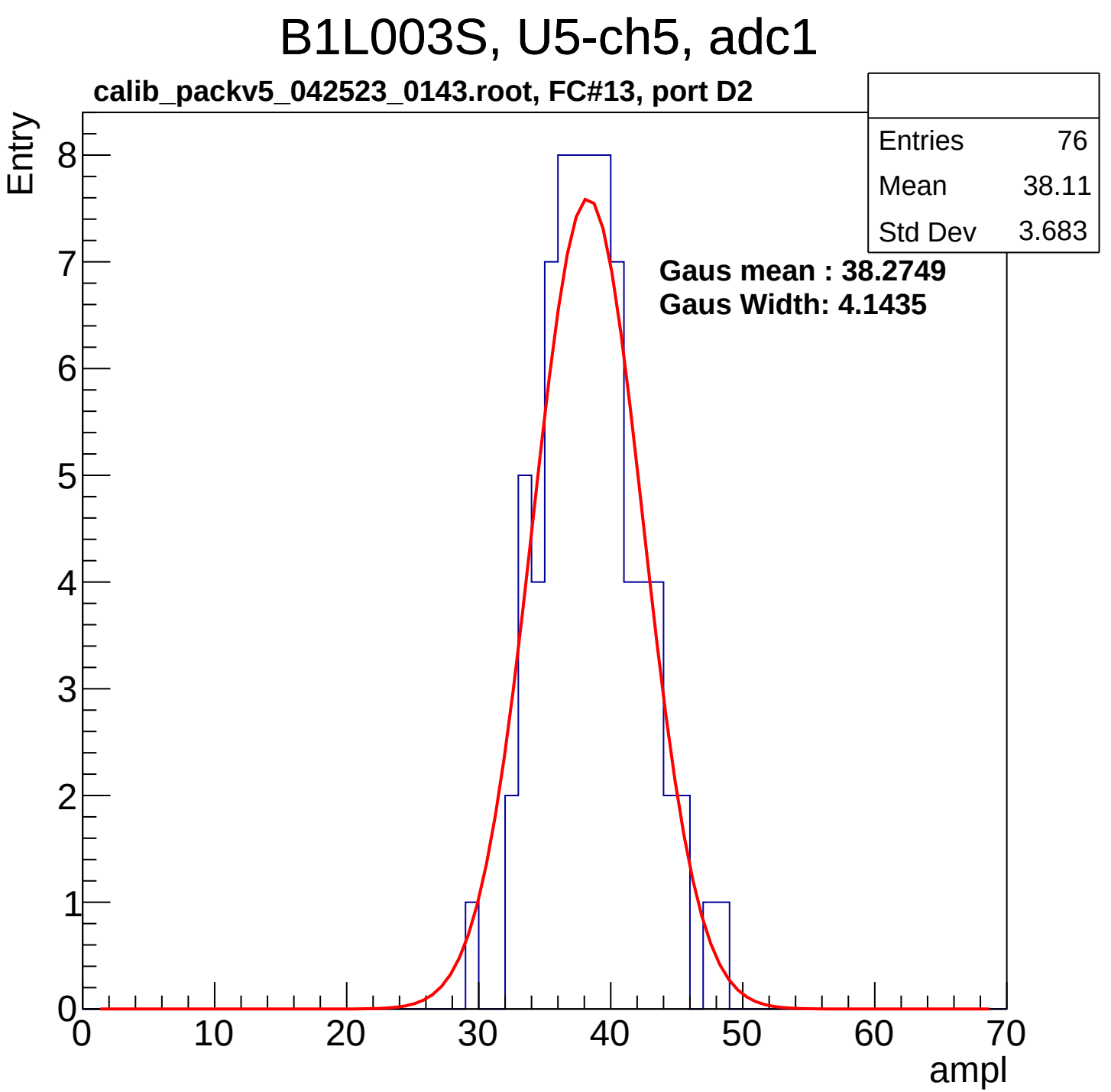
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch5, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.71
Std Dev 3.49
Gaus mean : 29.9034
Gaus Width: 4.0289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch5, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 38.11
Std Dev 3.683
Gaus mean : 38.2749
Gaus Width: 4.1435
Entry
ampl
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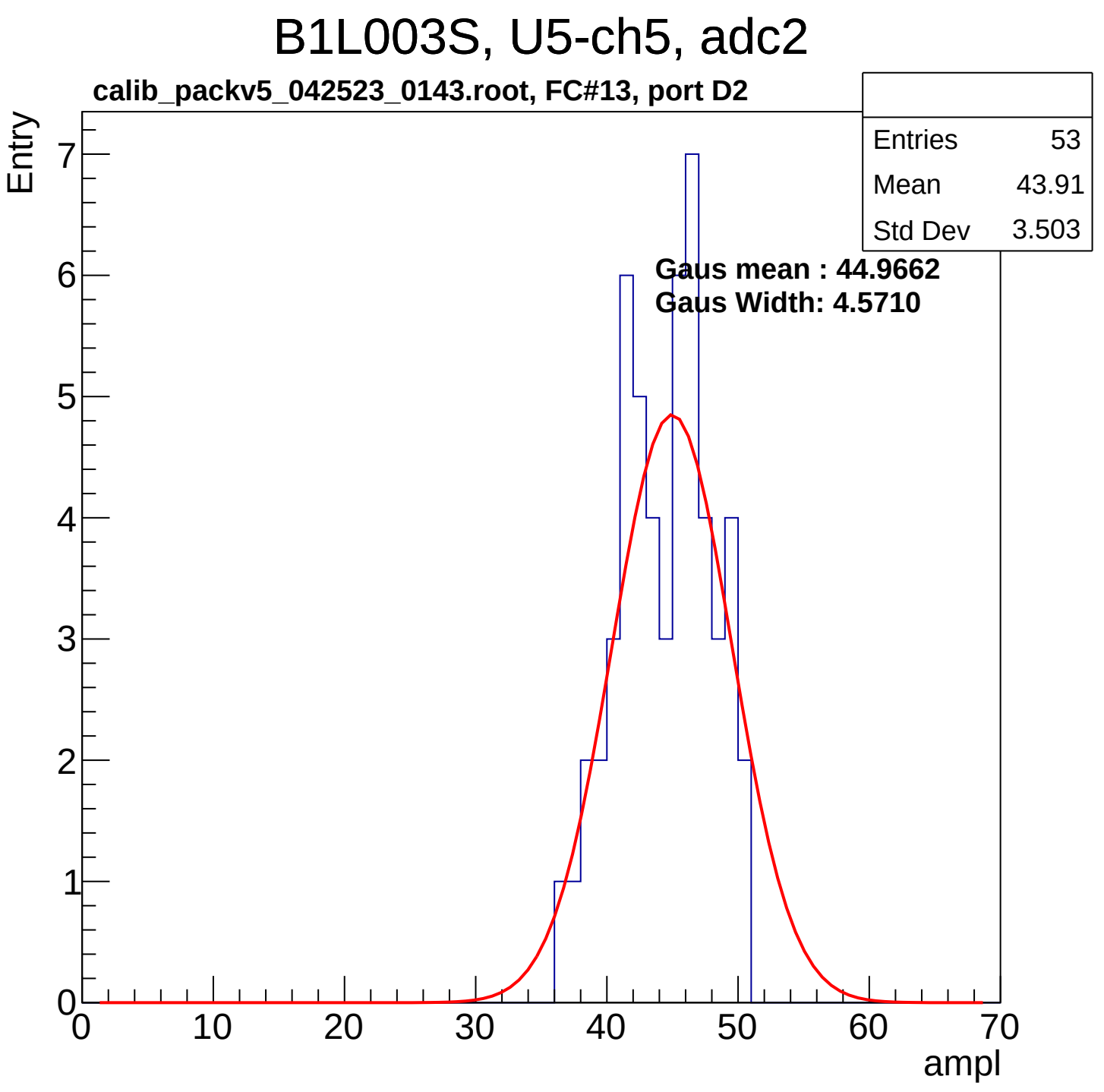
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch5, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 43.91
Std Dev 3.503
Gaus mean : 44.9662
Gaus Width: 4.5710
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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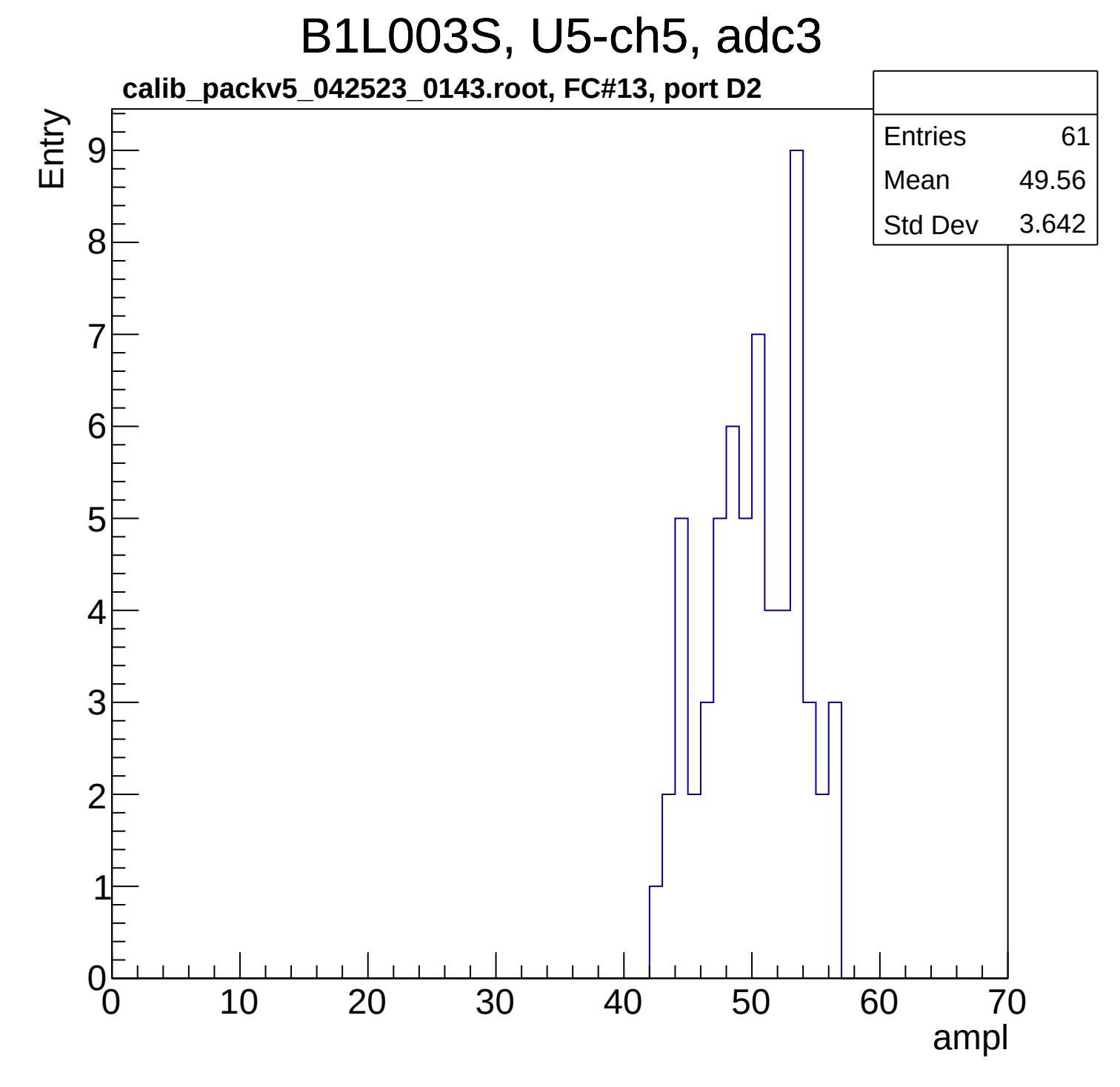
B1L003S, U5-ch5, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.56
Std Dev 3.642
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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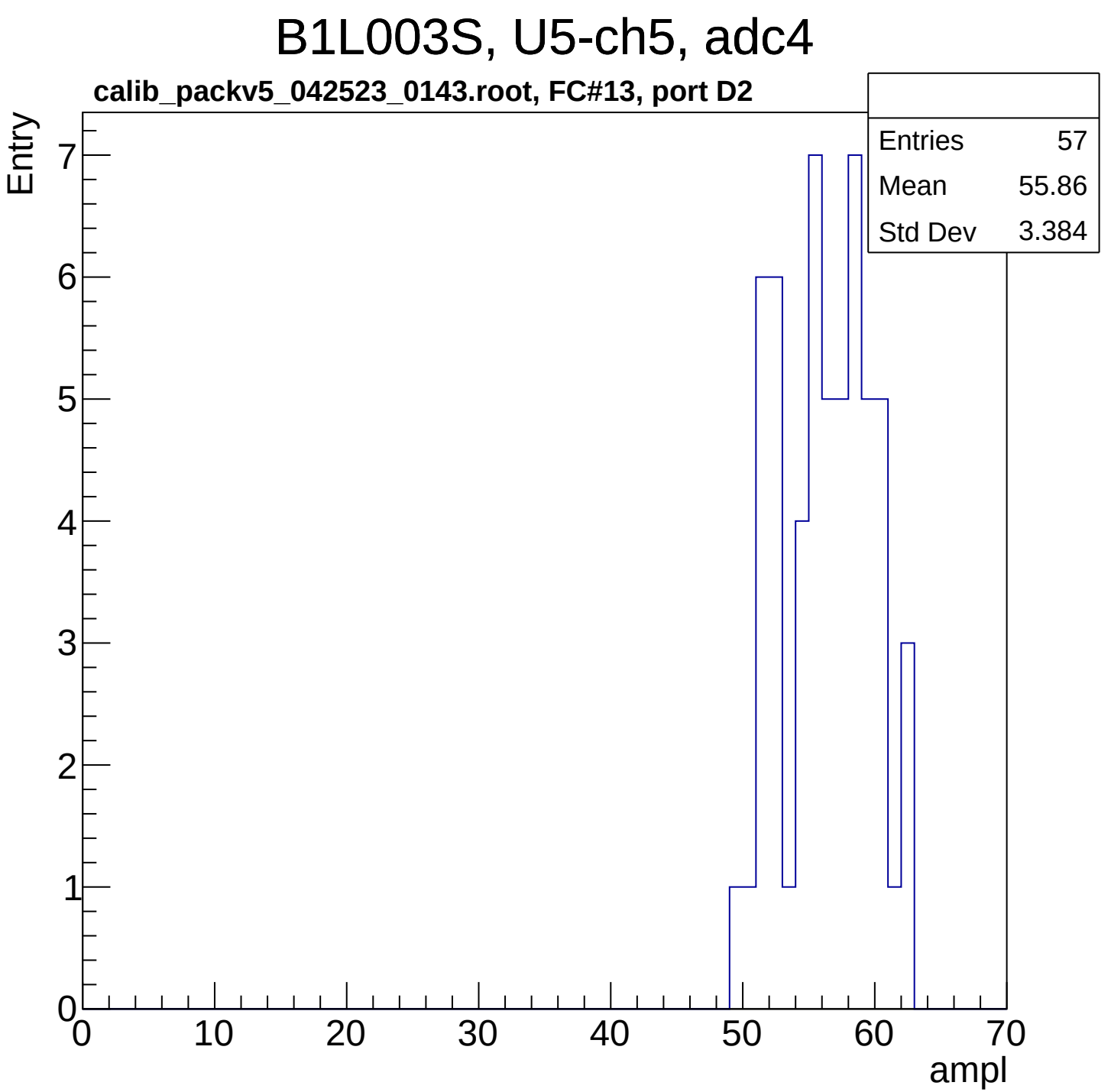
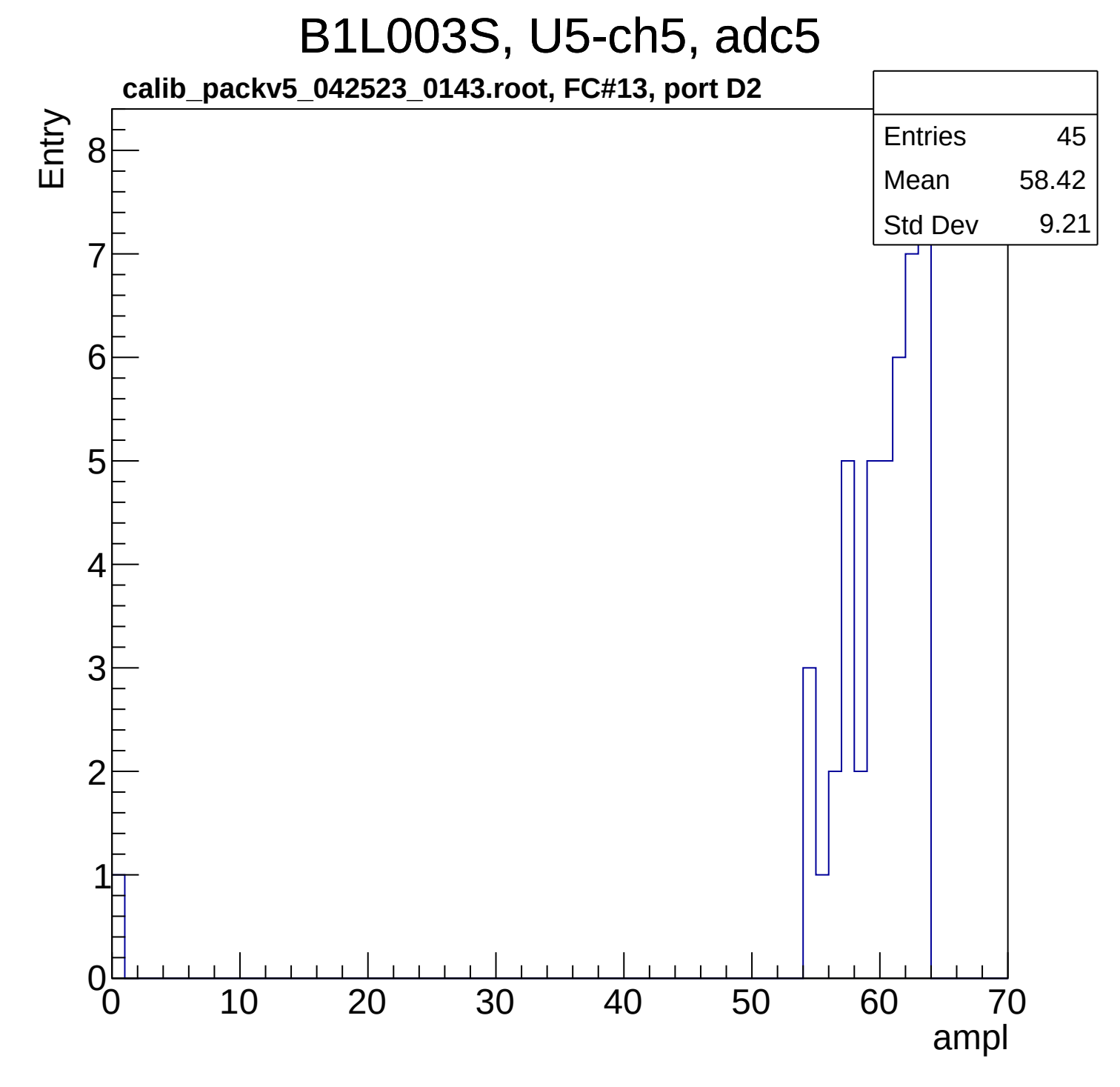

B1L003S, U5-ch5, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.86
Std Dev 3.384
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch5, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.42
Std Dev 9.21
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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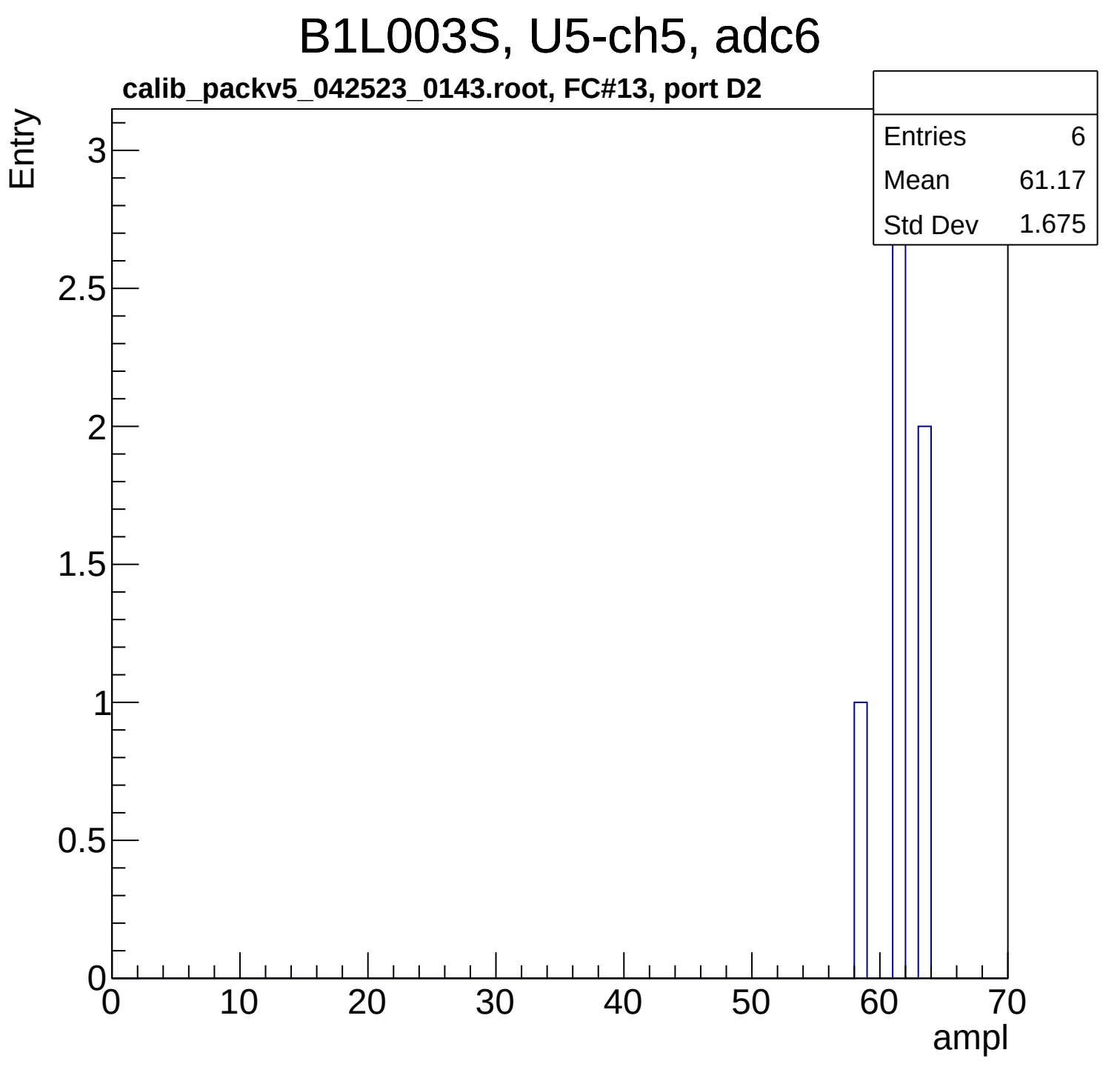
B1L003S, U5-ch5, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.17
Std Dev 1.675
Entry
ampl
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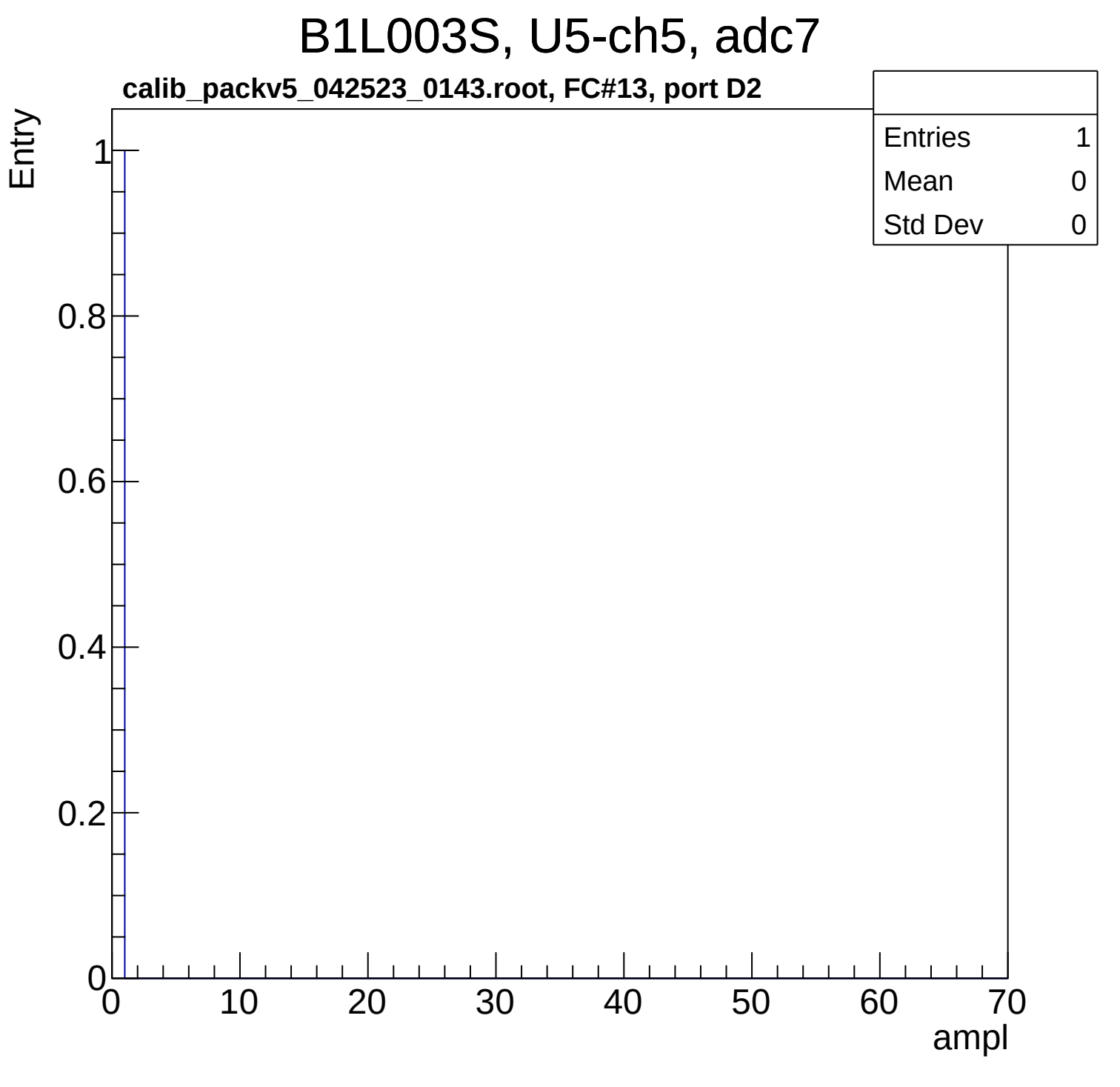
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch5, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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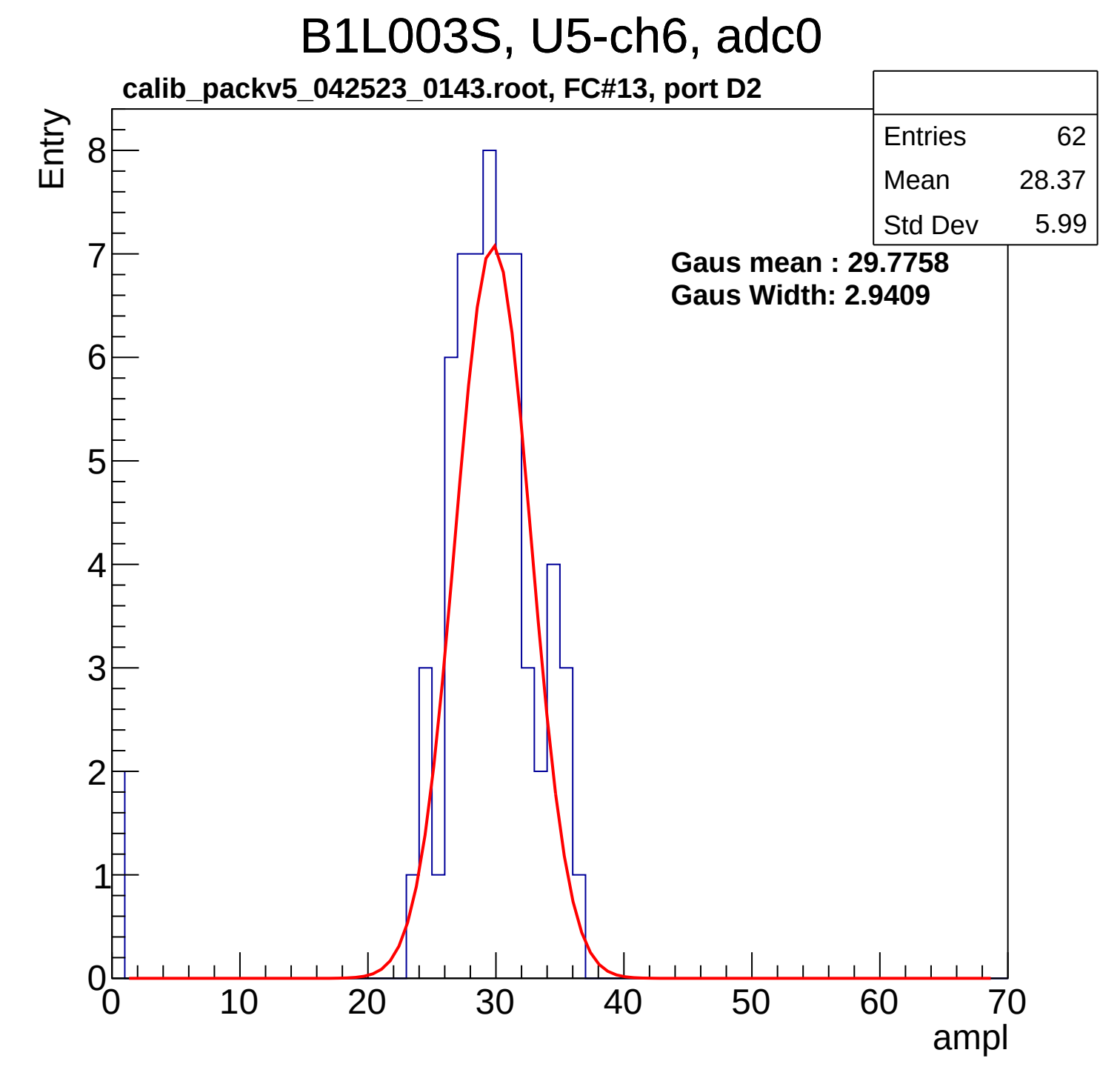
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch6, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.37
Std Dev 5.99
Gaus mean : 29.7758
Gaus Width: 2.9409
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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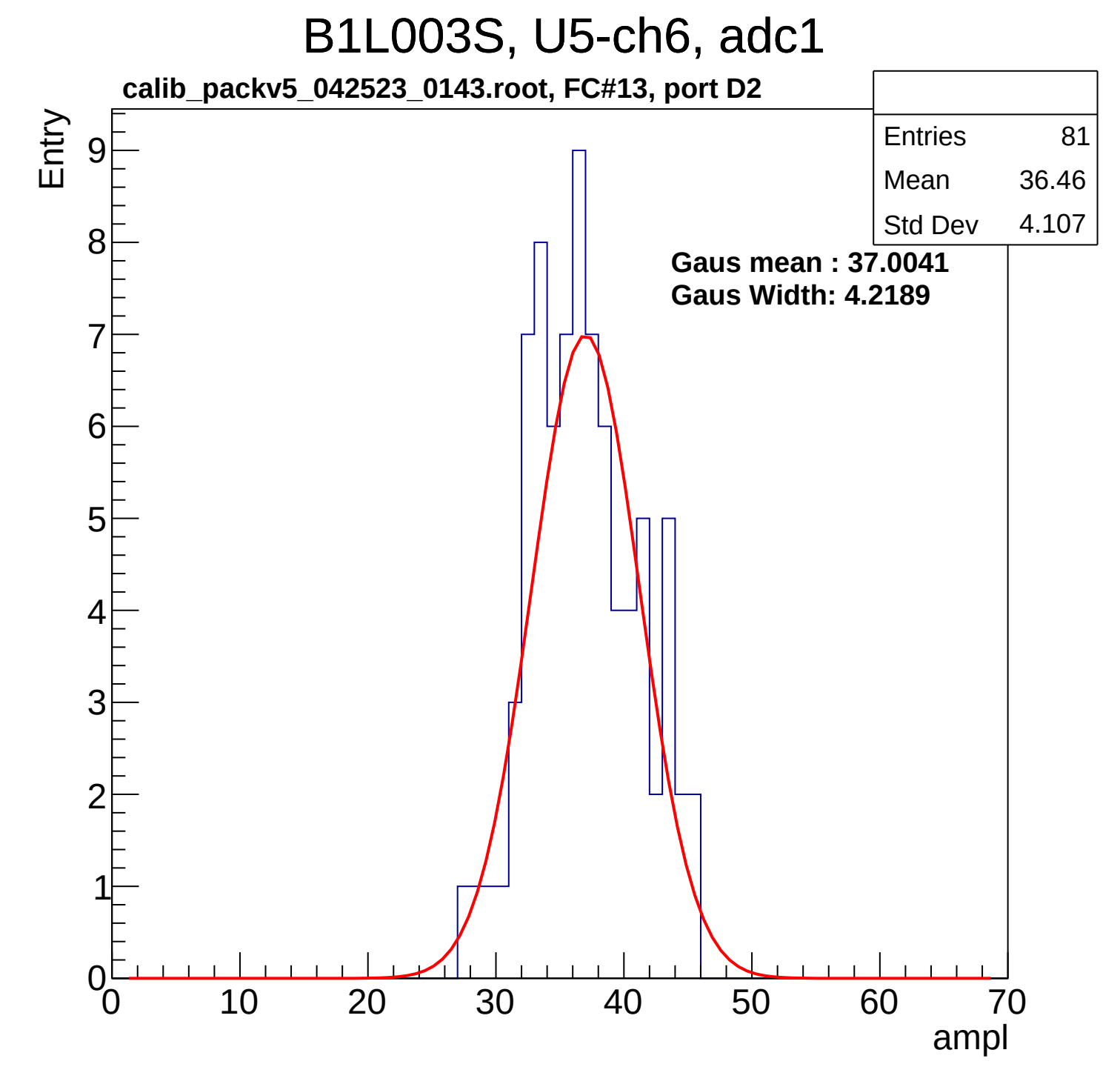
60
70


B1L003S, U5-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.46
Std Dev 4.107
Gaus mean : 37.0041
Gaus Width: 4.2189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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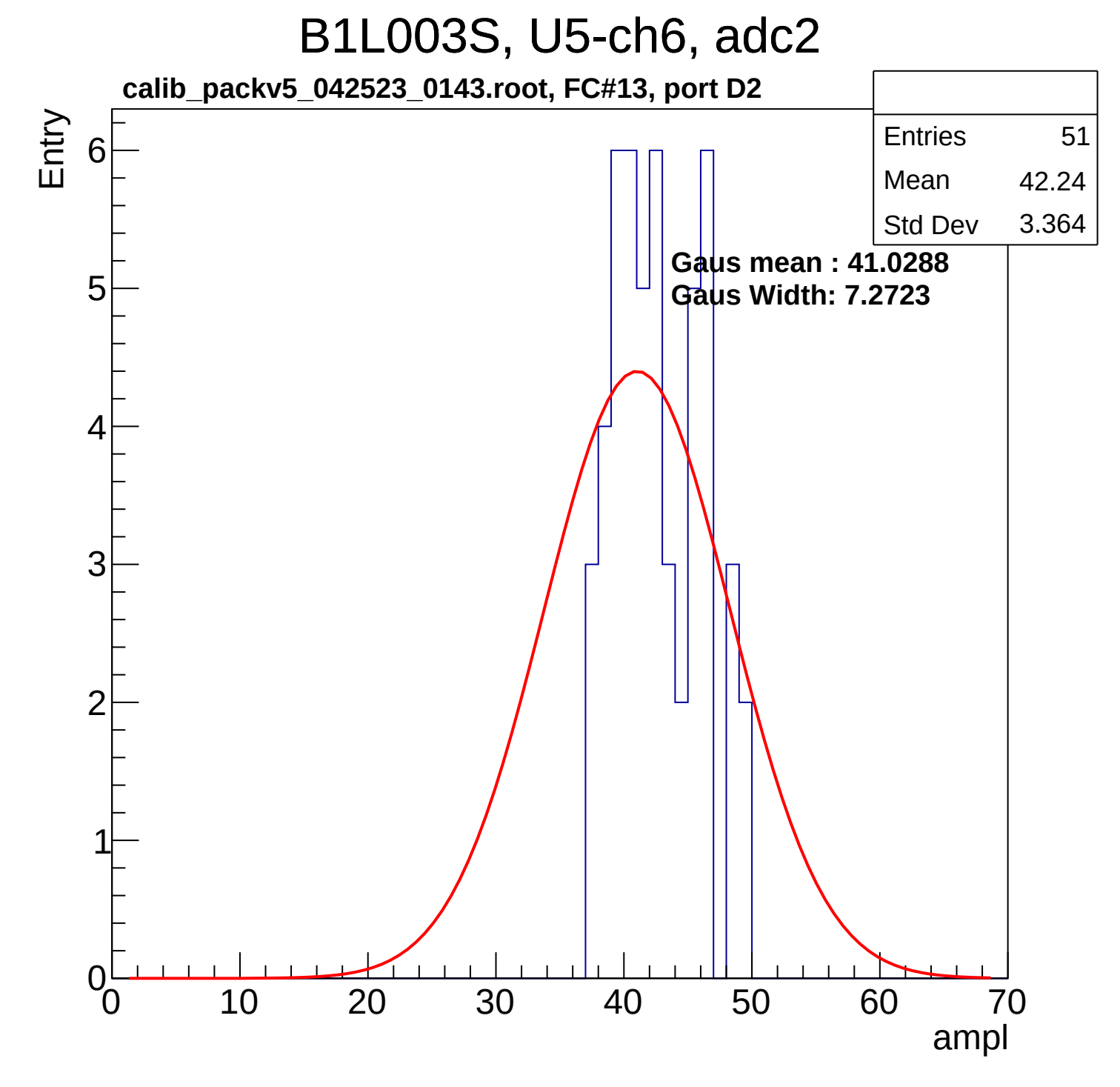
B1L003S, U5-ch6, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 42.24
Std Dev 3.364
Gaus mean : 41.0288
Gaus Width: 7.2723
Entry
ampl
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3
4
5
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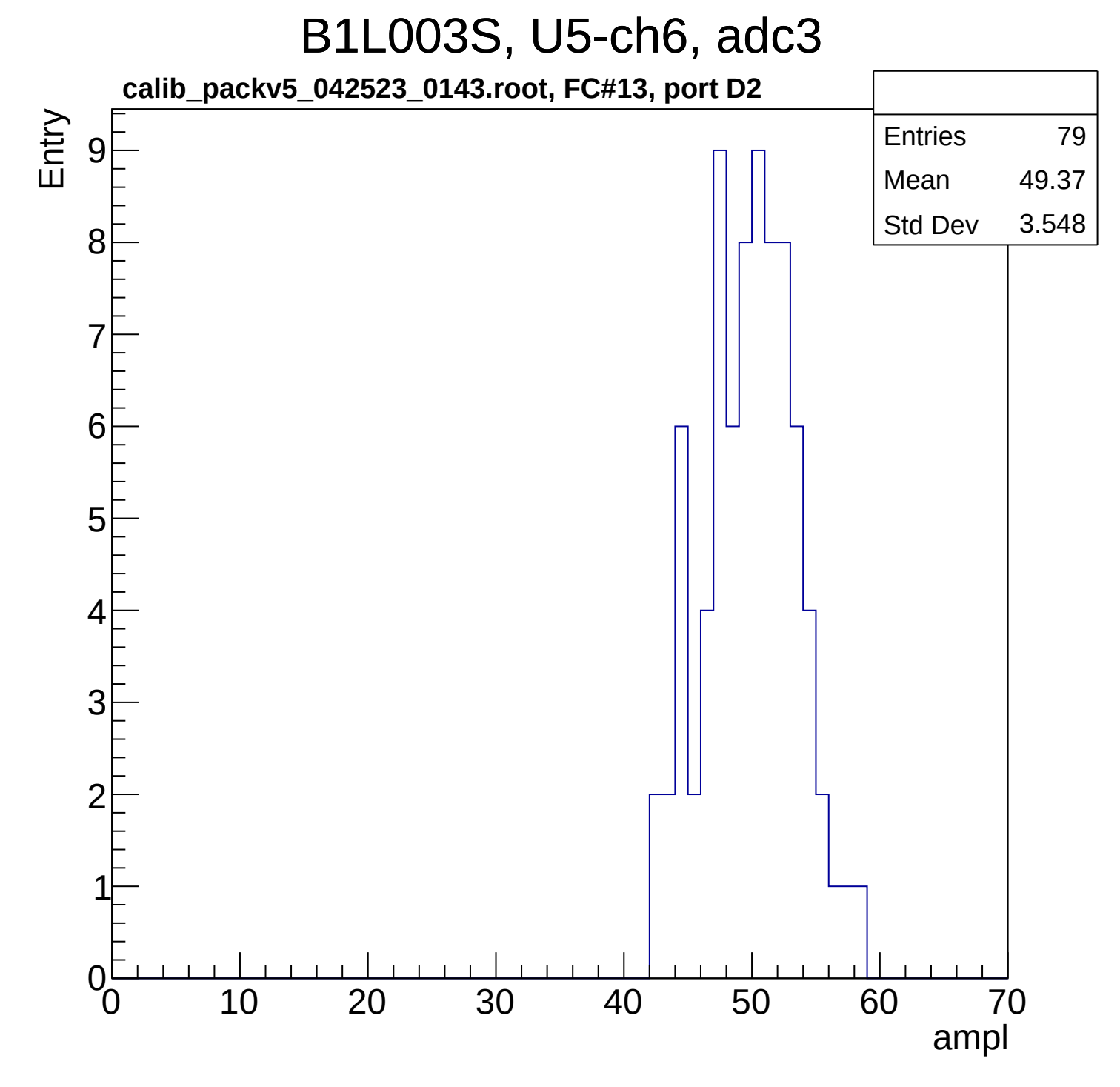
B1L003S, U5-ch6, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.37
Std Dev 3.548
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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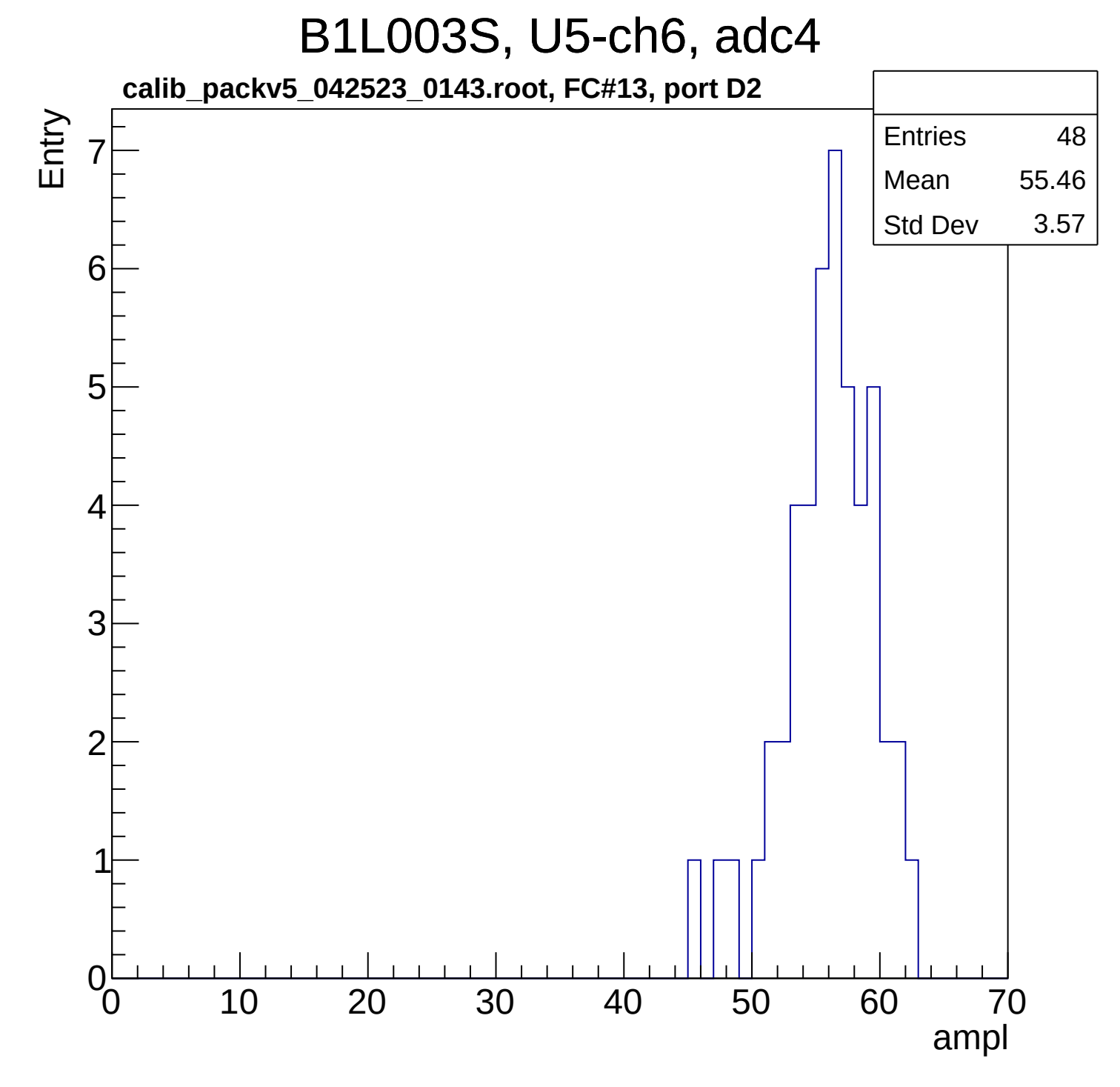
B1L003S, U5-ch6, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.46
Std Dev 3.57
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
50
60
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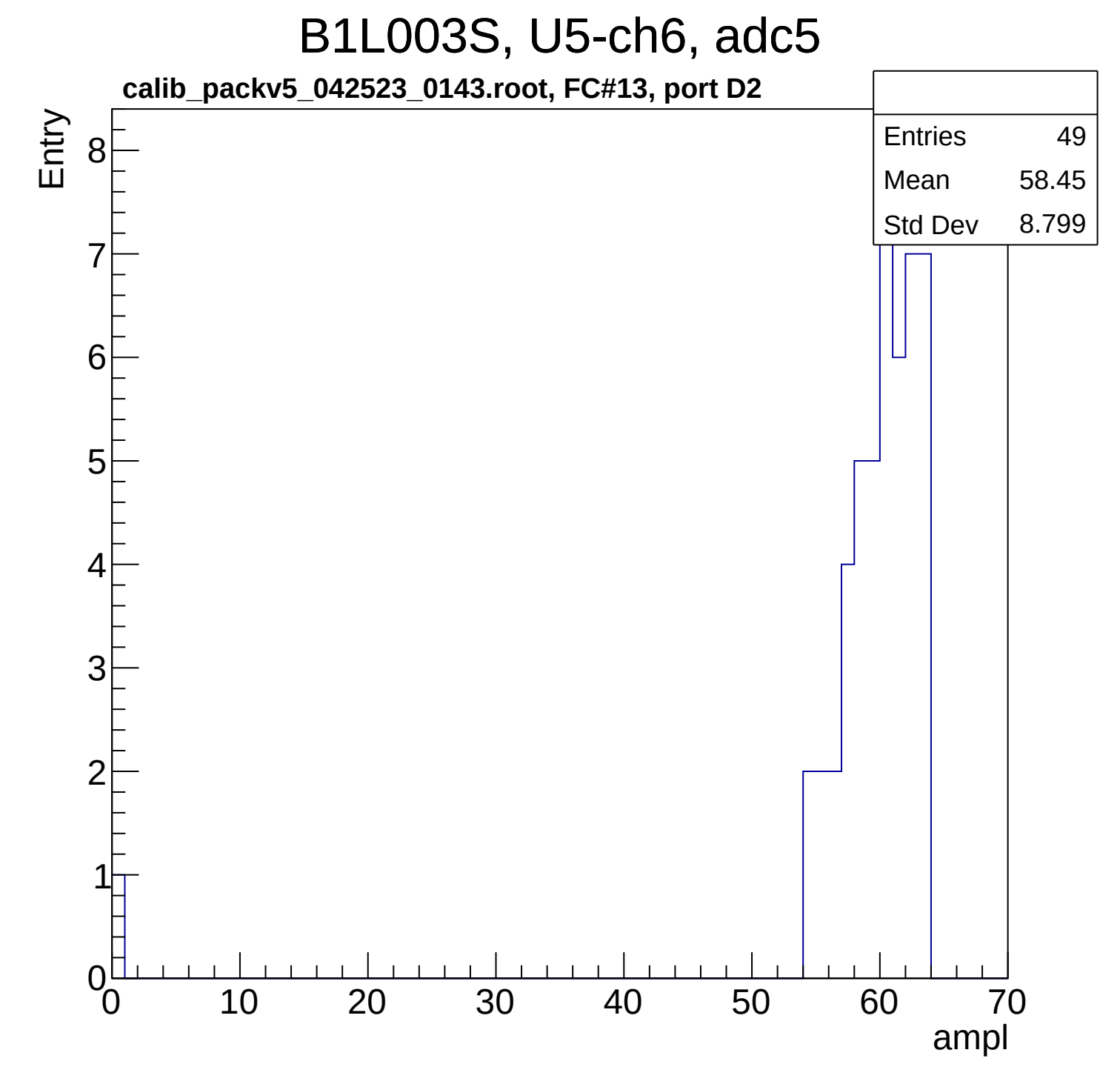
B1L003S, U5-ch6, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.45
Std Dev 8.799
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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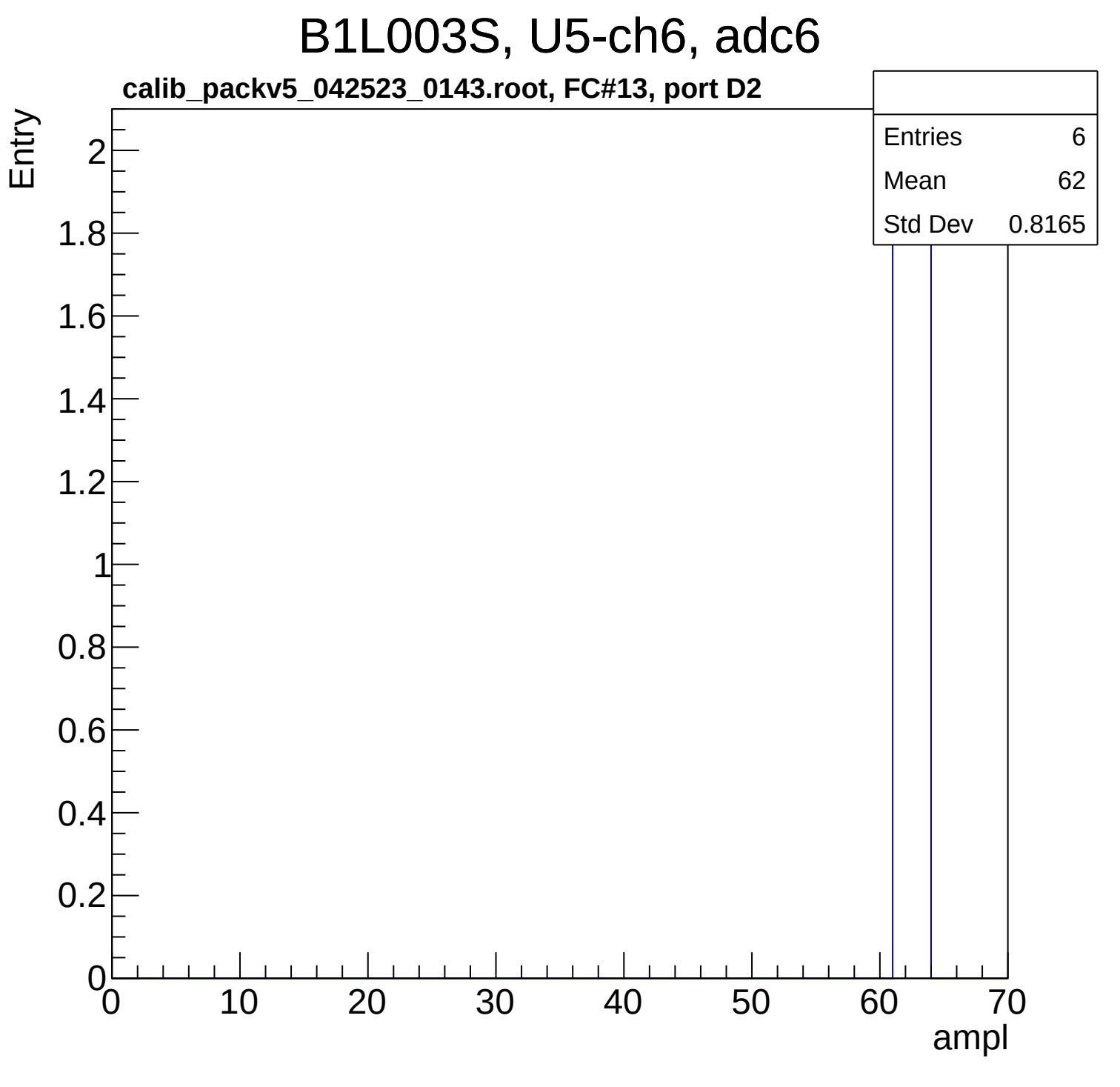
B1L003S, U5-ch6, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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40
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60
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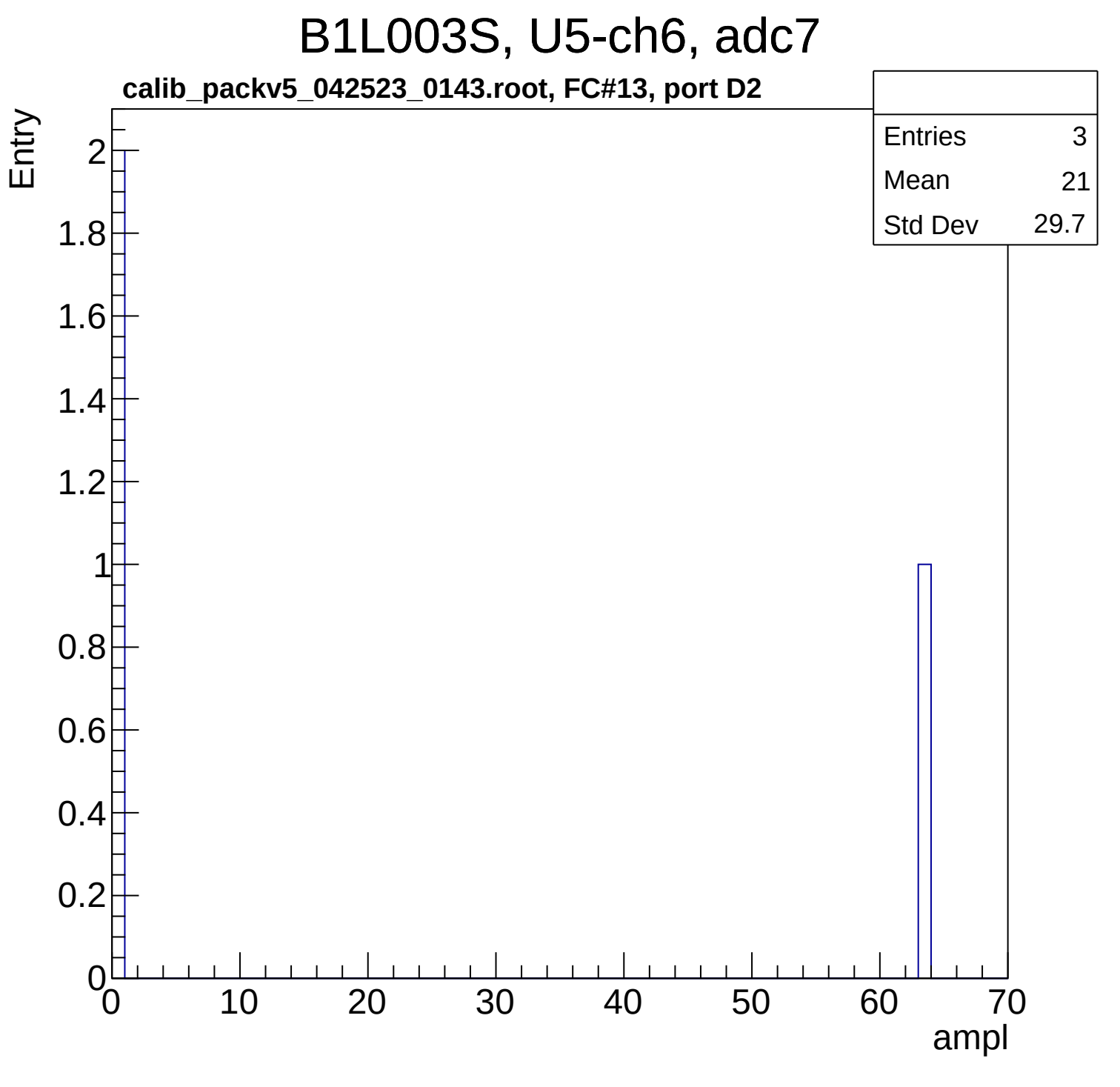
B1L003S, U5-ch6, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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0
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60
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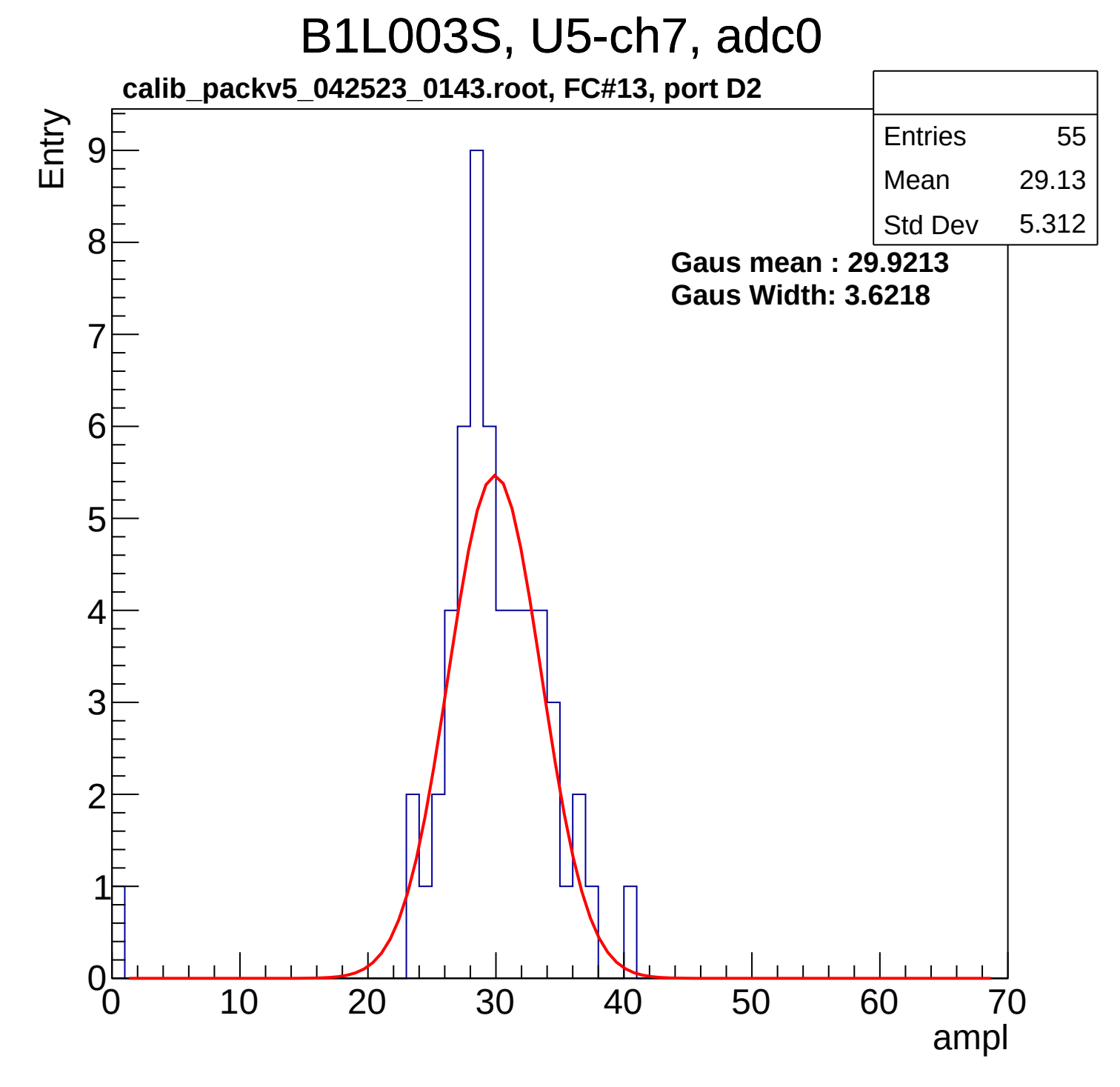
B1L003S, U5-ch7, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.13
Std Dev 5.312
Gaus mean : 29.9213
Gaus Width: 3.6218
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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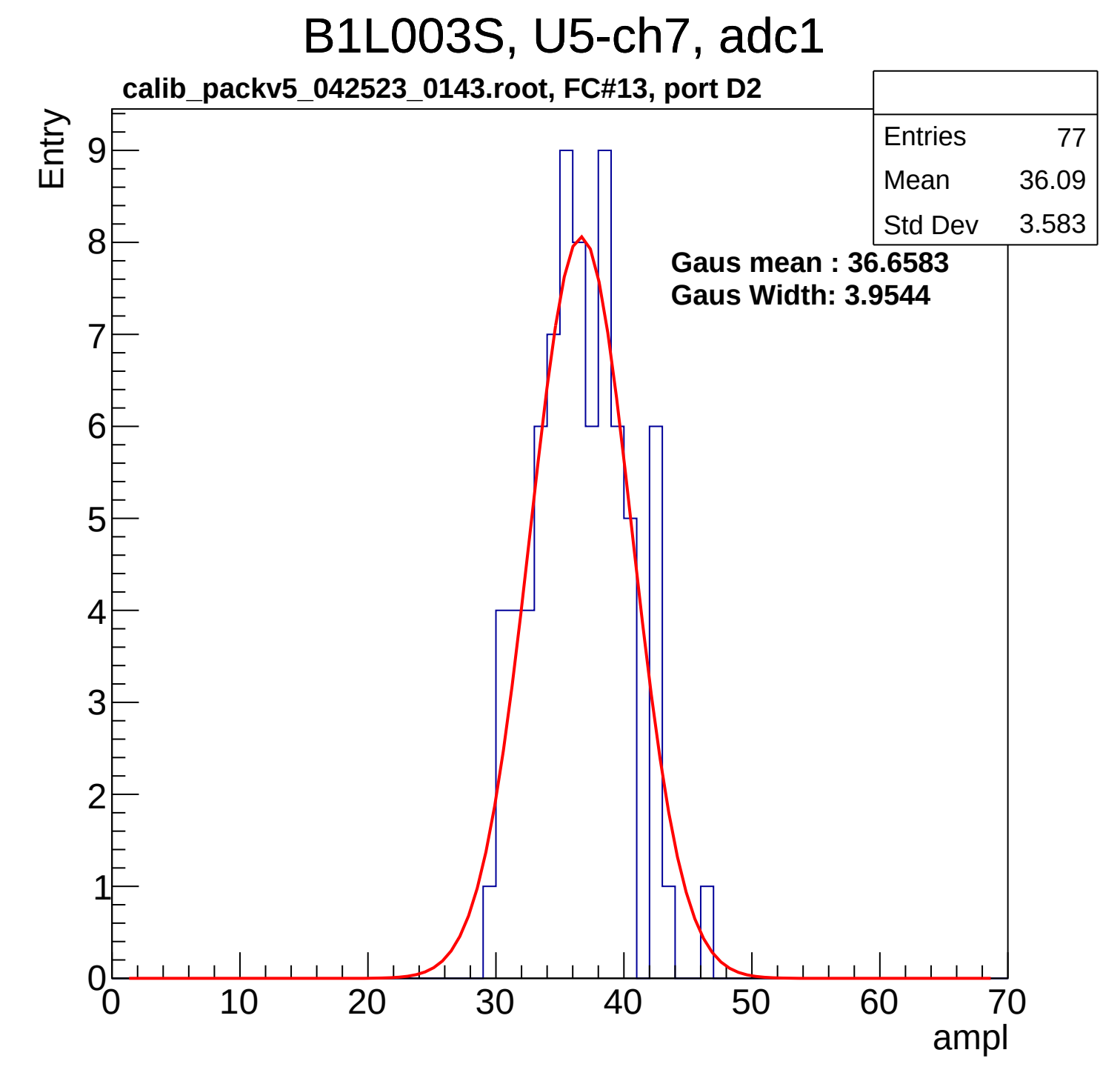
B1L003S, U5-ch7, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.09
Std Dev 3.583
Gaus mean : 36.6583
Gaus Width: 3.9544
Entry
ampl
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2
3
4
5
6
7
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9
0
10
20
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40
50
60
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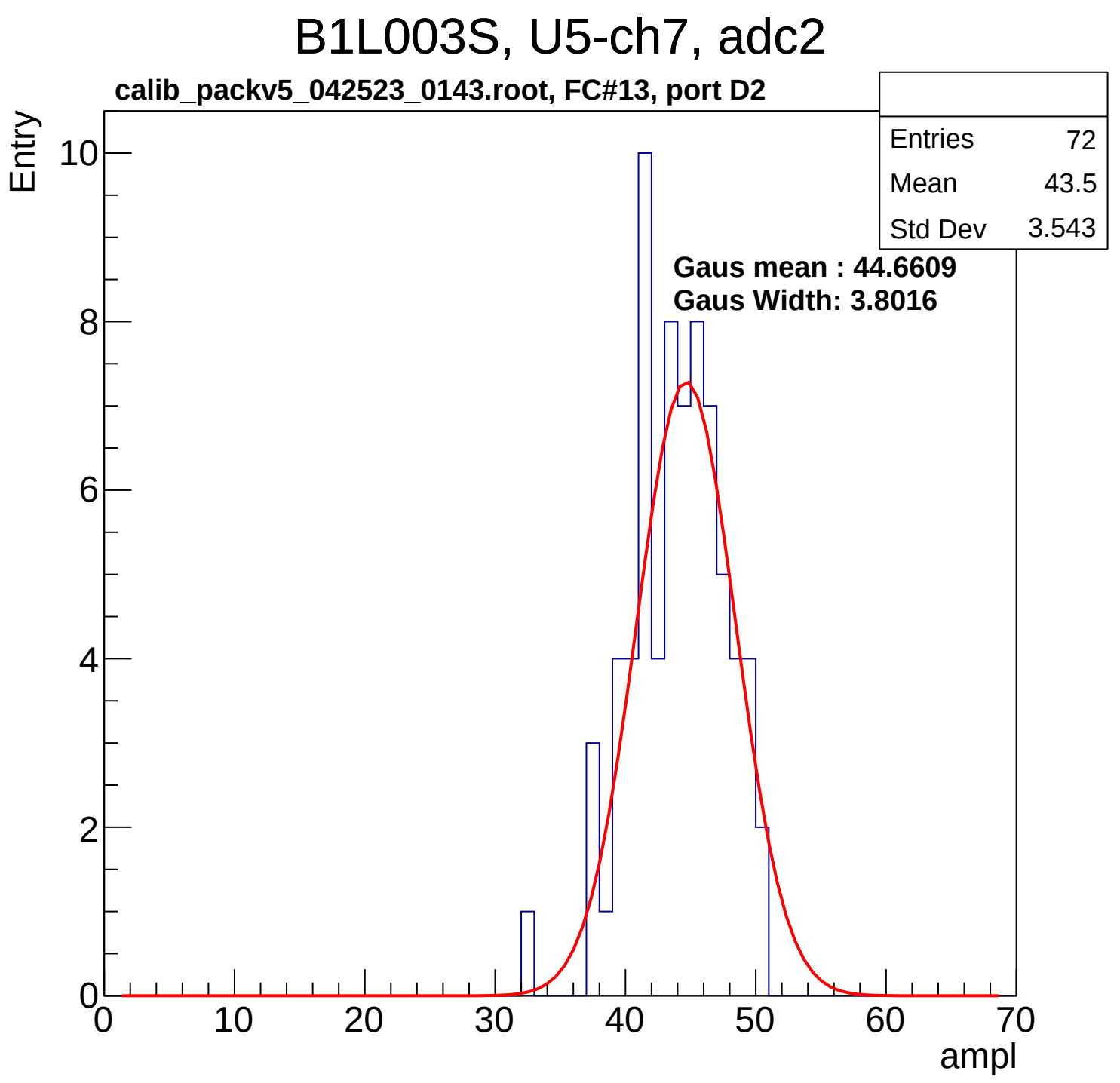
B1L003S, U5-ch7, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 43.5
Std Dev 3.543
Gaus mean : 44.6609
Gaus Width: 3.8016
Entry
ampl
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4
6
8
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0
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20
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40
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60
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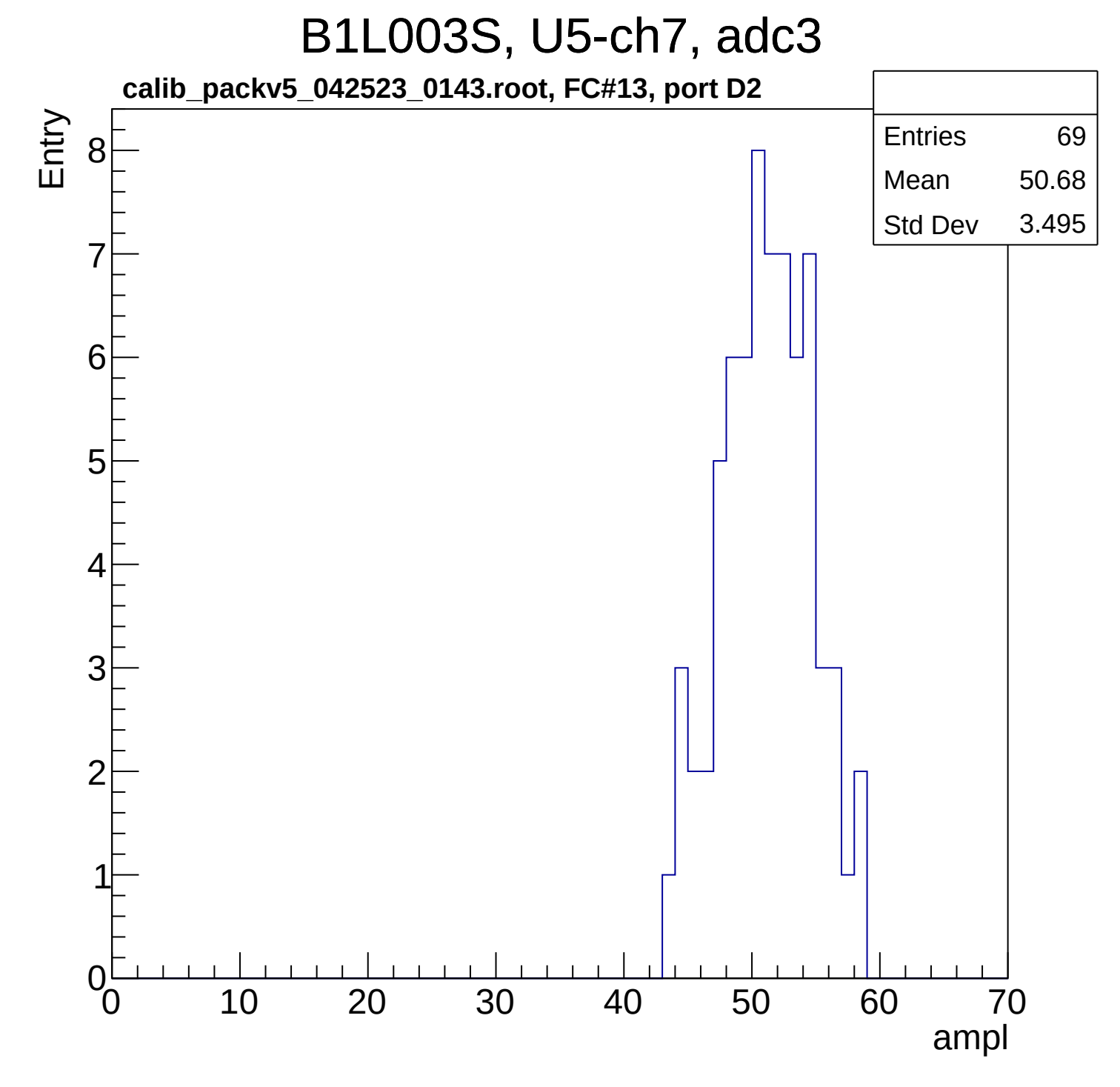
B1L003S, U5-ch7, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.68
Std Dev 3.495
Entry
ampl
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3
4
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6
7
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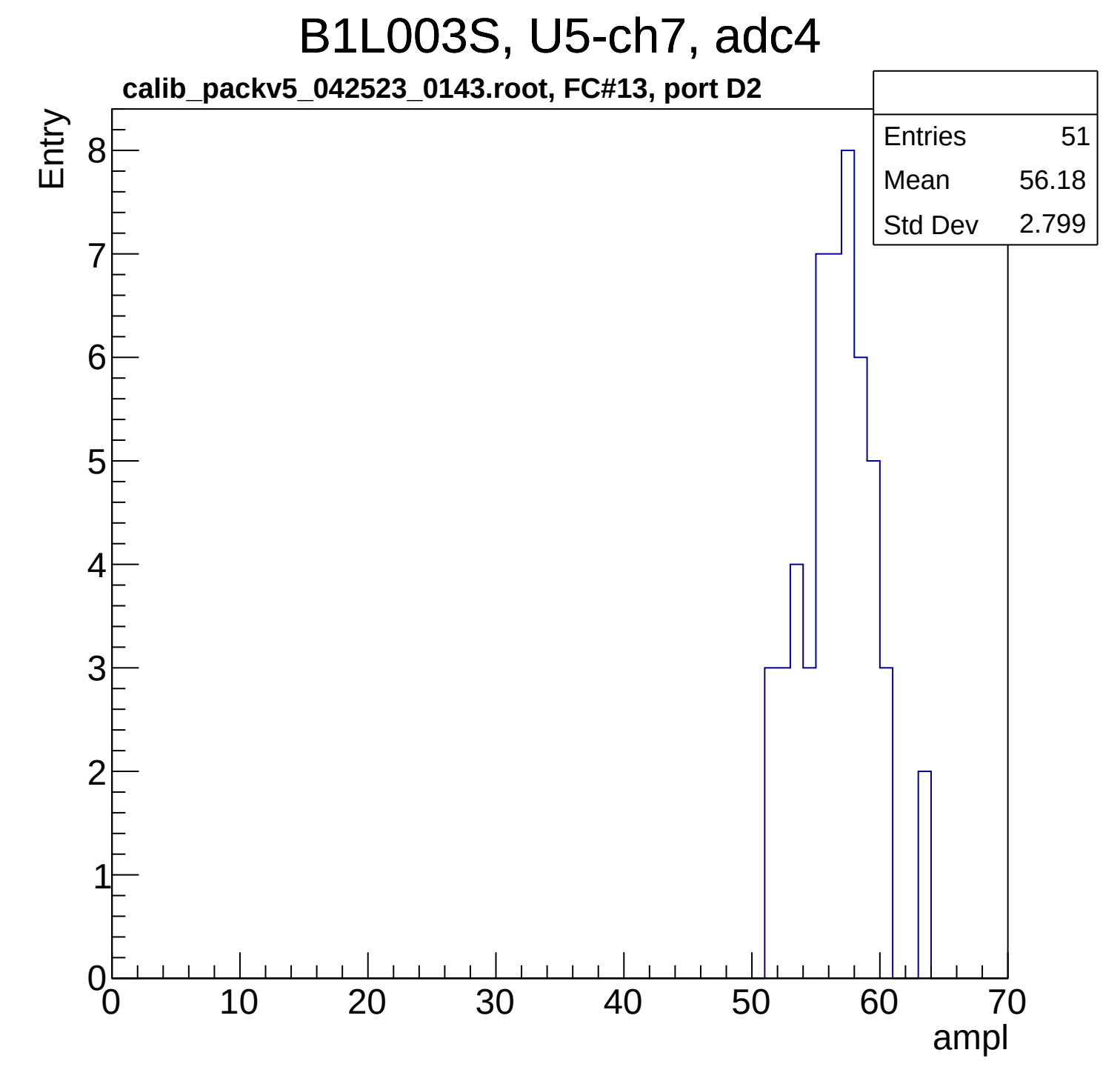
B1L003S, U5-ch7, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 56.18
Std Dev 2.799
Entry
ampl
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5
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7
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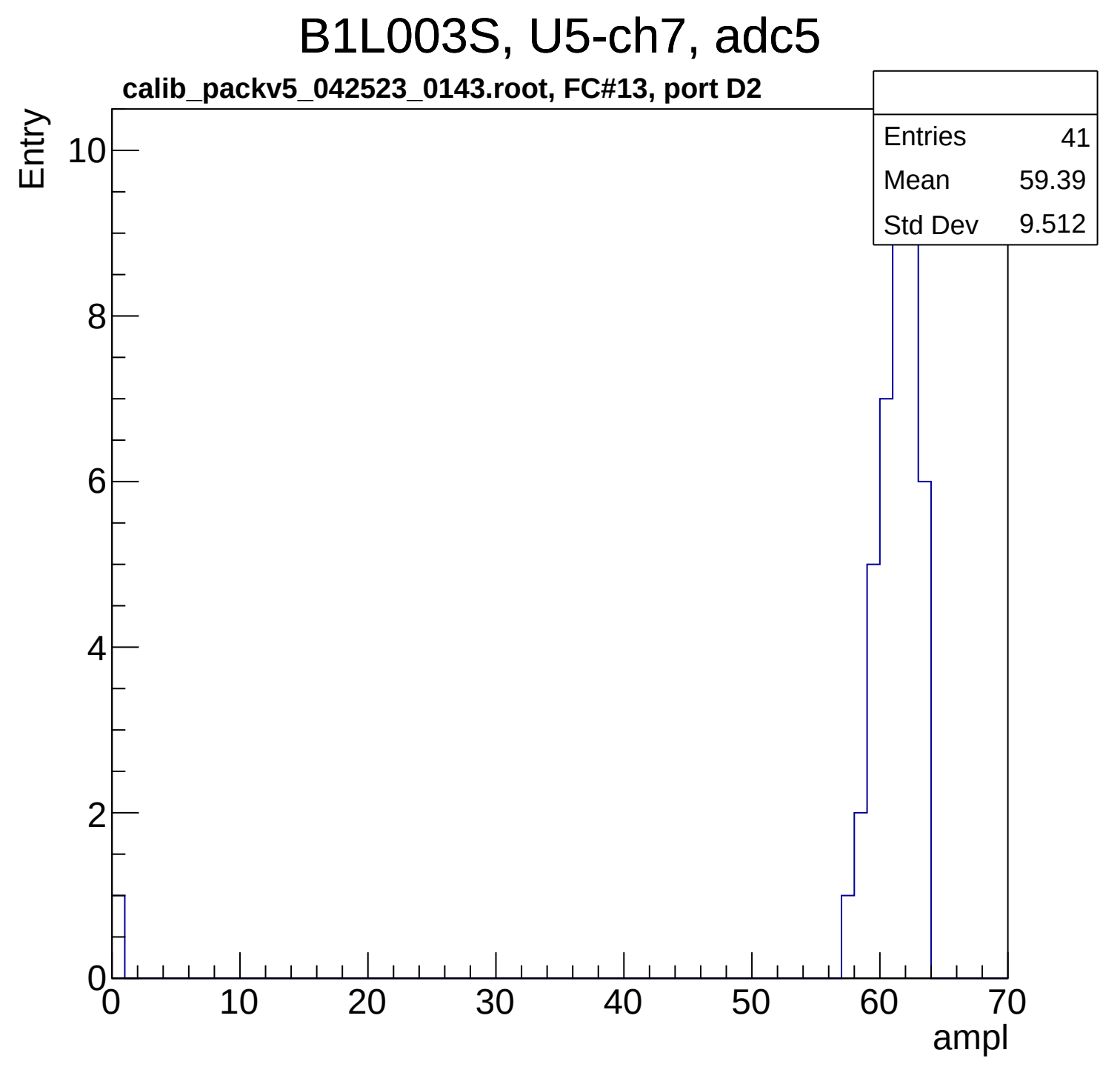
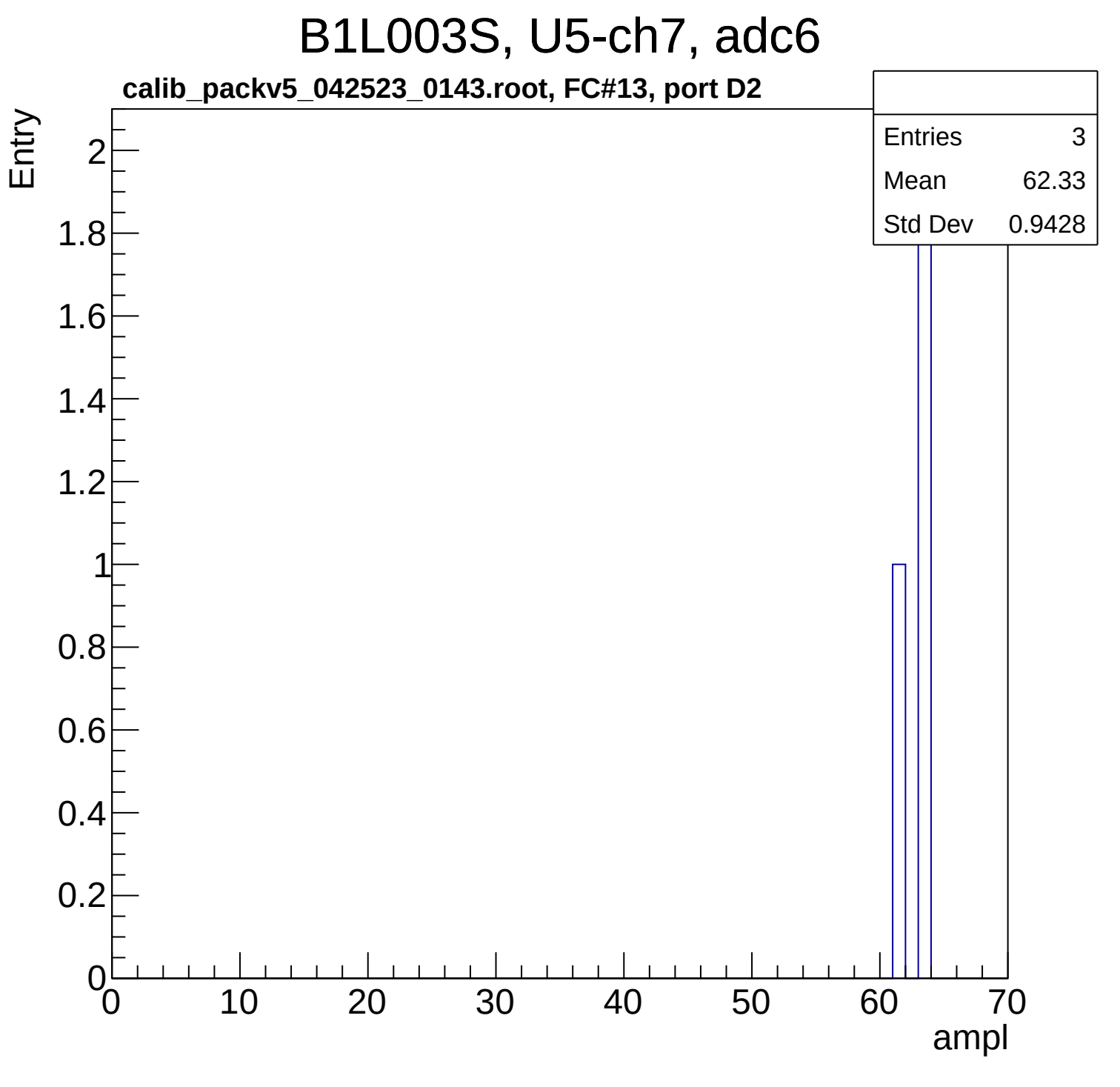

B1L003S, U5-ch7, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.39
Std Dev 9.512
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch7, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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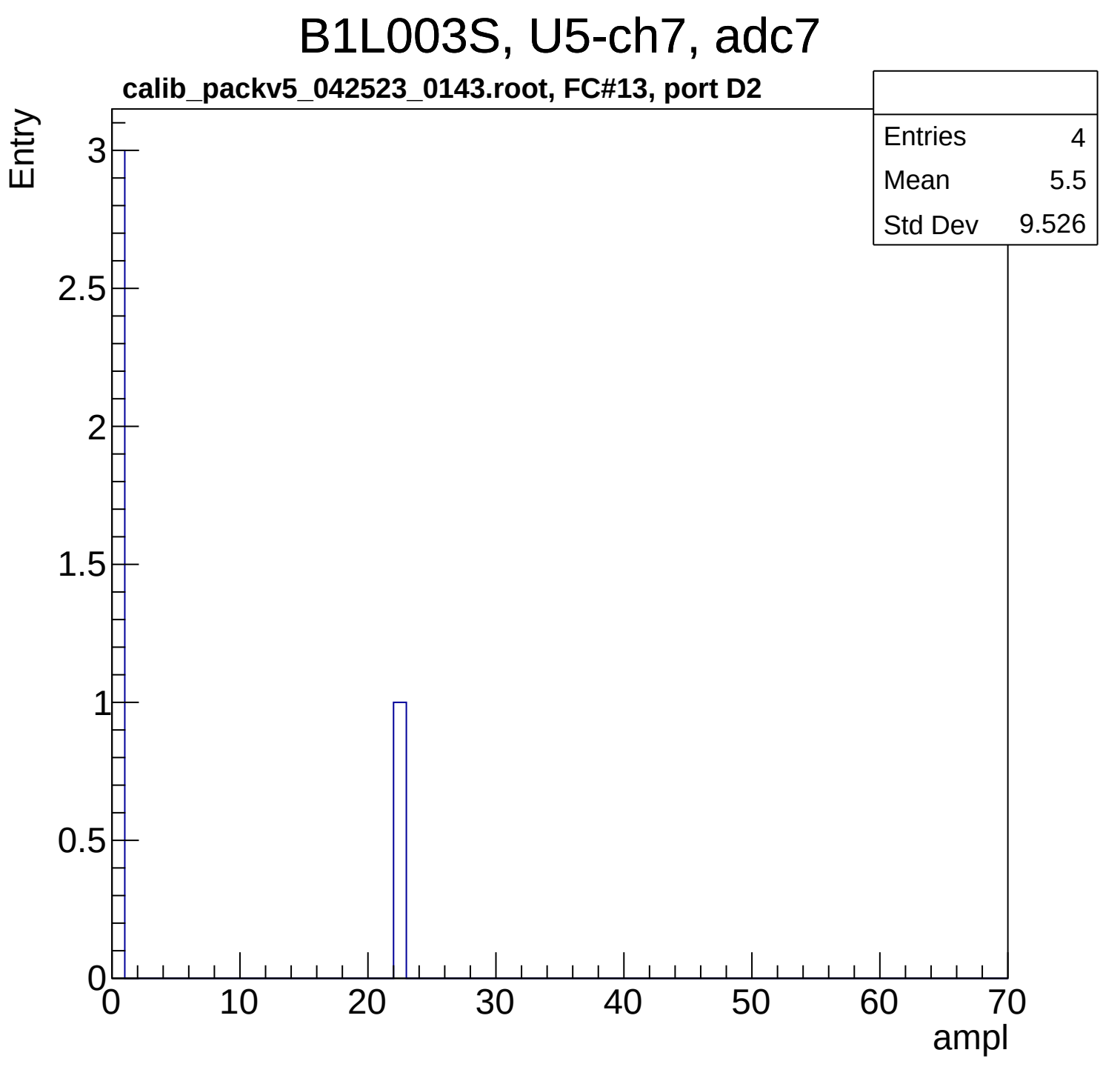
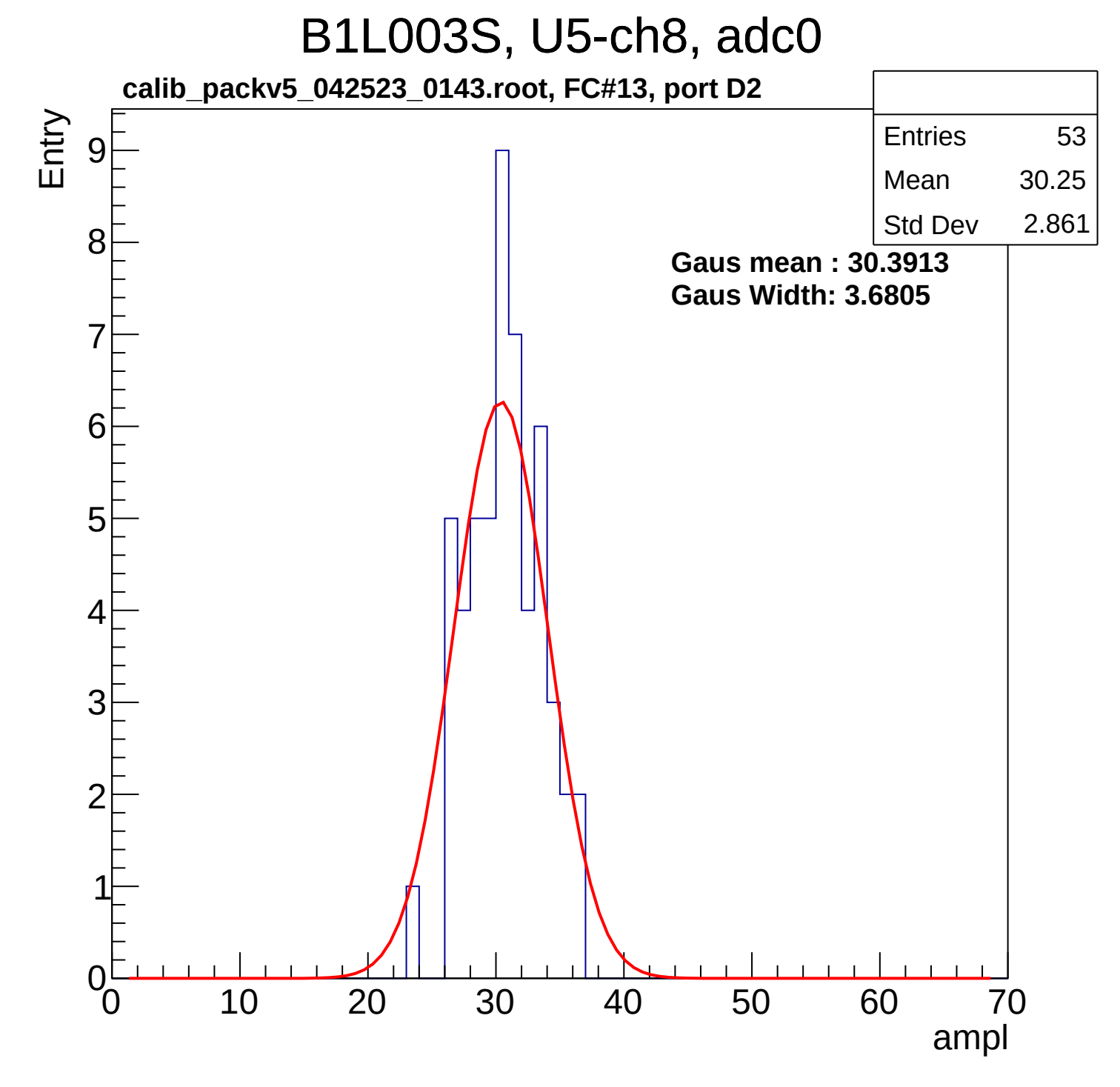
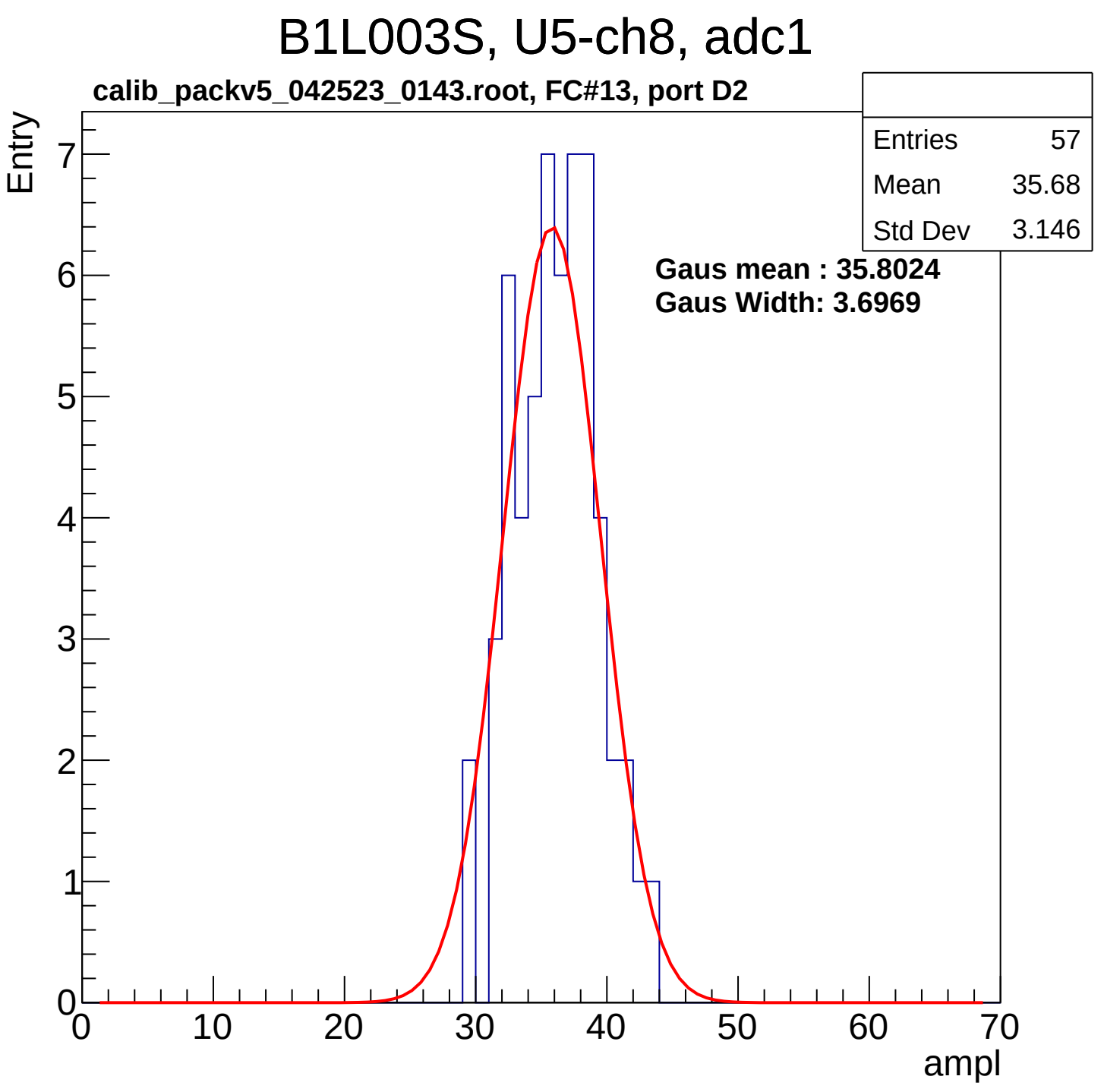
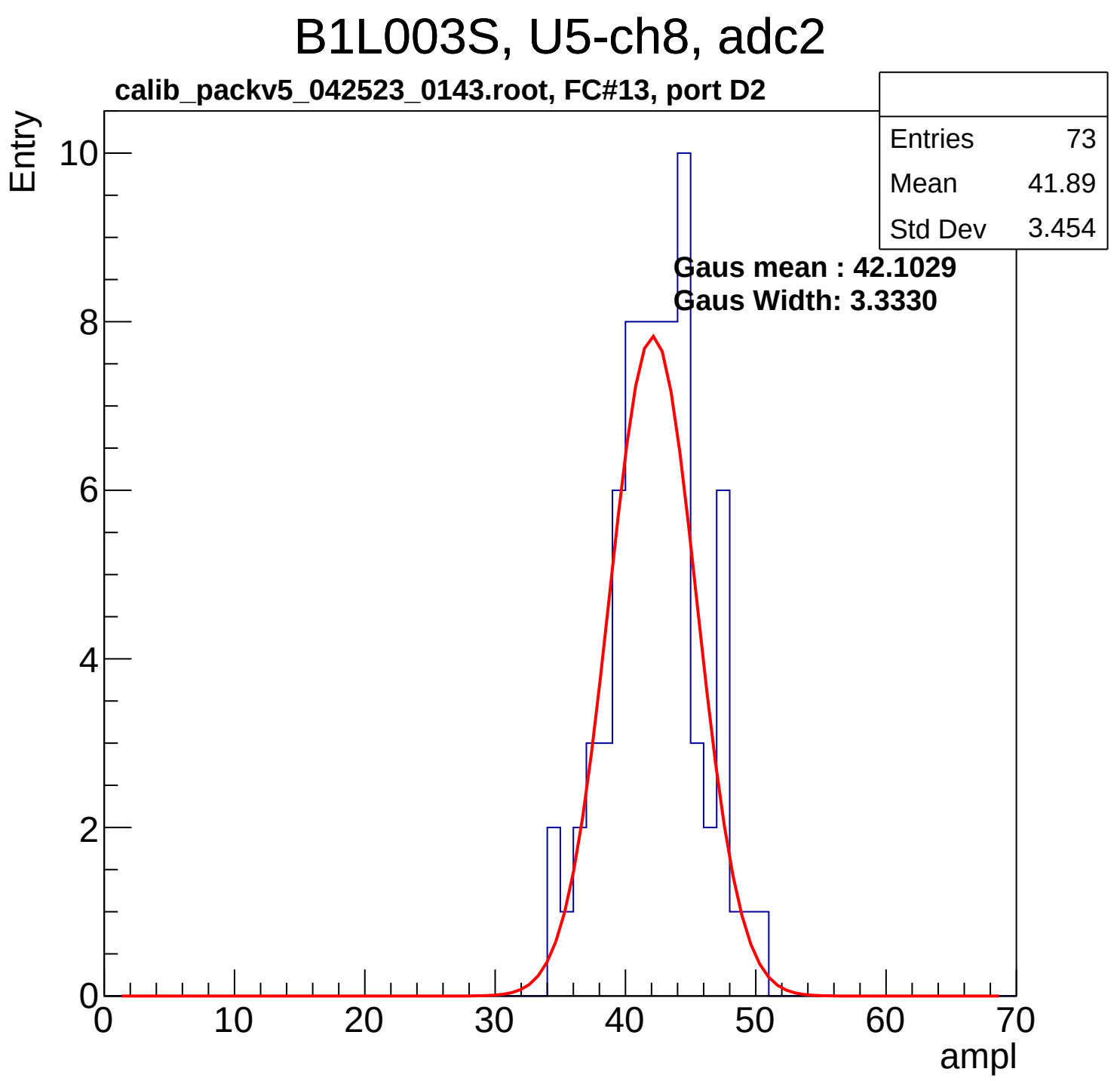
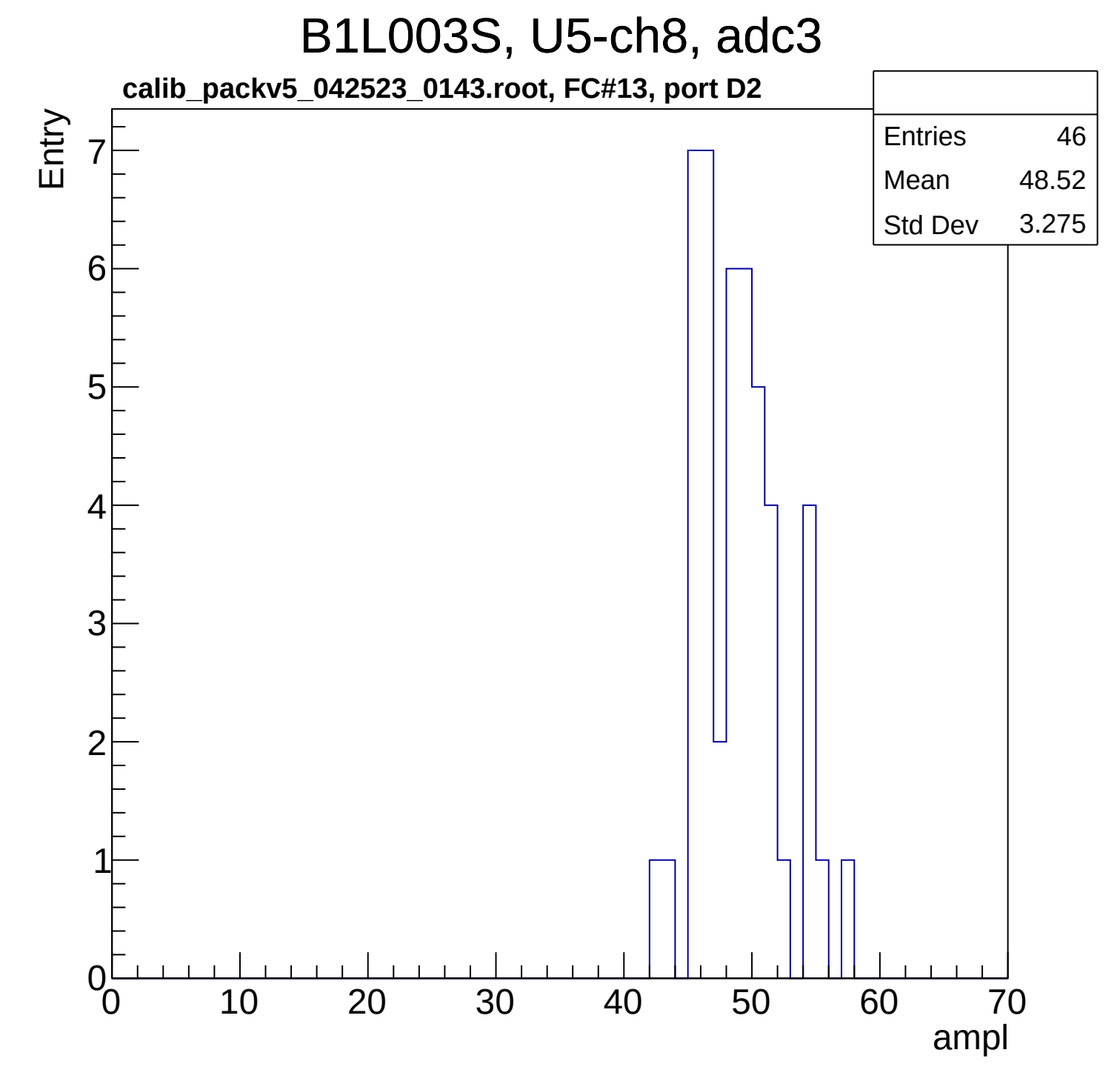
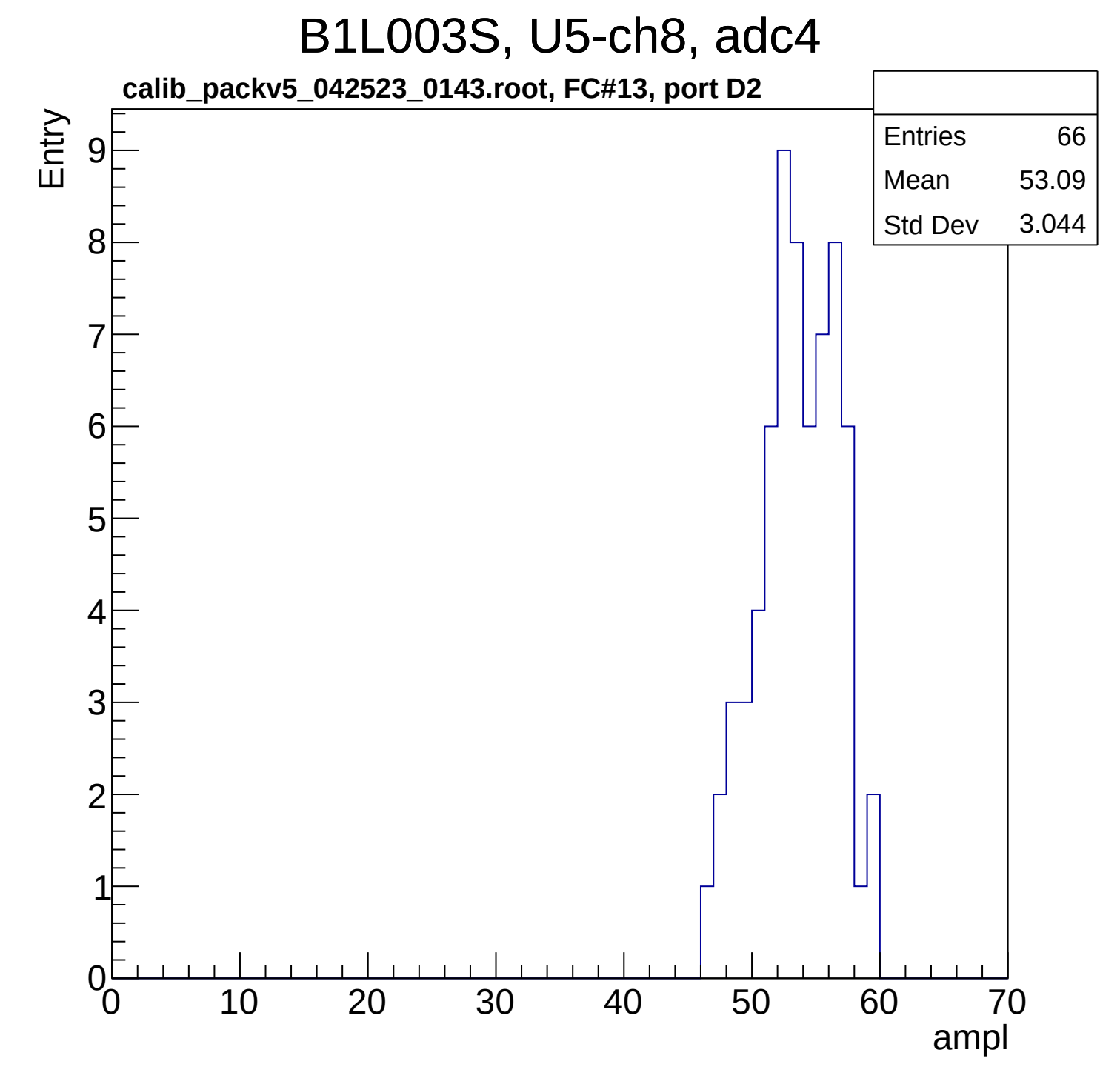

B1L003S, U5-ch7, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch8, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 30.25
Std Dev 2.861
Gaus mean : 30.3913
Gaus Width: 3.6805
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch8, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.68
Std Dev 3.146
Gaus mean : 35.8024
Gaus Width: 3.6969
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch8, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.89
Std Dev 3.454
Gaus mean : 42.1029
Gaus Width: 3.3330
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch8, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 48.52
Std Dev 3.275
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch8, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.09
Std Dev 3.044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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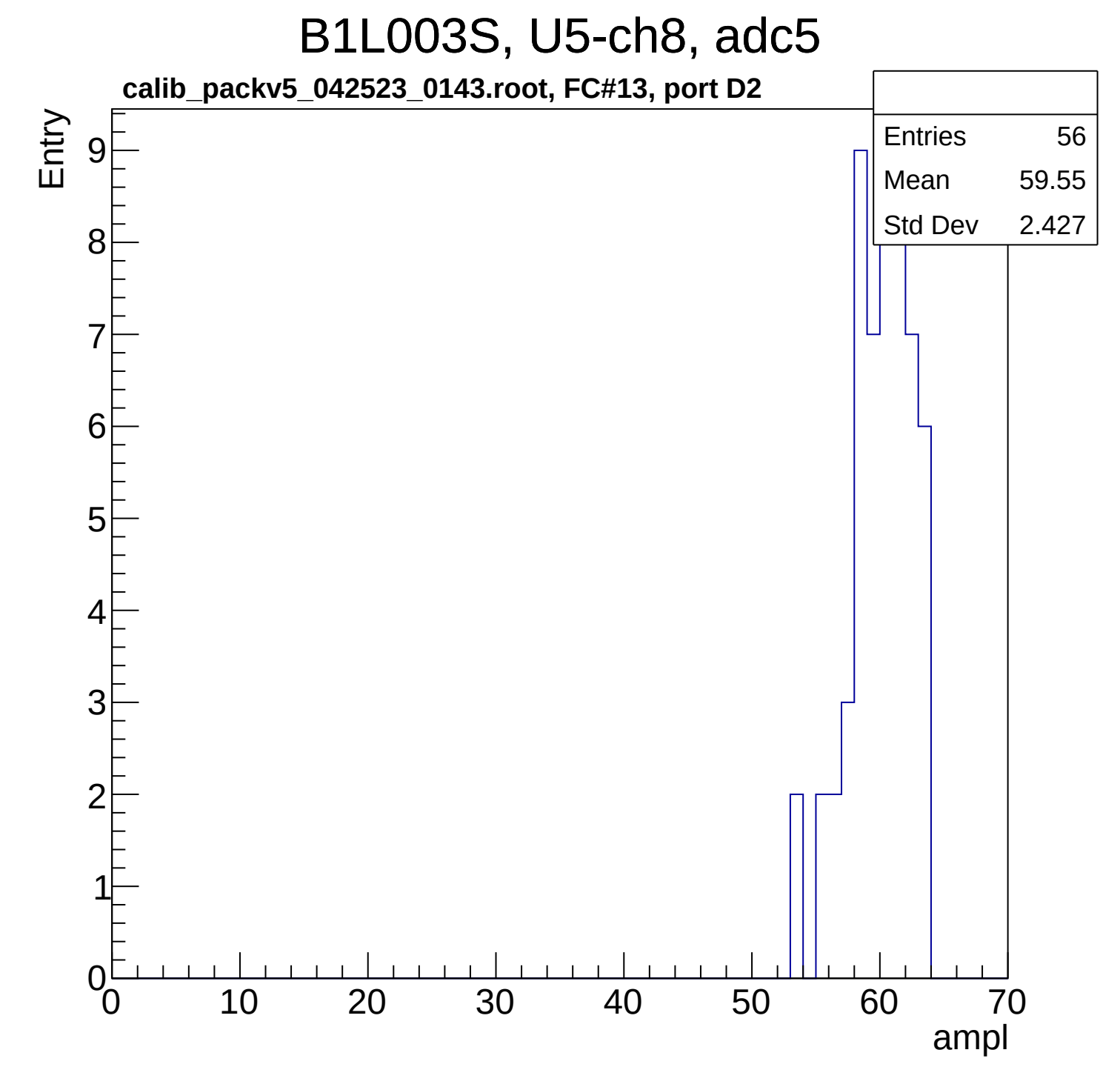
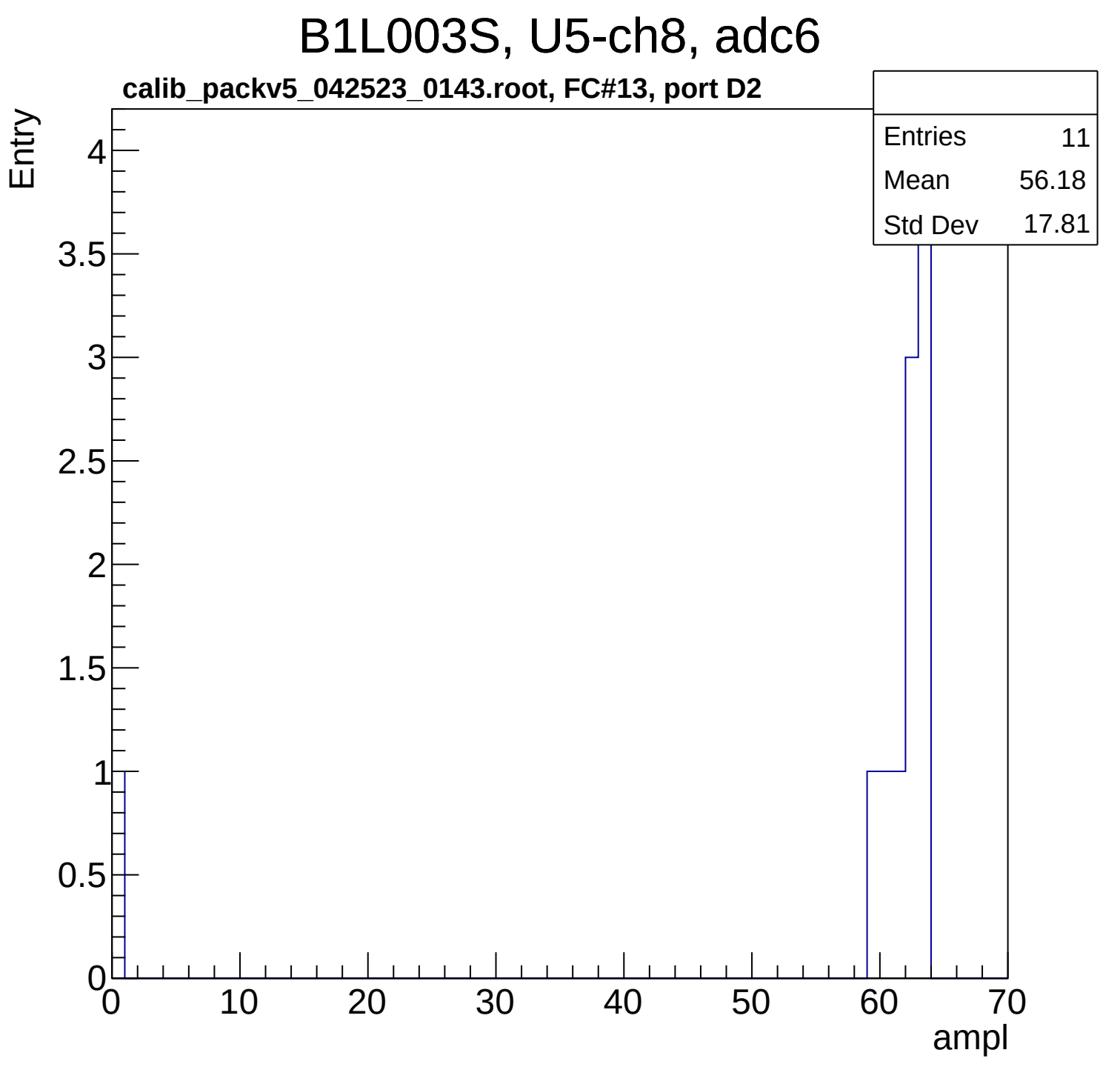
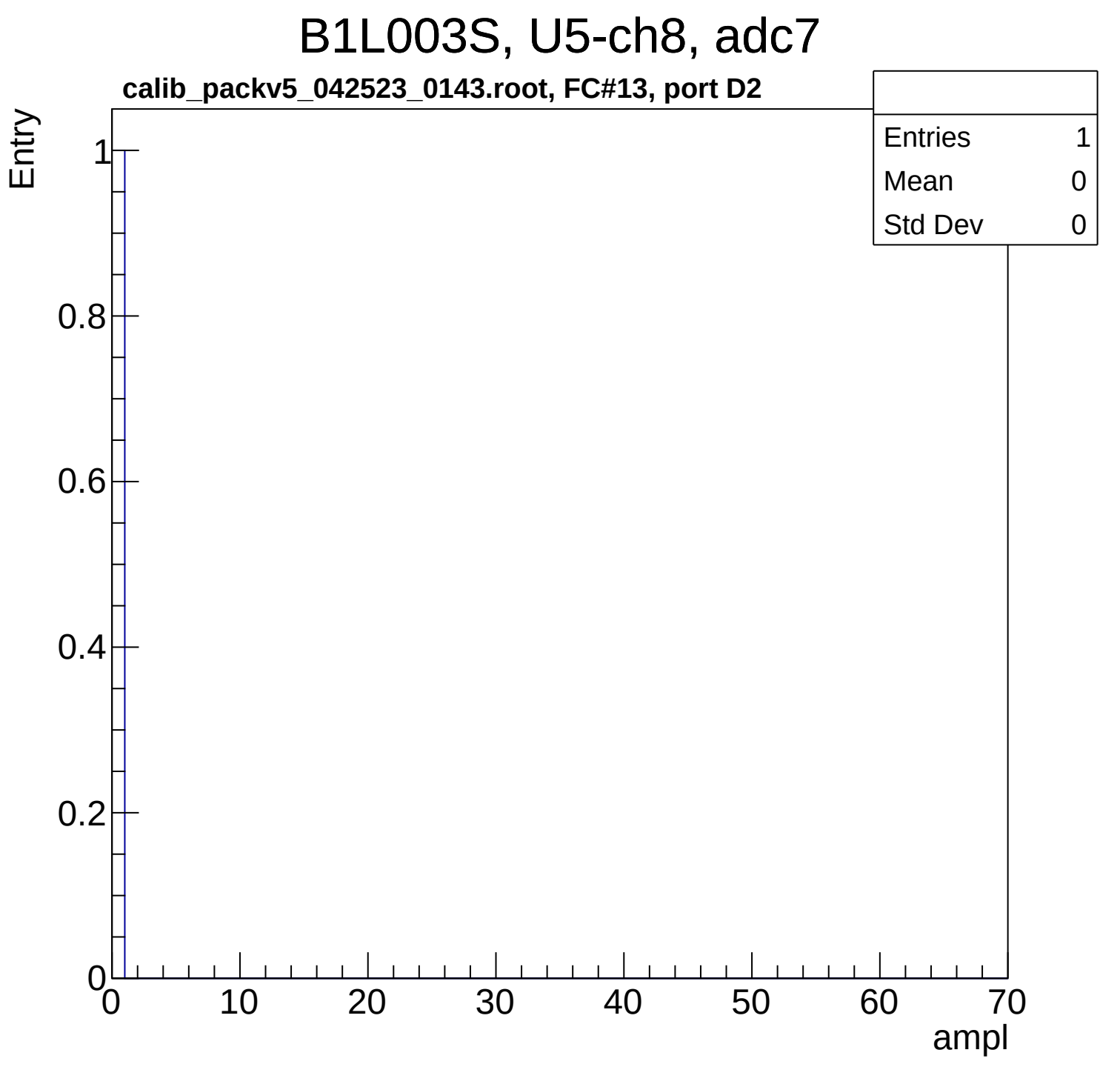
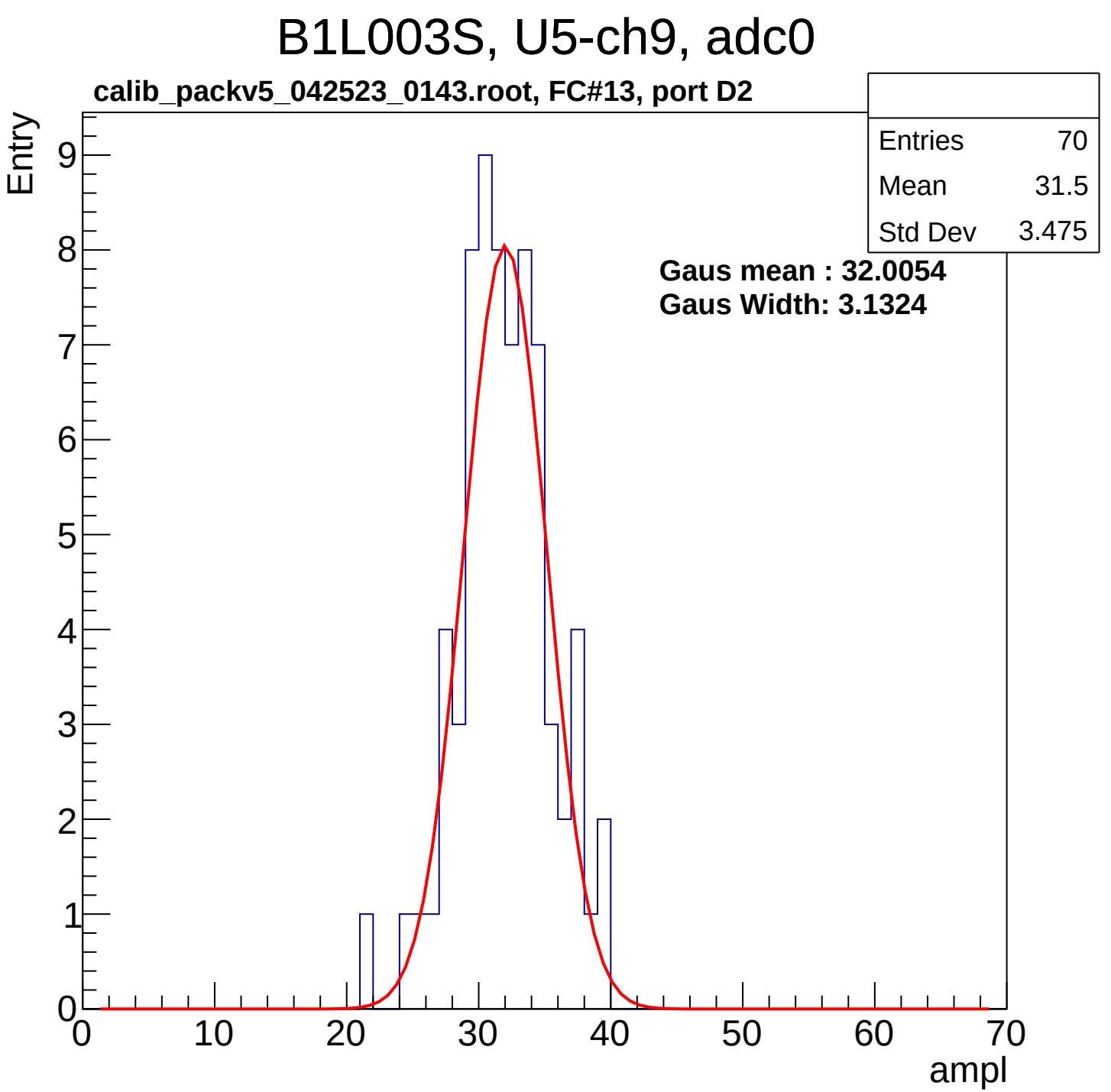
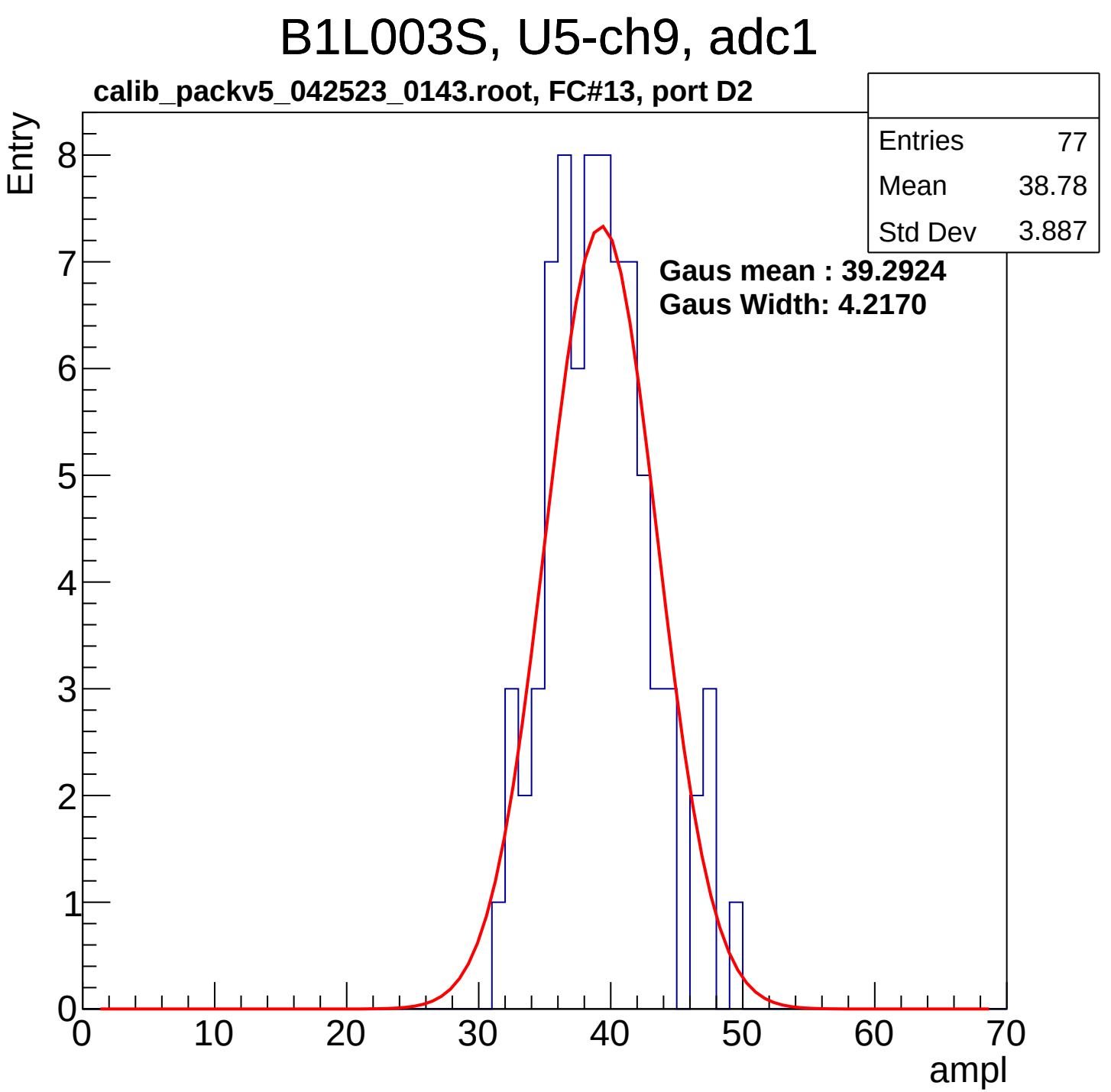
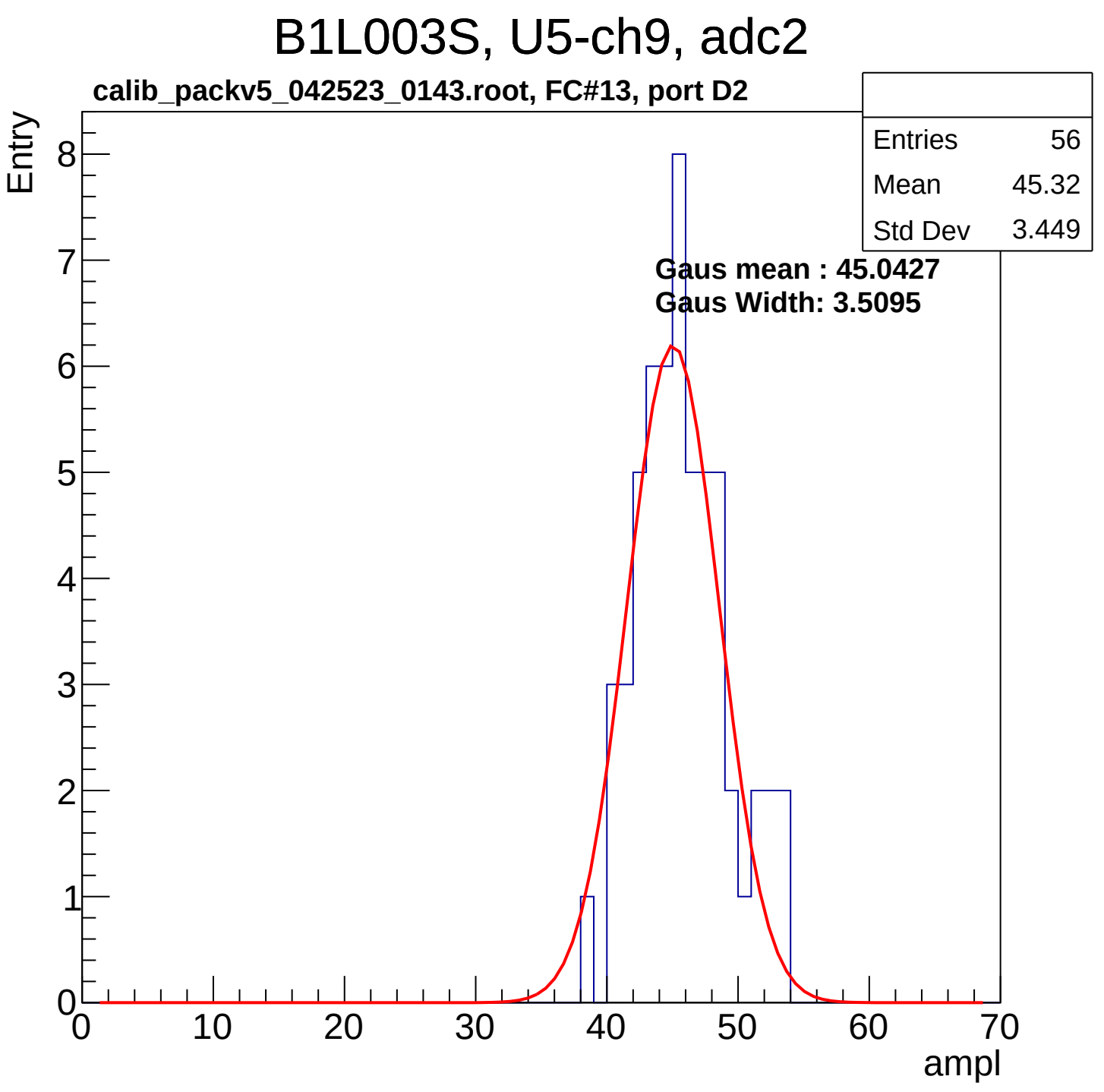

B1L003S, U5-ch8, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.55
Std Dev 2.427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

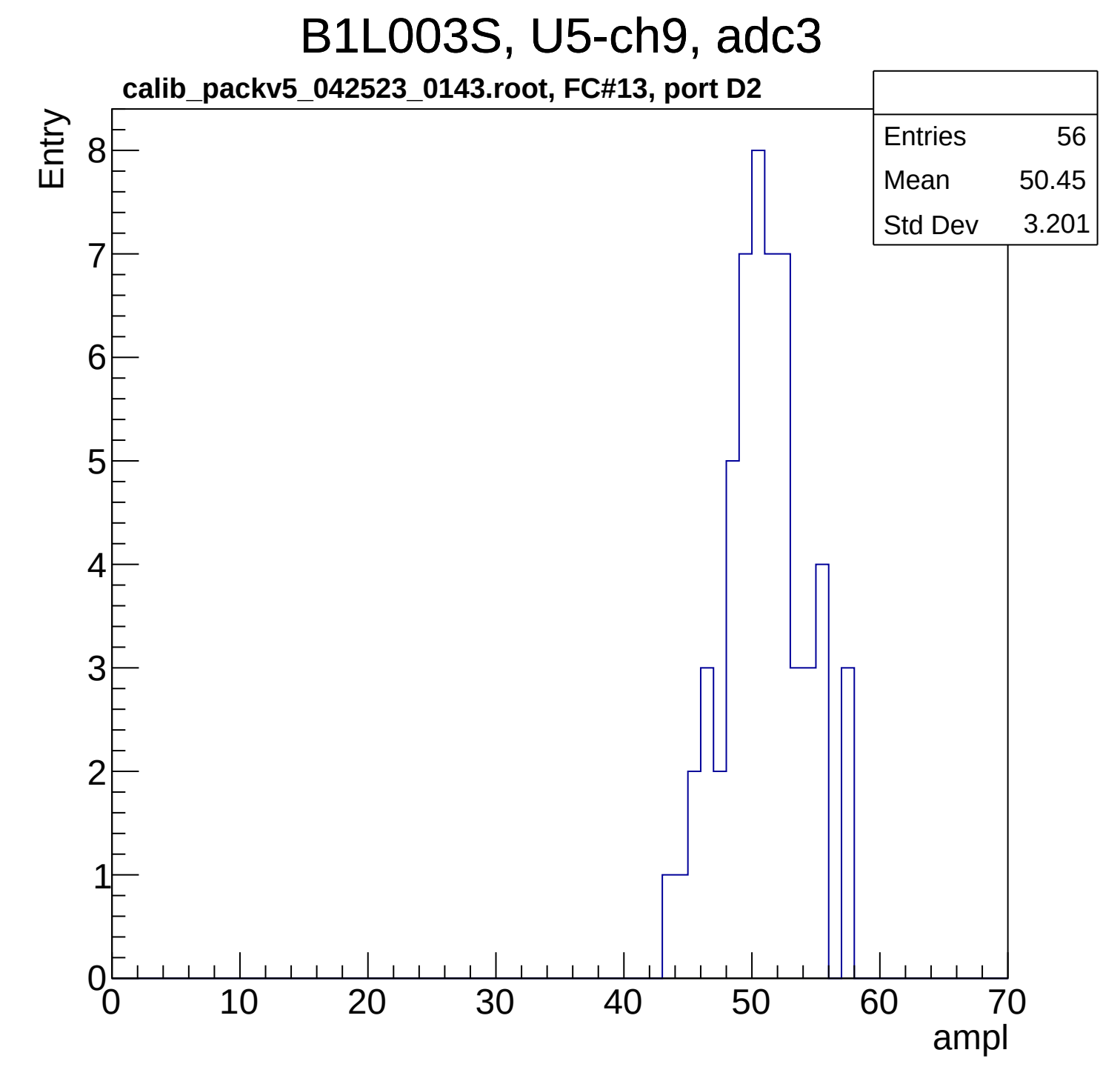

B1L003S, U5-ch8, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.18
Std Dev 17.81
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

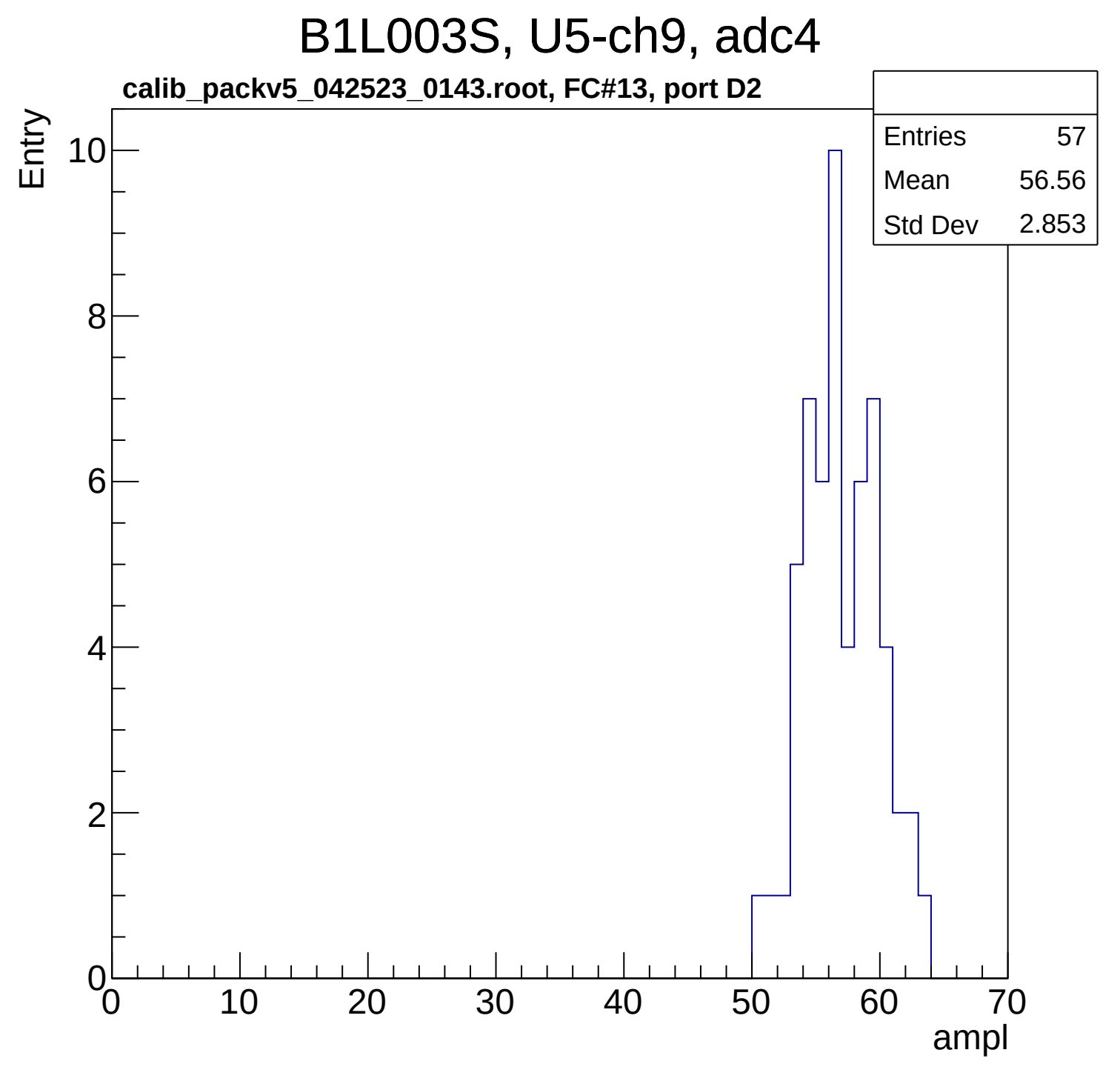

B1L003S, U5-ch8, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

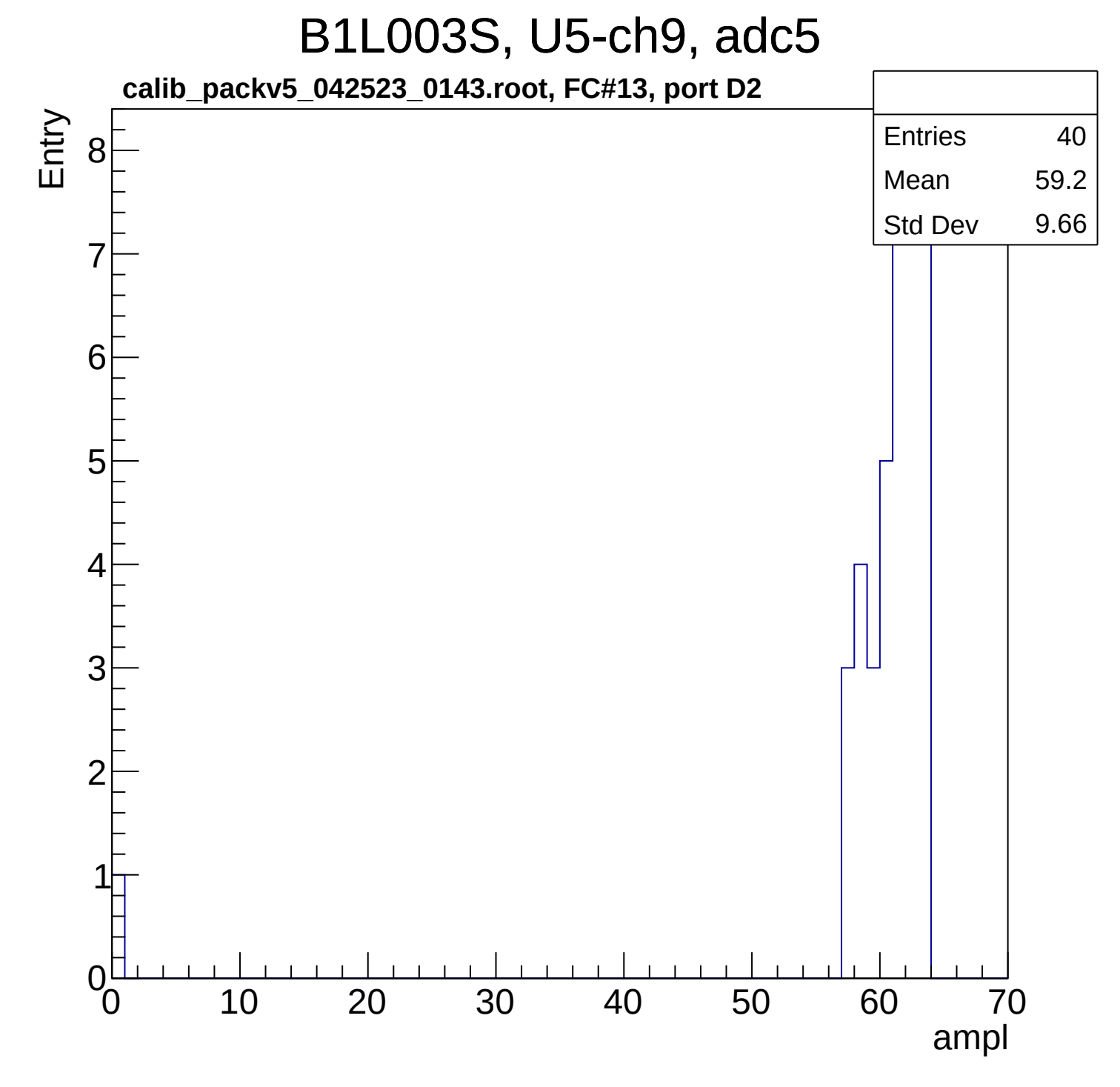

B1L003S, U5-ch9, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 31.5
Std Dev 3.475
Gaus mean : 32.0054
Gaus Width: 3.1324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

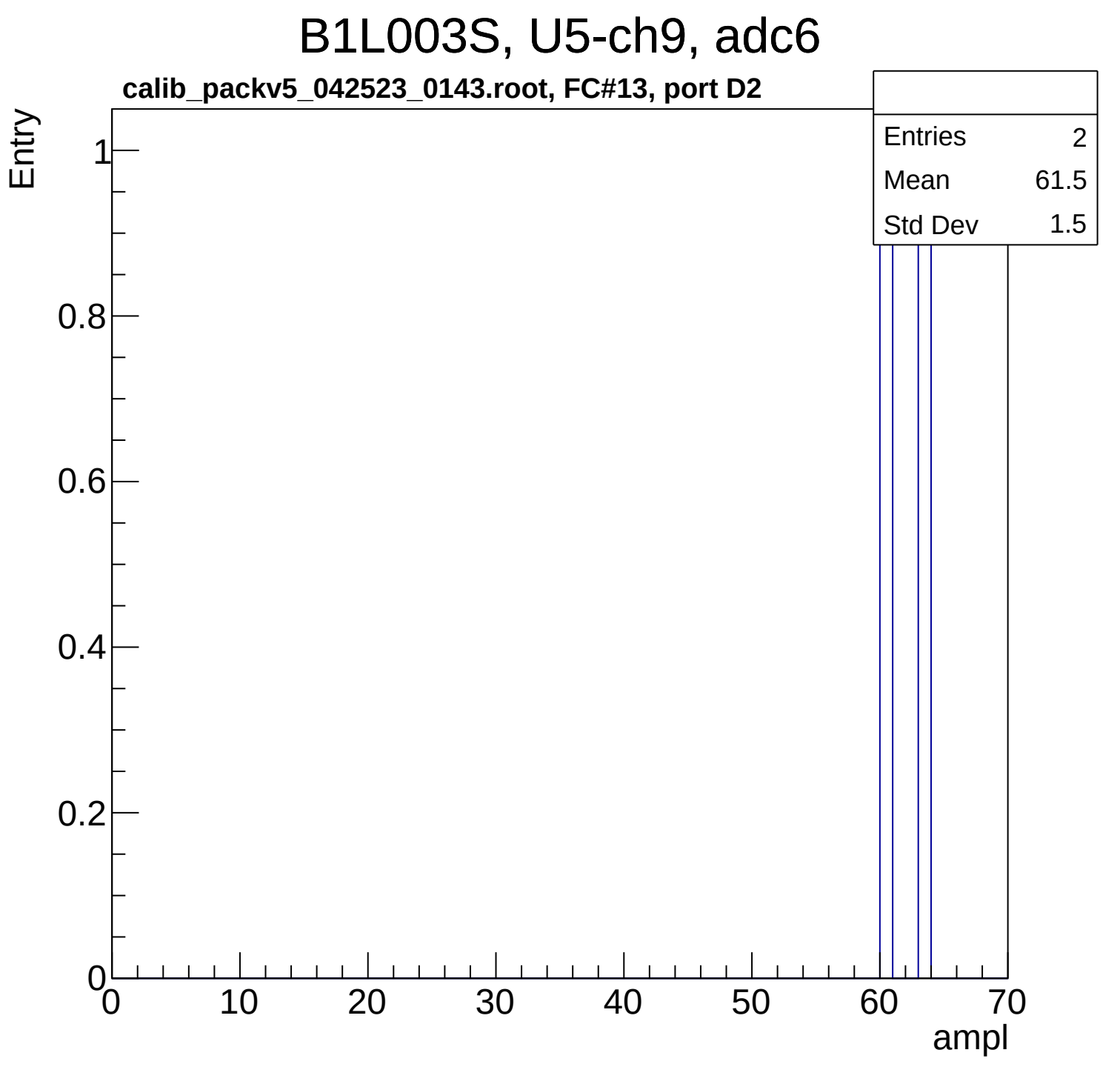

B1L003S, U5-ch9, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 38.78
Std Dev 3.887
Gaus mean : 39.2924
Gaus Width: 4.2170
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

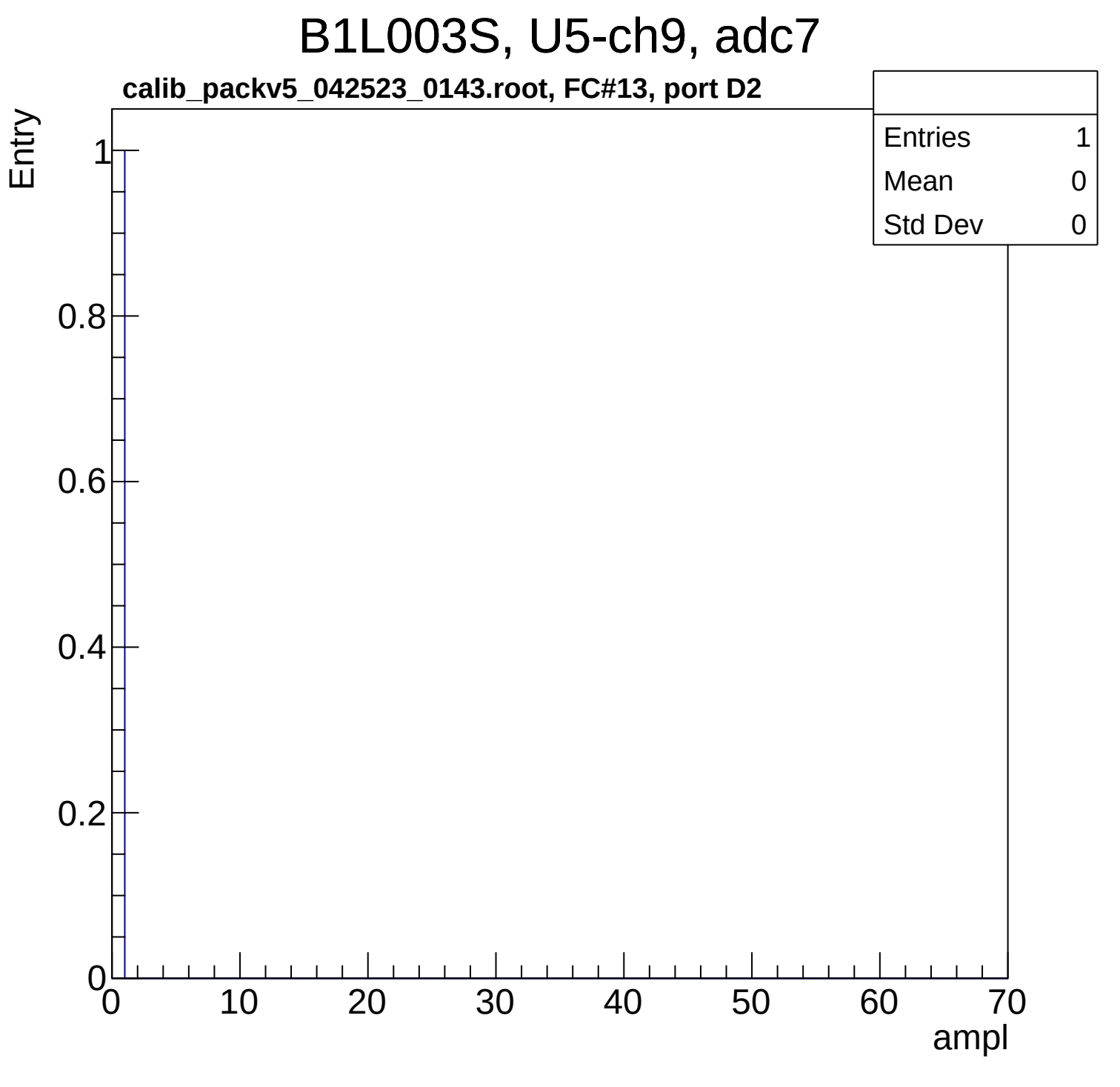

B1L003S, U5-ch9, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 45.32
Std Dev 3.449
Gaus mean : 45.0427
Gaus Width: 3.5095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

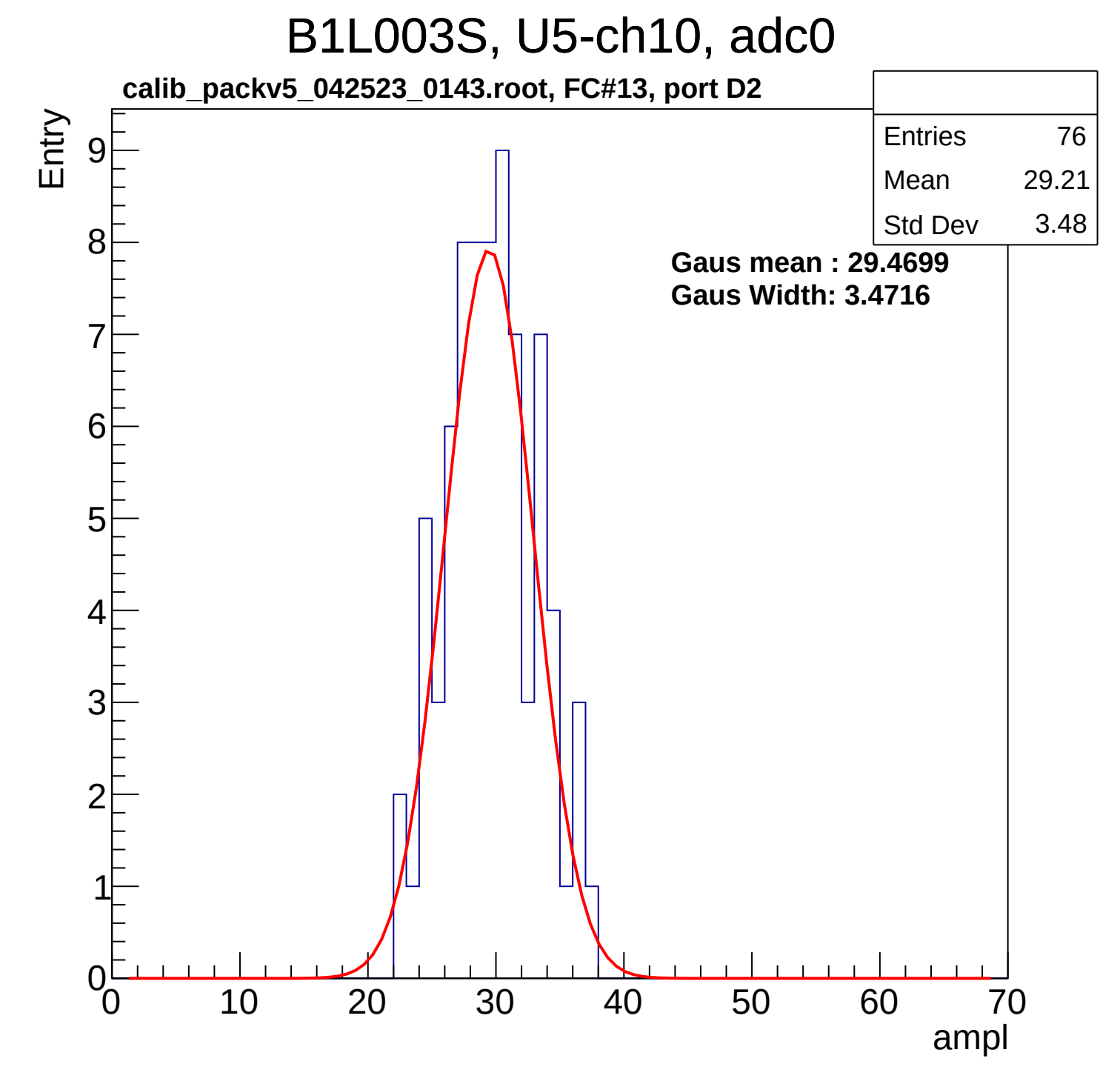

B1L003S, U5-ch9, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.45
Std Dev 3.201
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

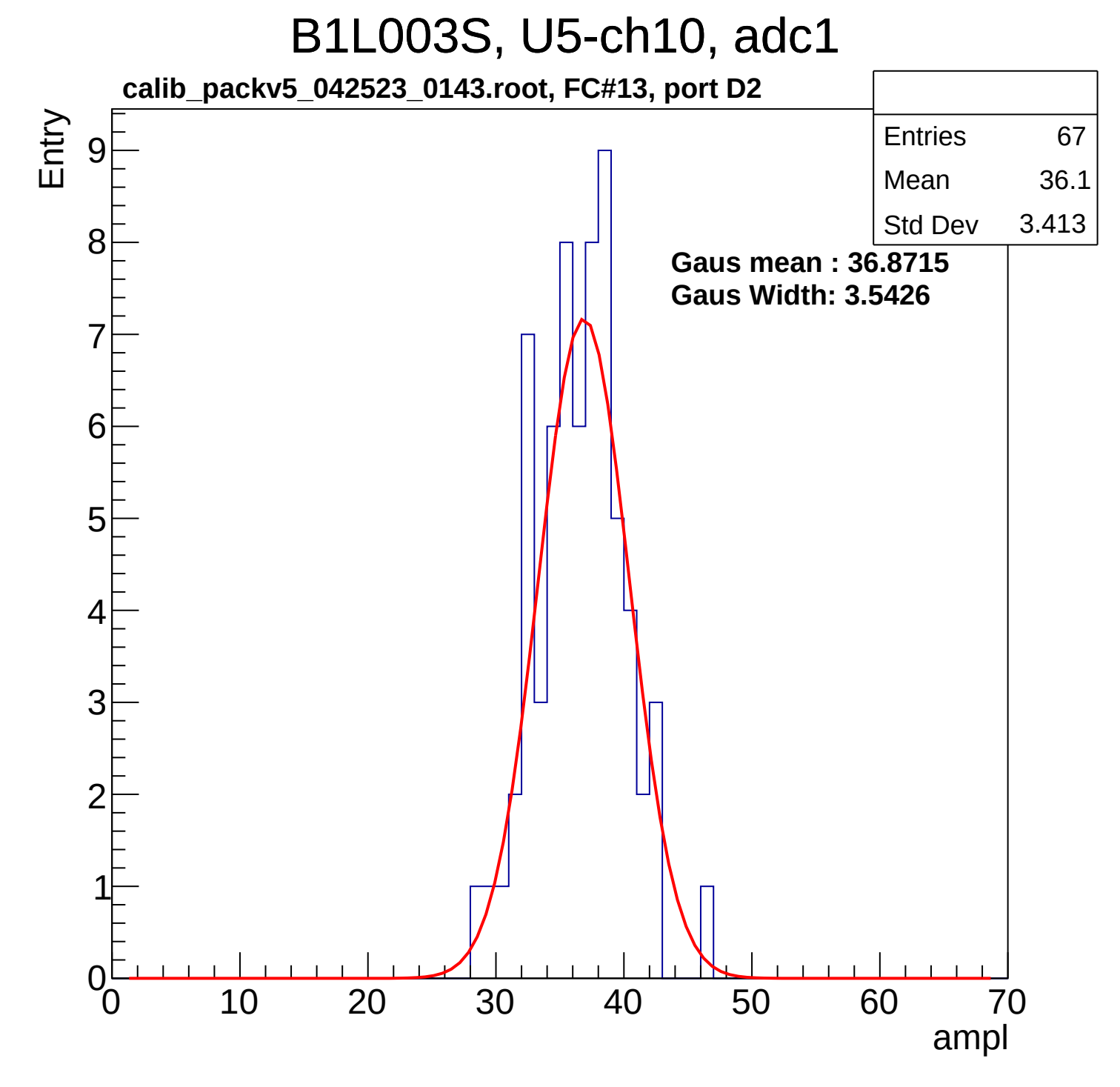

B1L003S, U5-ch9, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.56
Std Dev 2.853
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

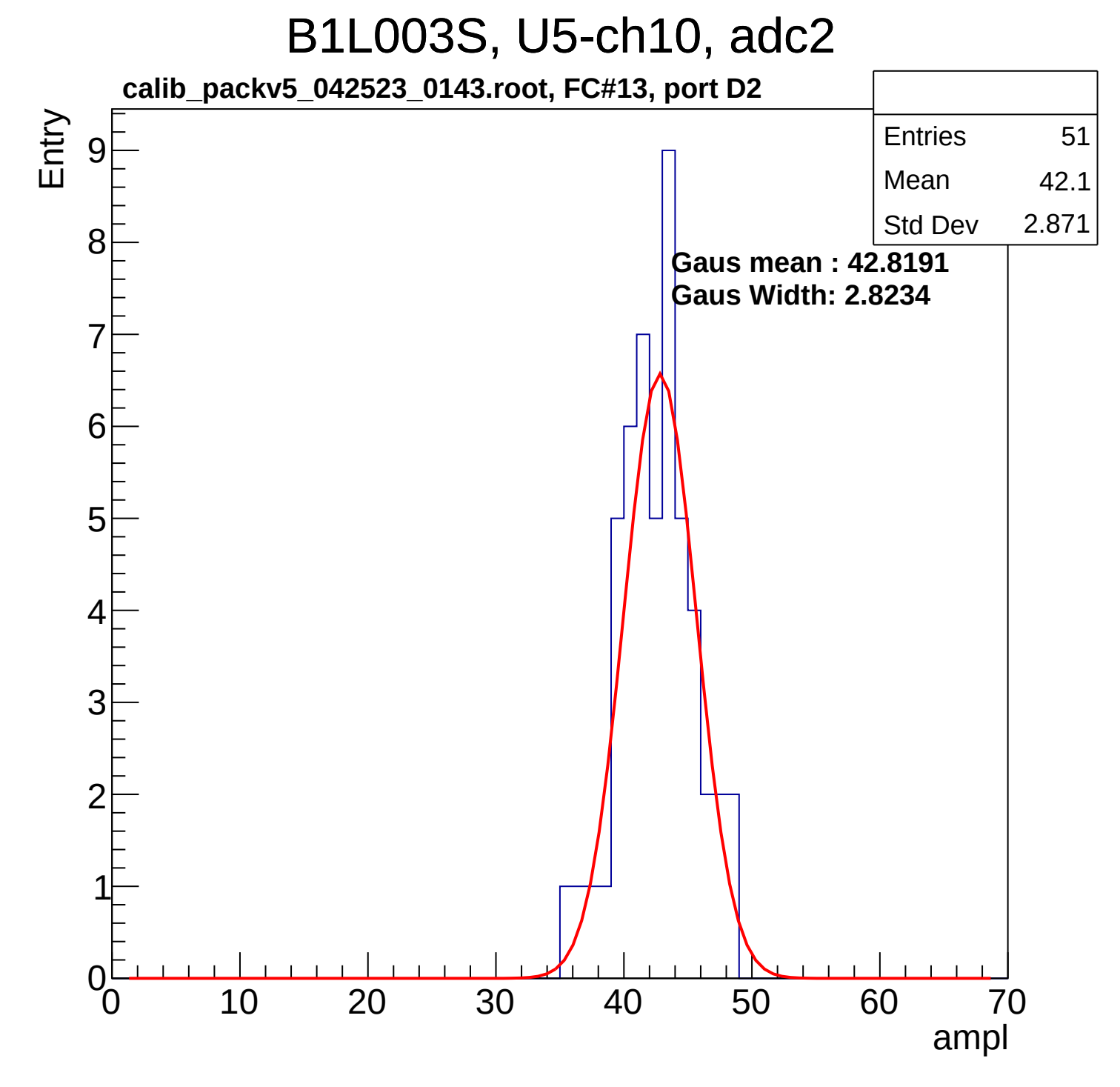

B1L003S, U5-ch9, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.2
Std Dev 9.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

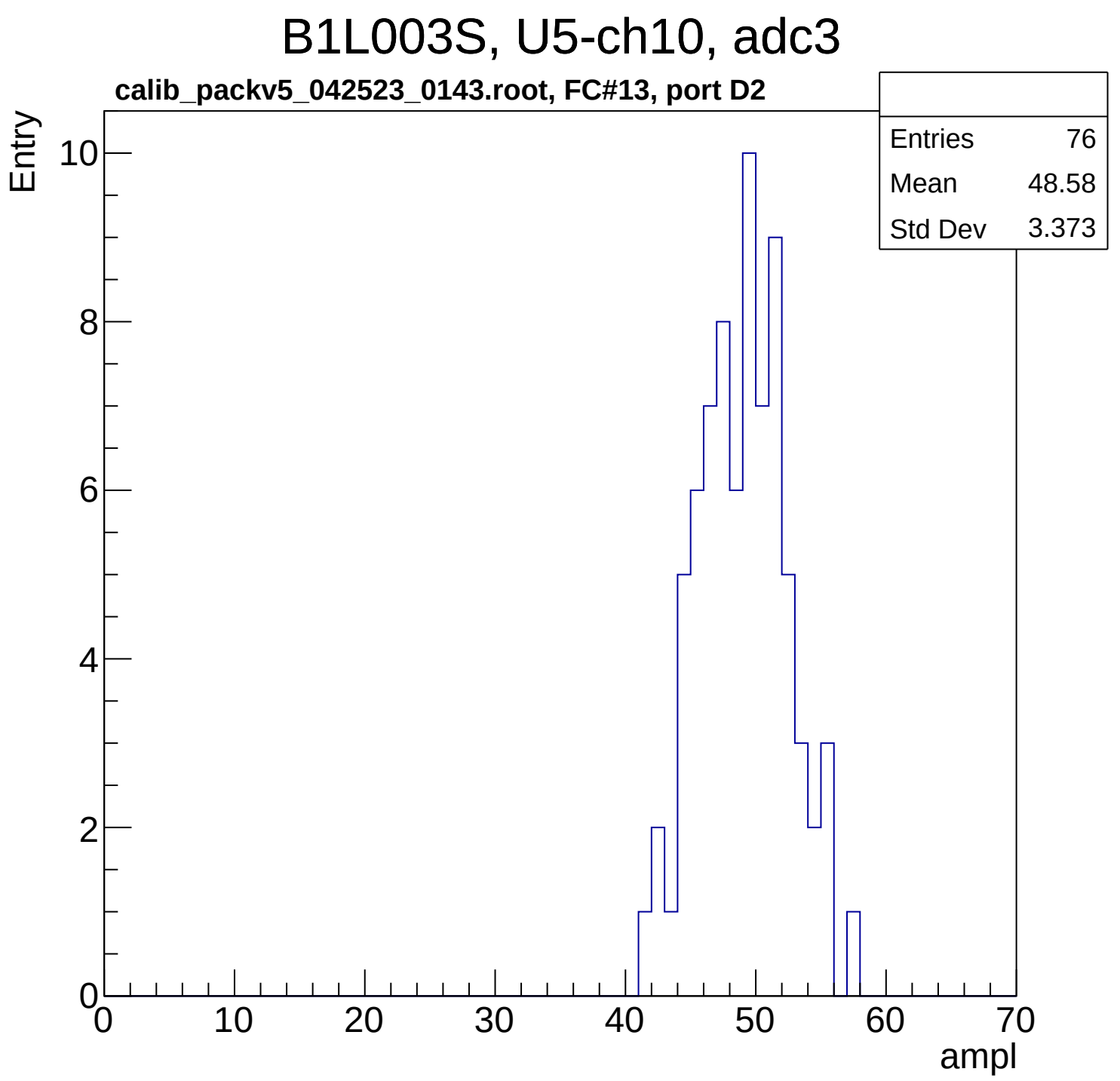

B1L003S, U5-ch9, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

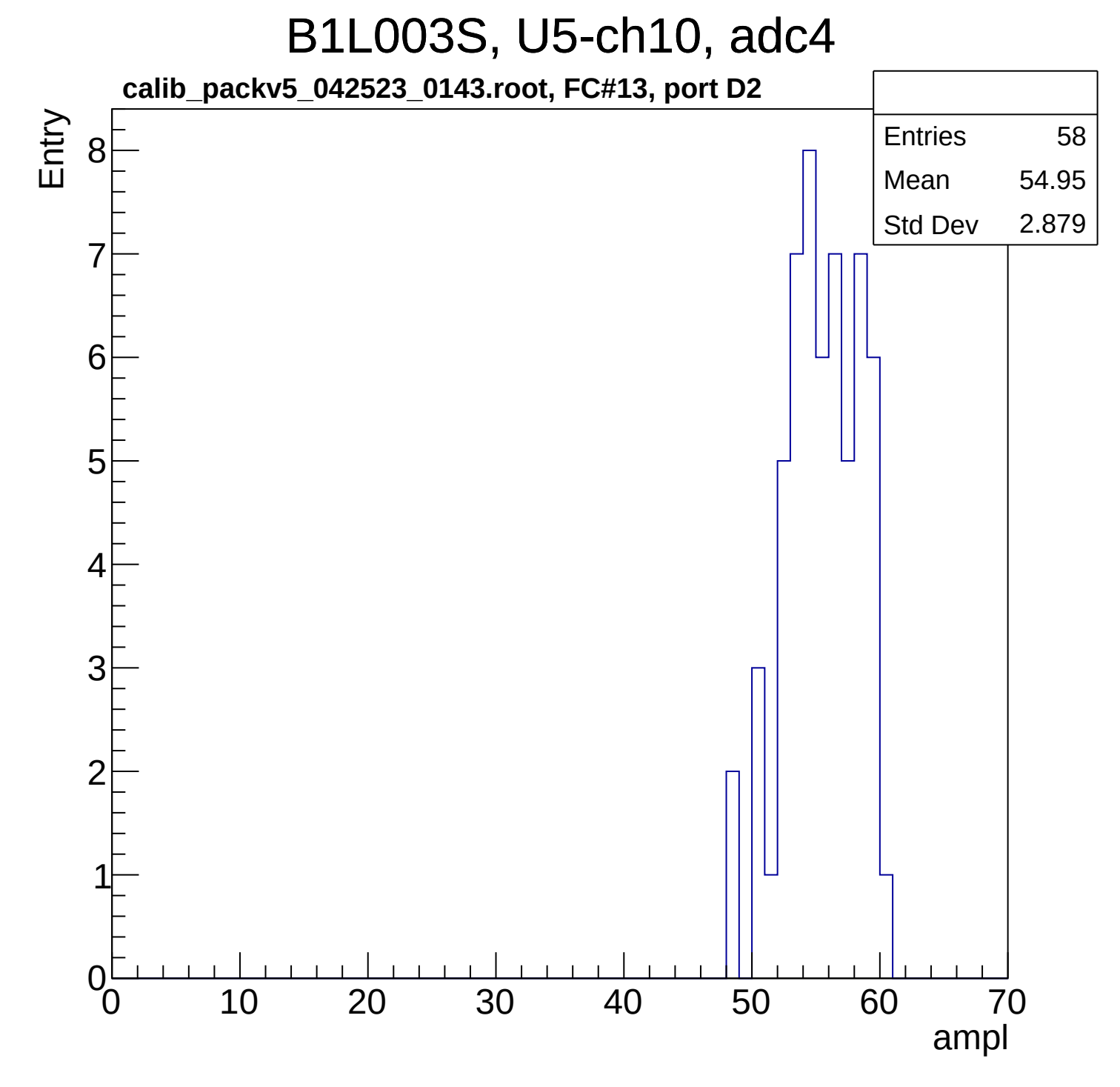

B1L003S, U5-ch9, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch10, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 29.21
Std Dev 3.48
Gaus mean : 29.4699
Gaus Width: 3.4716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch10, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.1
Std Dev 3.413
Gaus mean : 36.8715
Gaus Width: 3.5426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch10, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 42.1
Std Dev 2.871
Gaus mean : 42.8191
Gaus Width: 2.8234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch10, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.58
Std Dev 3.373
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch10, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.95
Std Dev 2.879
Entry
ampl
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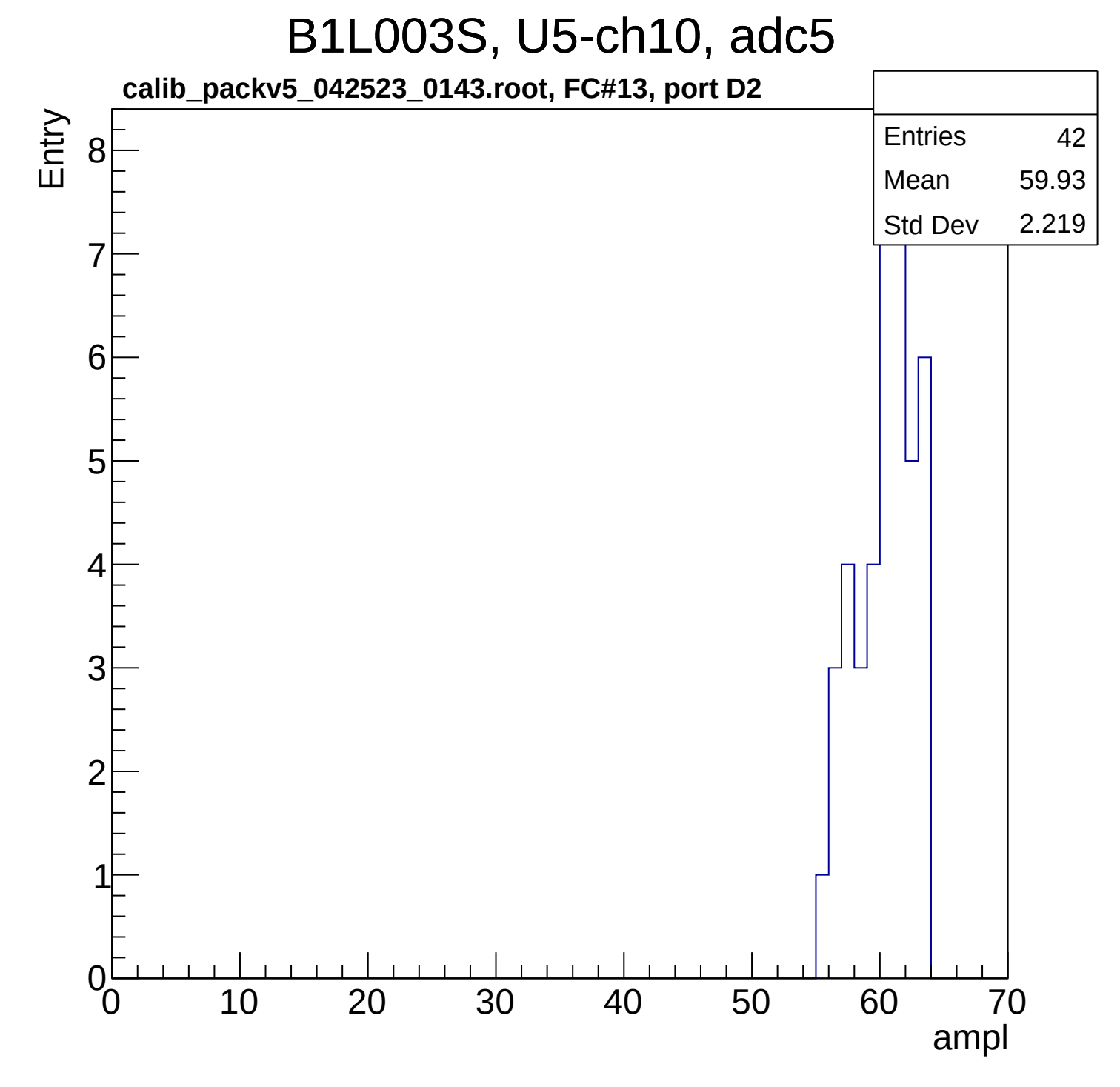
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch10, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.93
Std Dev 2.219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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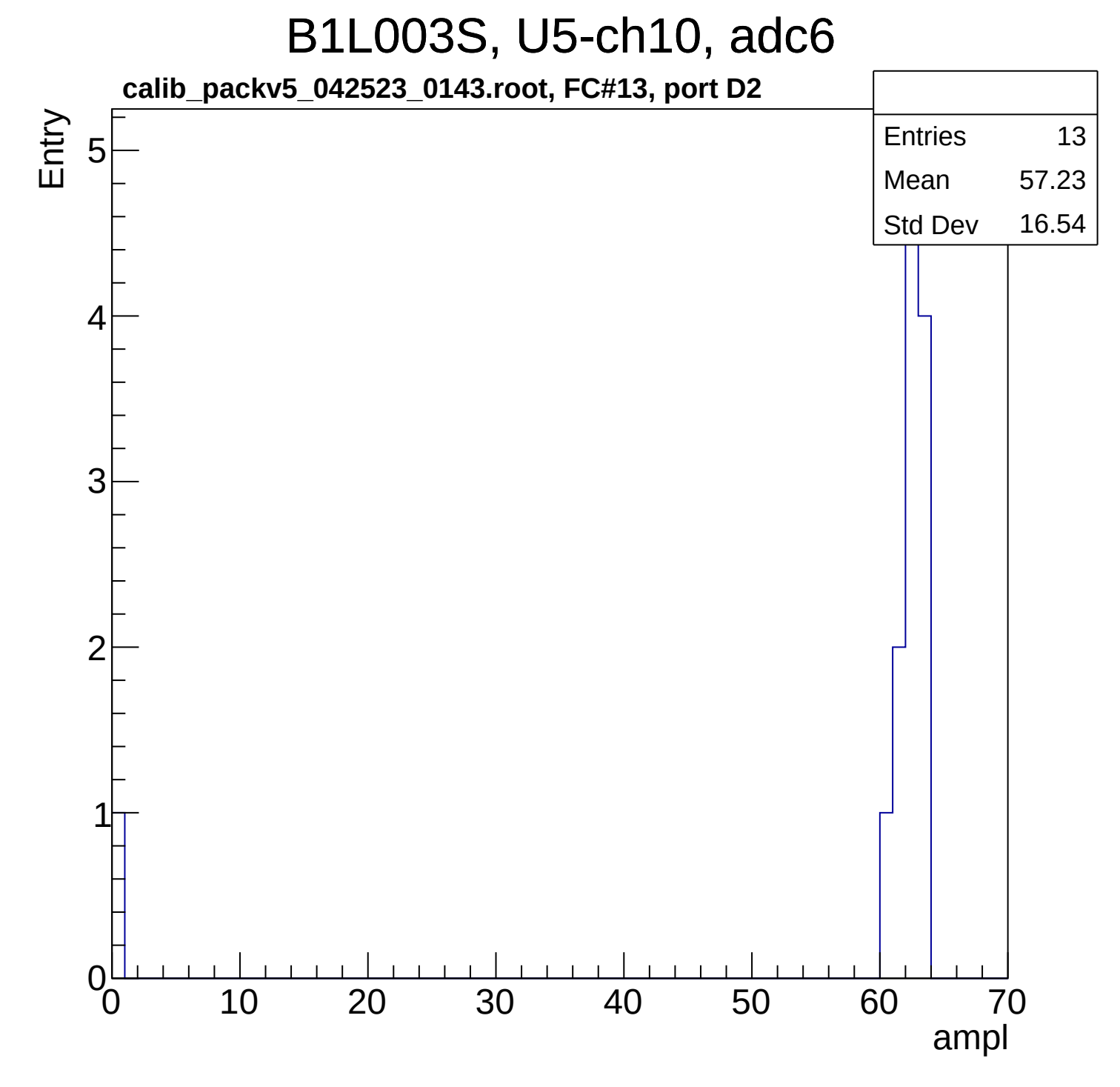
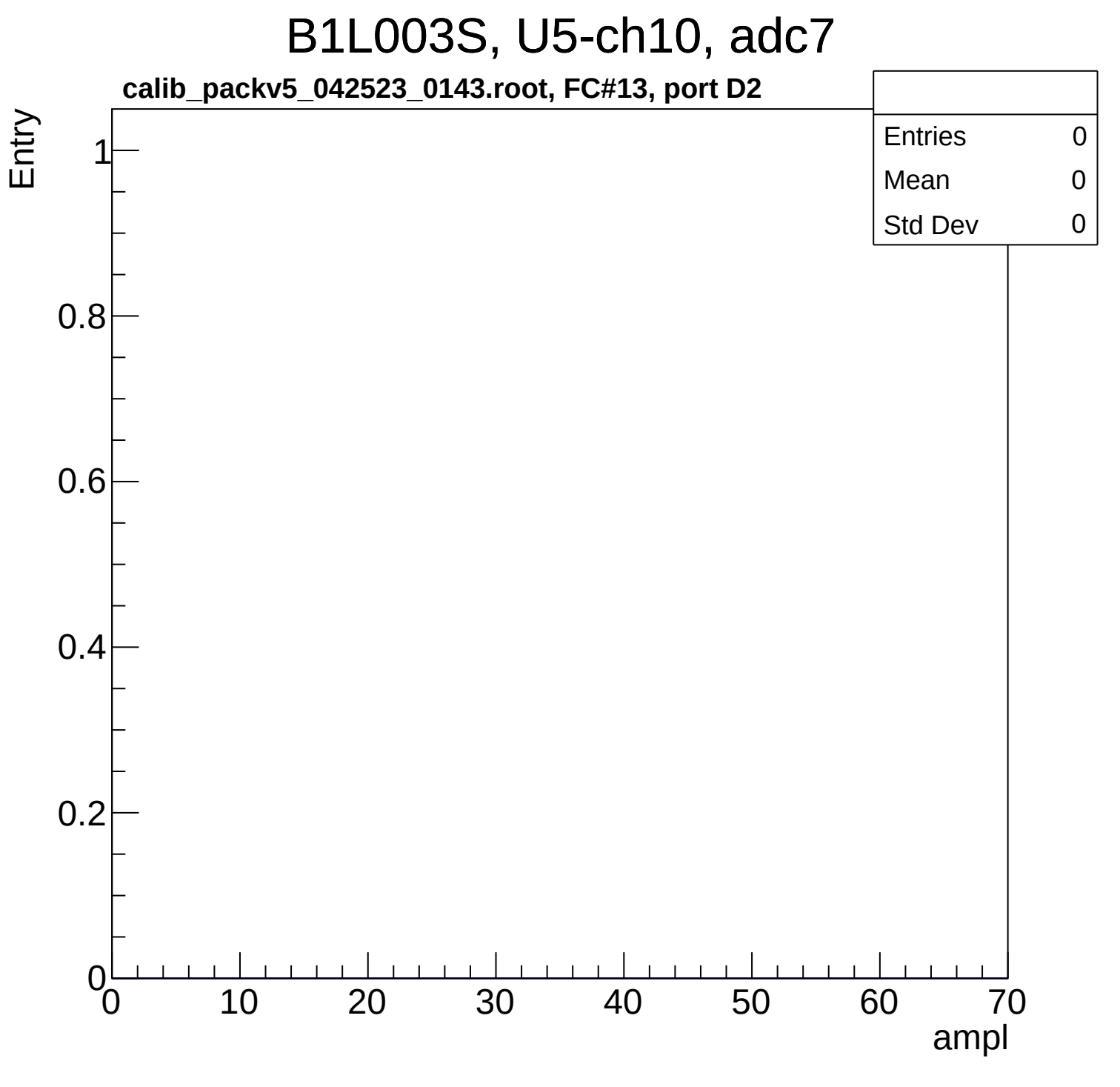
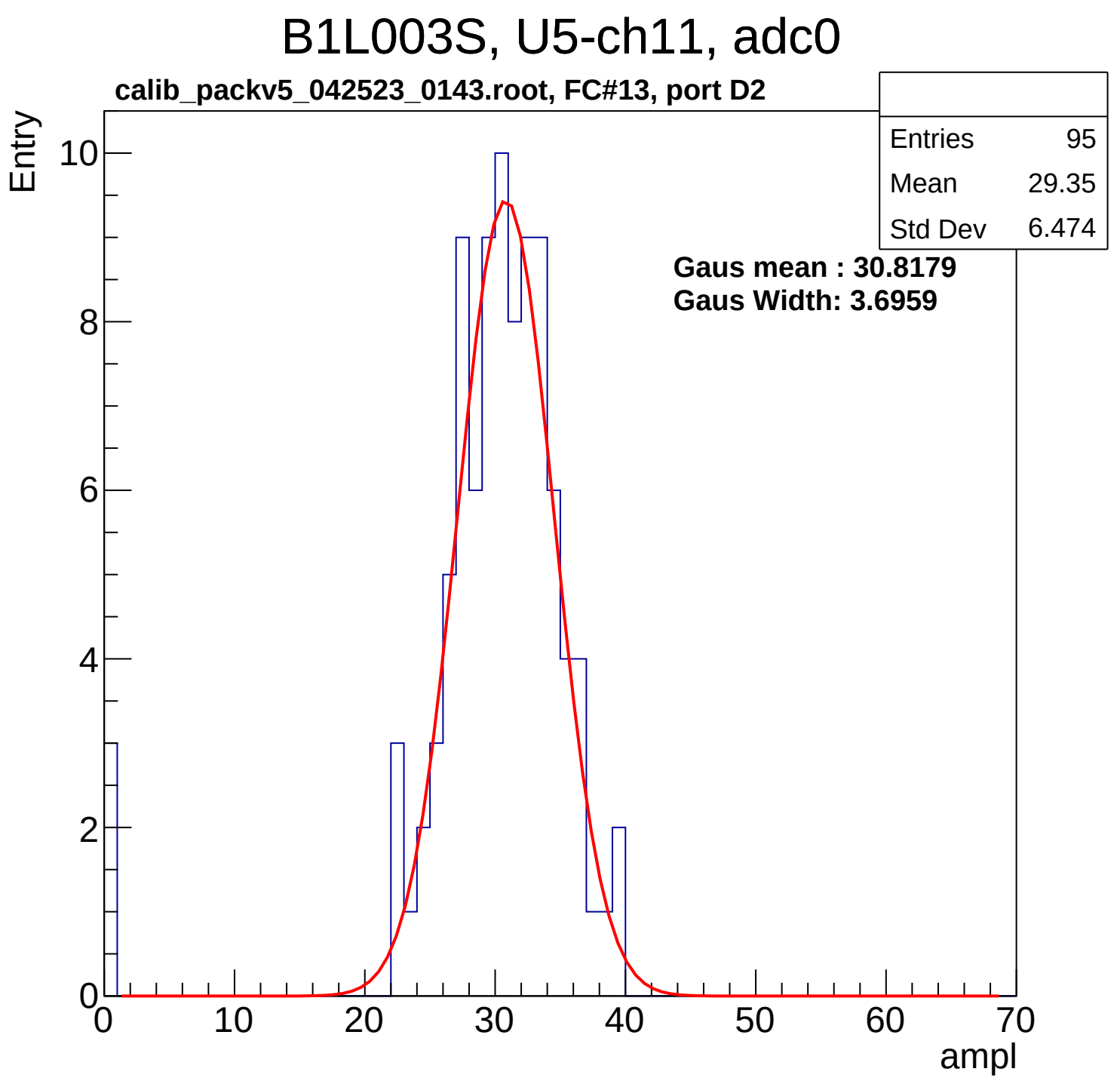
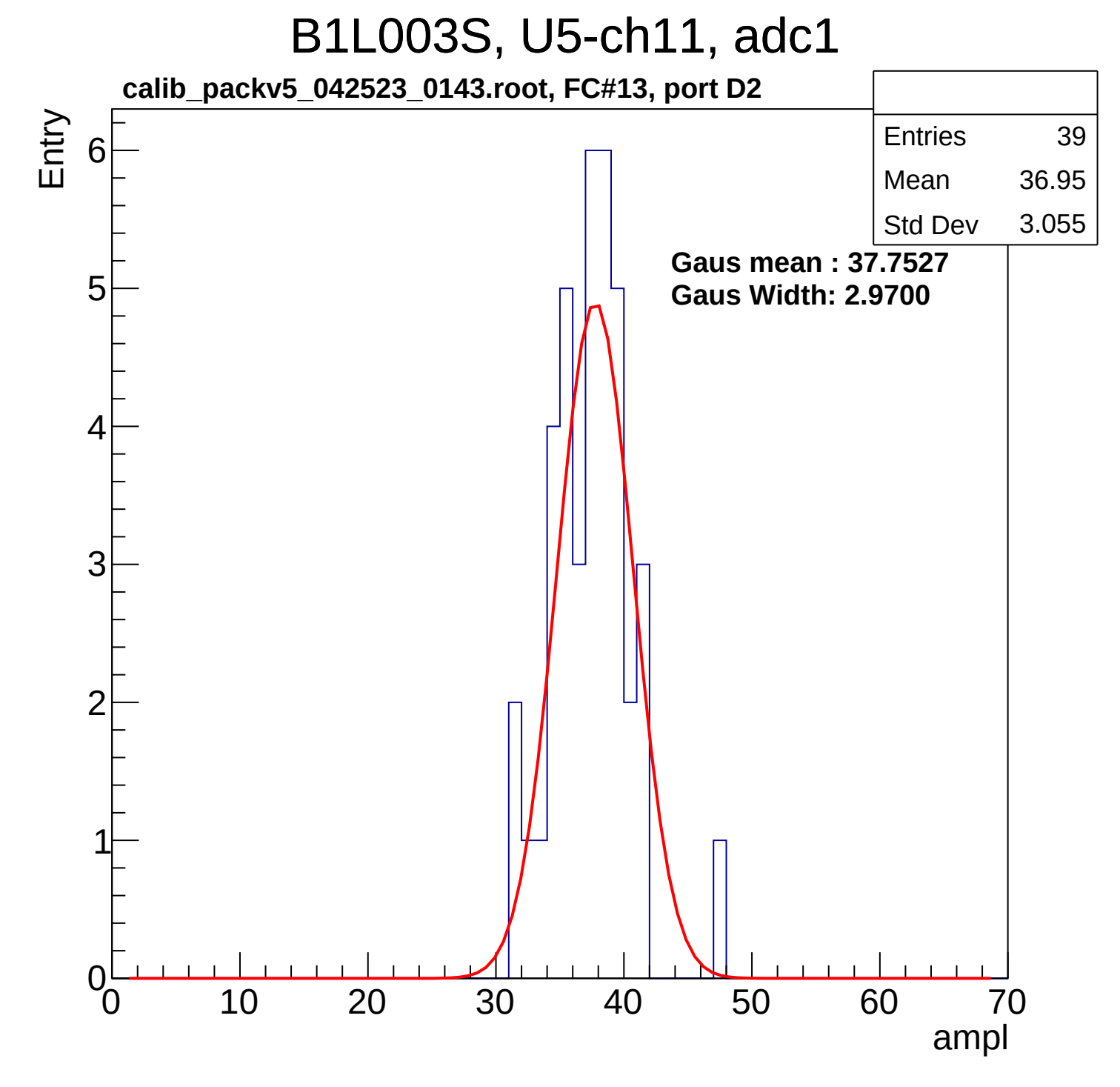

B1L003S, U5-ch10, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 57.23
Std Dev 16.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch10, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch11, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 95
Mean 29.35
Std Dev 6.474
Gaus mean : 30.8179
Gaus Width: 3.6959
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch11, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 36.95
Std Dev 3.055
Gaus mean : 37.7527
Gaus Width: 2.9700
Entry
ampl
0
1
2
3
4
5
6
0
10
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40
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60
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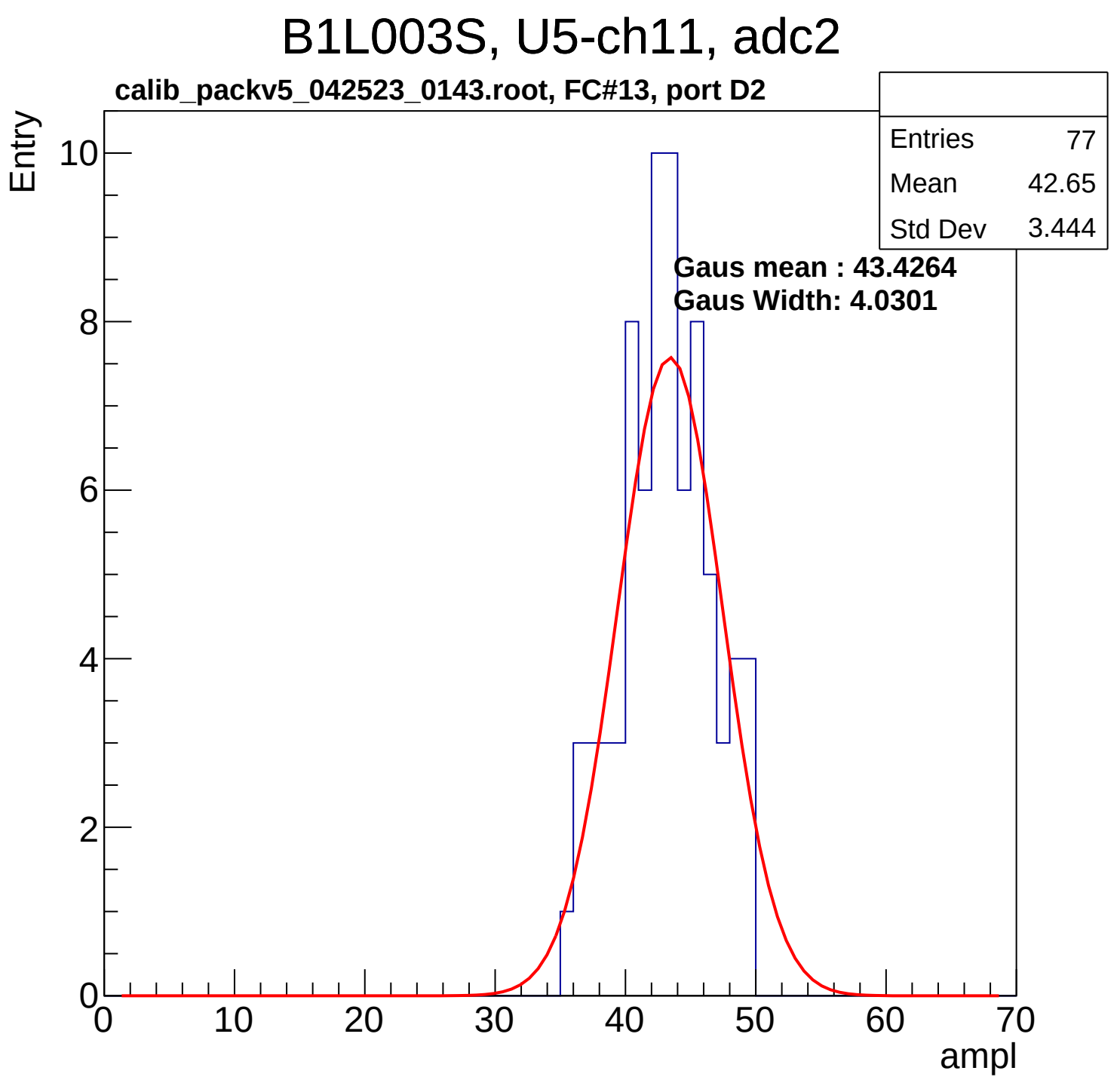
B1L003S, U5-ch11, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 42.65
Std Dev 3.444
Gaus mean : 43.4264
Gaus Width: 4.0301
Entry
ampl
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2
4
6
8
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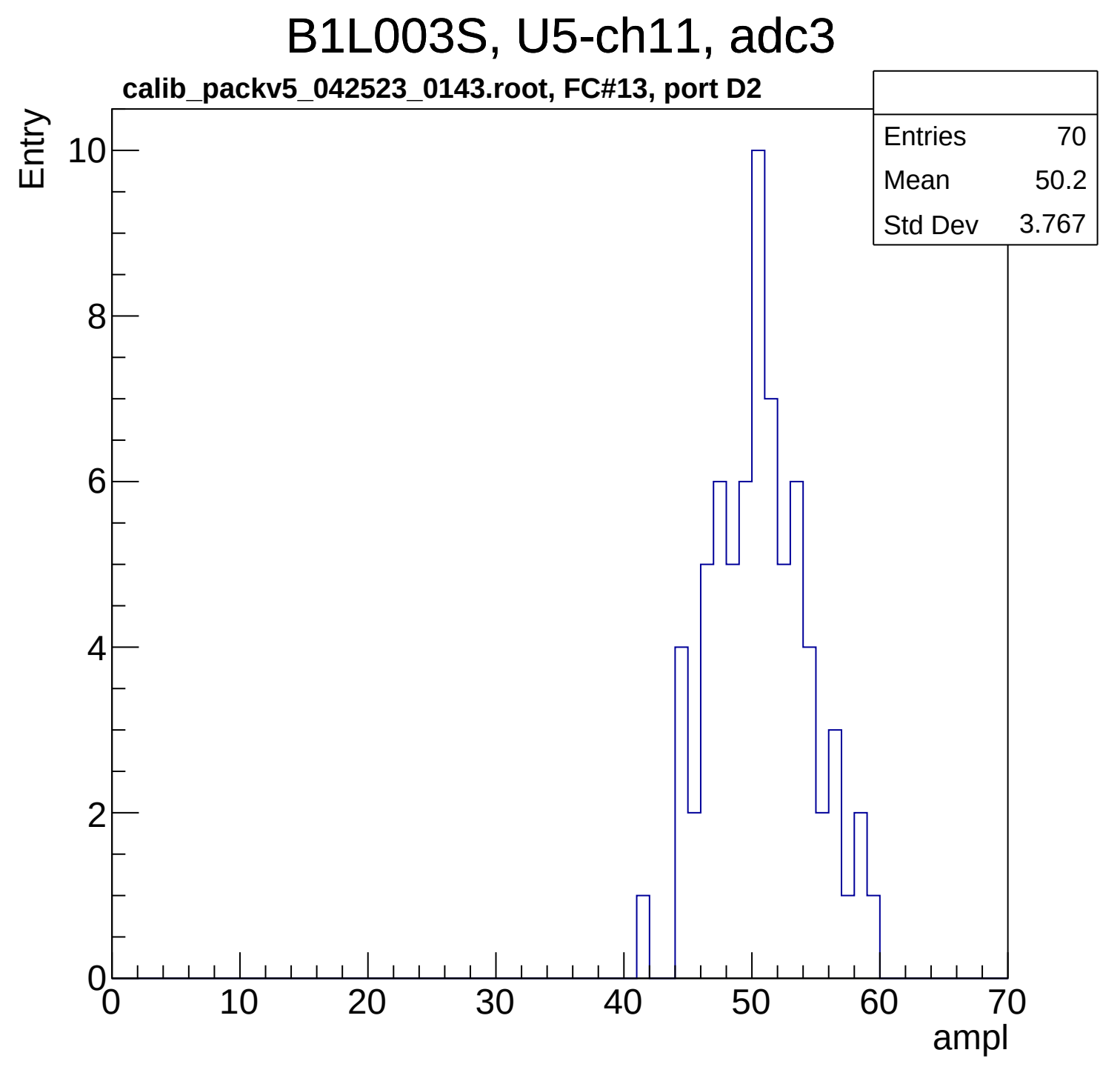
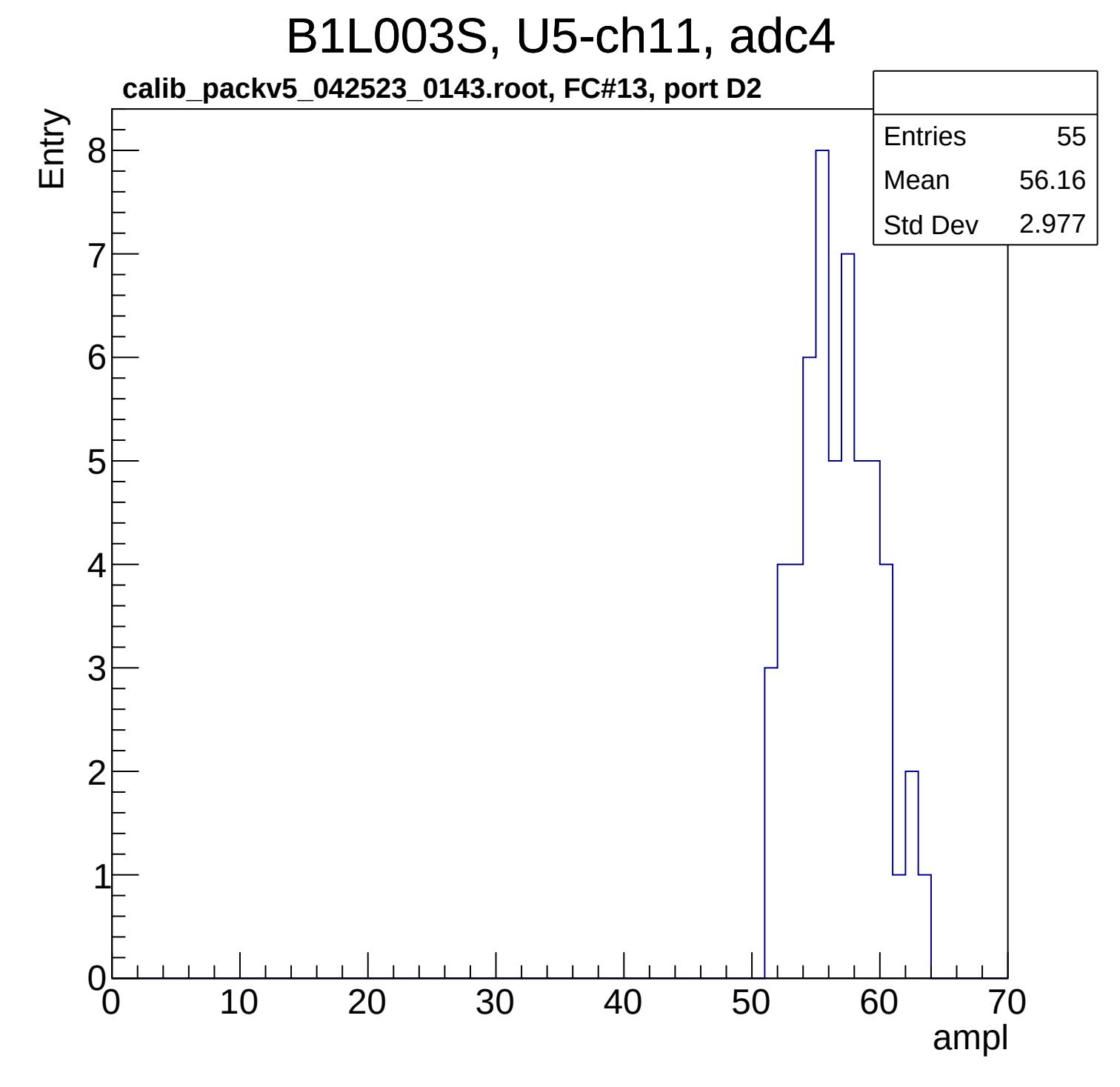
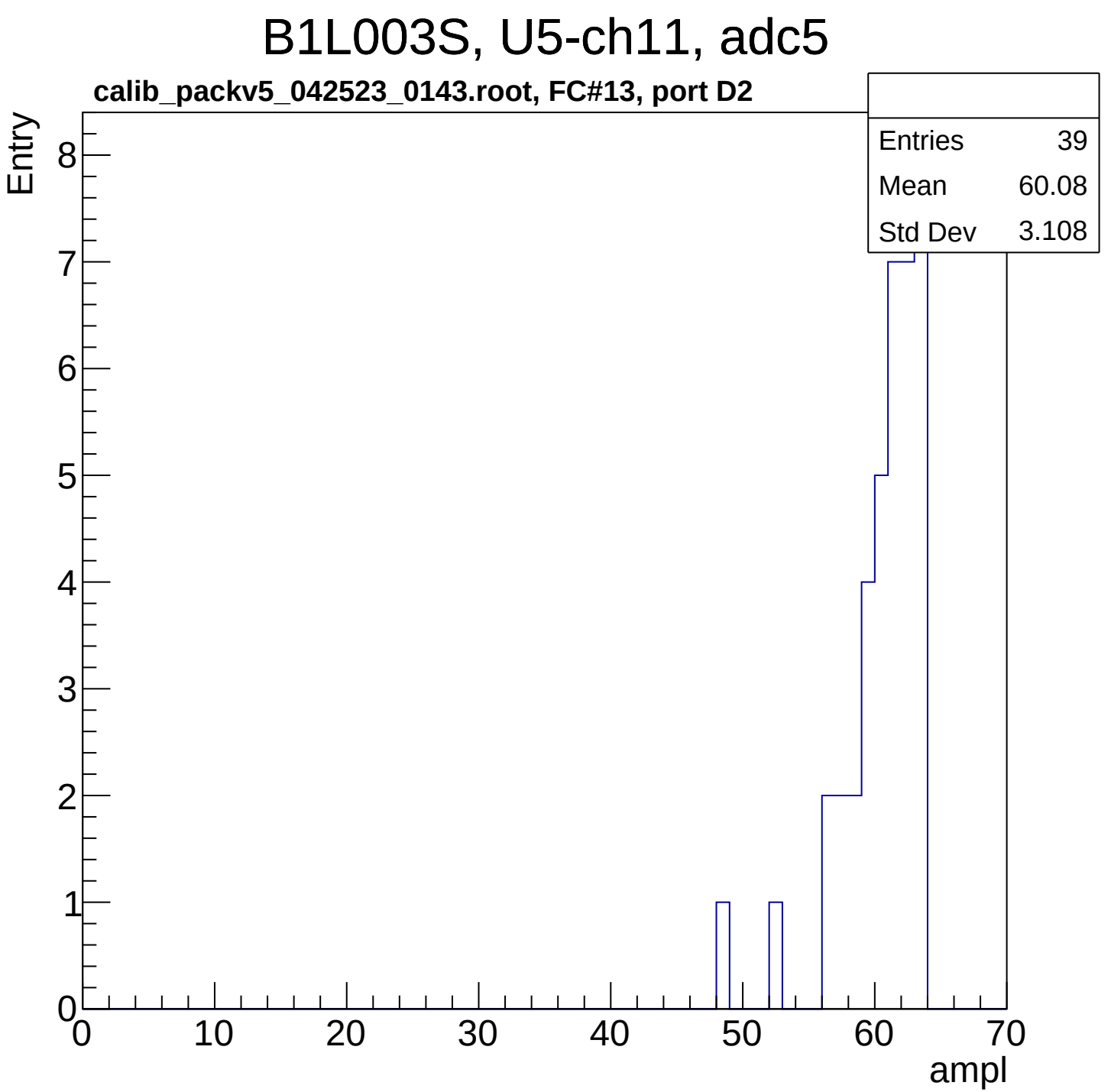
0
10
20
30
40
50
60
70


B1L003S, U5-ch11, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 50.2
Std Dev 3.767
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch11, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56.16
Std Dev 2.977
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch11, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 60.08
Std Dev 3.108
Entry
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2
3
4
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6
7
8
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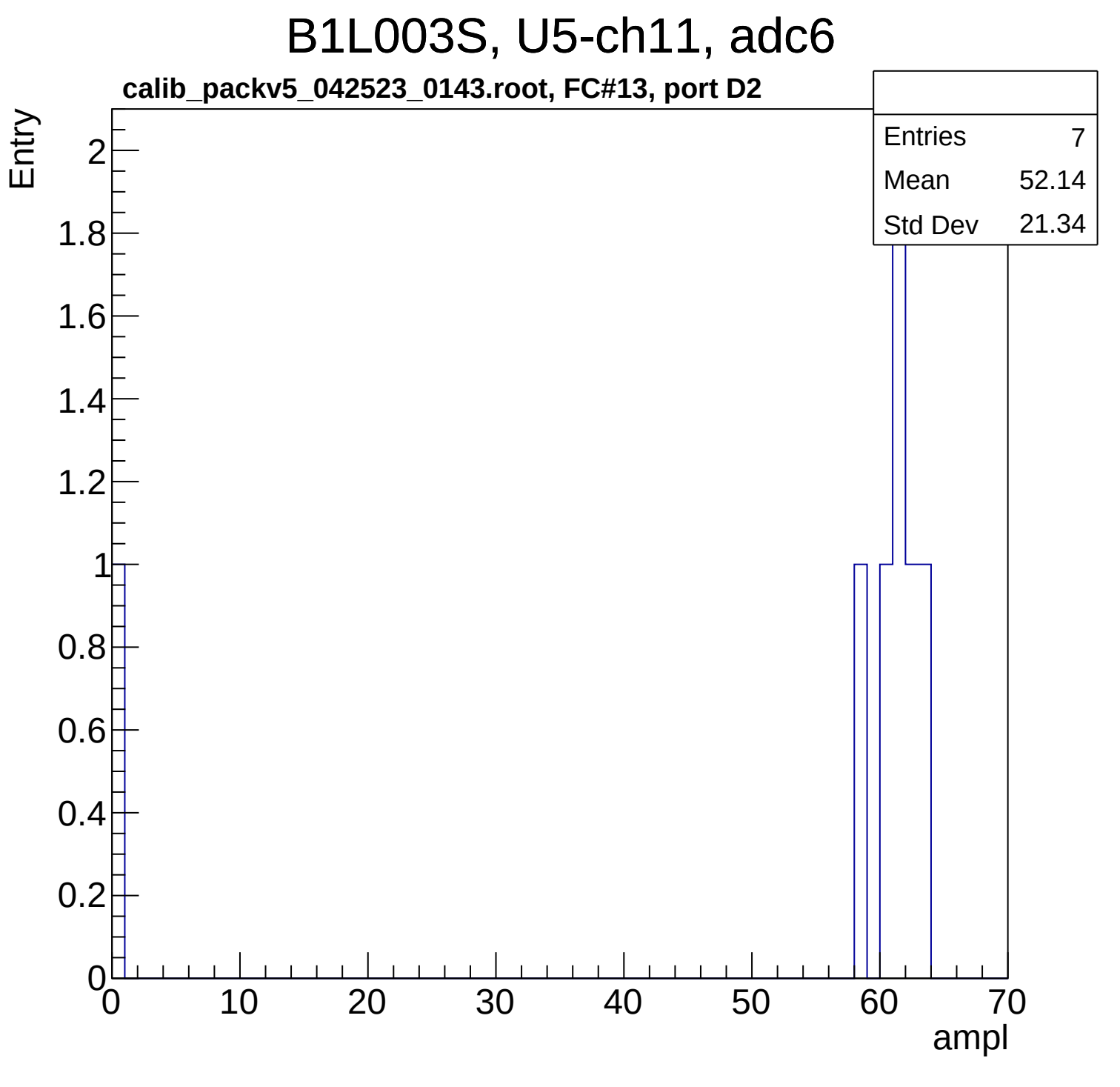
70
ampl


B1L003S, U5-ch11, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.14
Std Dev 21.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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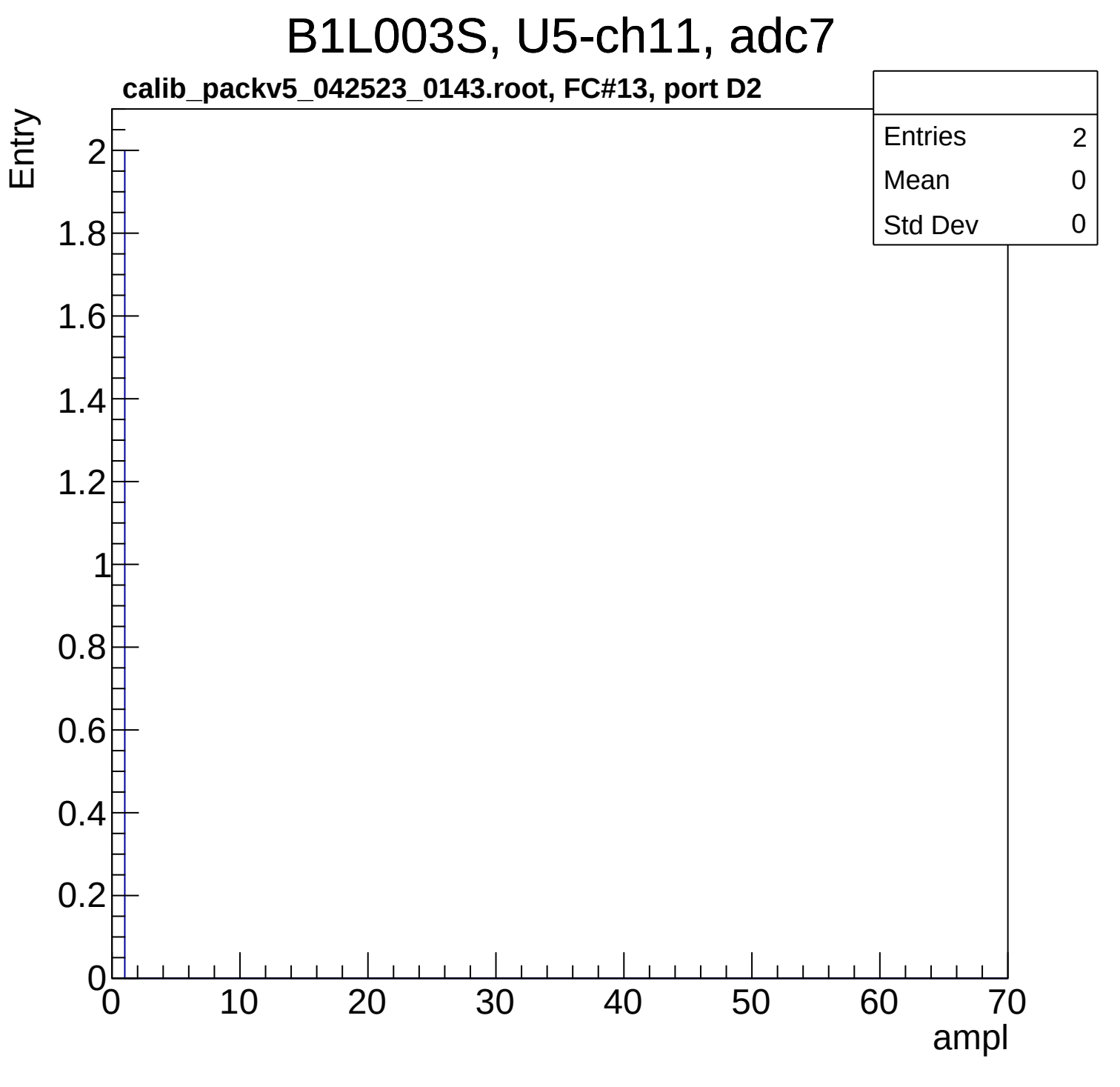
40
50
60
70


B1L003S, U5-ch11, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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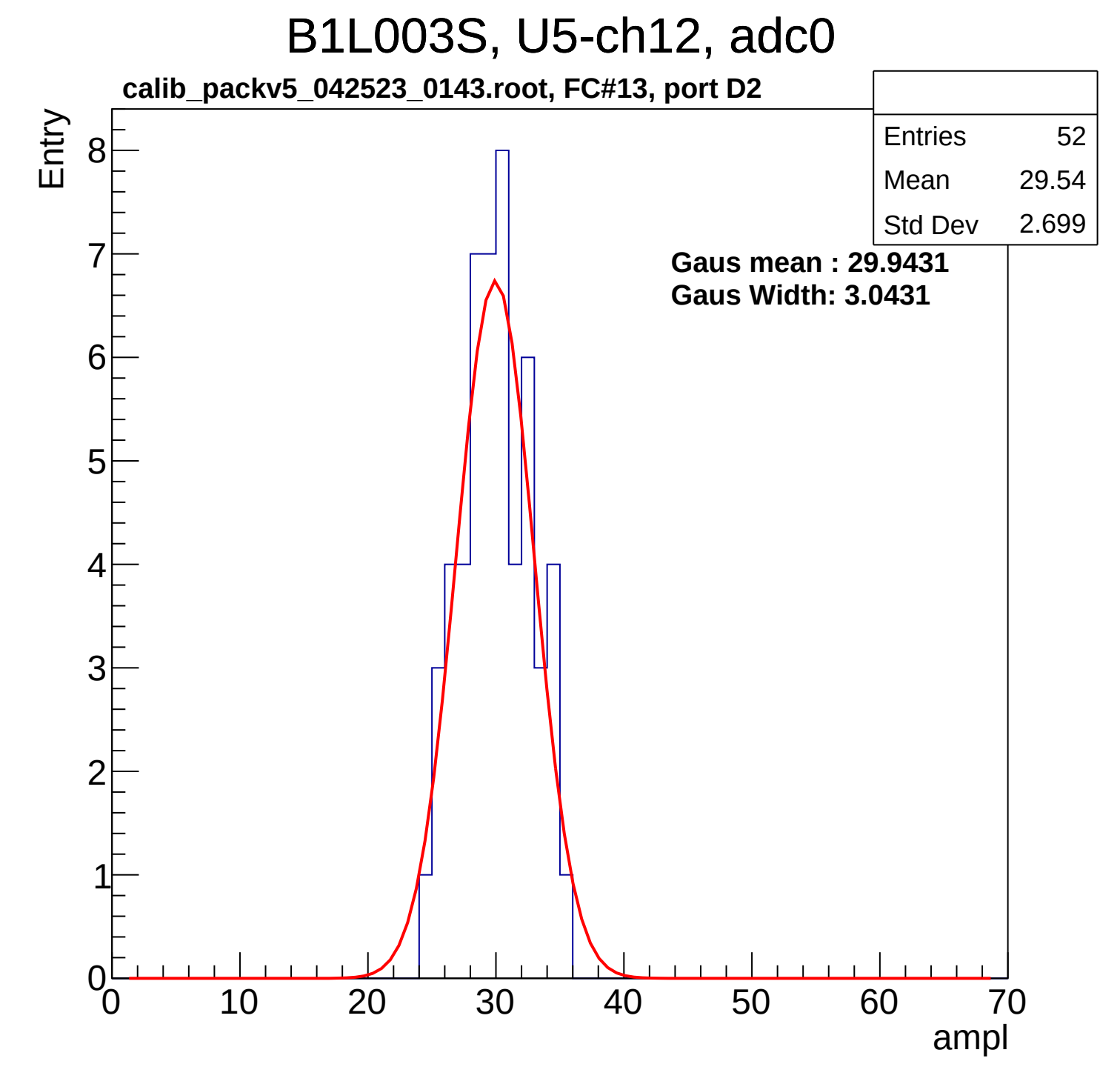
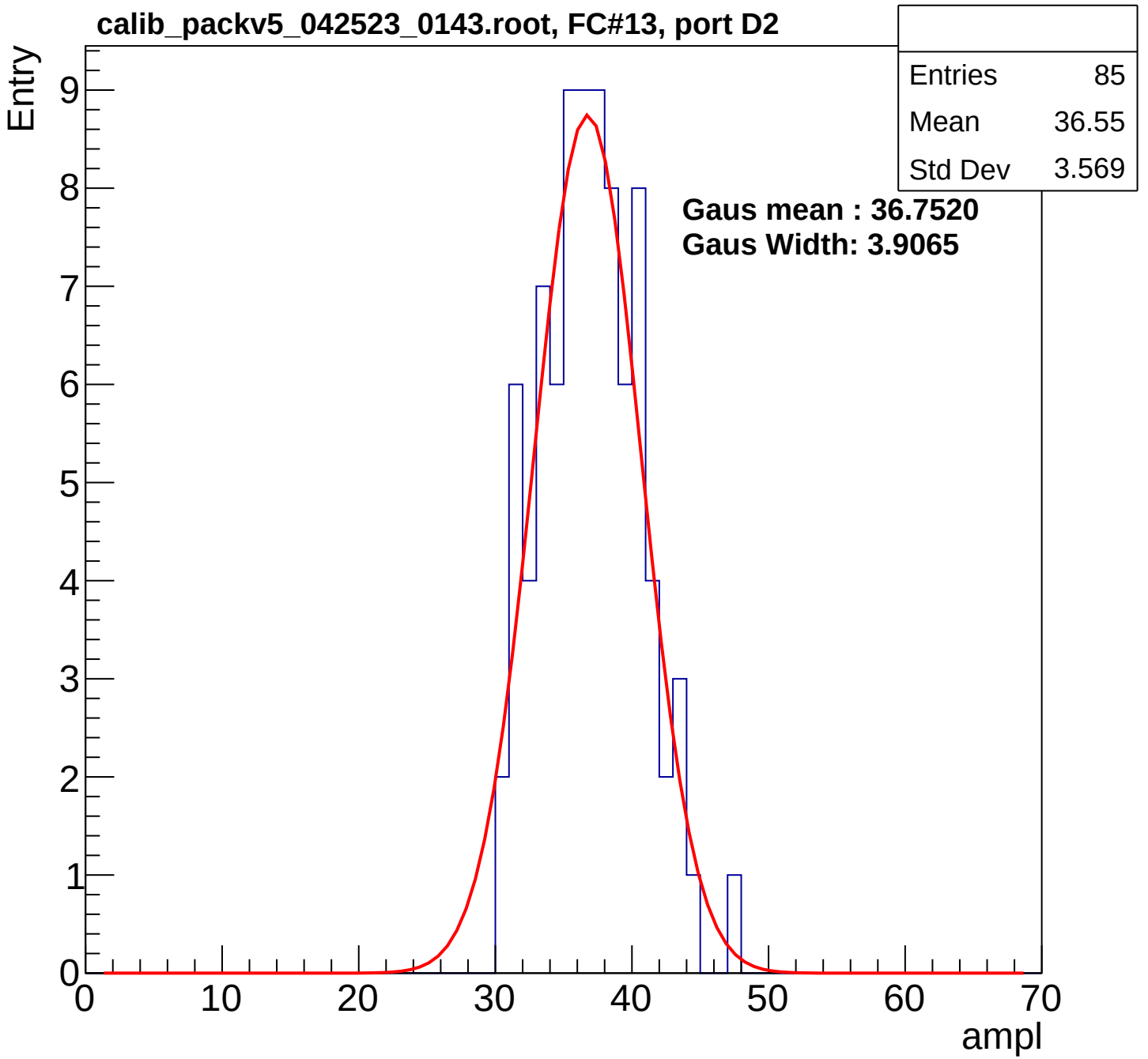

B1L003S, U5-ch12, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.54
Std Dev 2.699
Gaus mean : 29.9431
Gaus Width: 3.0431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 36.55
Std Dev 3.569
Gaus mean : 36.7520
Gaus Width: 3.9065
Entry
ampl
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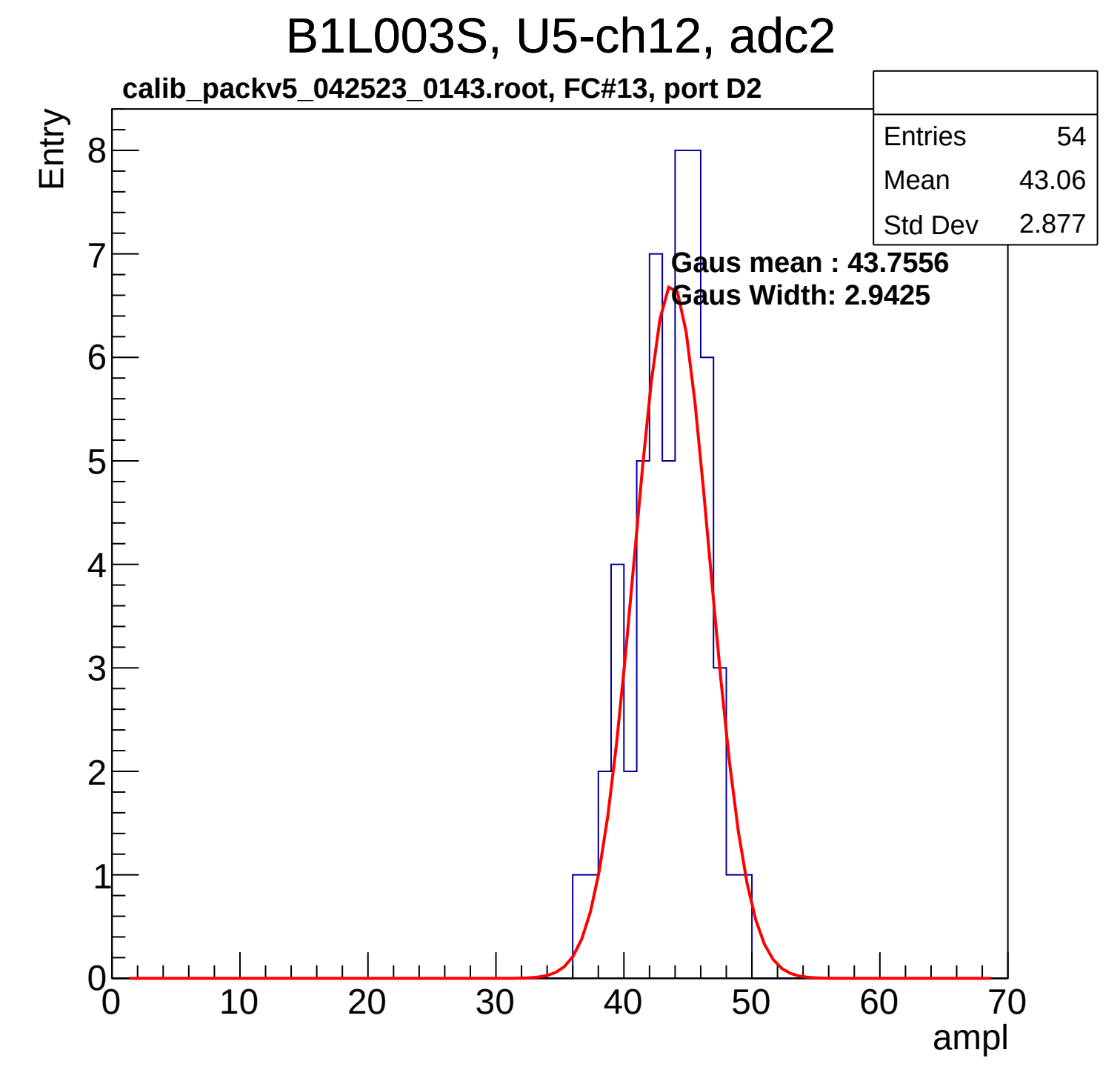
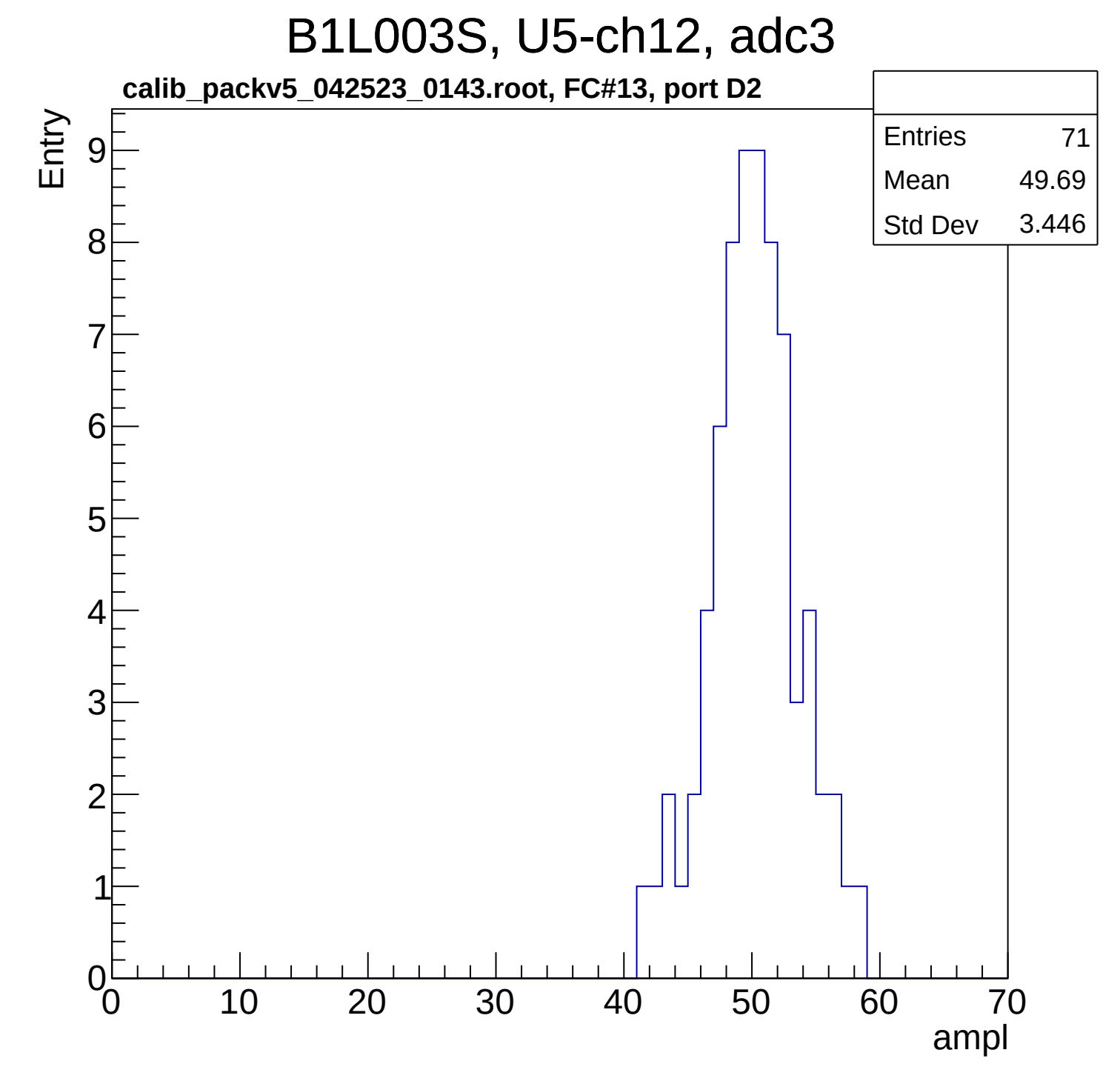
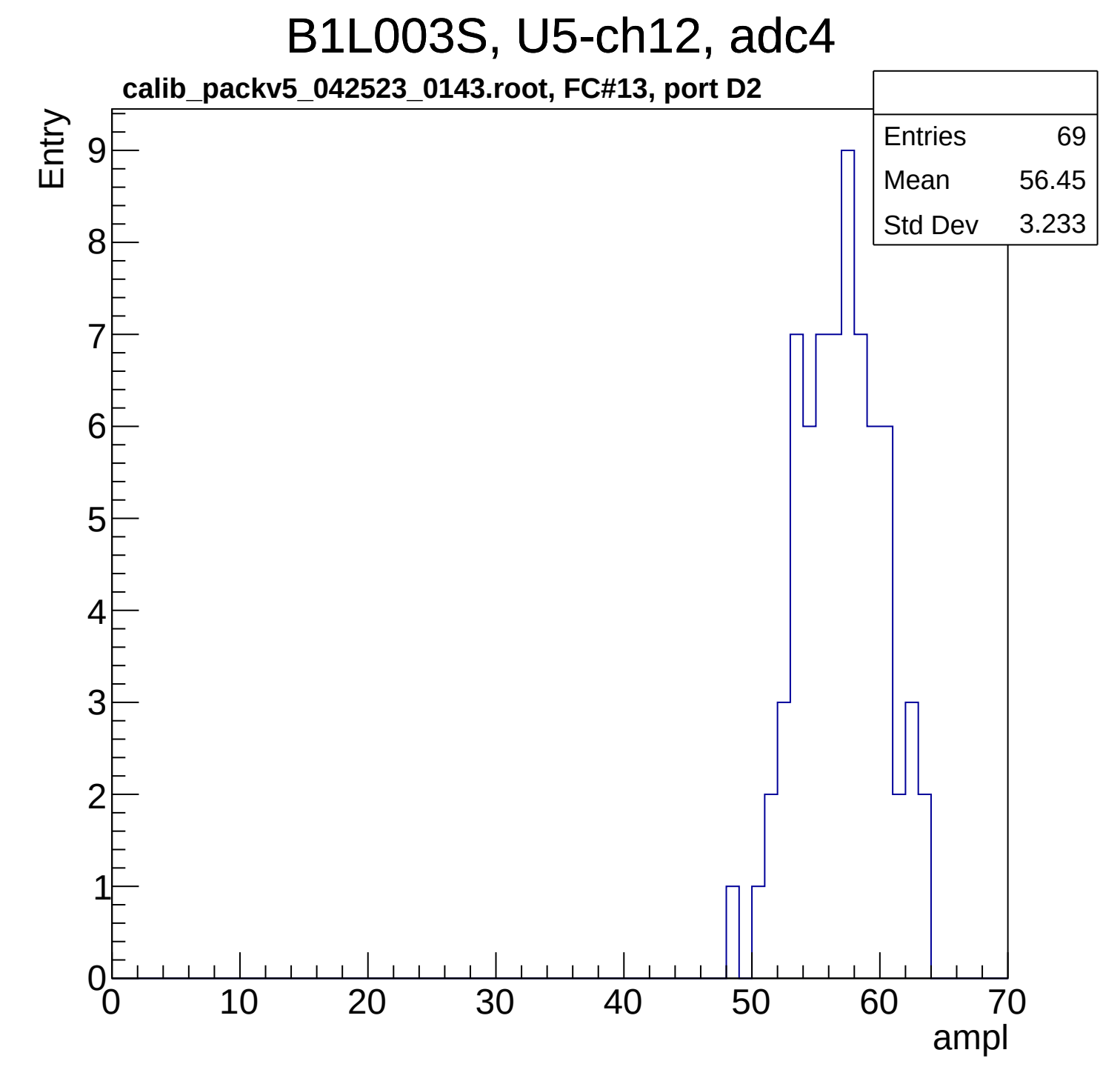
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.06
Std Dev 2.877
Gaus mean : 43.7556
Gaus Width: 2.9425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.69
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 56.45
Std Dev 3.233
Entry
ampl
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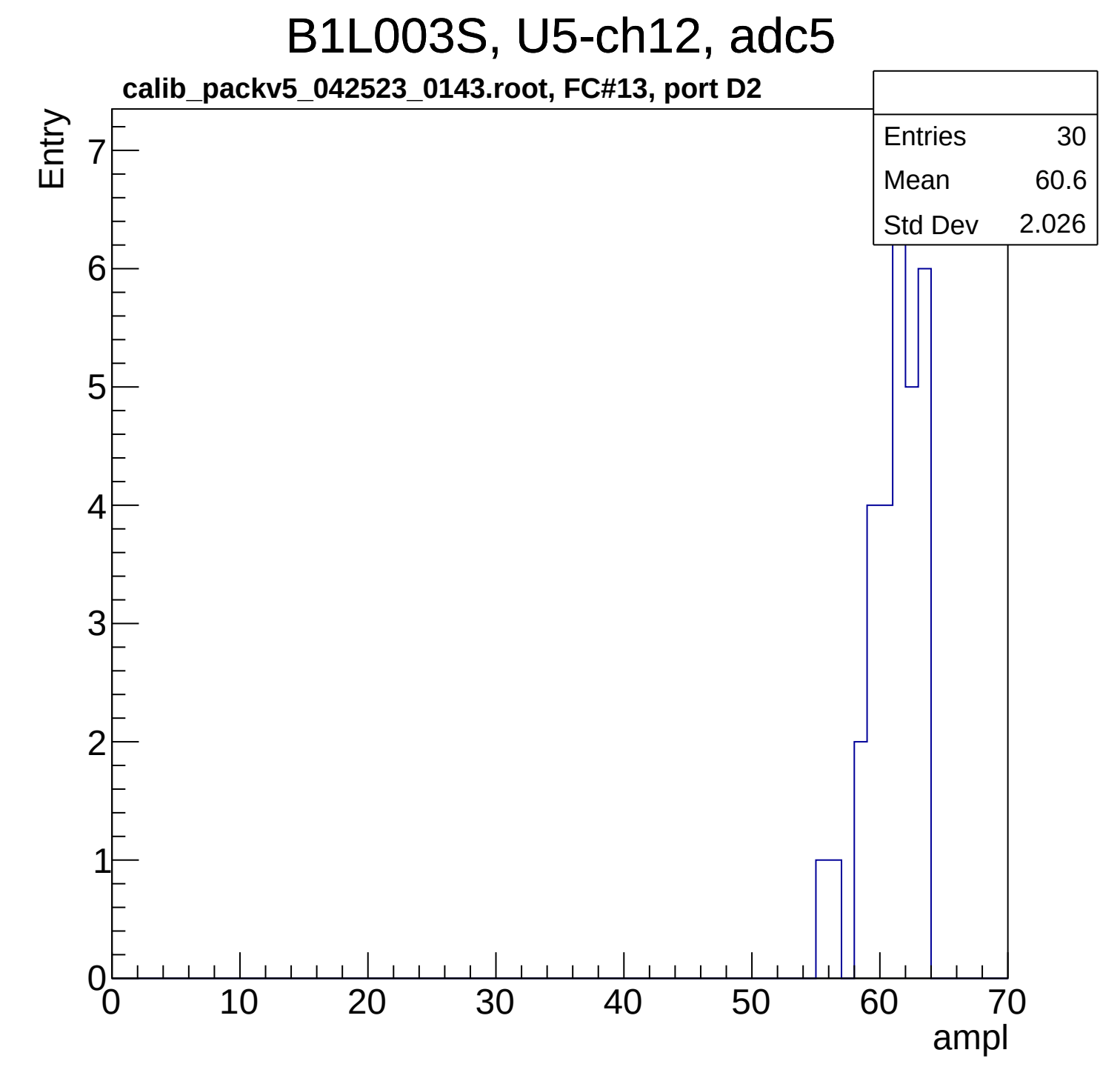
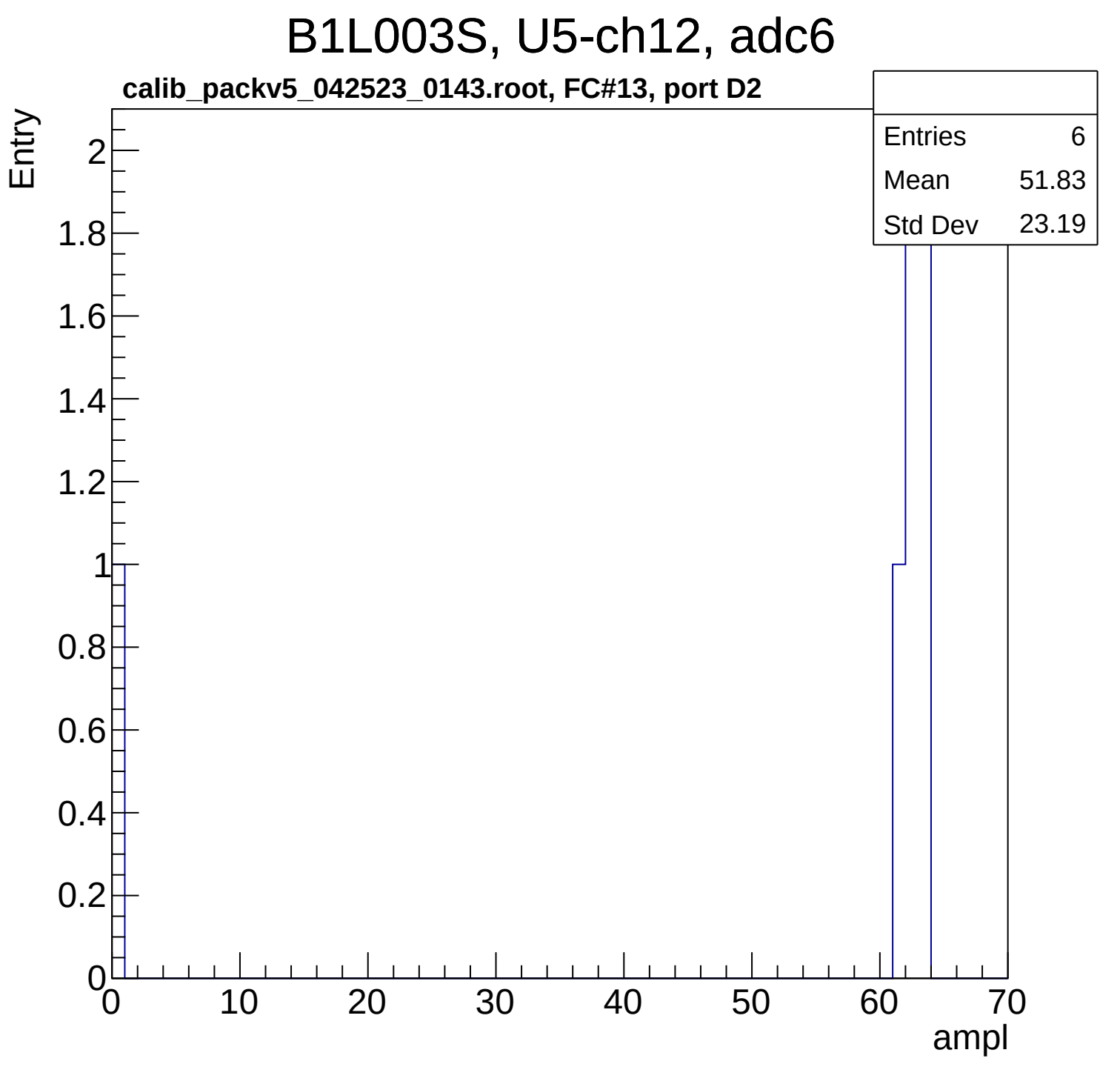
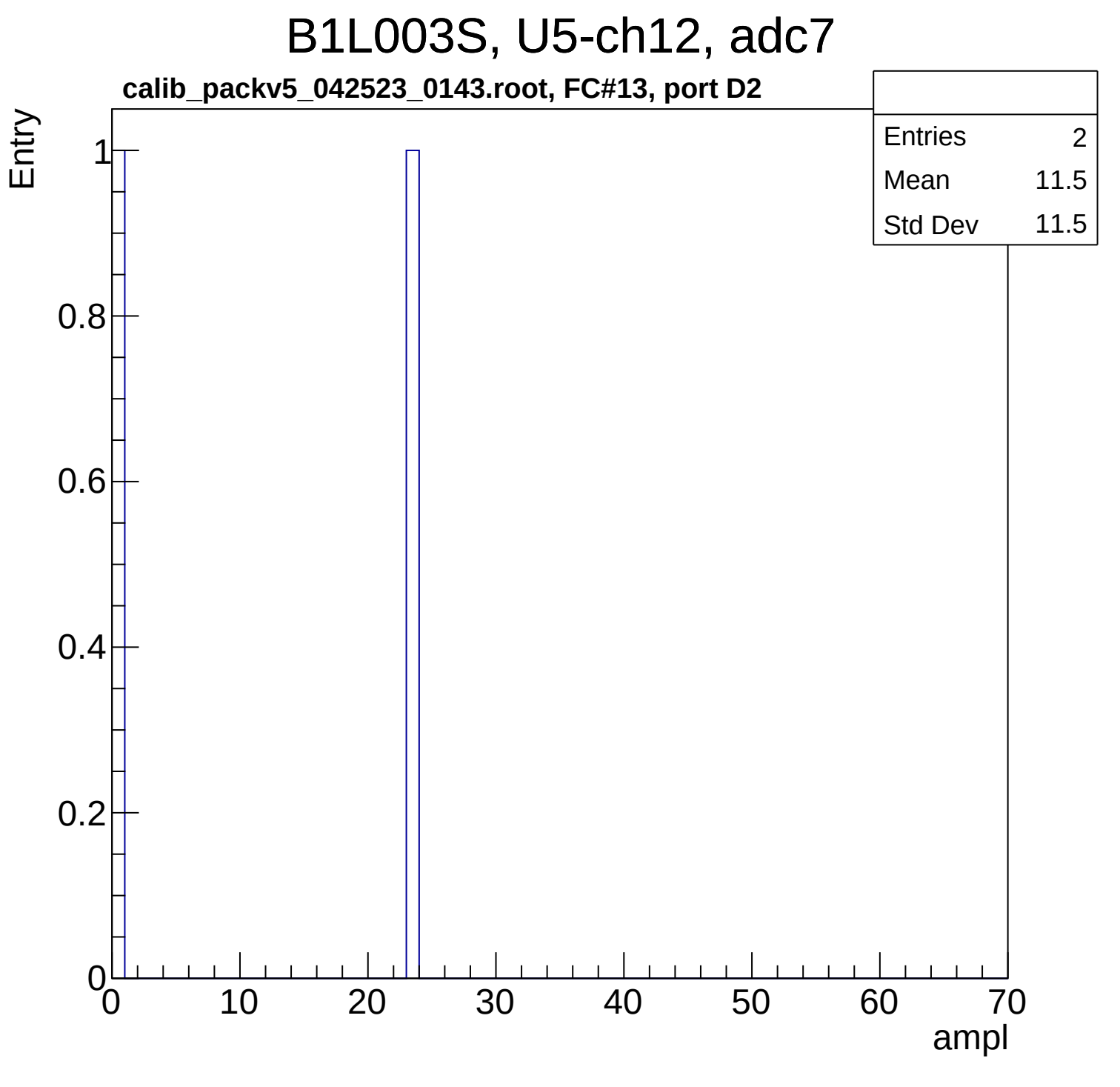
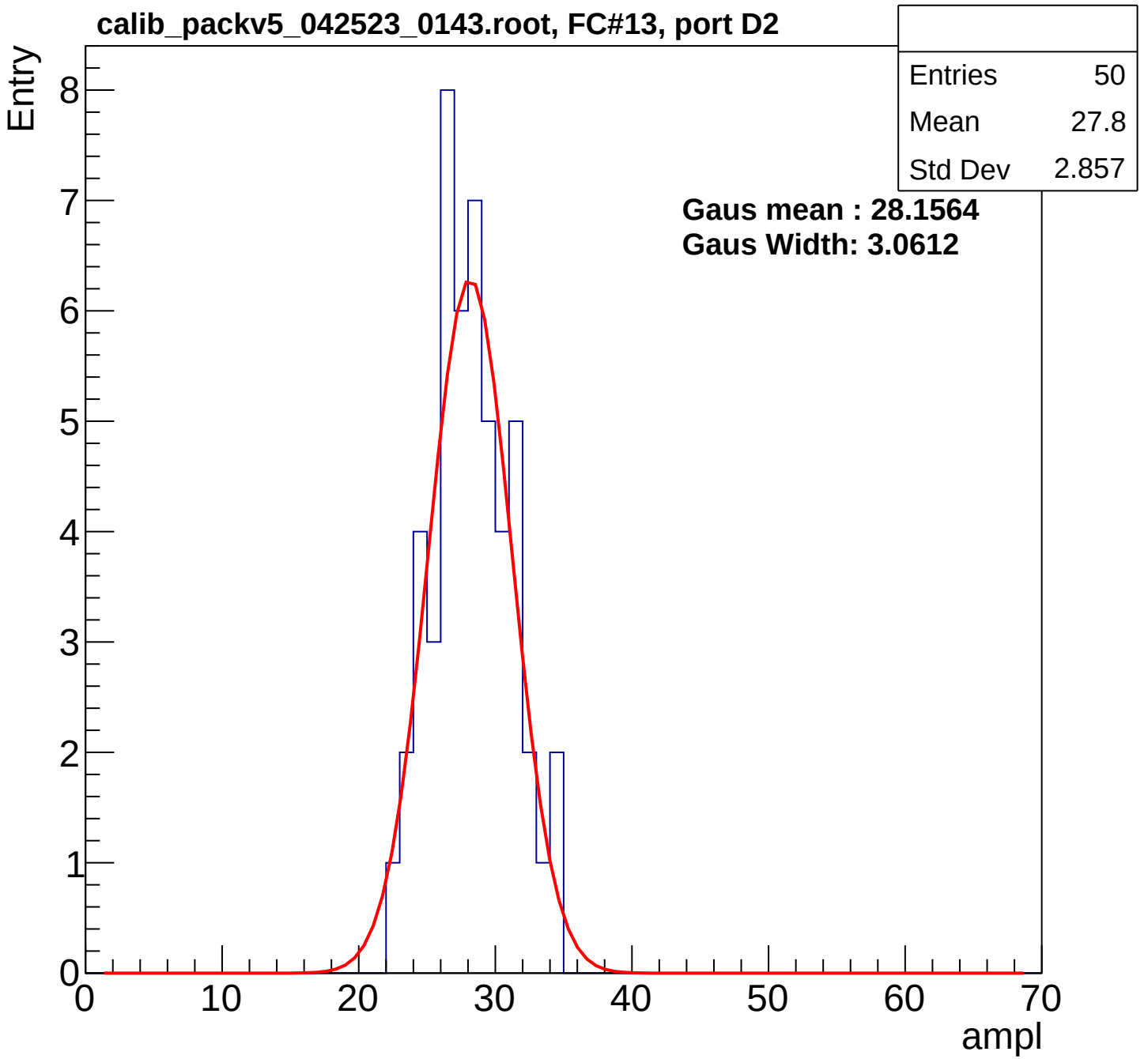
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 60.6
Std Dev 2.026
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 27.8
Std Dev 2.857
Gaus mean : 28.1564
Gaus Width: 3.0612
Entry
ampl
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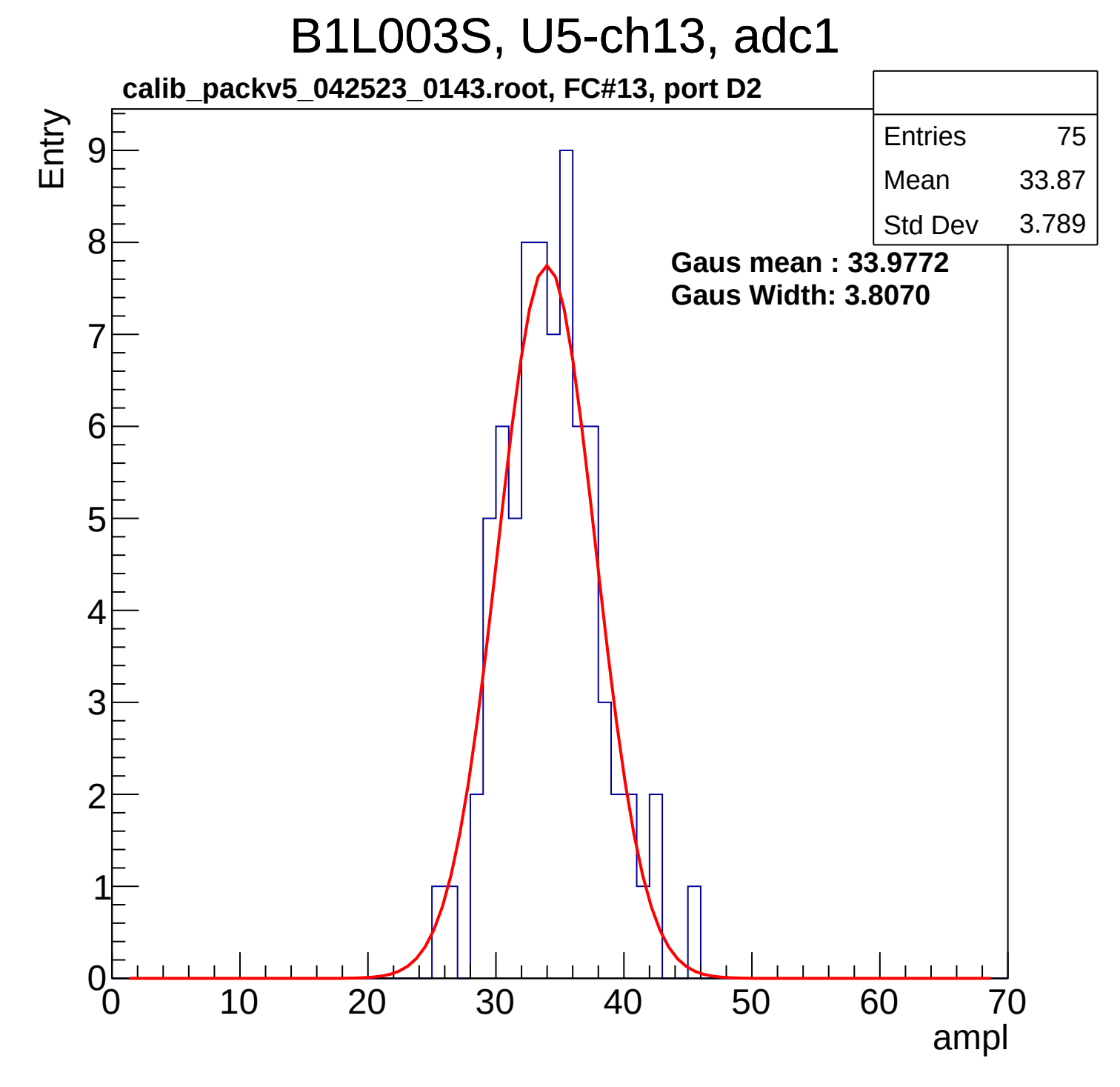
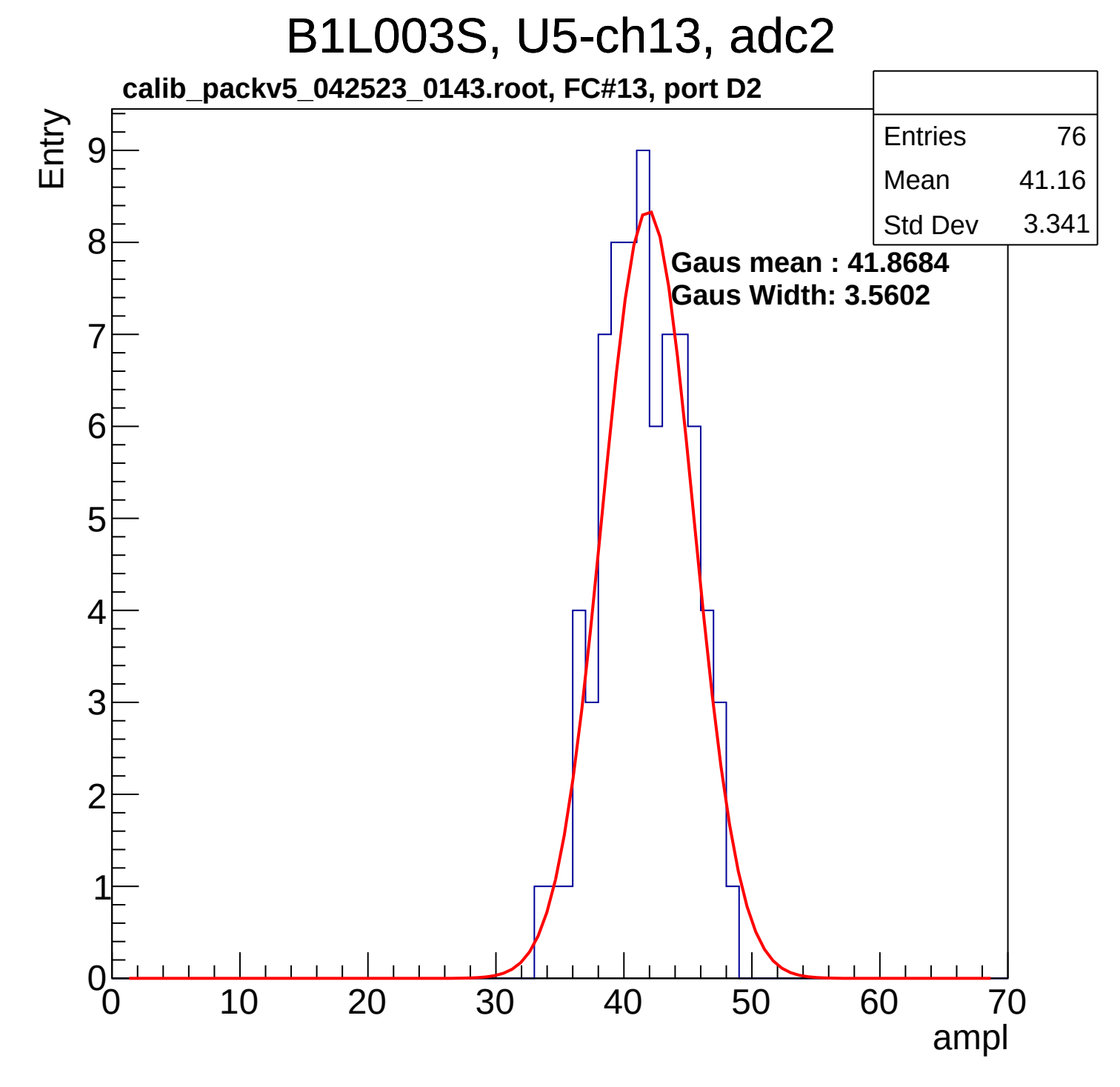
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 33.87
Std Dev 3.789
Gaus mean : 33.9772
Gaus Width: 3.8070
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 41.16
Std Dev 3.341
Gaus mean : 41.8684
Gaus Width: 3.5602
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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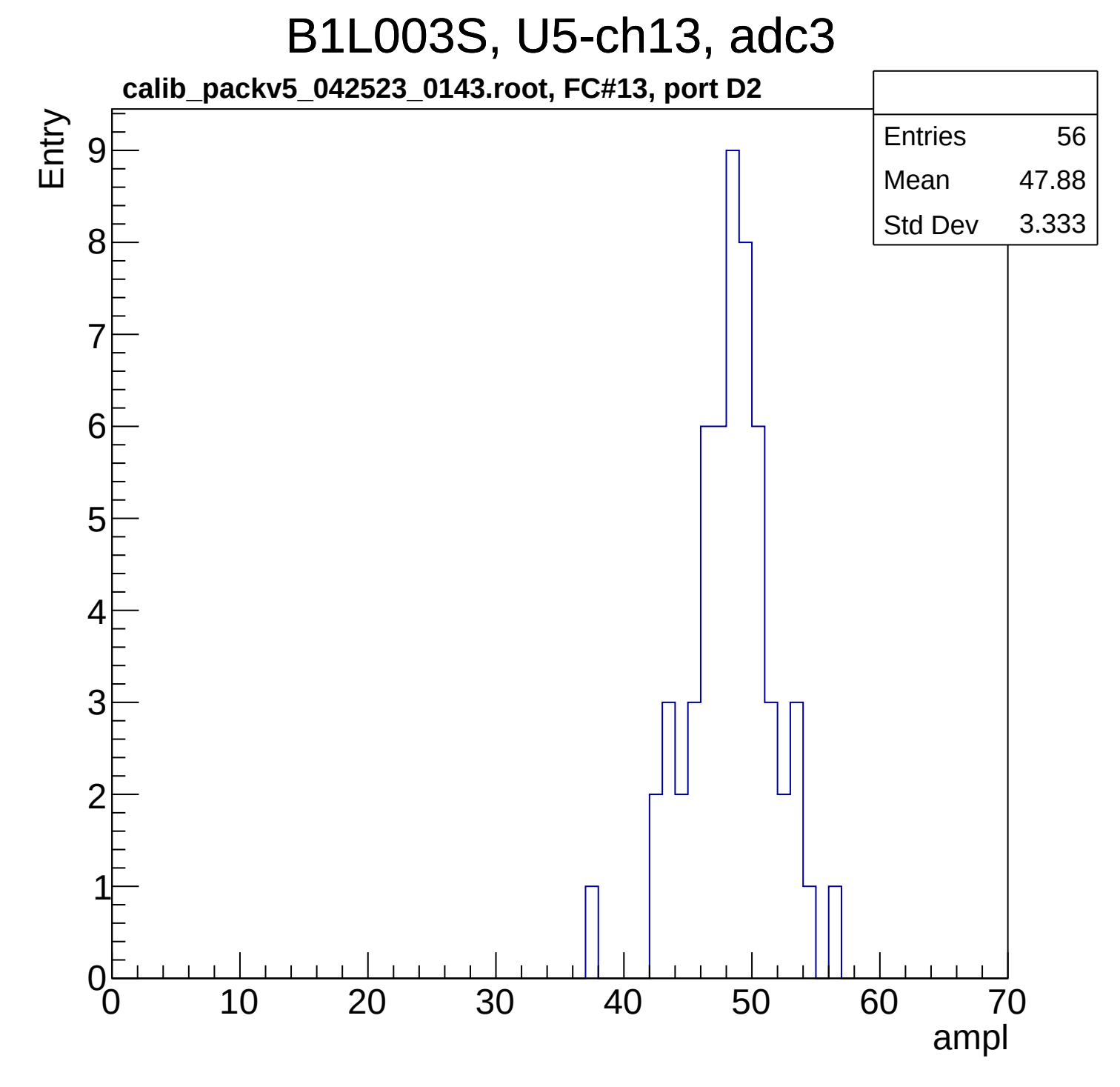
B1L003S, U5-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 47.88
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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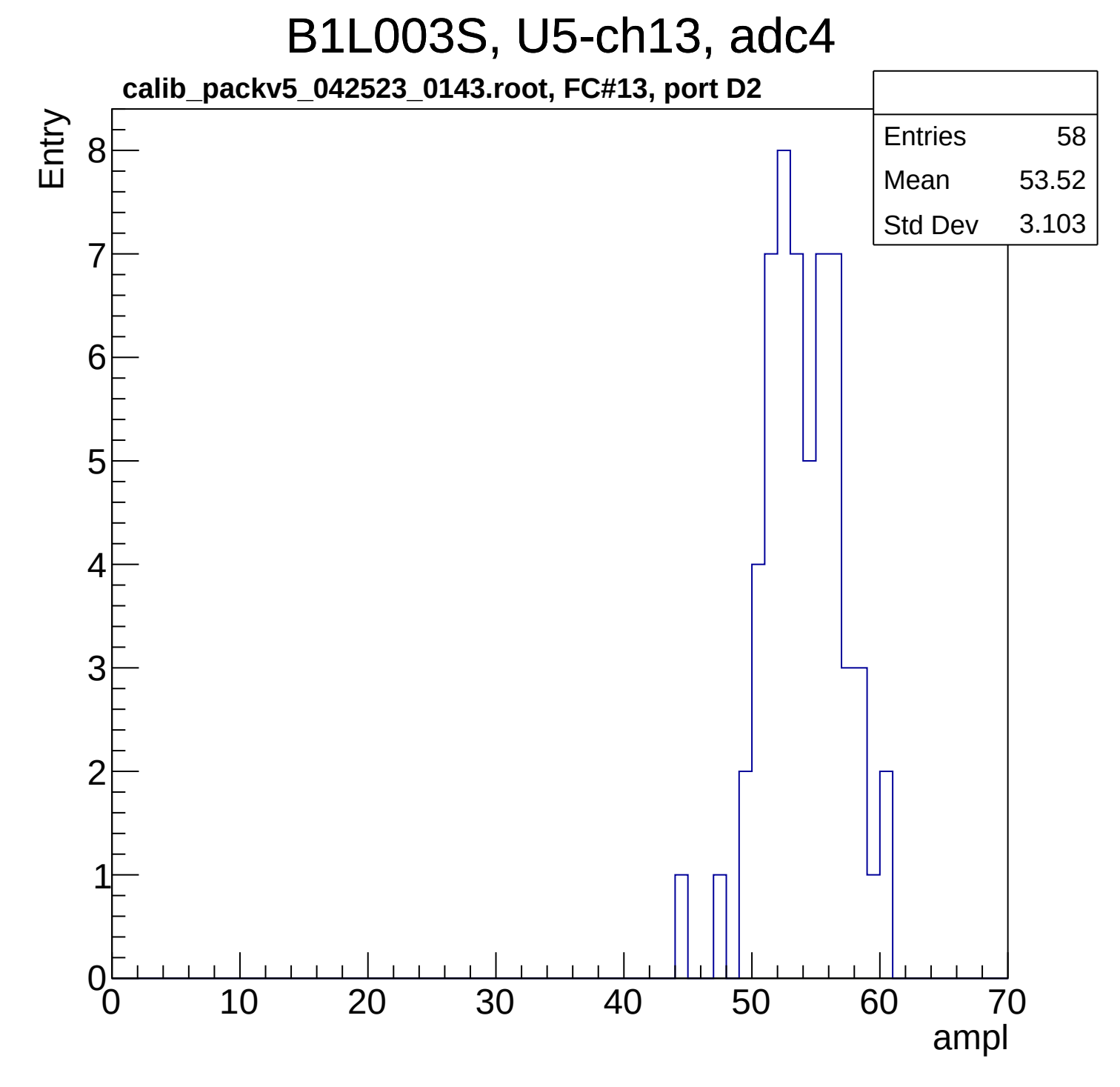
B1L003S, U5-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.52
Std Dev 3.103
Entry
ampl
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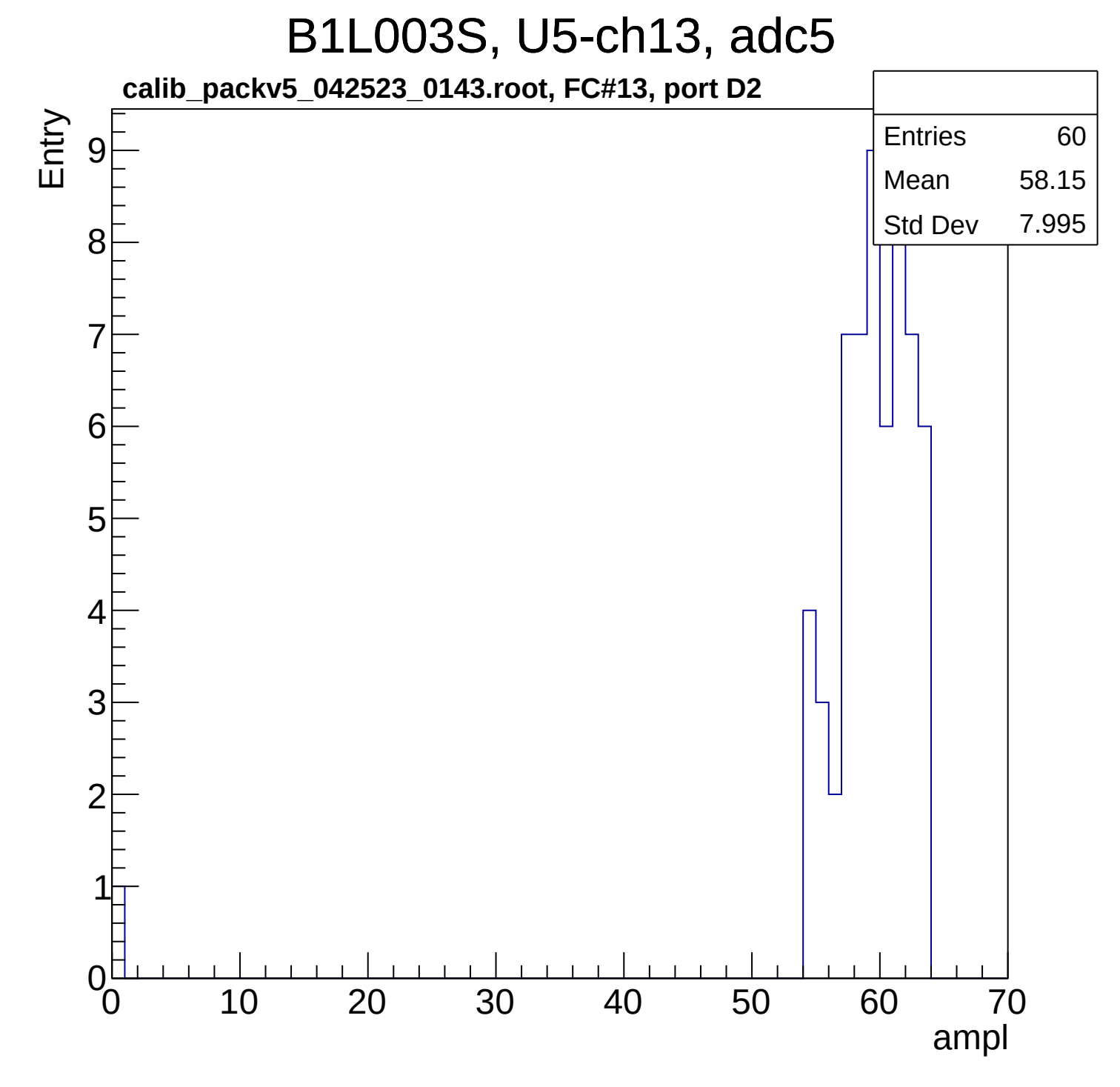
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.15
Std Dev 7.995
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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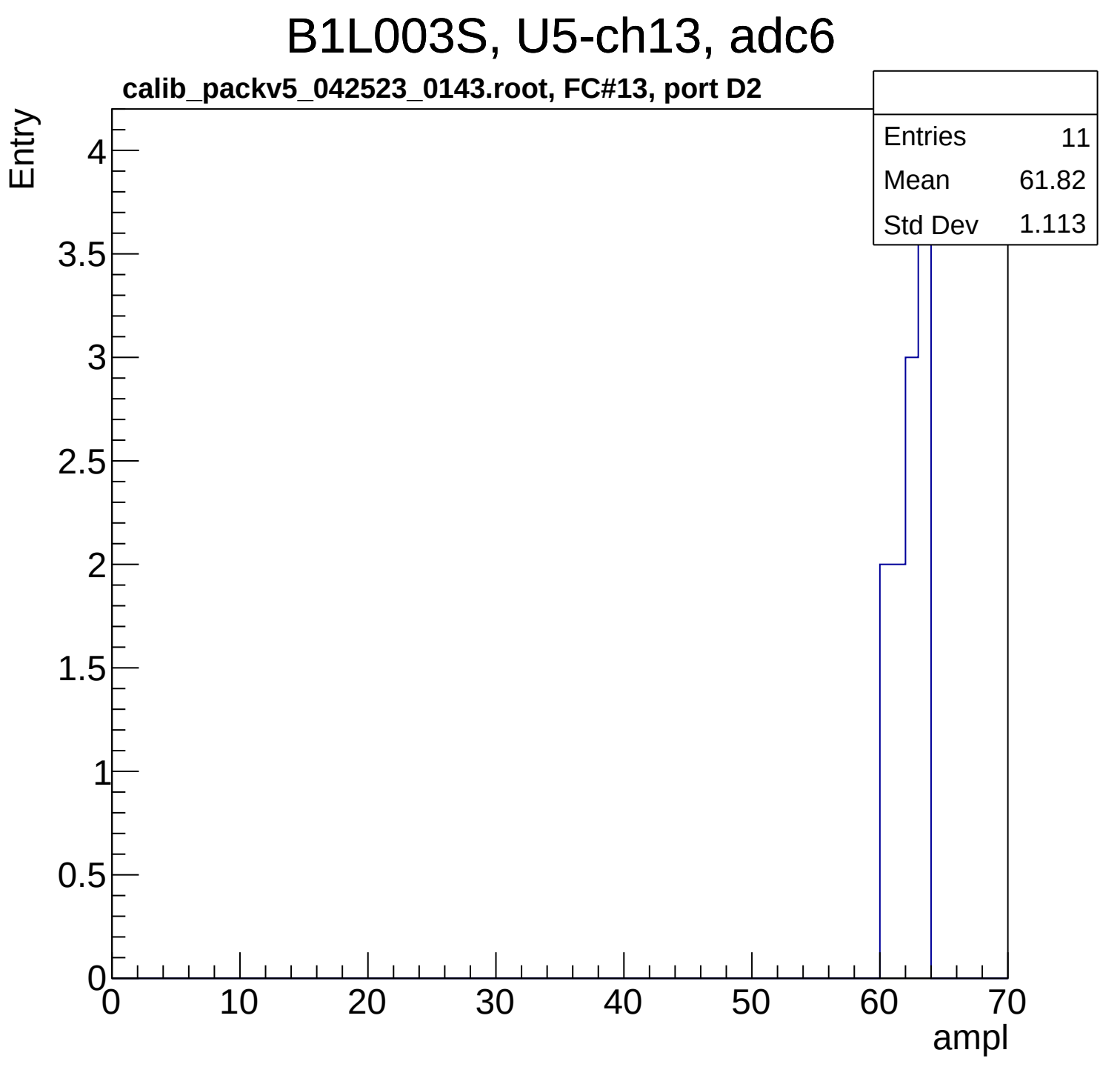
40
50
60
70


B1L003S, U5-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.82
Std Dev 1.113
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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60
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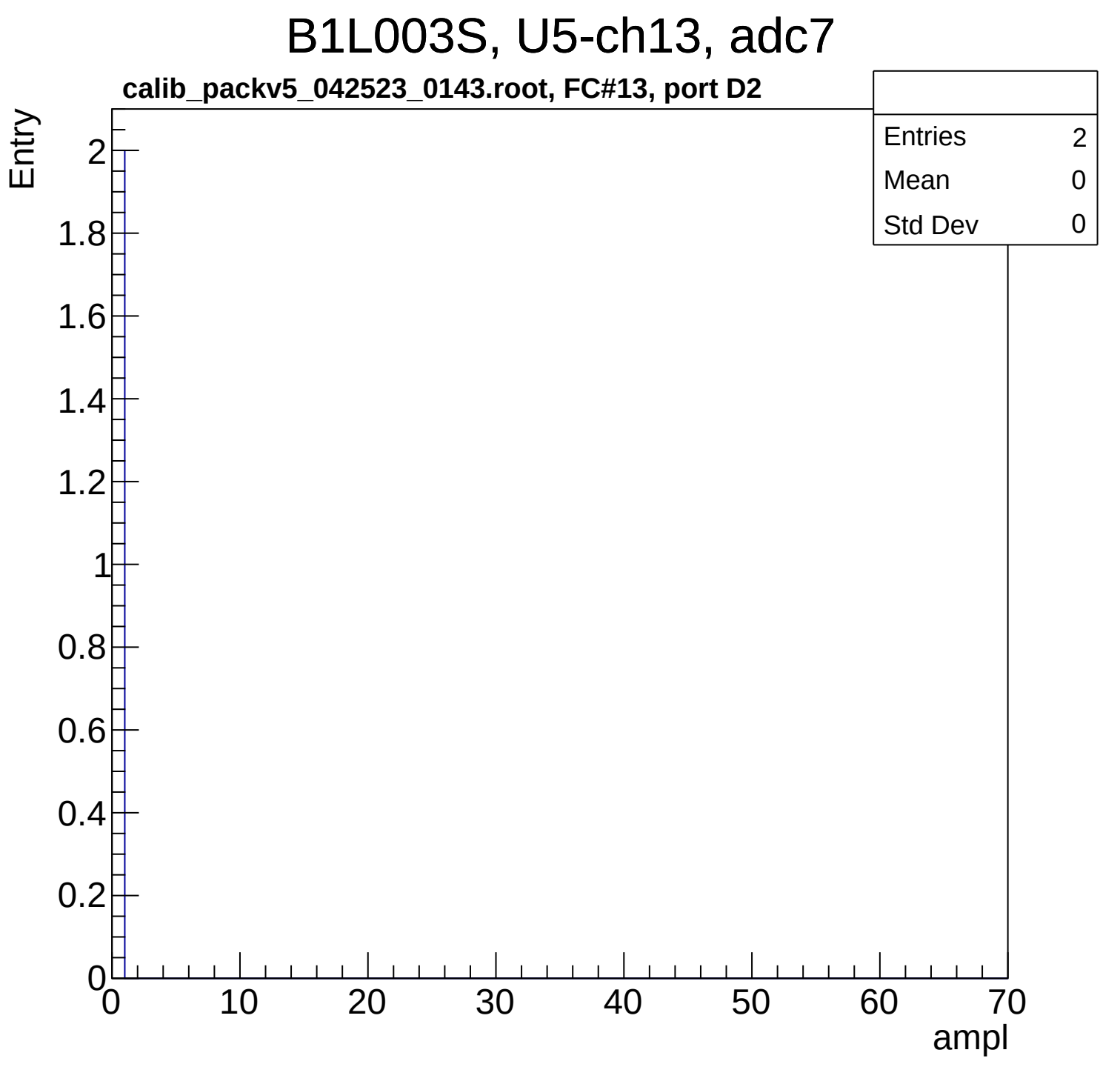
B1L003S, U5-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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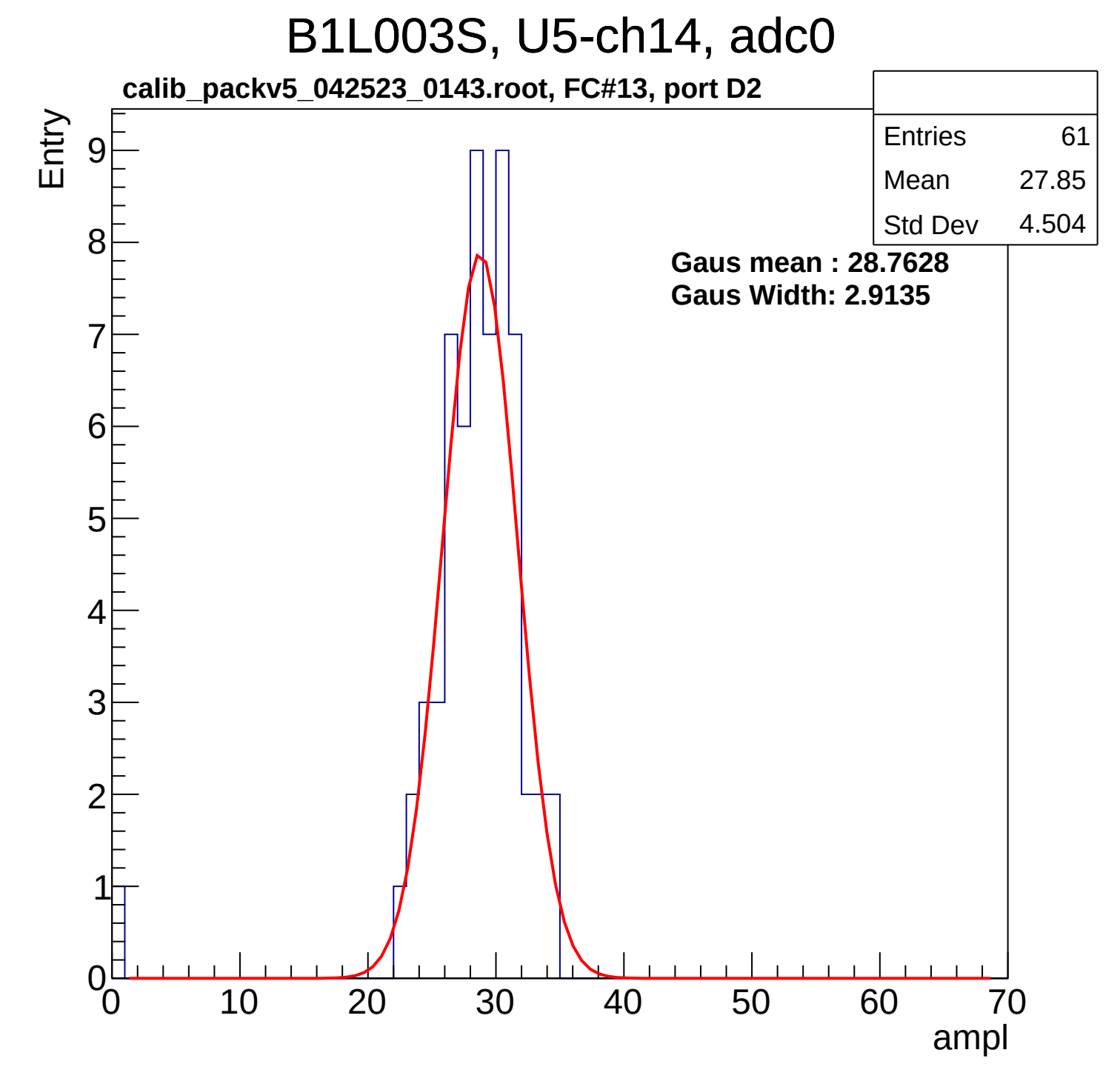
B1L003S, U5-ch14, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.85
Std Dev 4.504
Gaus mean : 28.7628
Gaus Width: 2.9135
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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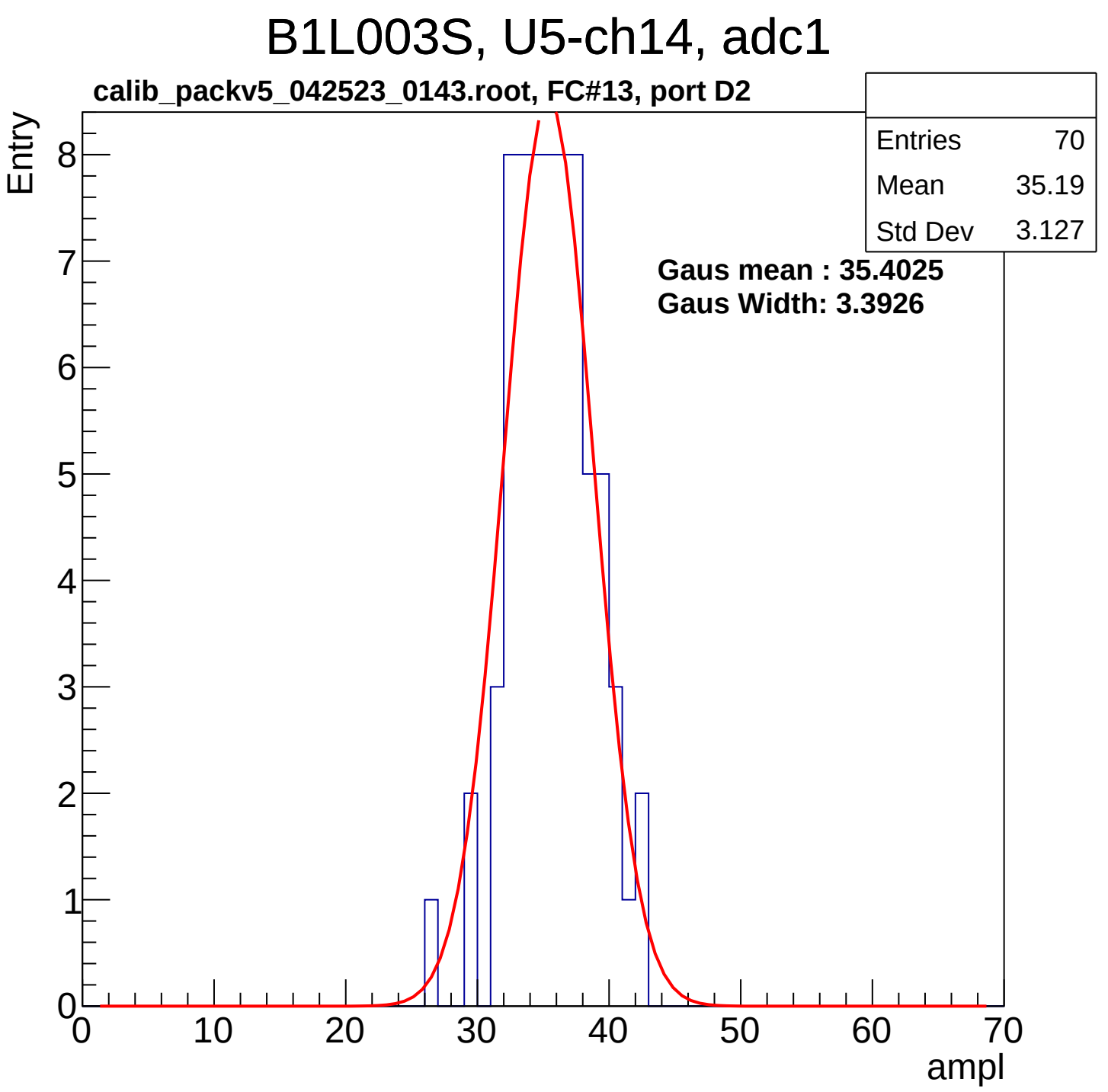
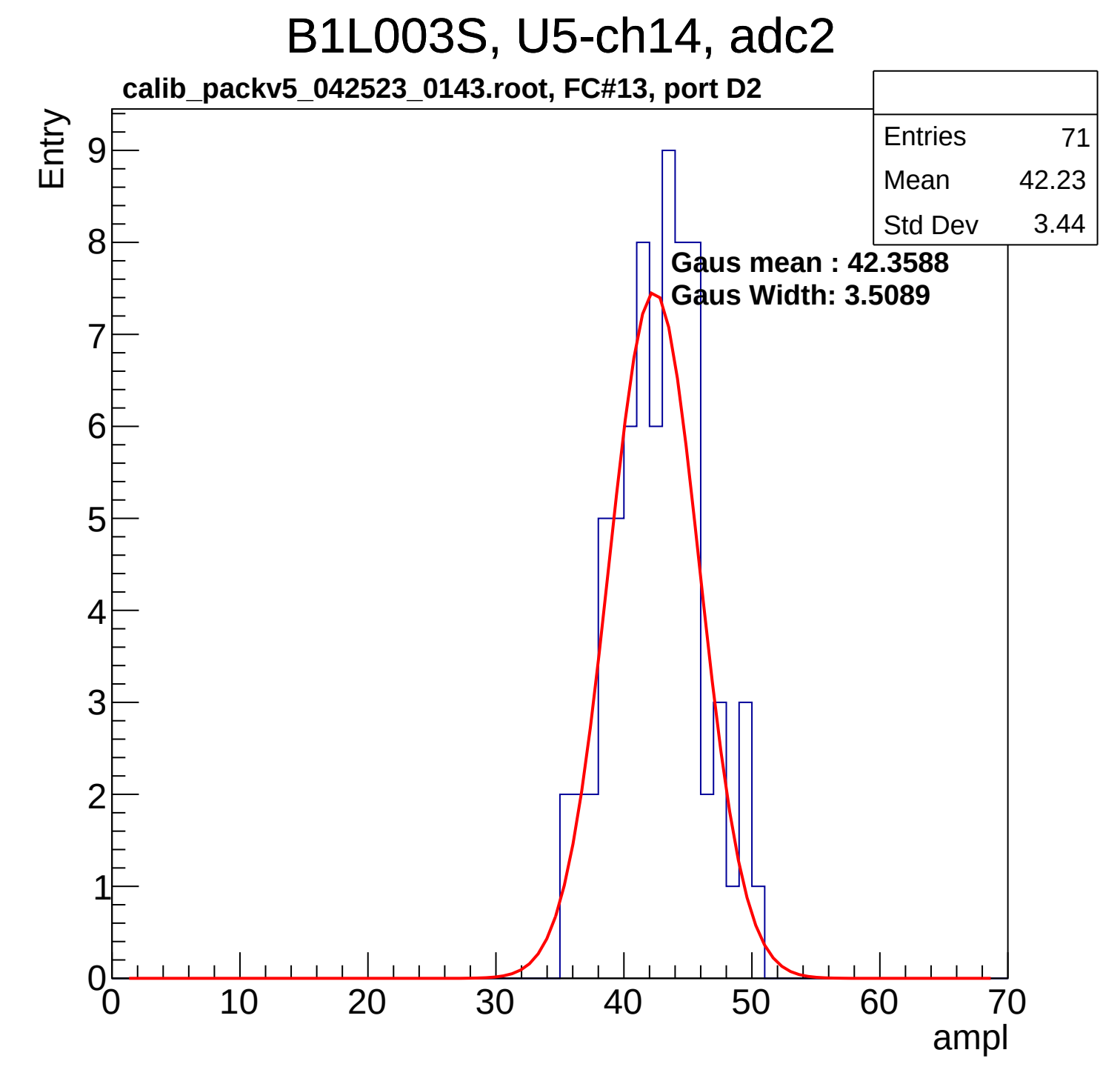
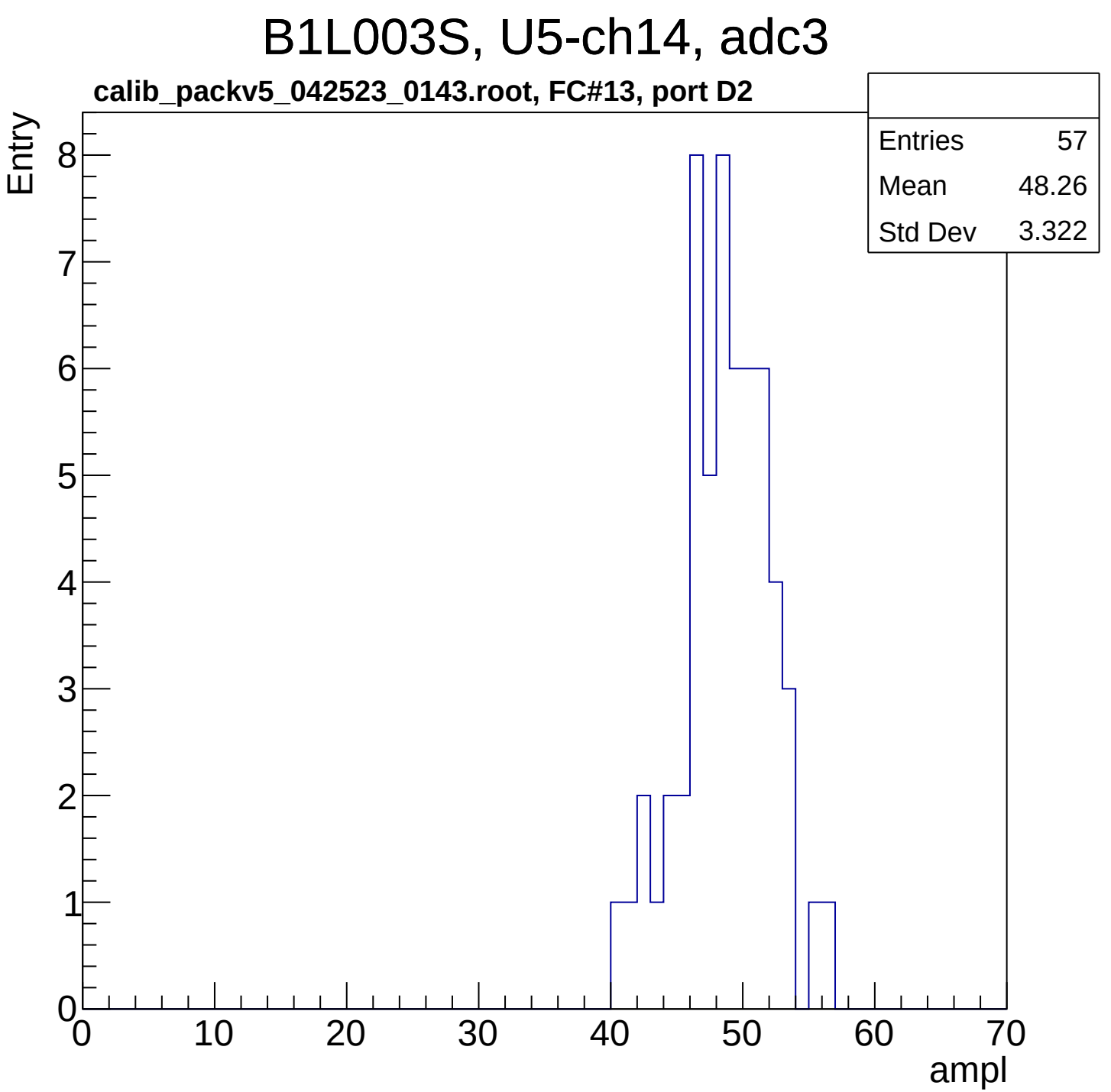

B1L003S, U5-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.19
Std Dev 3.127
Gaus mean : 35.4025
Gaus Width: 3.3926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch14, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.23
Std Dev 3.44
Gaus mean : 42.3588
Gaus Width: 3.5089
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch14, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.26
Std Dev 3.322
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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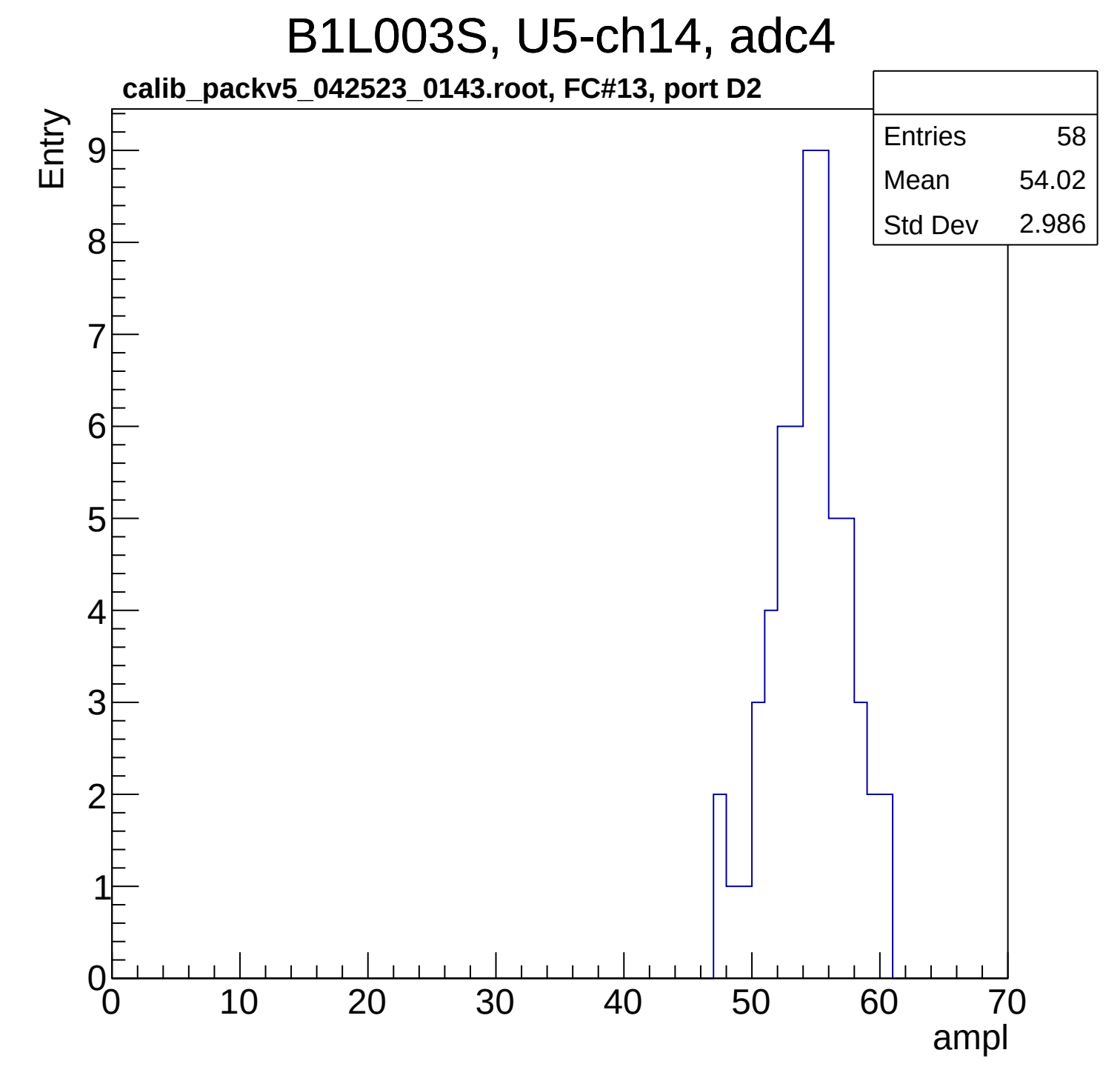
60
70


B1L003S, U5-ch14, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.02
Std Dev 2.986
Entry
ampl
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2
3
4
5
6
7
8
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0
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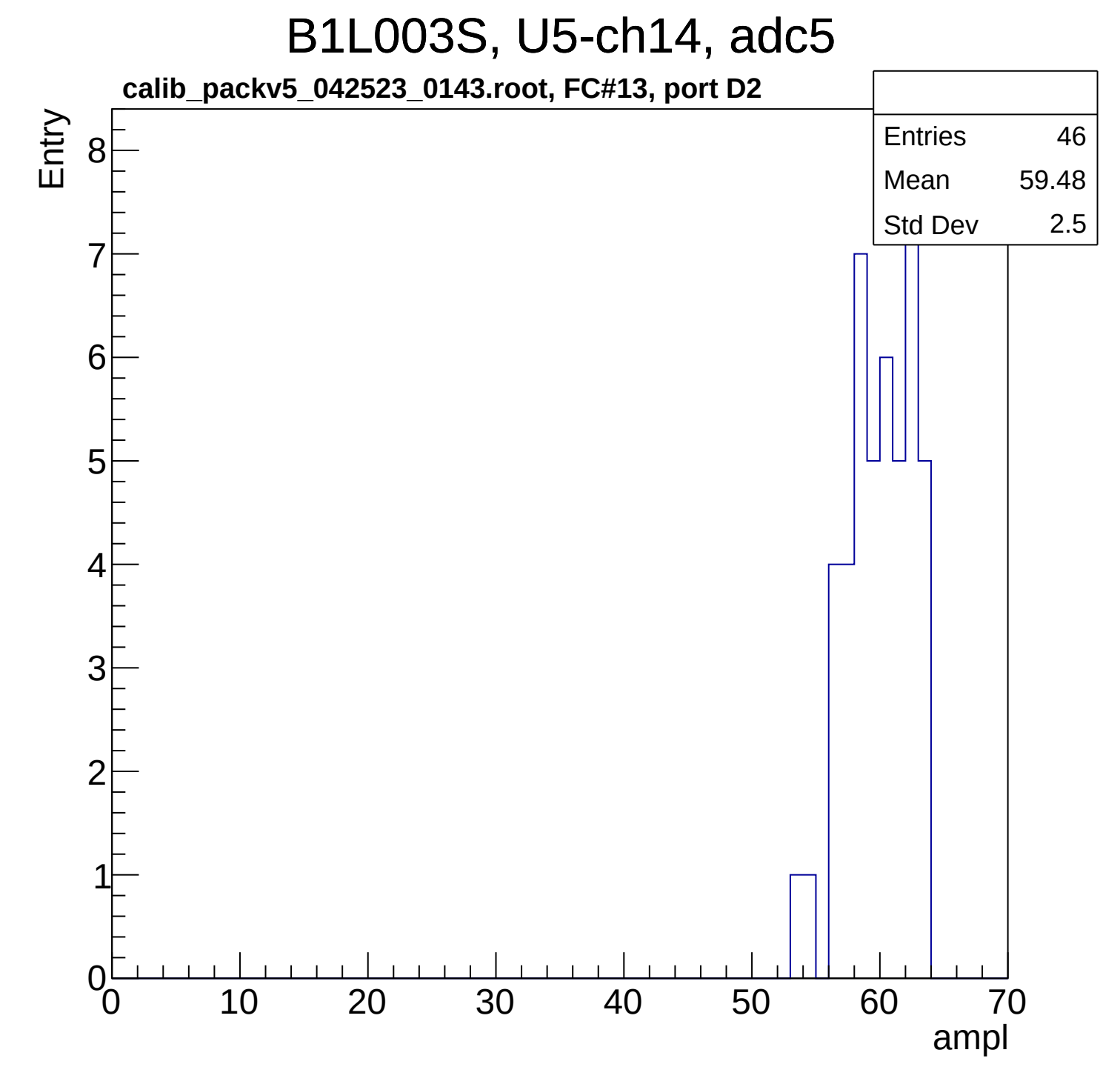
B1L003S, U5-ch14, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.48
Std Dev 2.5
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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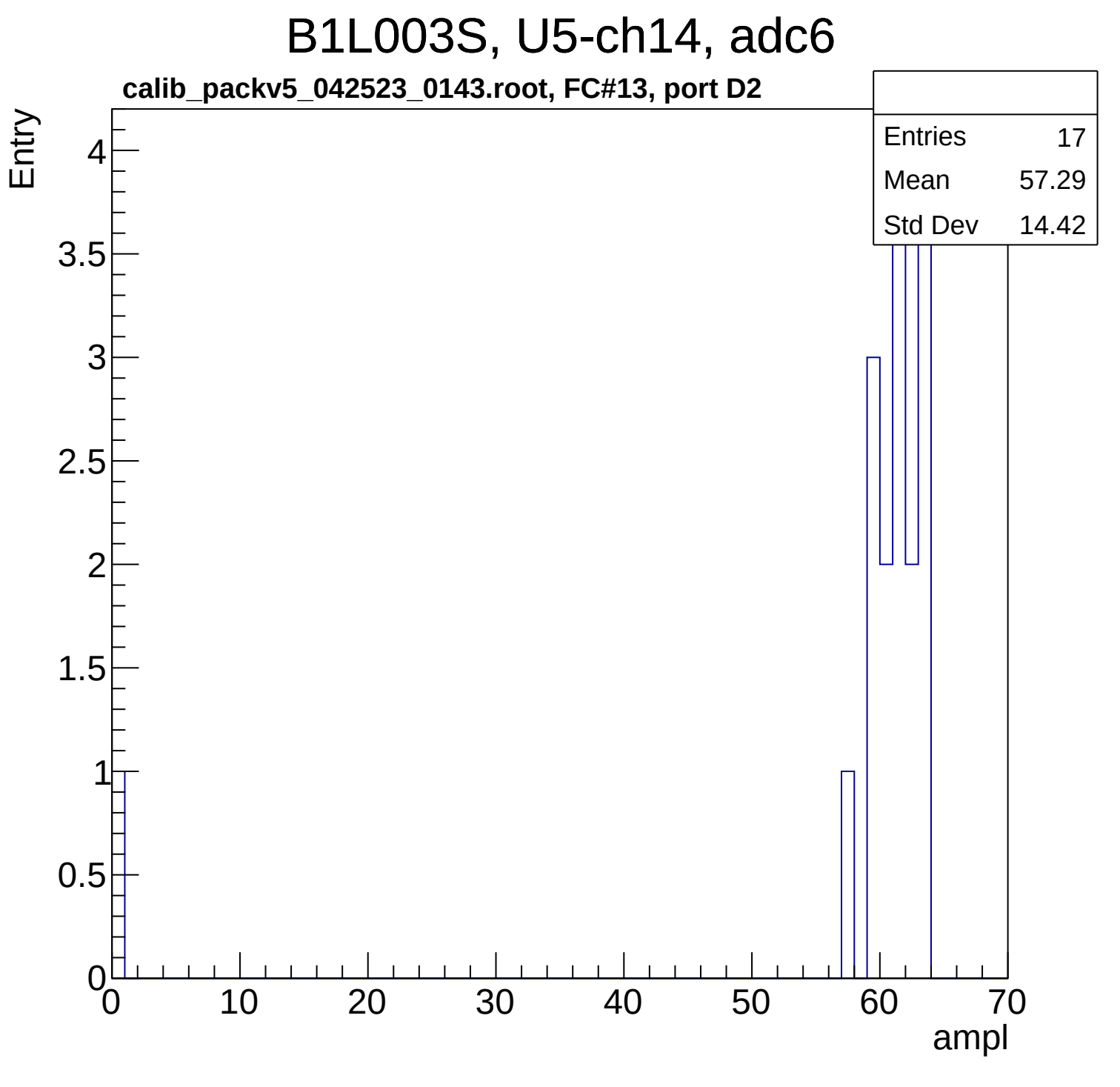
B1L003S, U5-ch14, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.29
Std Dev 14.42
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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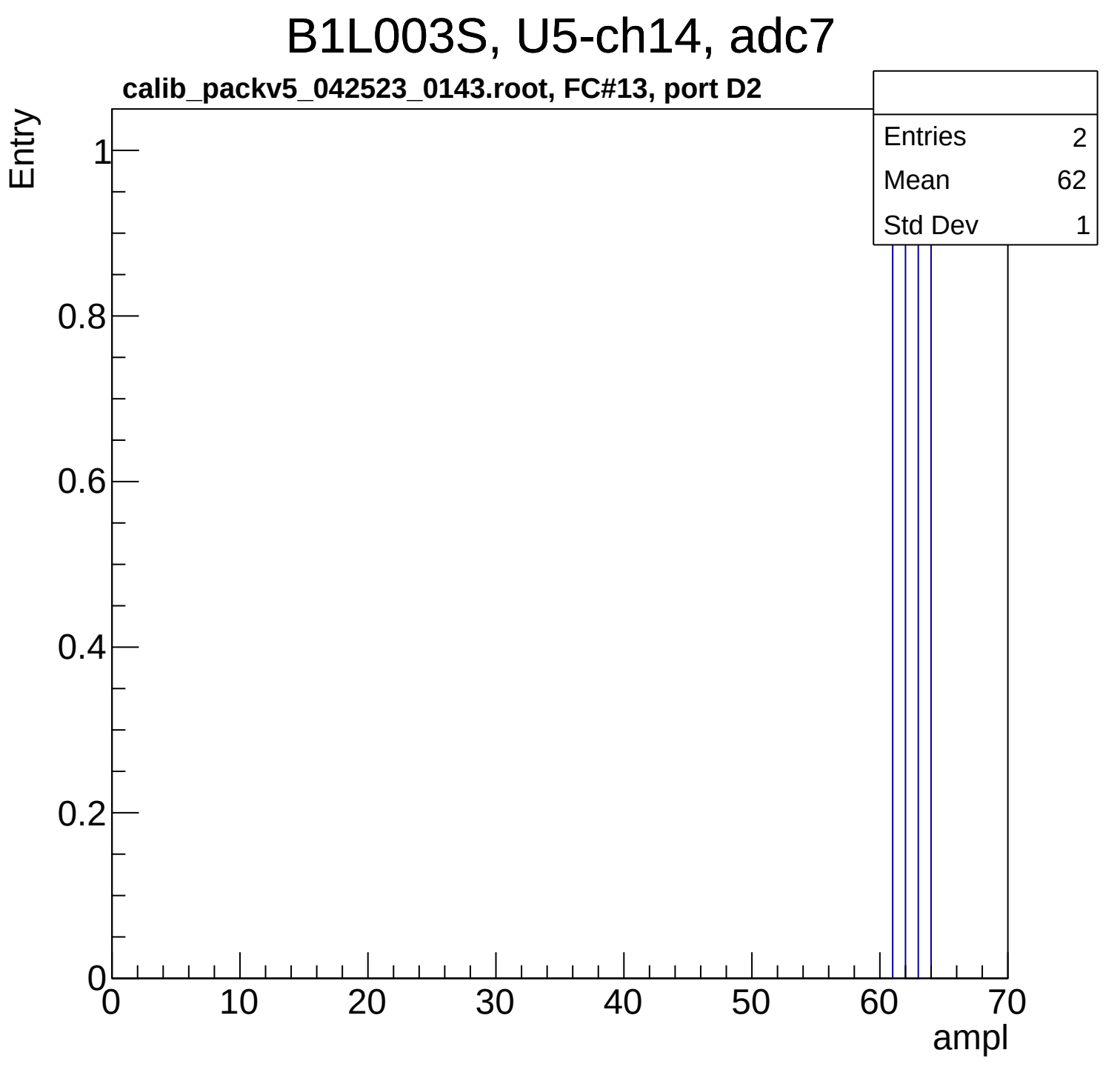
B1L003S, U5-ch14, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62
Std Dev 1
Entry
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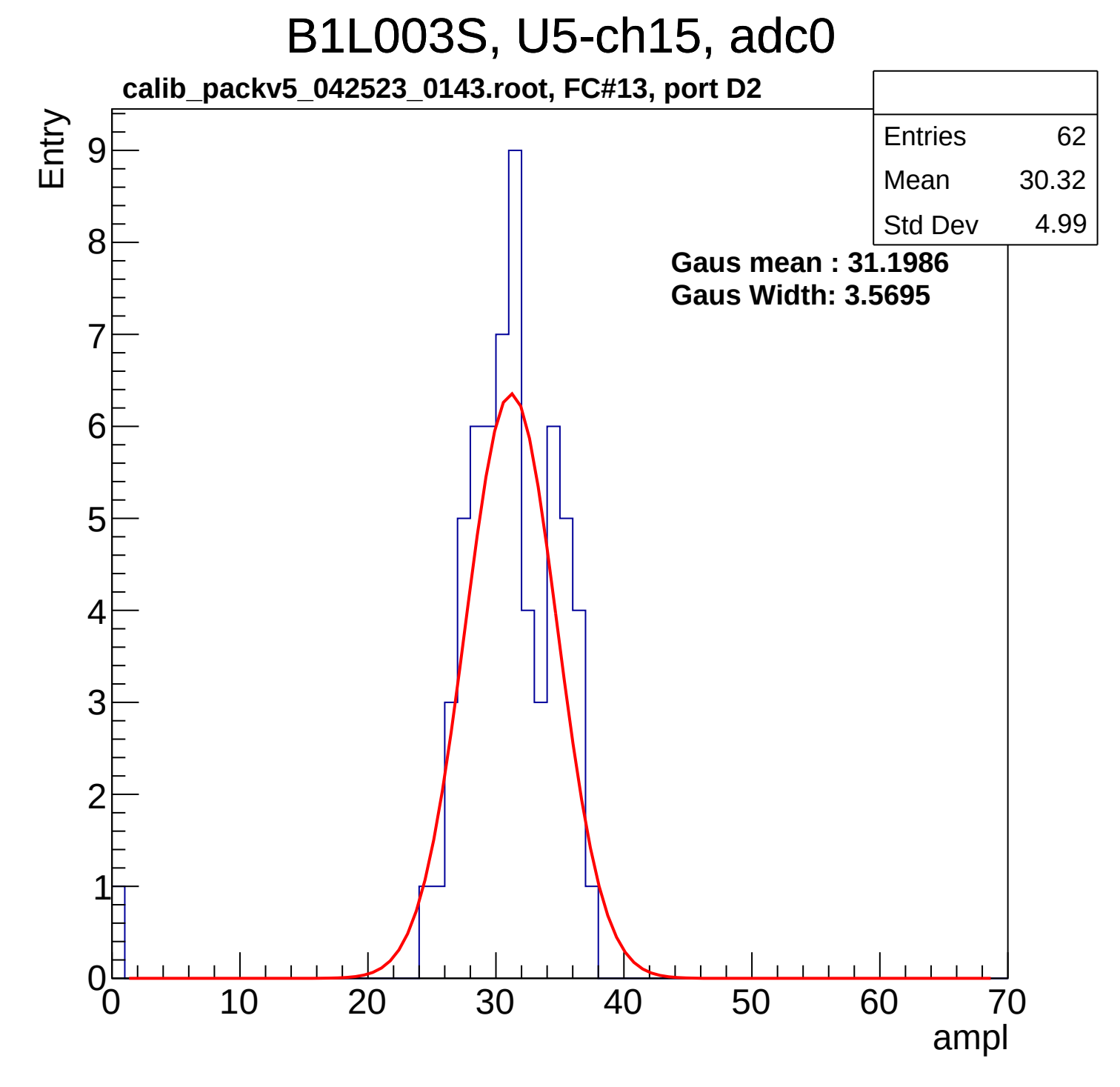
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch15, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 30.32
Std Dev 4.99
Gaus mean : 31.1986
Gaus Width: 3.5695
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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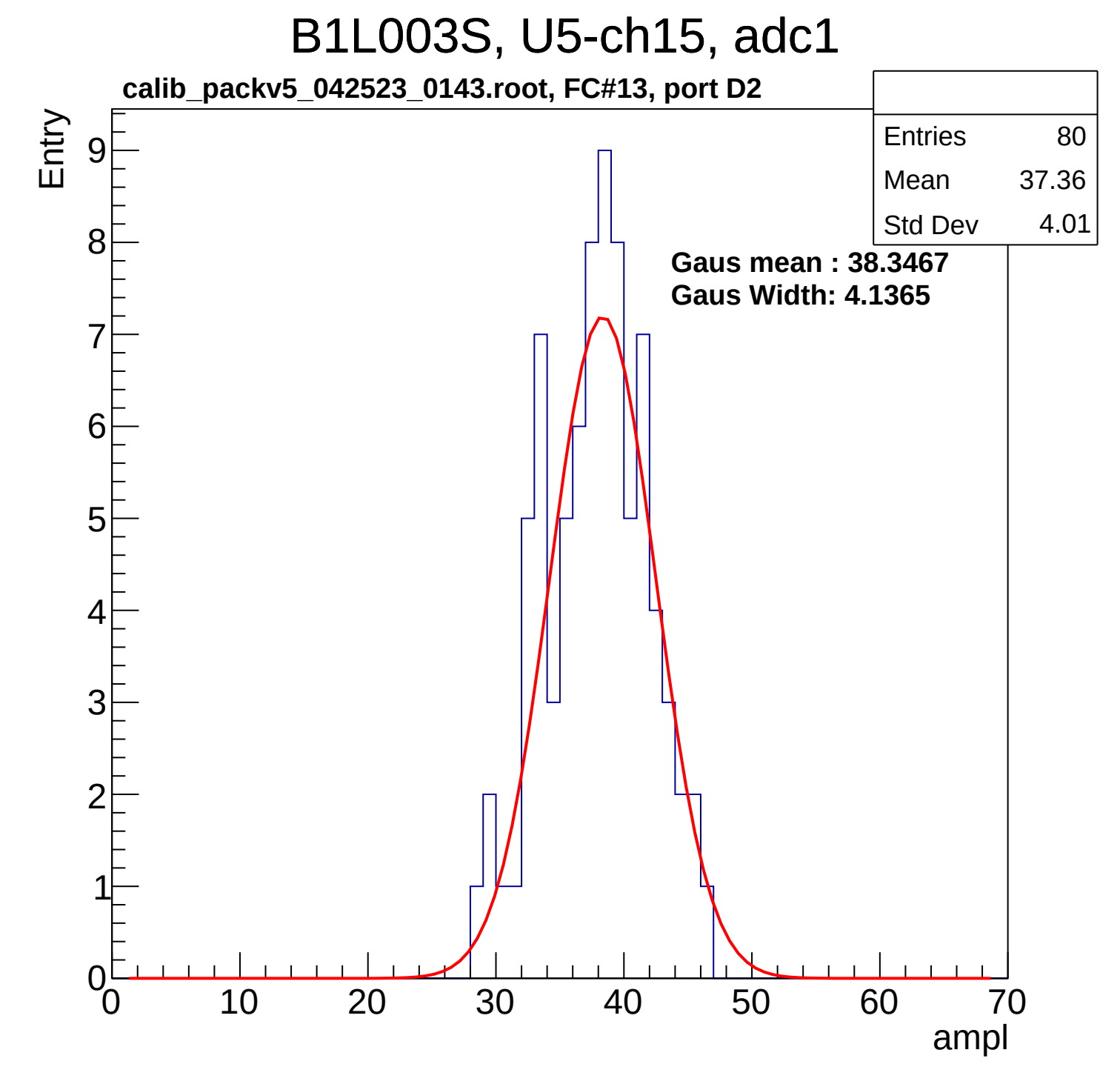
B1L003S, U5-ch15, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 37.36
Std Dev 4.01
Gaus mean : 38.3467
Gaus Width: 4.1365
Entry
ampl
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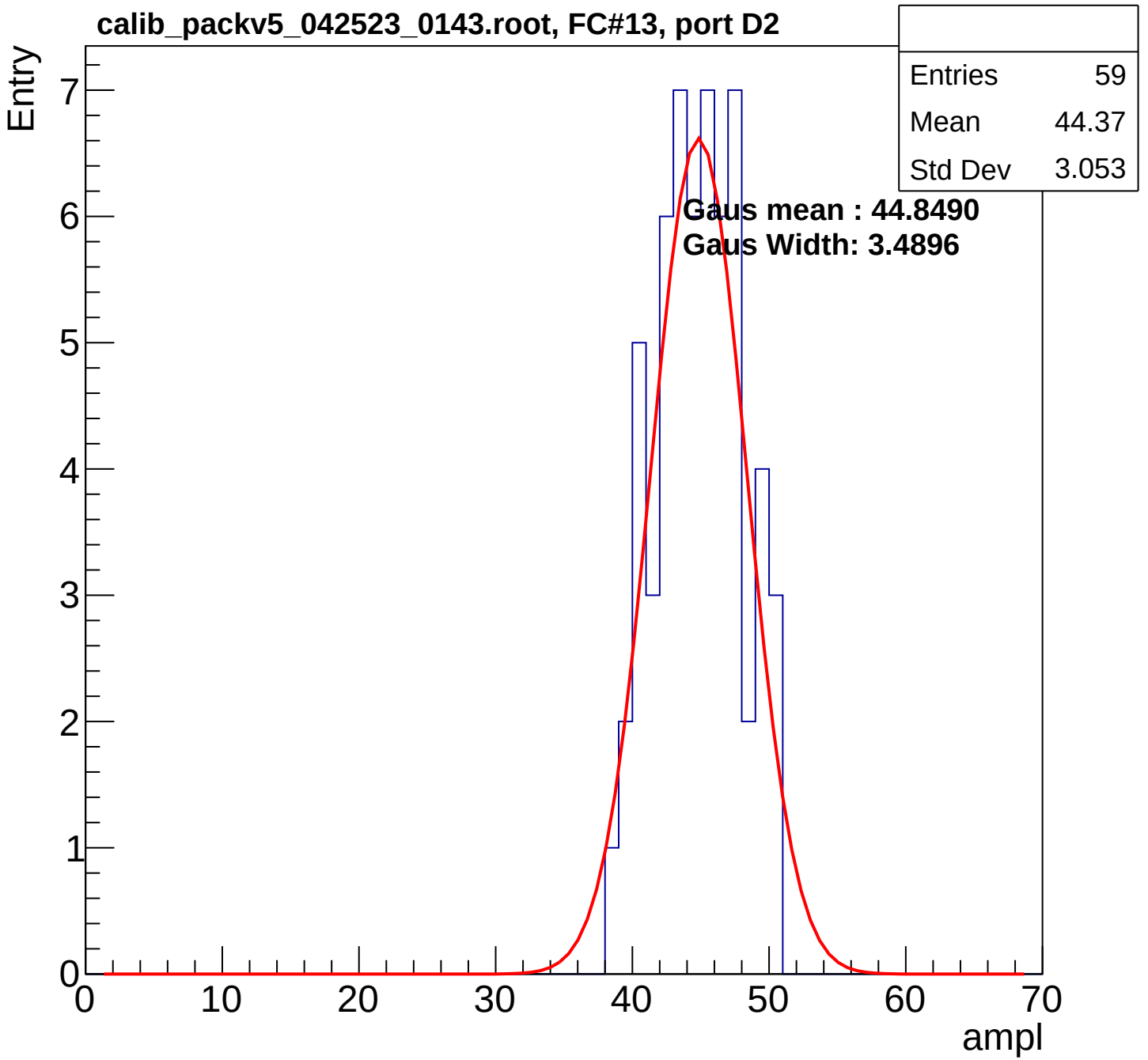
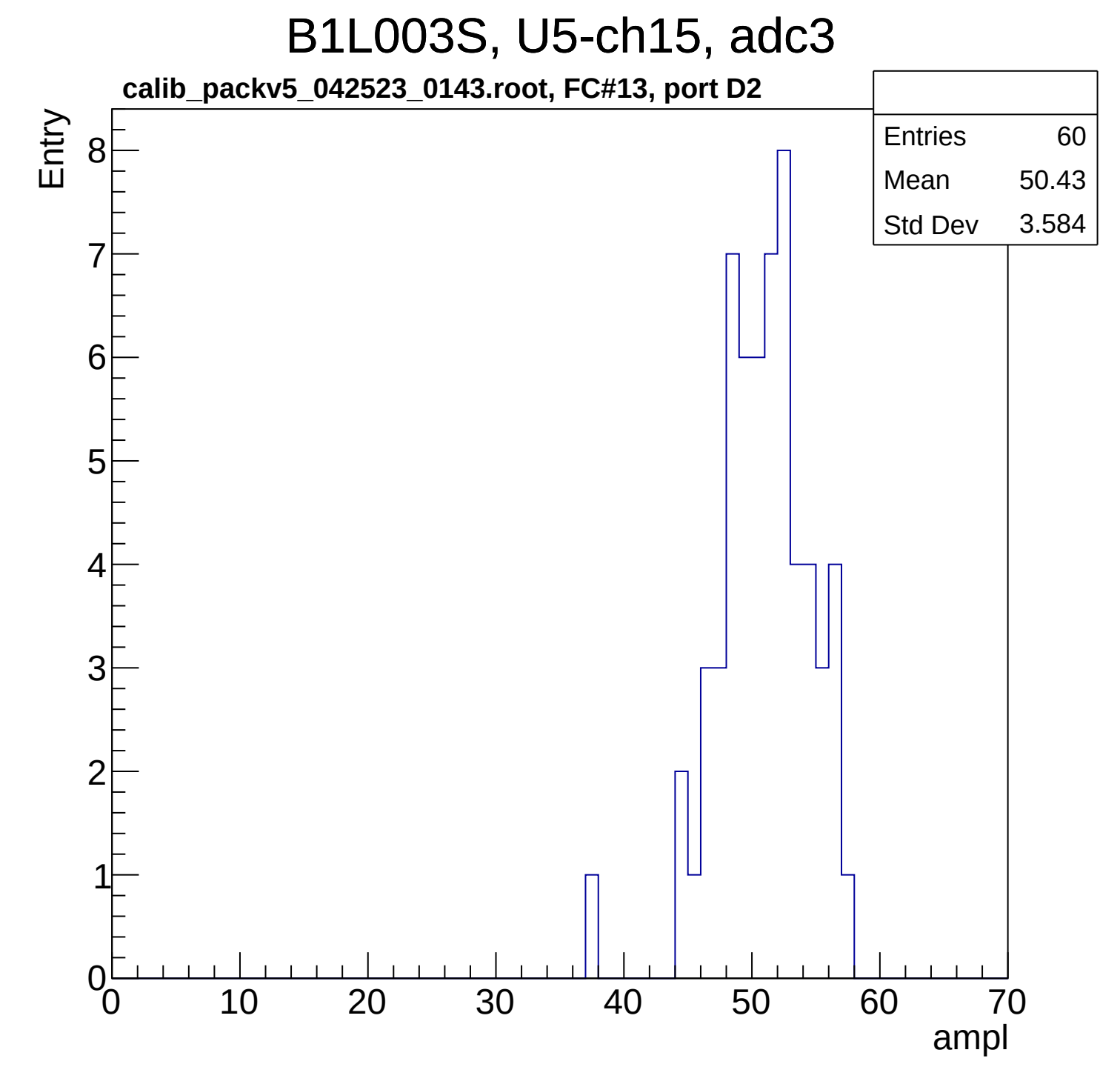
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch15, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 44.37
Std Dev 3.053
Gaus mean : 44.8490
Gaus Width: 3.4896
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 50.43
Std Dev 3.584
Entry
ampl
0
1
2
3
4
5
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7
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0
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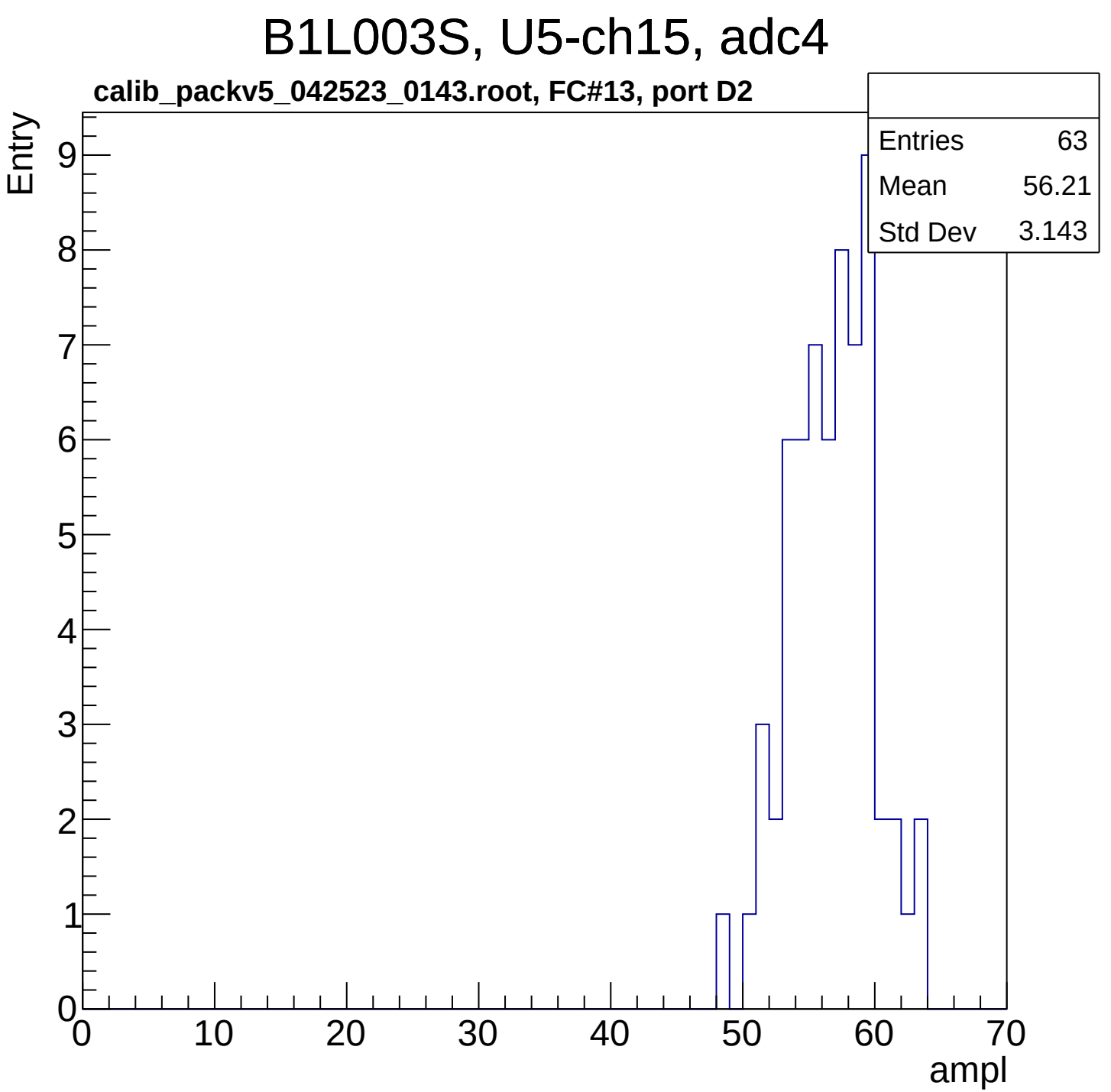
B1L003S, U5-ch15, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 56.21
Std Dev 3.143
Entry
ampl
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2
3
4
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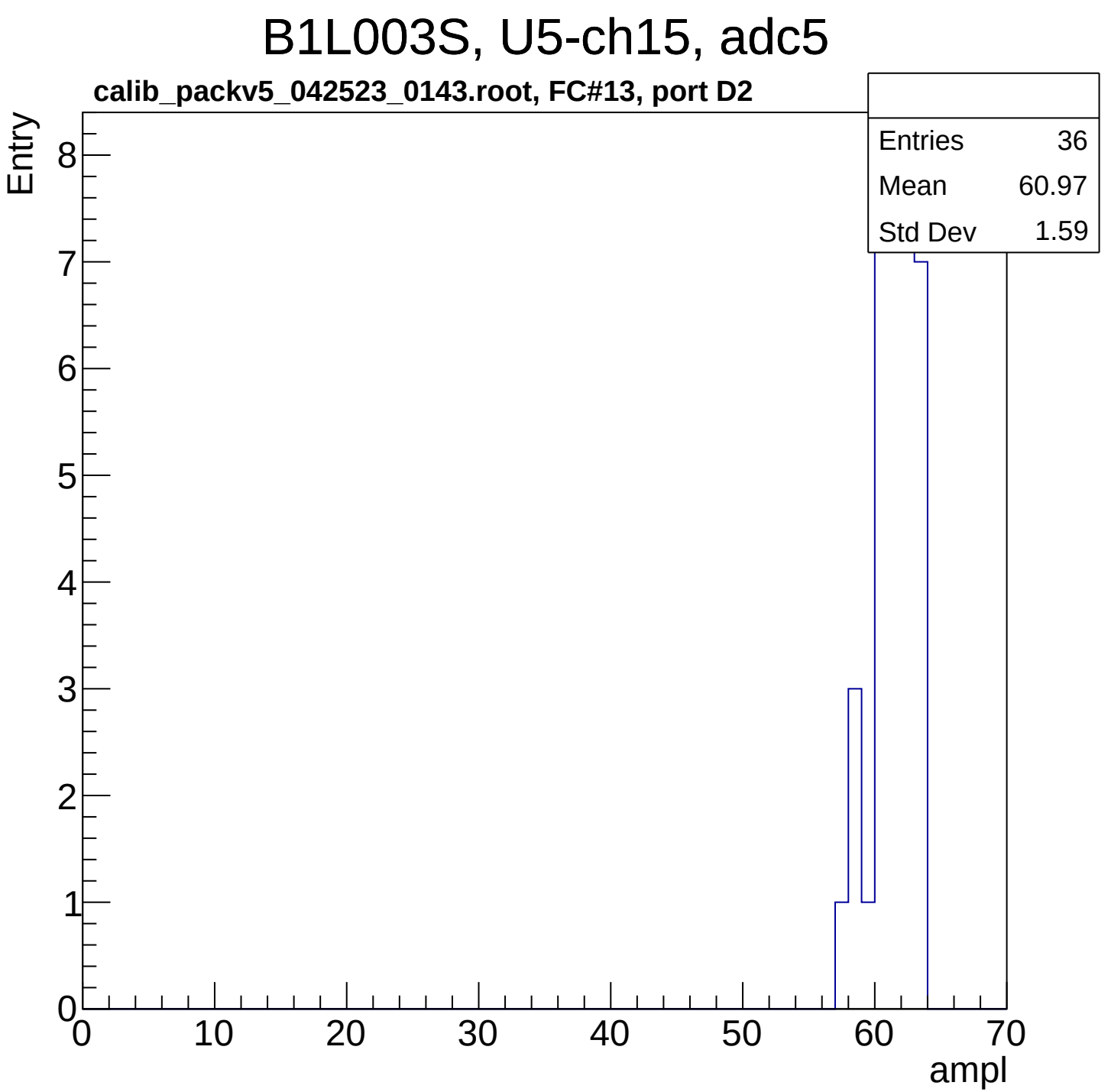
B1L003S, U5-ch15, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 60.97
Std Dev 1.59
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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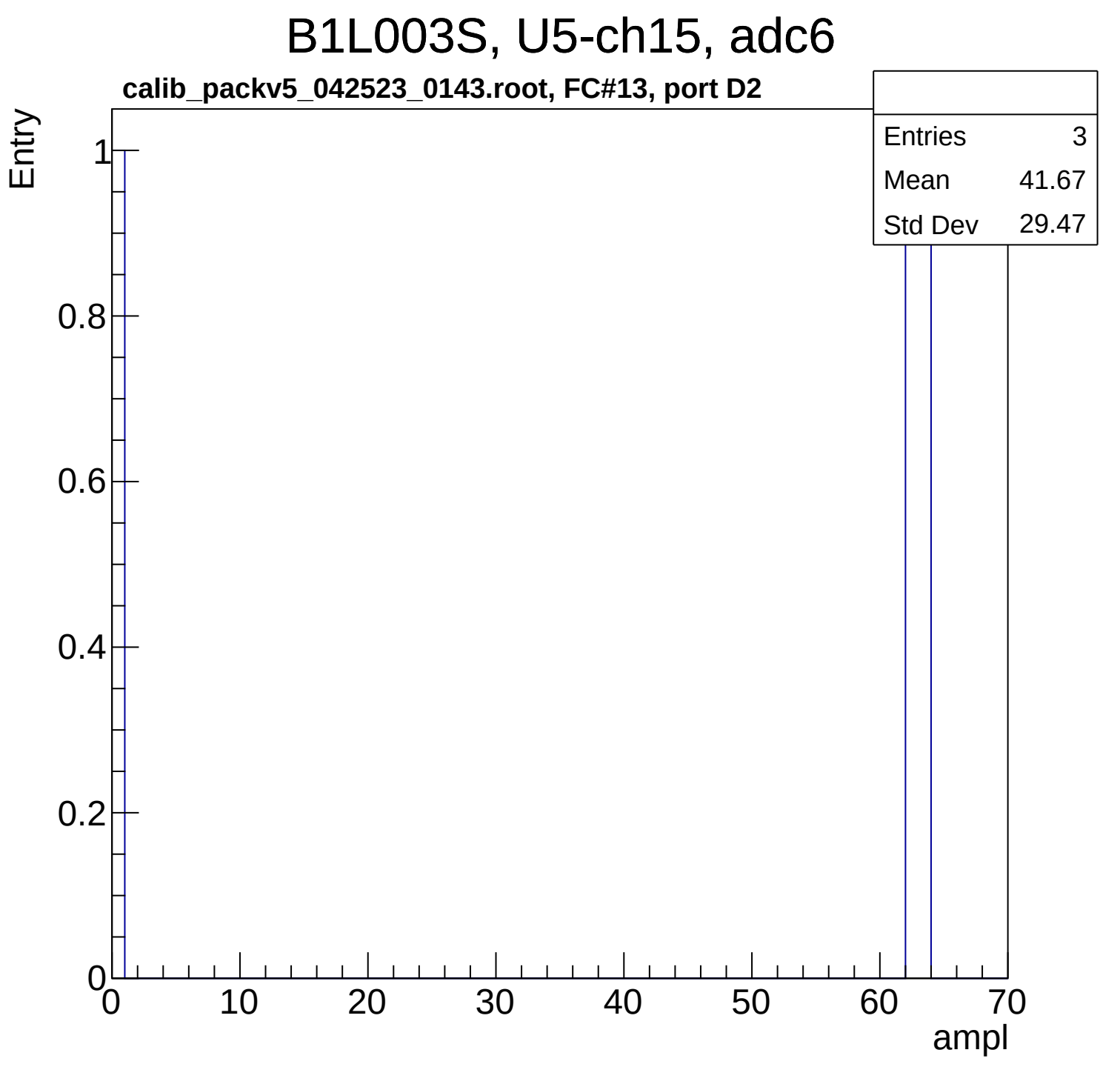
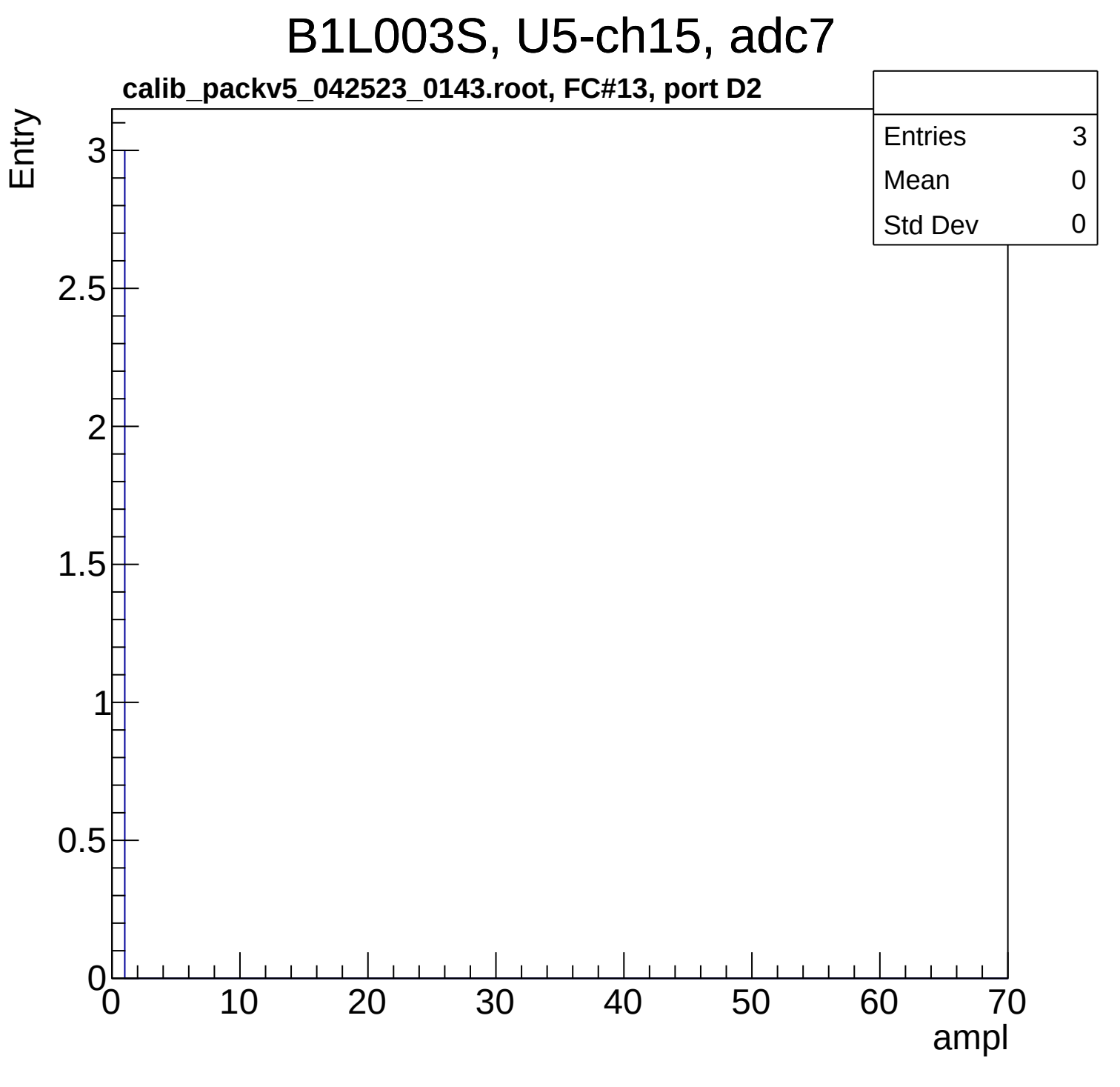
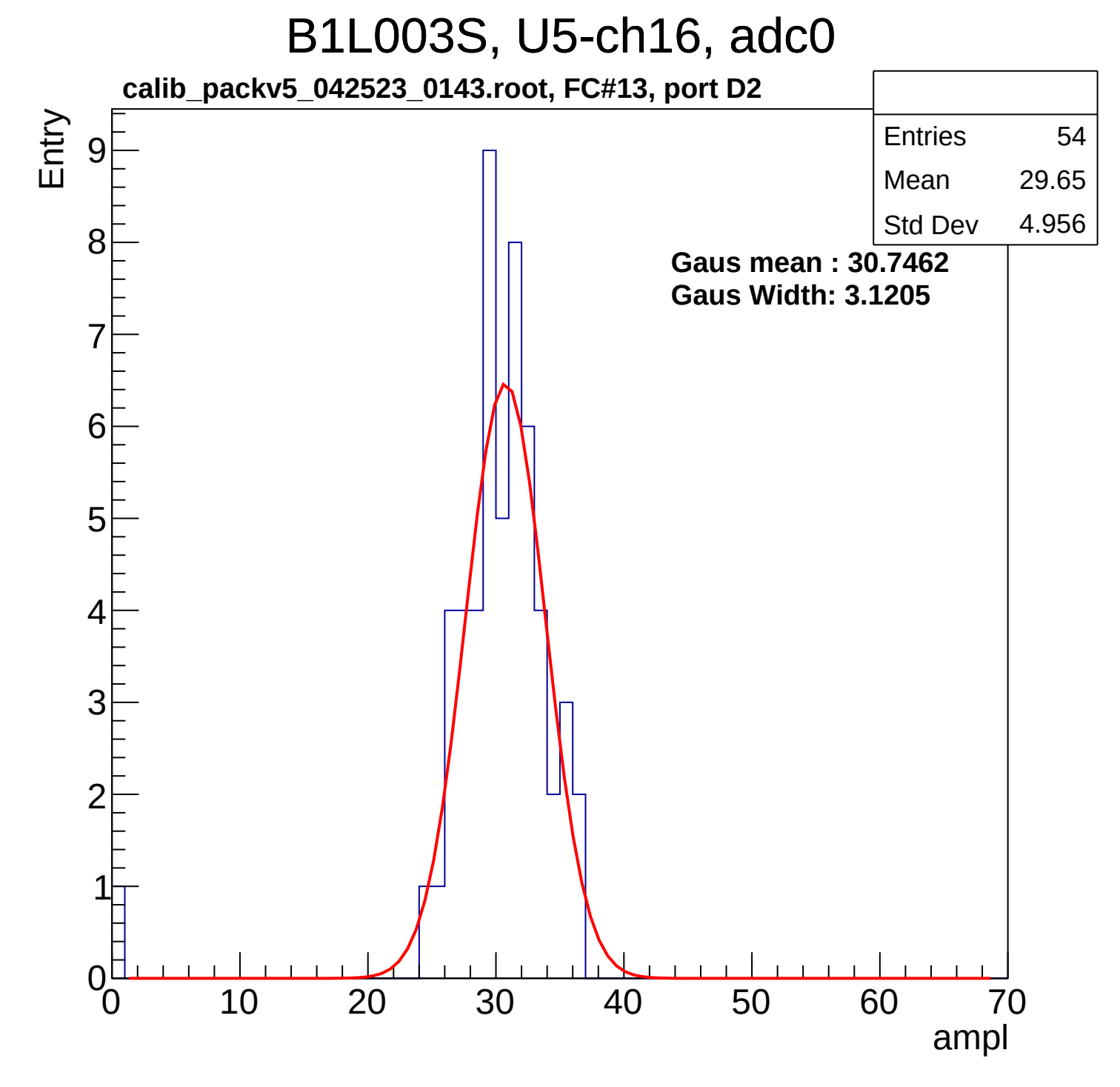

B1L003S, U5-ch15, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch15, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch16, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.65
Std Dev 4.956
Gaus mean : 30.7462
Gaus Width: 3.1205
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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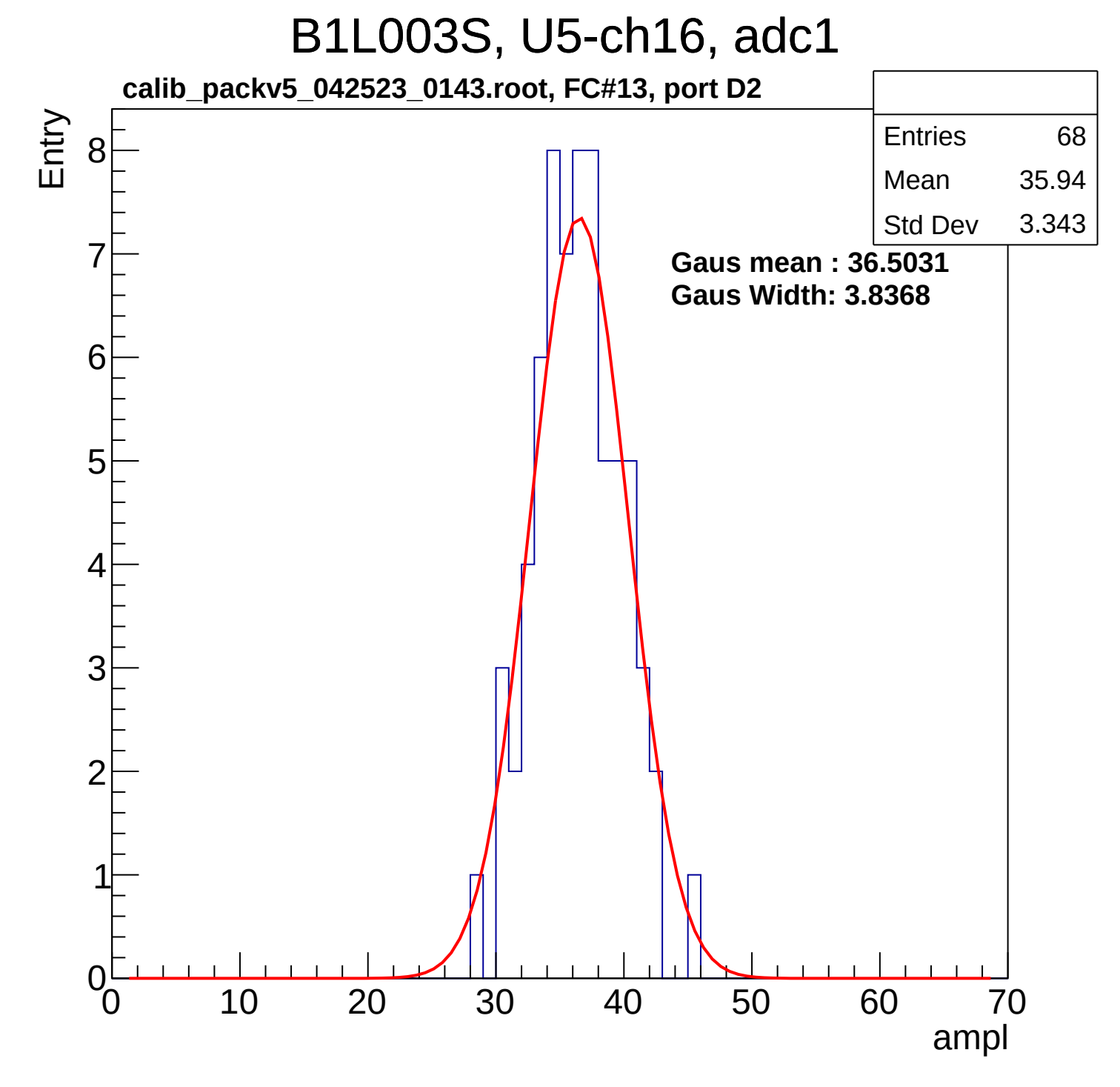
B1L003S, U5-ch16, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.94
Std Dev 3.343
Gaus mean : 36.5031
Gaus Width: 3.8368
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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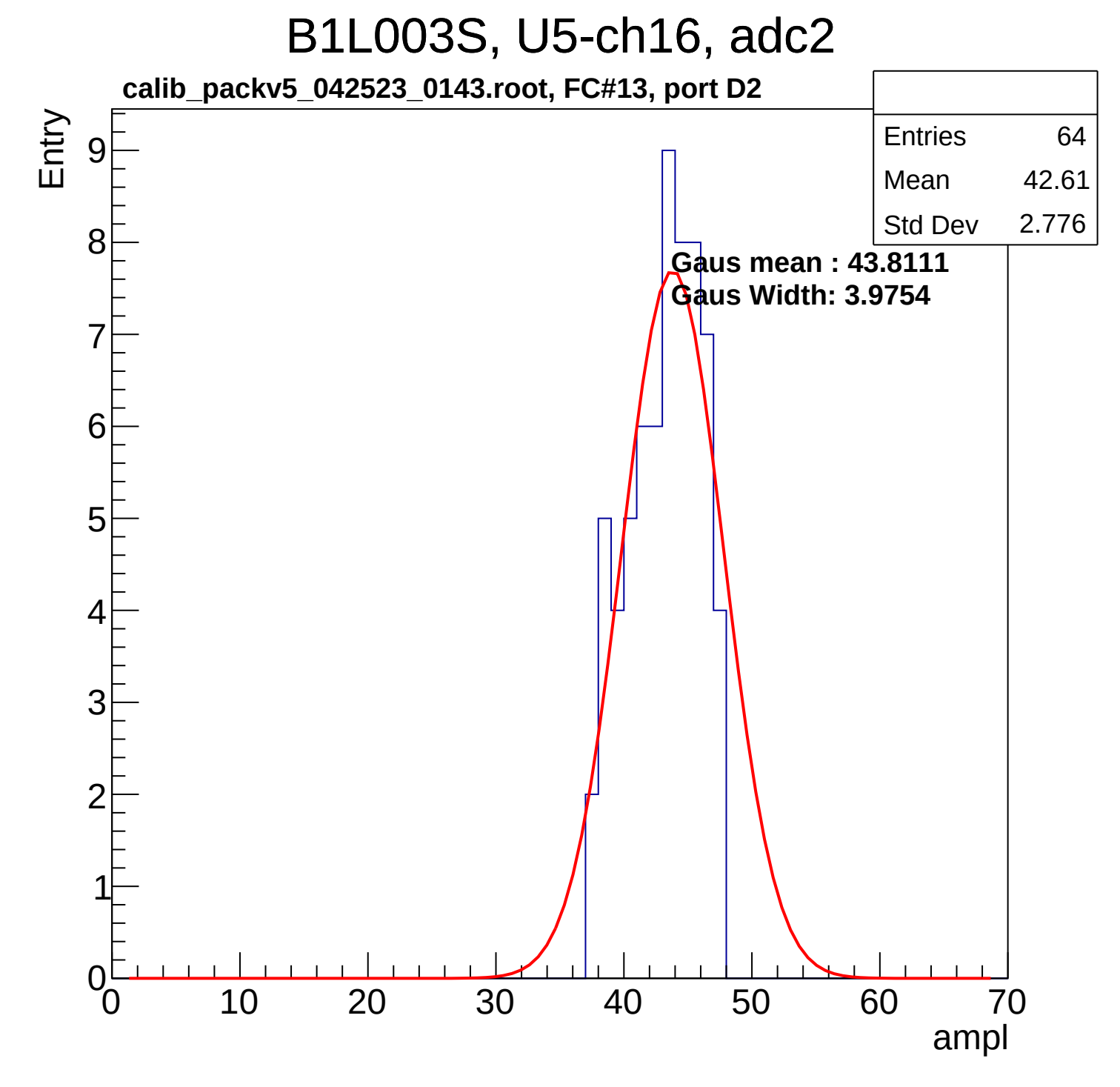
B1L003S, U5-ch16, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.61
Std Dev 2.776
Gaus mean : 43.8111
Gaus Width: 3.9754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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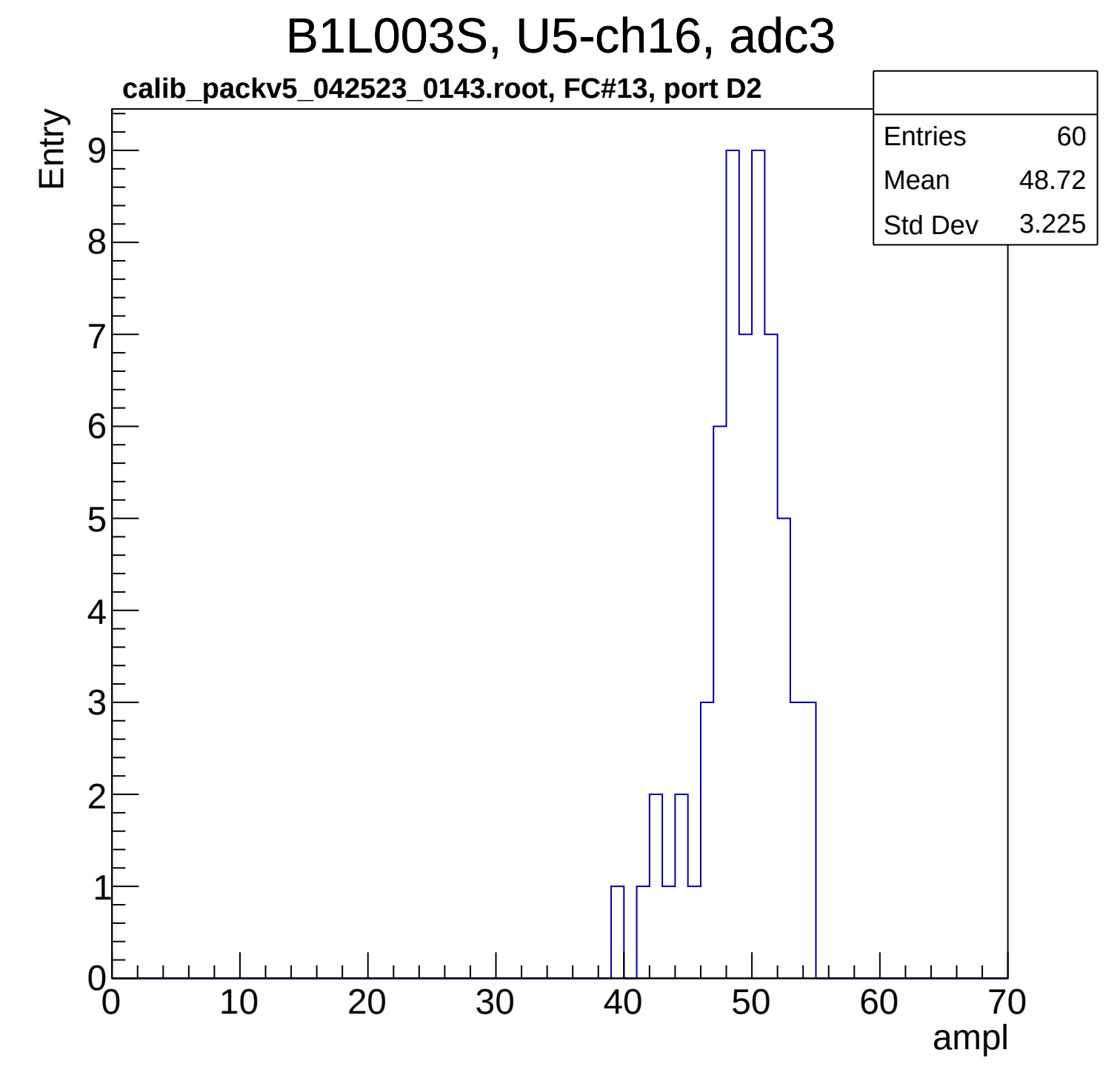
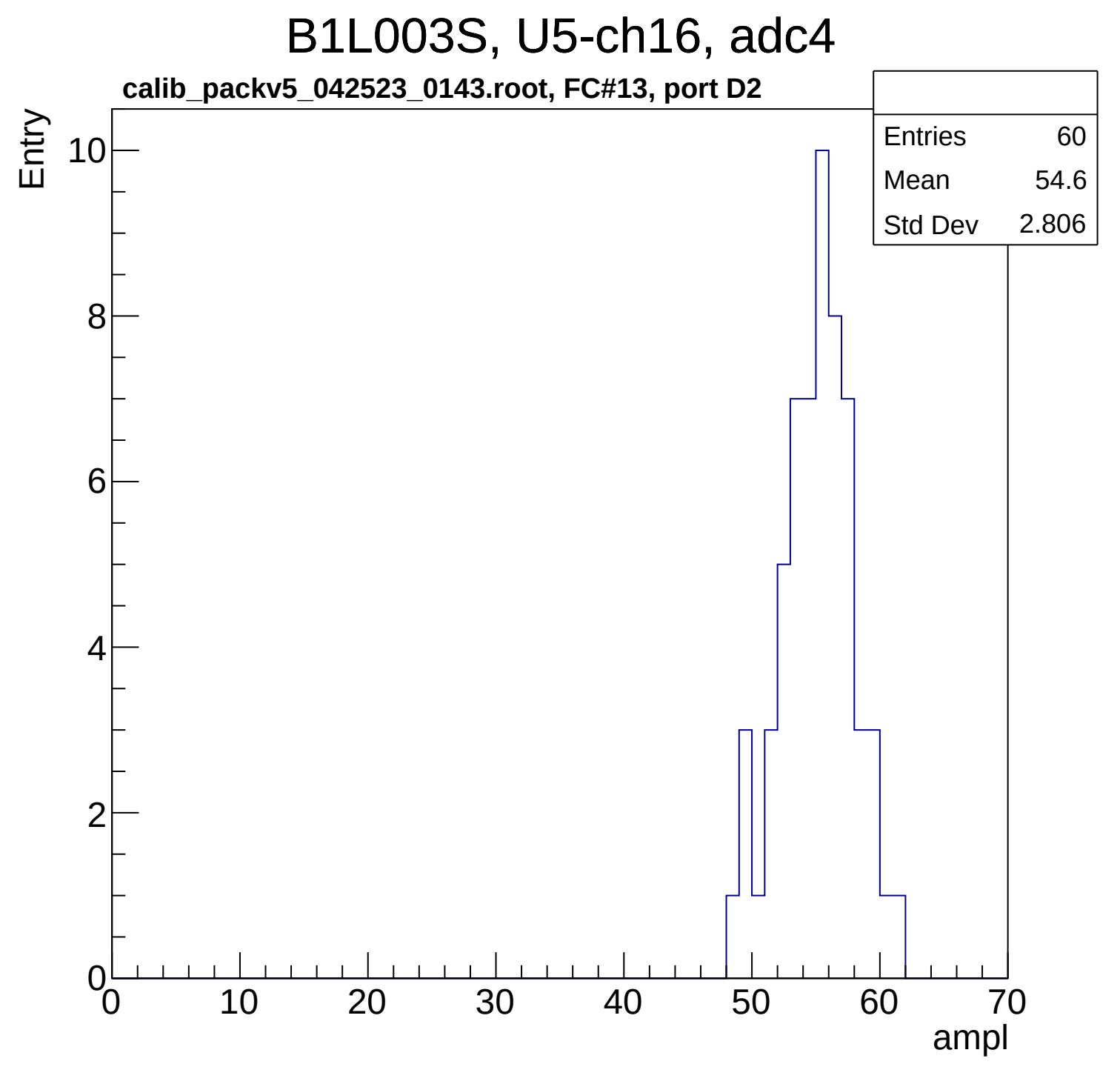
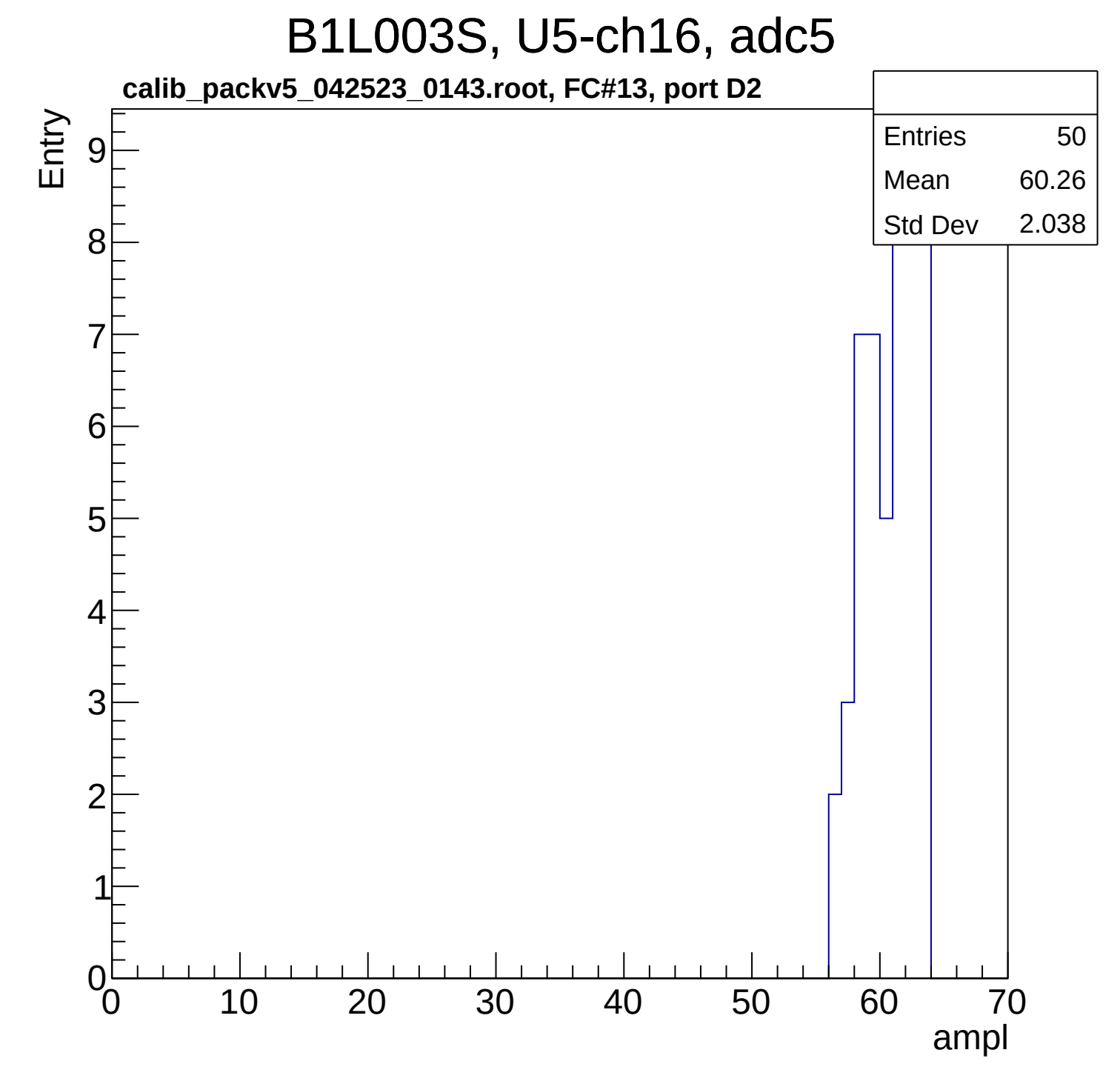
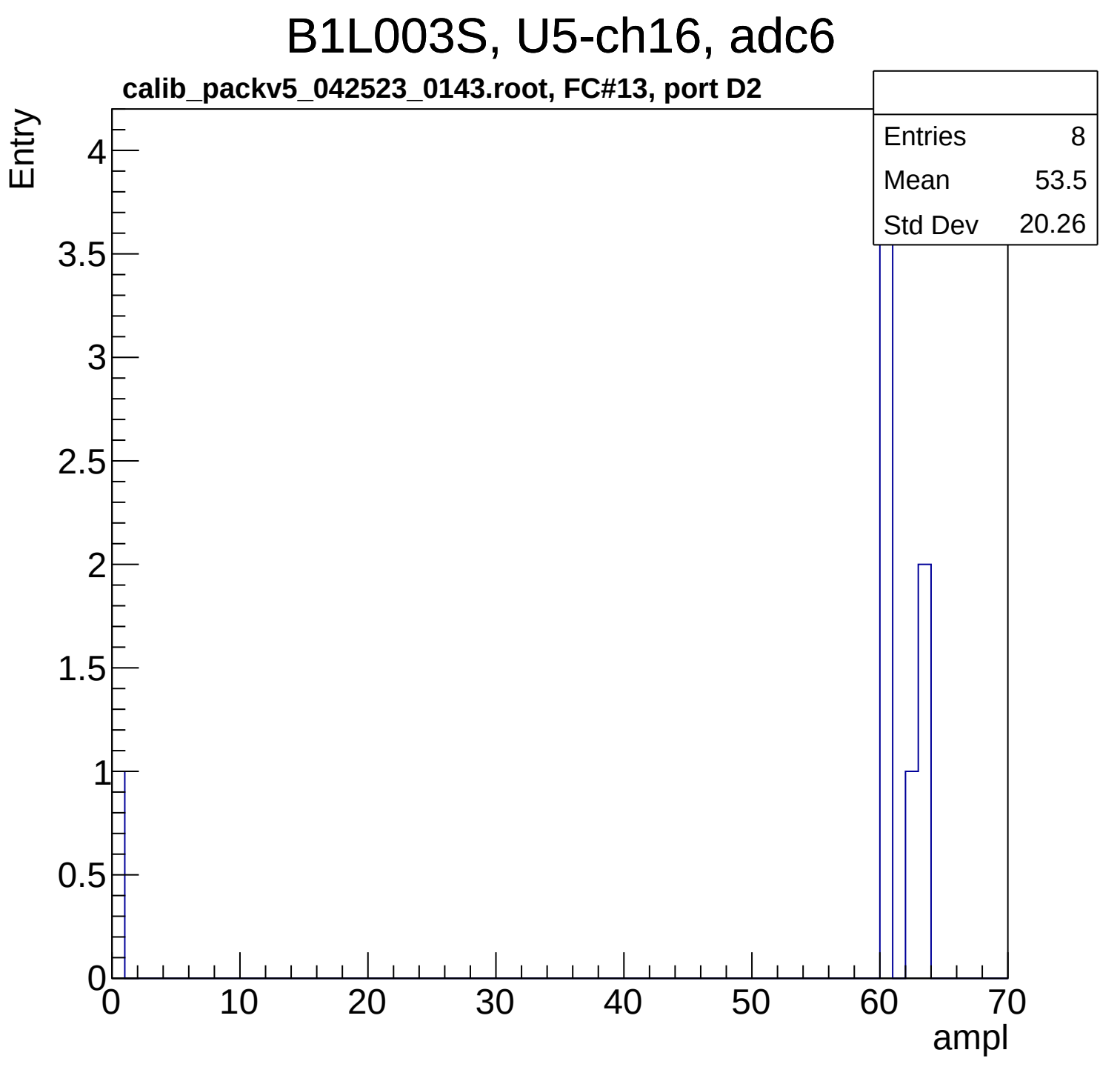
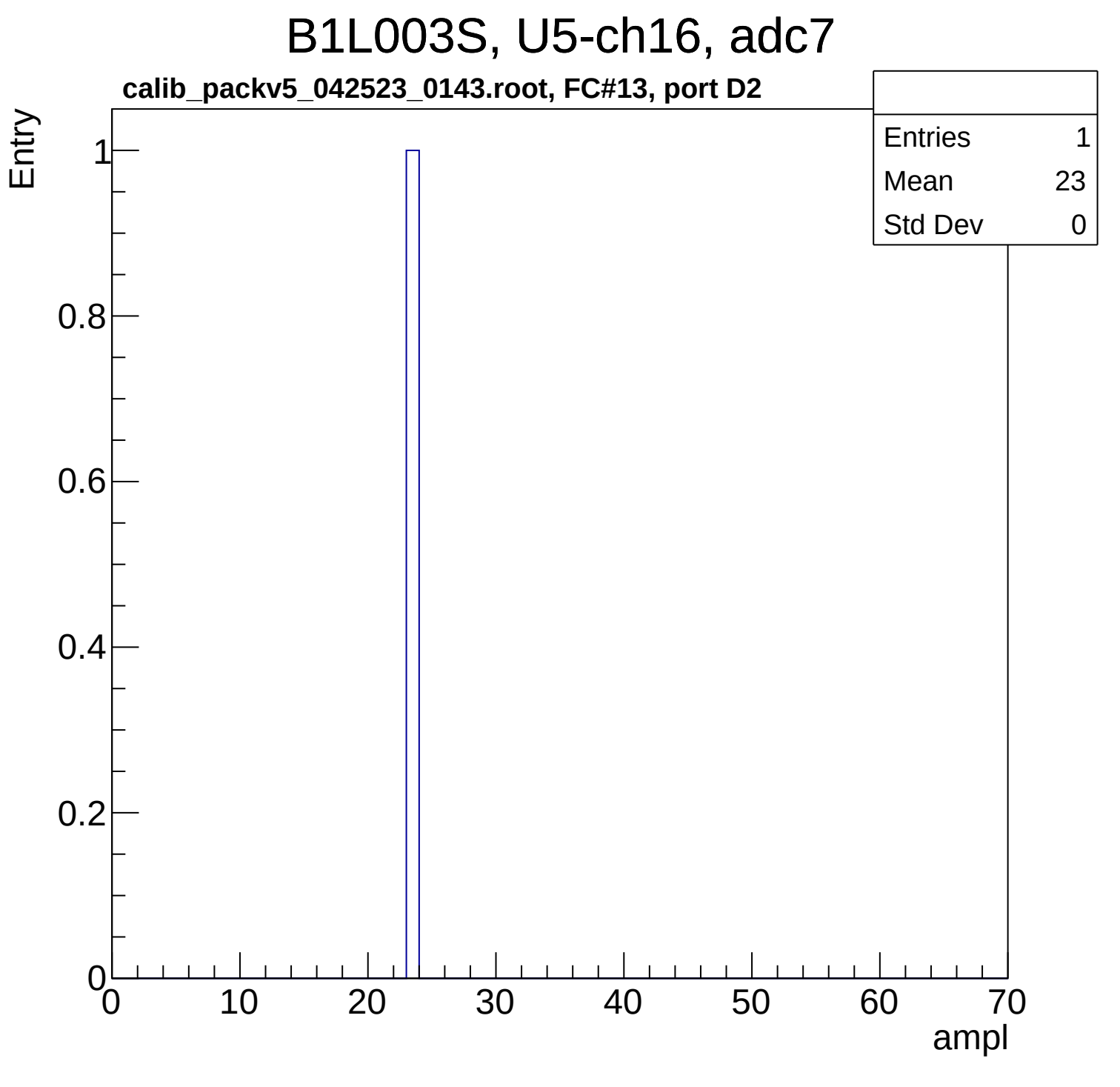

B1L003S, U5-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.72
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch16, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.6
Std Dev 2.806
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch16, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 60.26
Std Dev 2.038
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 53.5
Std Dev 20.26
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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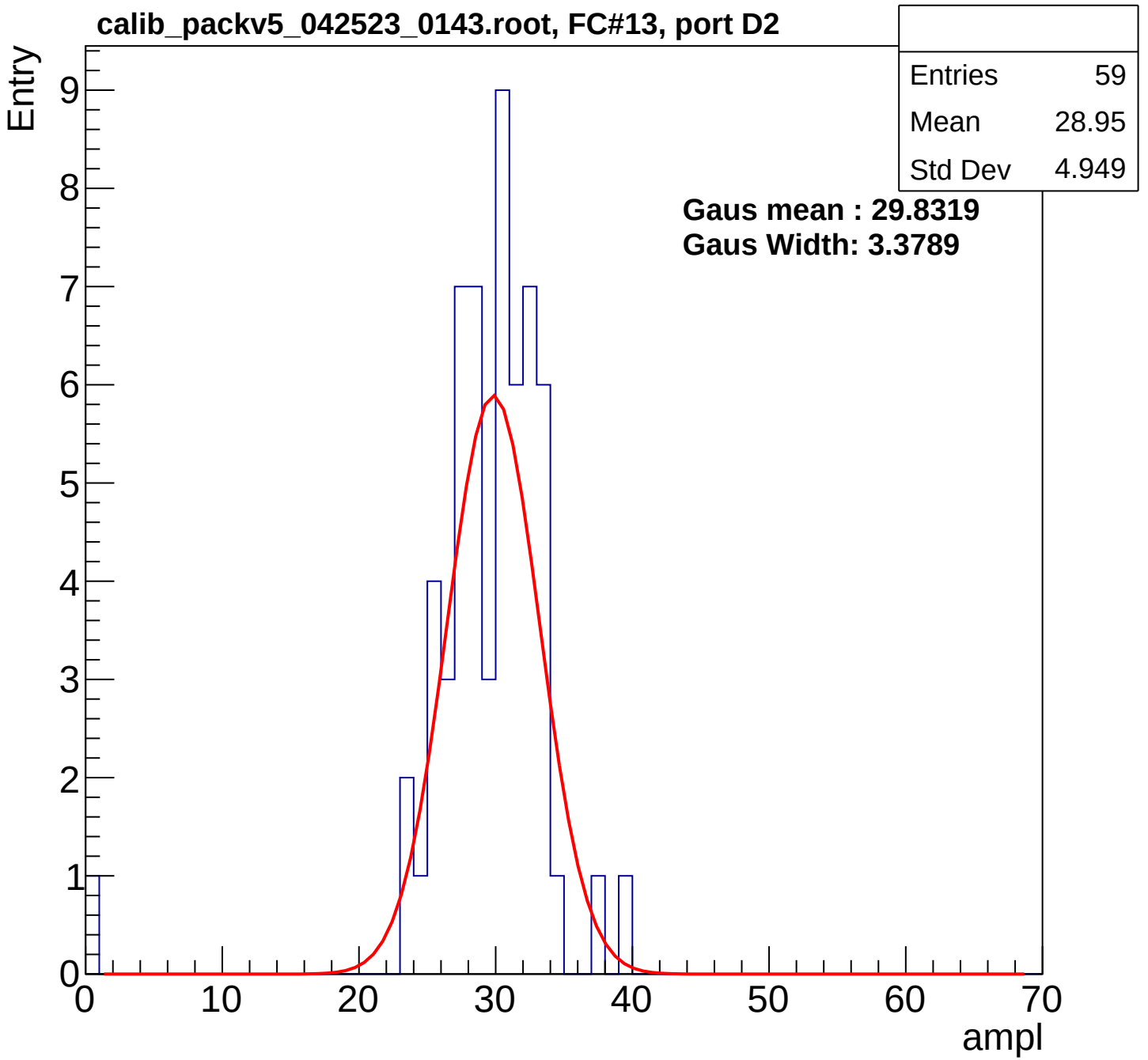
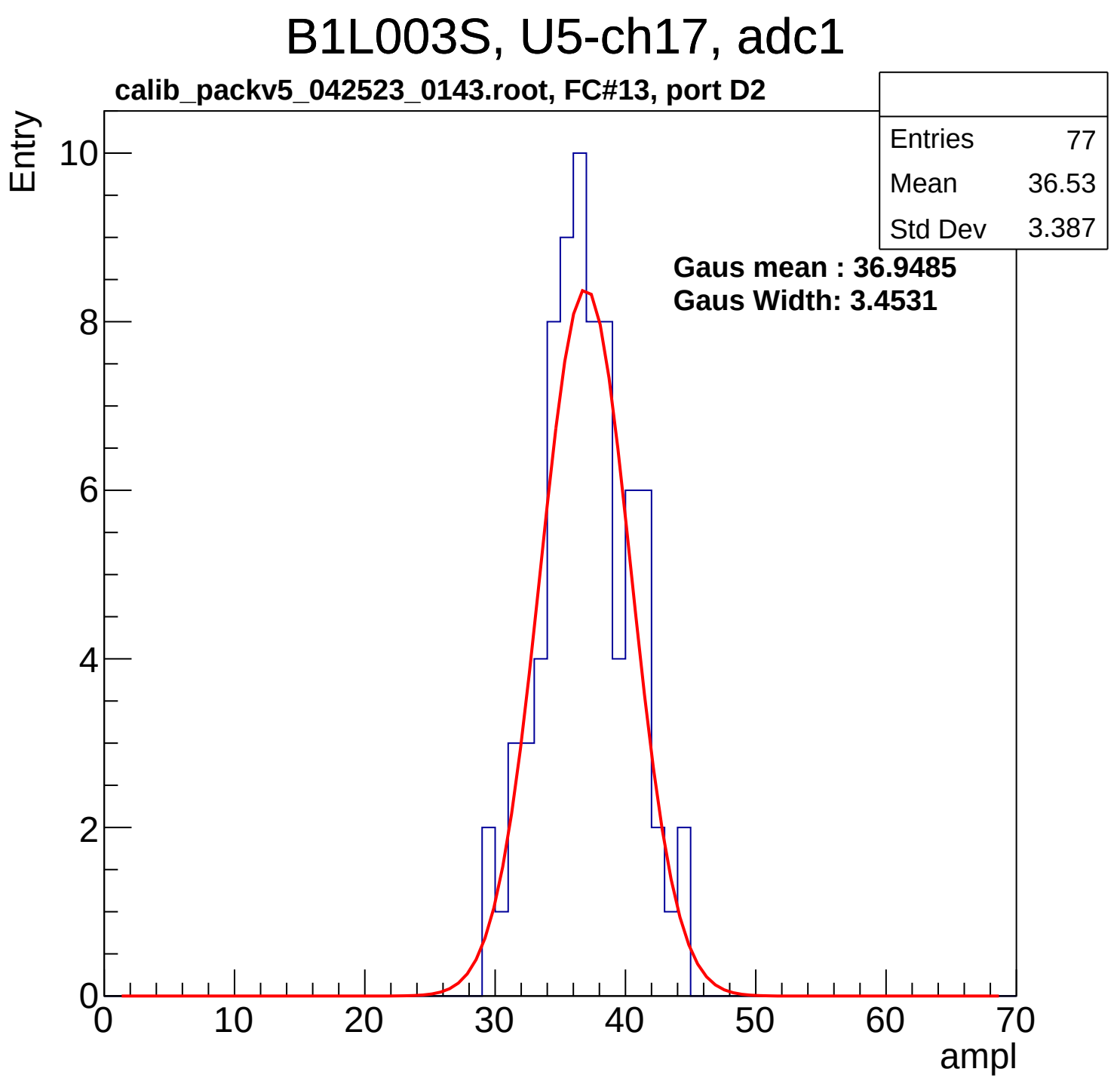
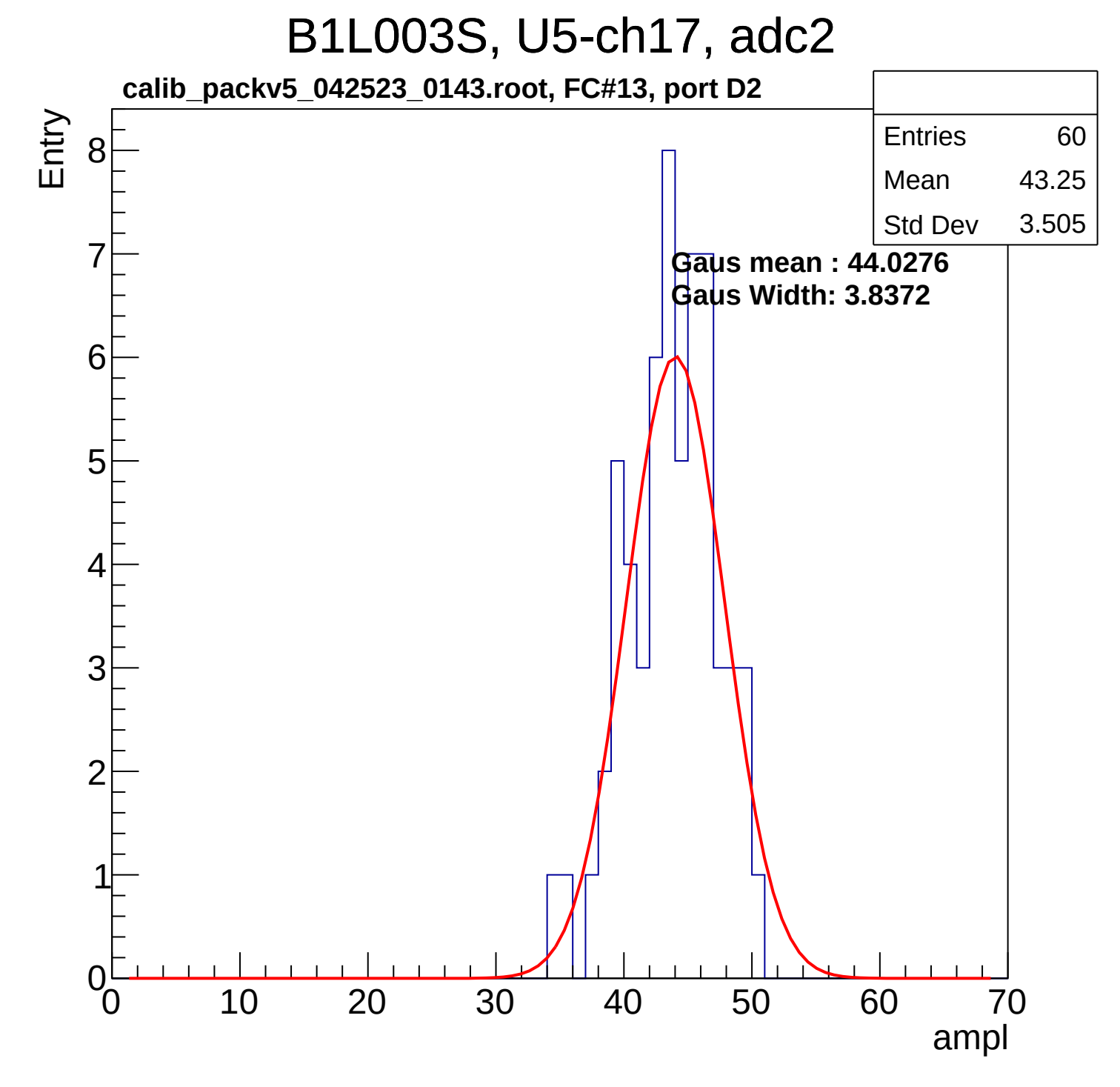
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch17, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.95
Std Dev 4.949
Gaus mean : 29.8319
Gaus Width: 3.3789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch17, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.53
Std Dev 3.387
Gaus mean : 36.9485
Gaus Width: 3.4531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch17, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.25
Std Dev 3.505
Gaus mean : 44.0276
Gaus Width: 3.8372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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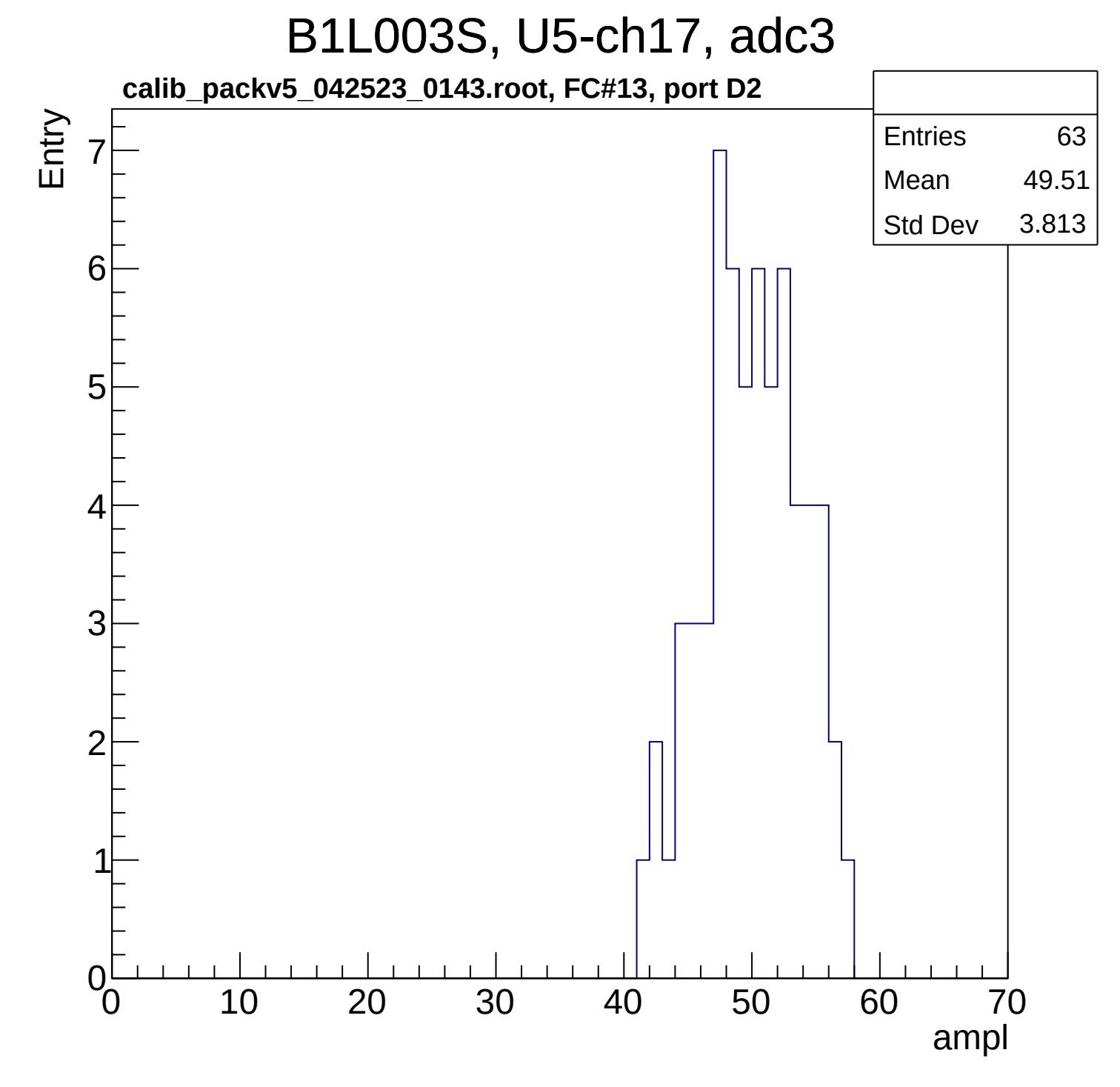
B1L003S, U5-ch17, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.51
Std Dev 3.813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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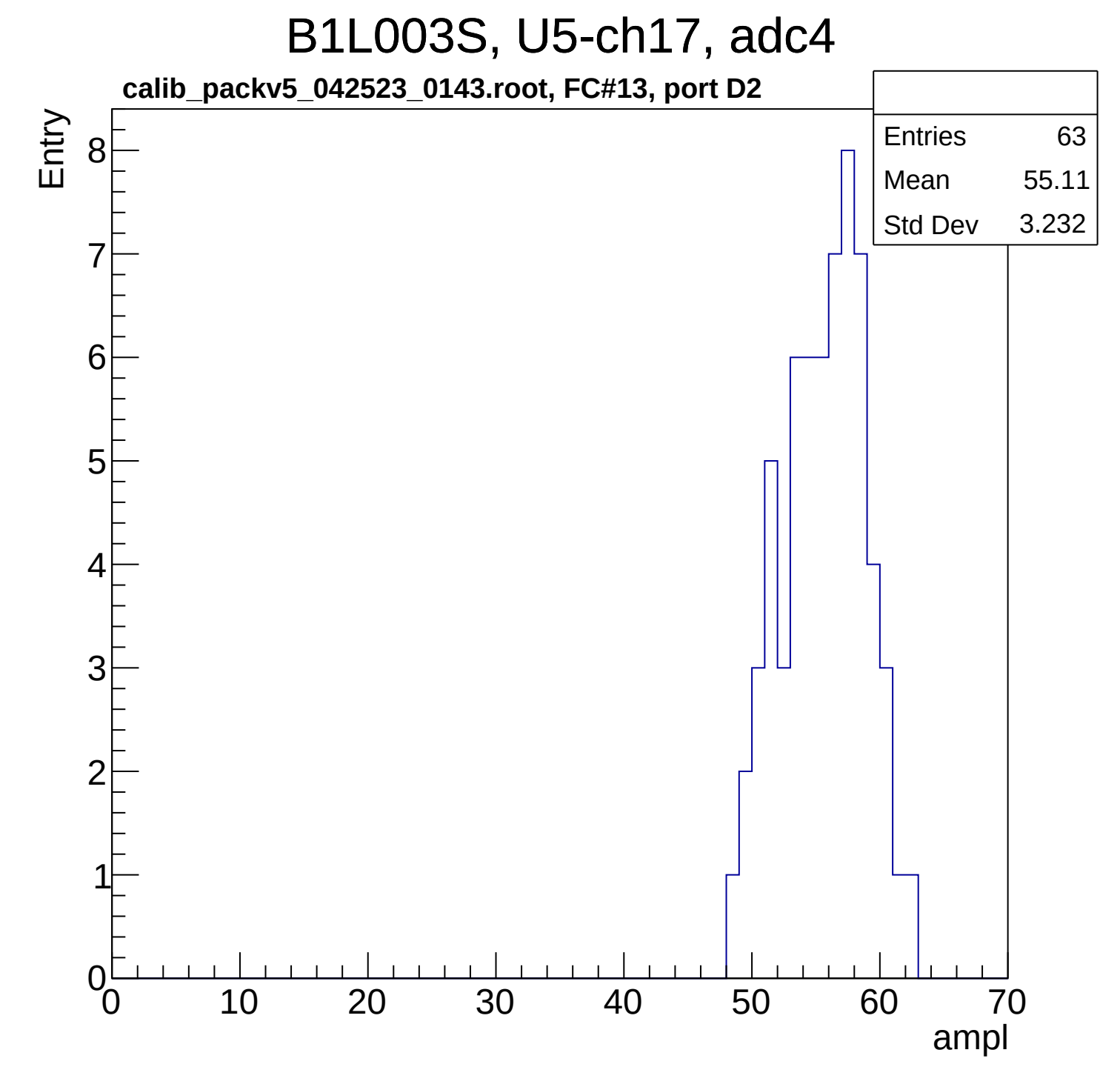
B1L003S, U5-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.11
Std Dev 3.232
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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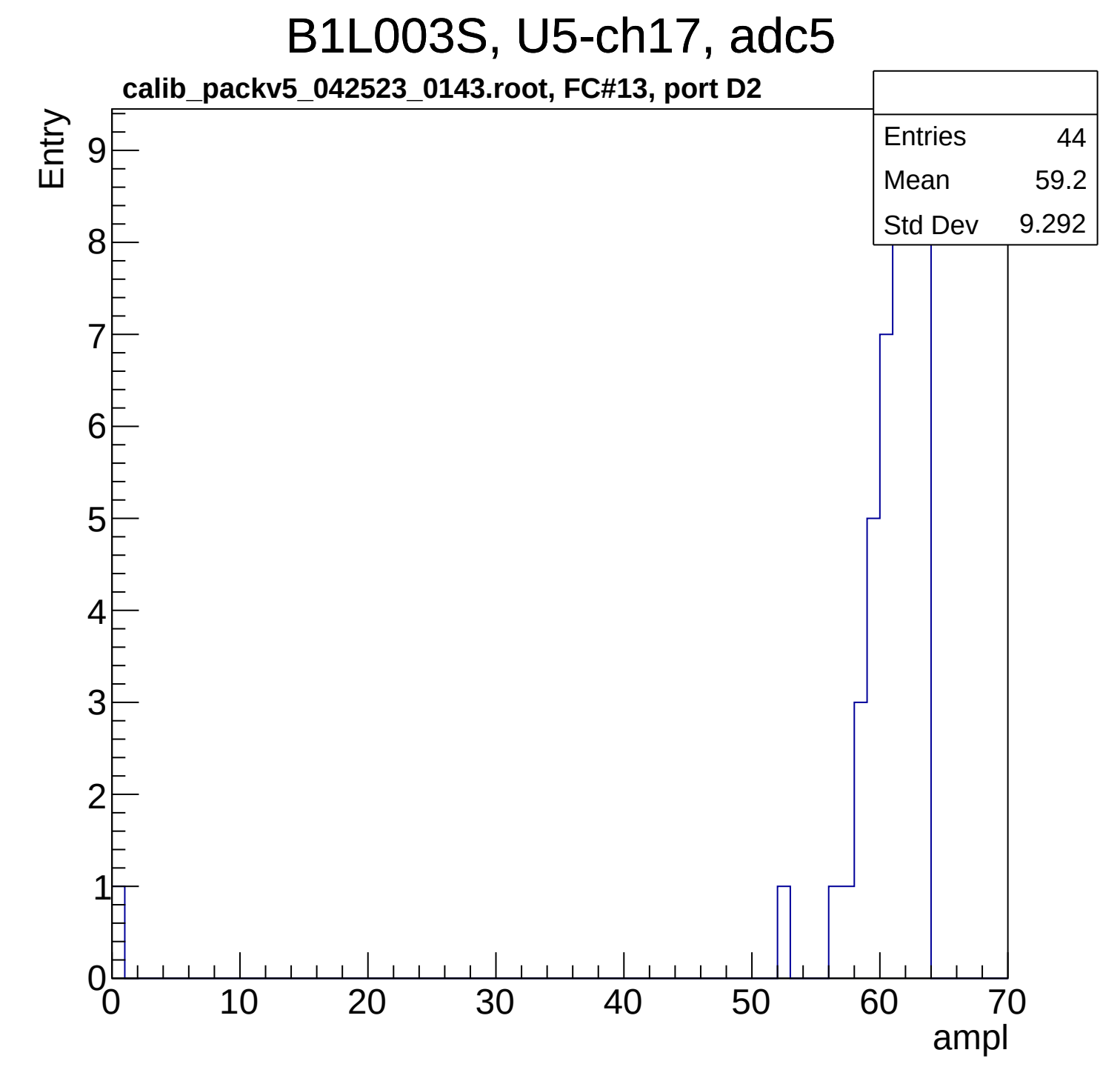
B1L003S, U5-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.2
Std Dev 9.292
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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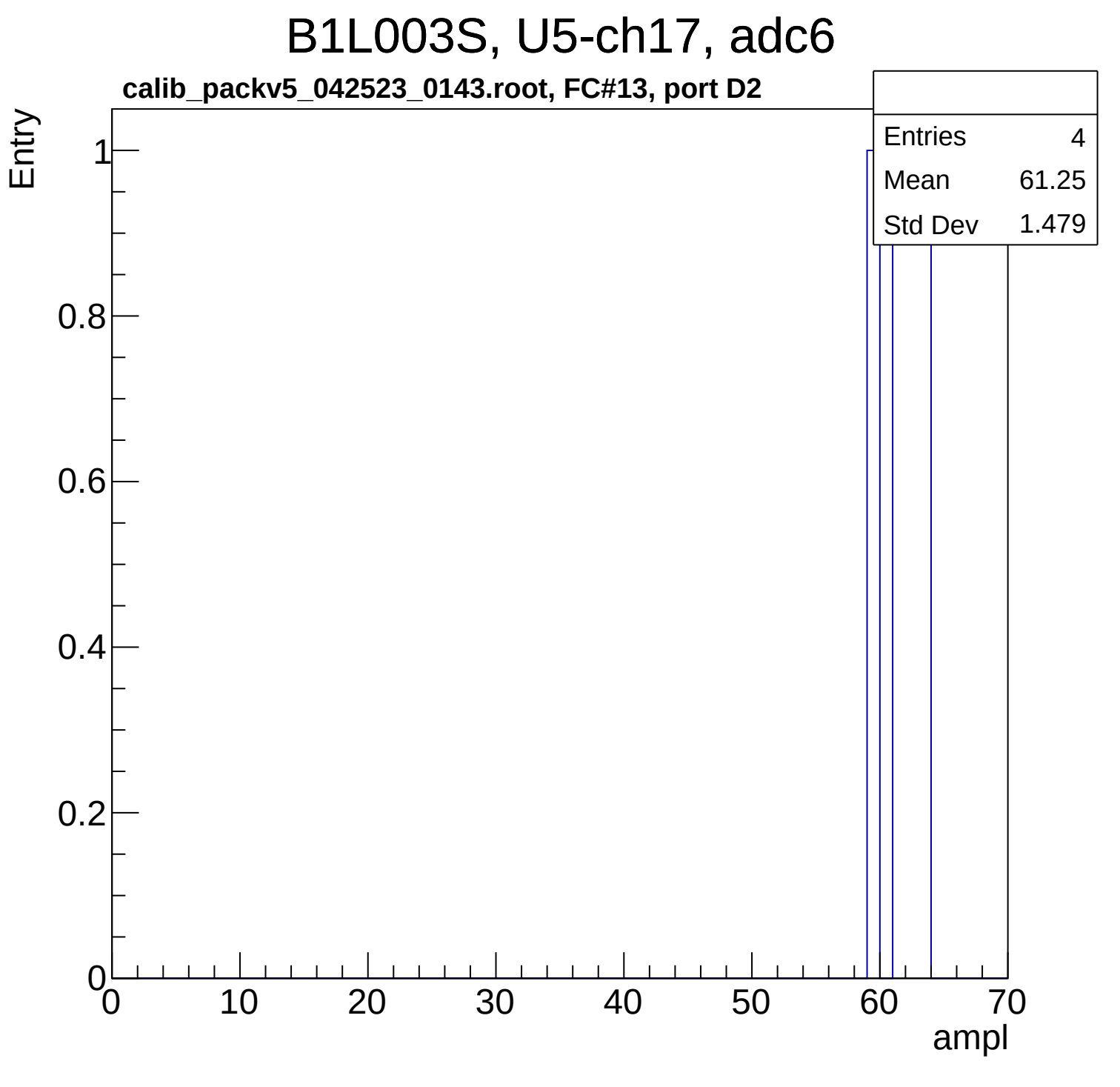
B1L003S, U5-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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0.4
0.6
0.8
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0
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60
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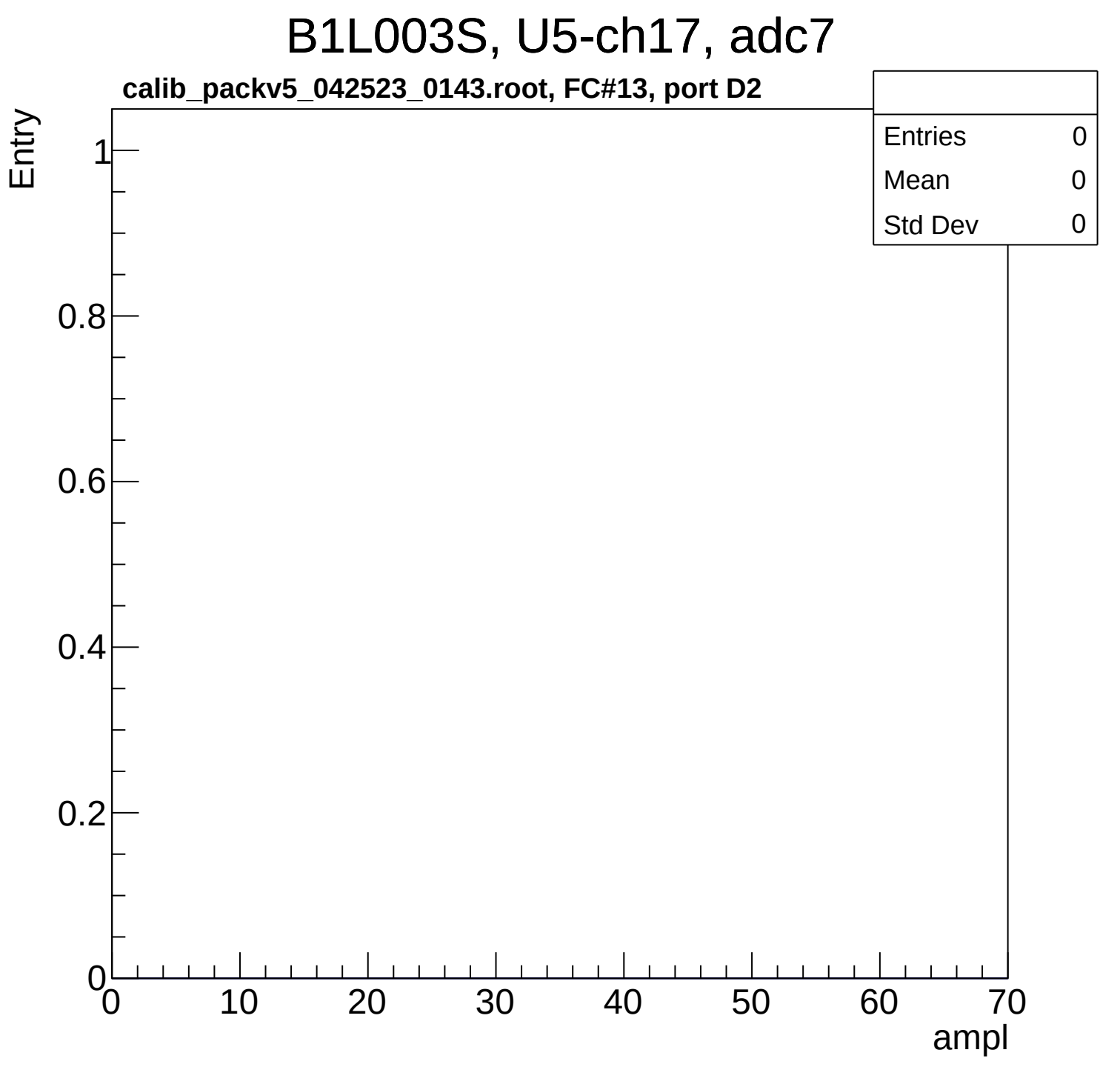
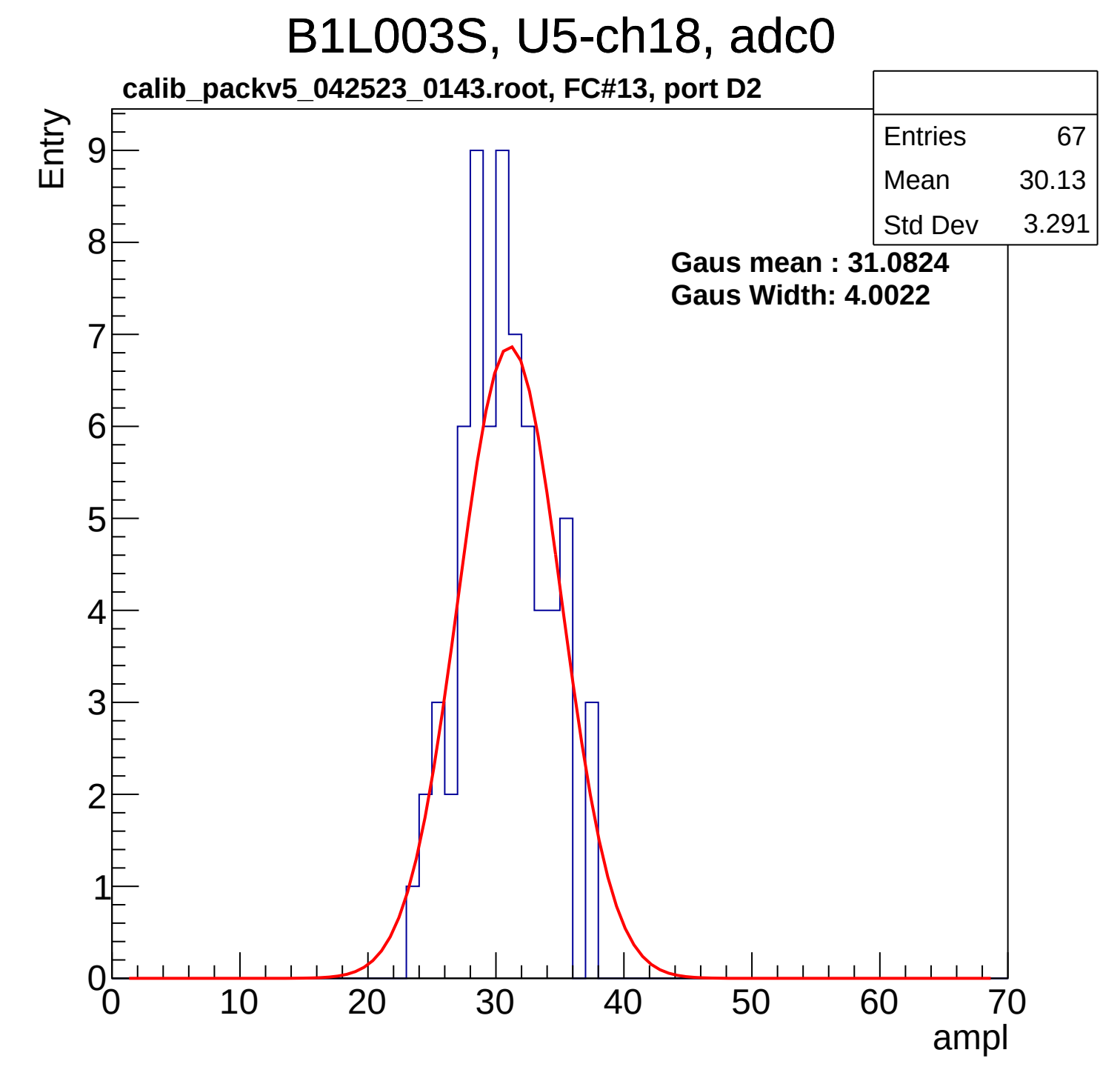

B1L003S, U5-ch17, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch18, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.13
Std Dev 3.291
Gaus mean : 31.0824
Gaus Width: 4.0022
Entry
ampl
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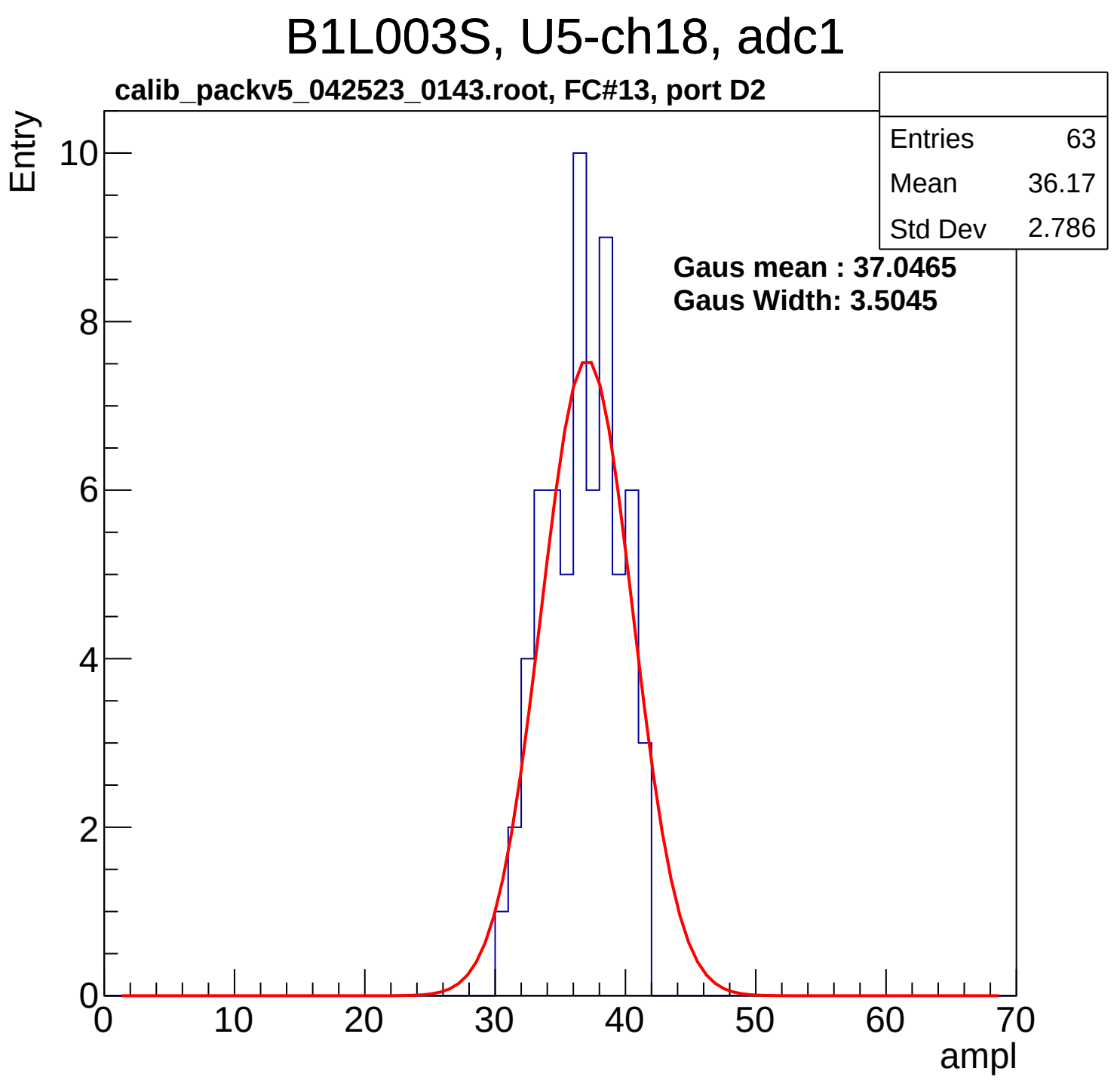
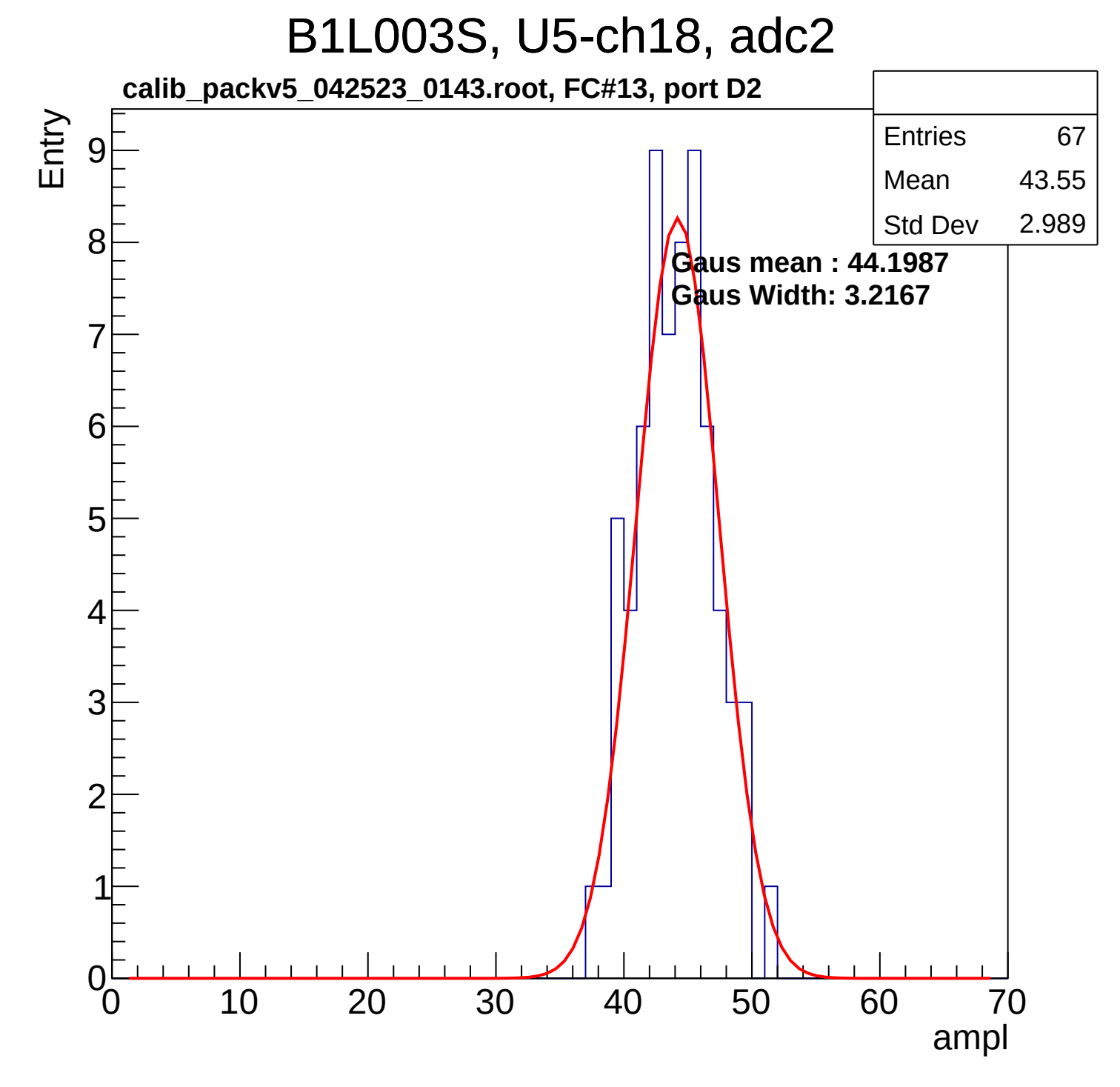
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch18, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.17
Std Dev 2.786
Gaus mean : 37.0465
Gaus Width: 3.5045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch18, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.55
Std Dev 2.989
Gaus mean : 44.1987
Gaus Width: 3.2167
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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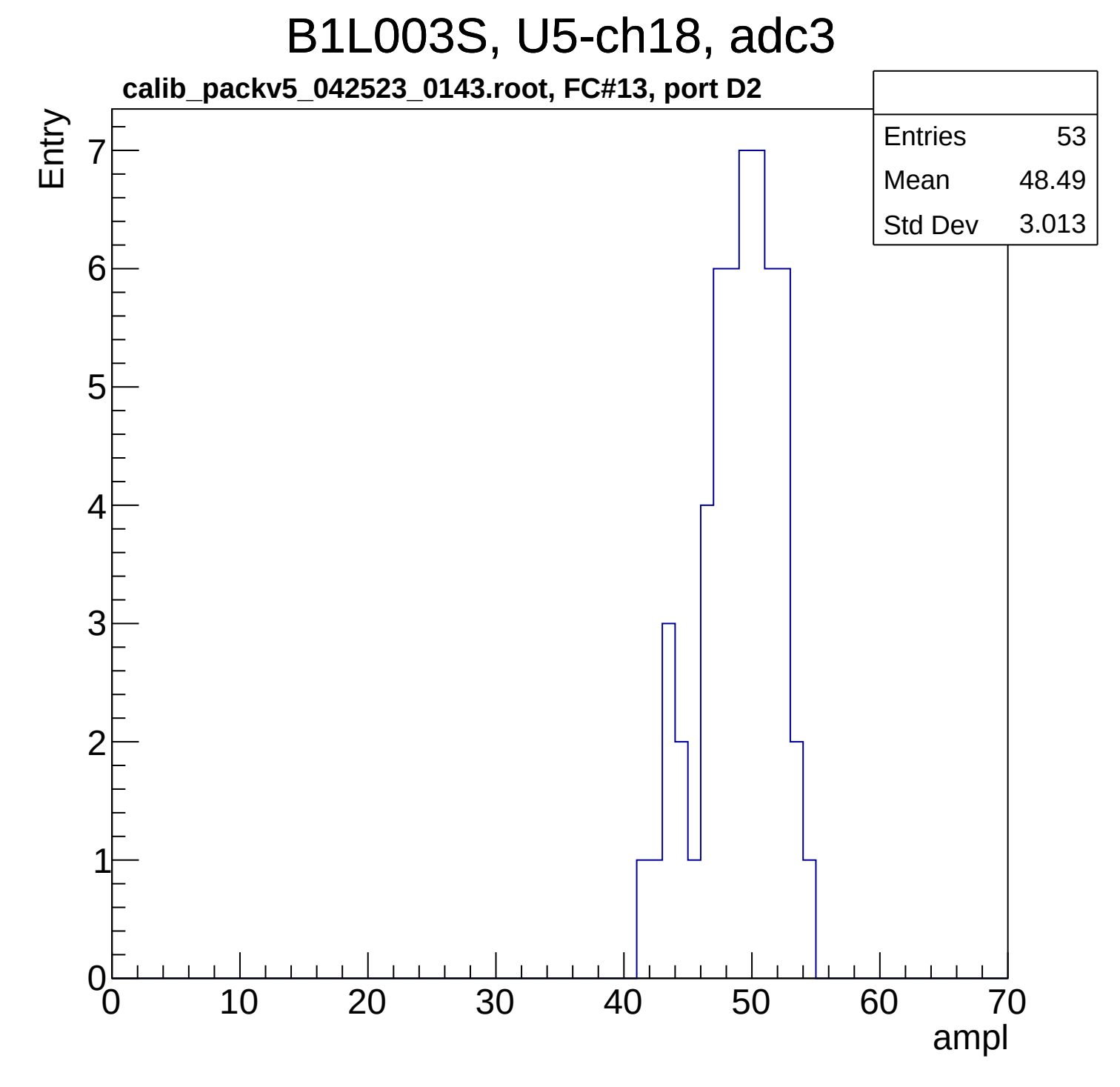
B1L003S, U5-ch18, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.49
Std Dev 3.013
Entry
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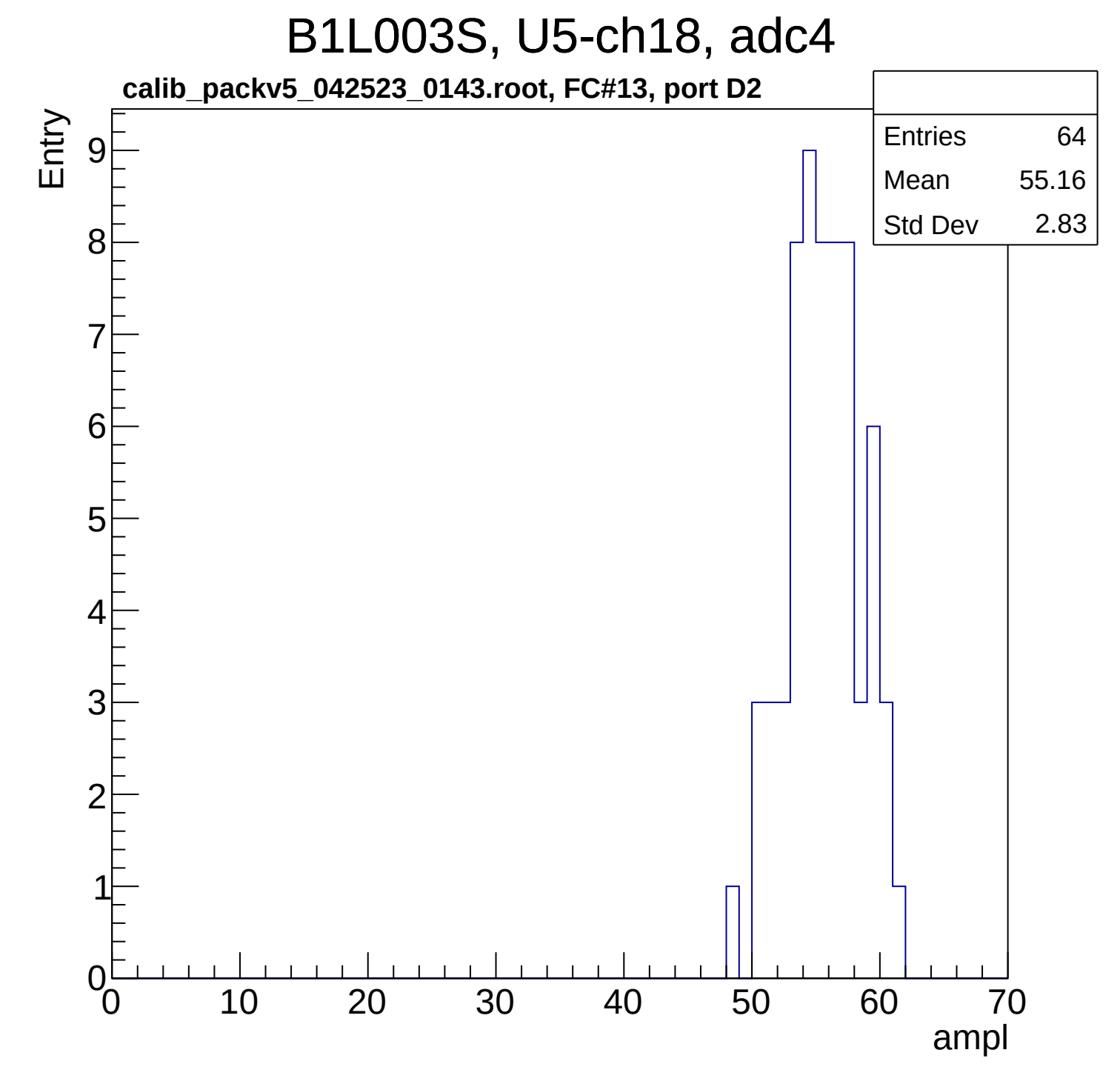
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch18, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 55.16
Std Dev 2.83
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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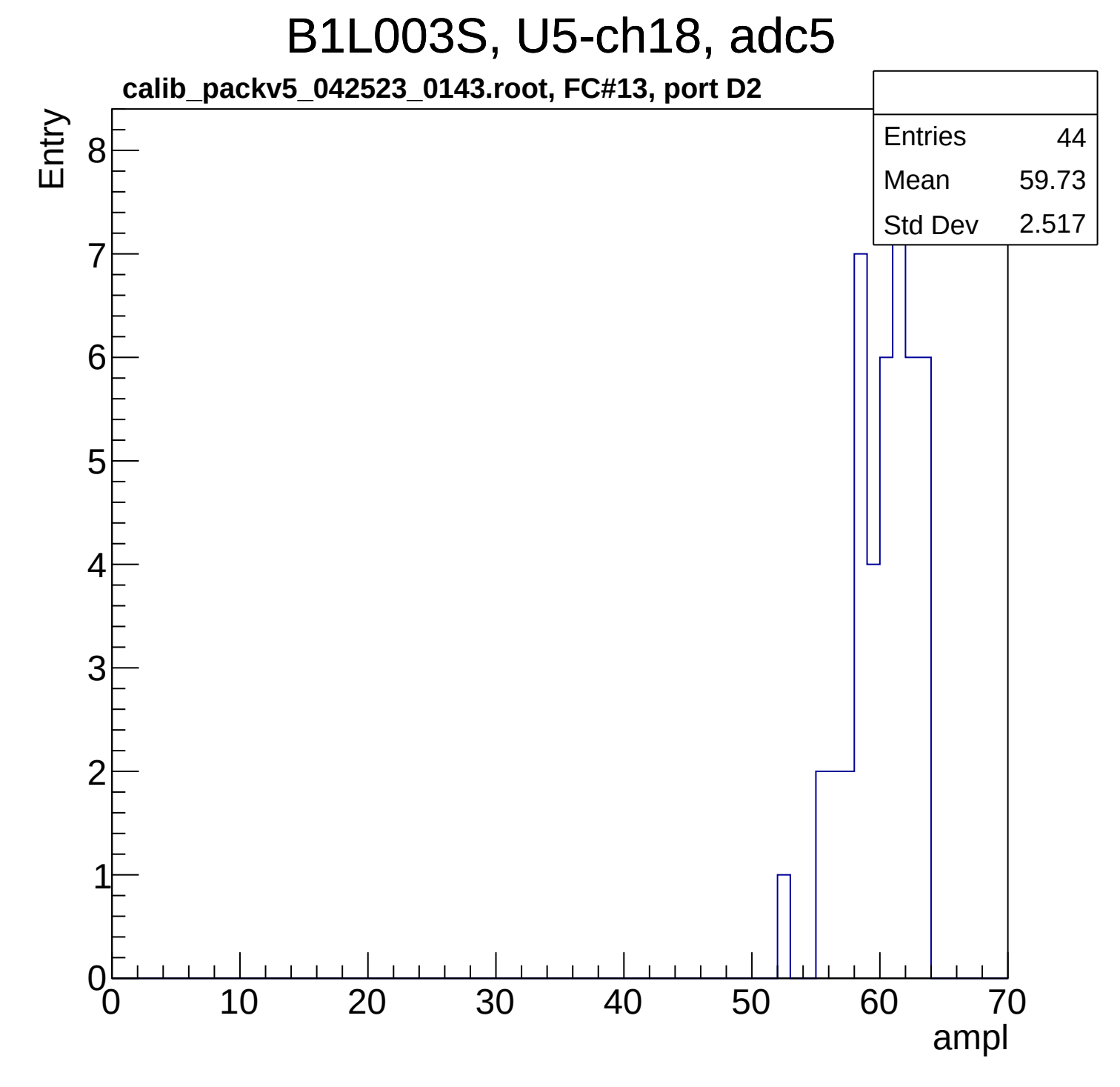
B1L003S, U5-ch18, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.73
Std Dev 2.517
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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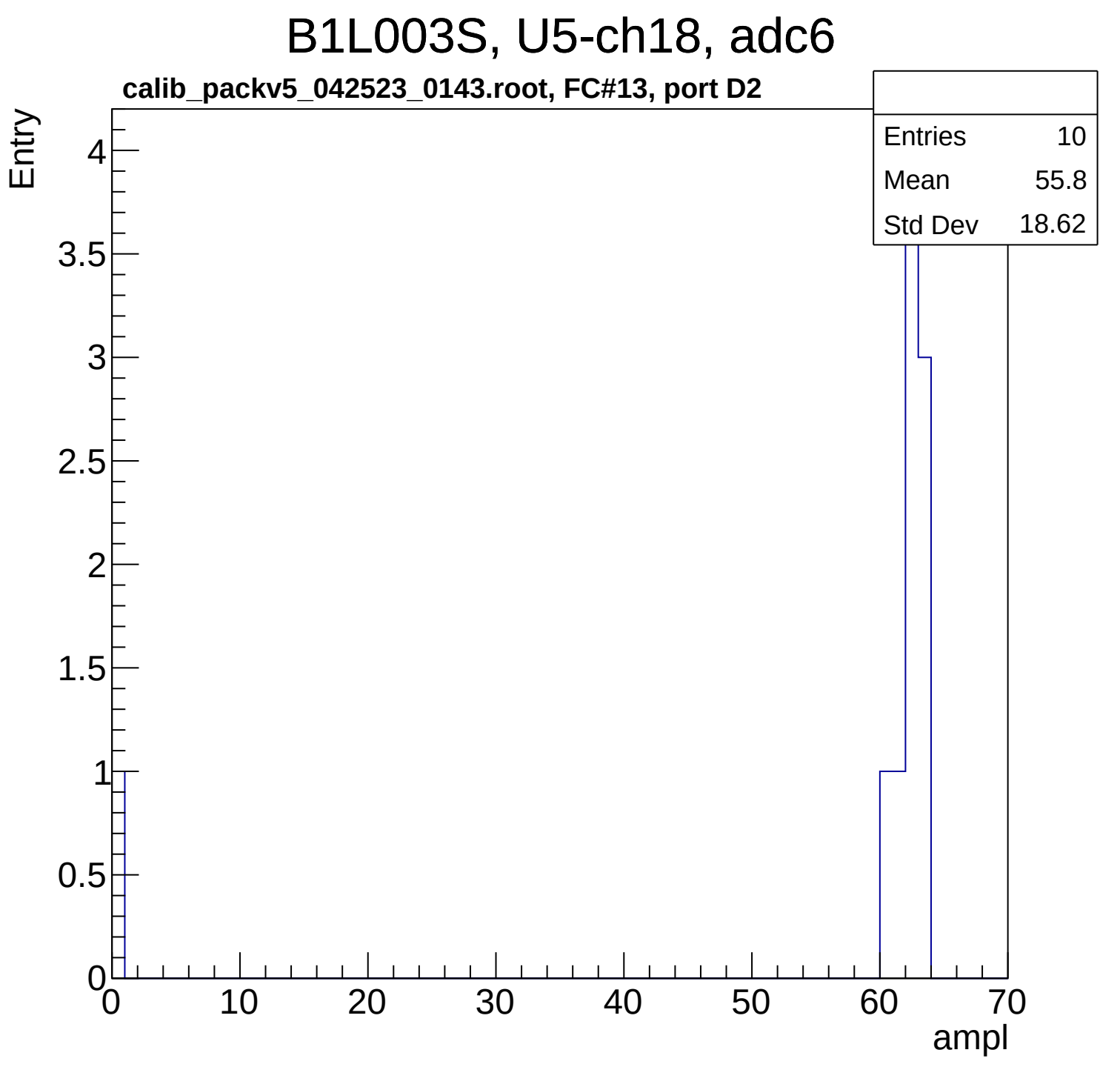
B1L003S, U5-ch18, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.62
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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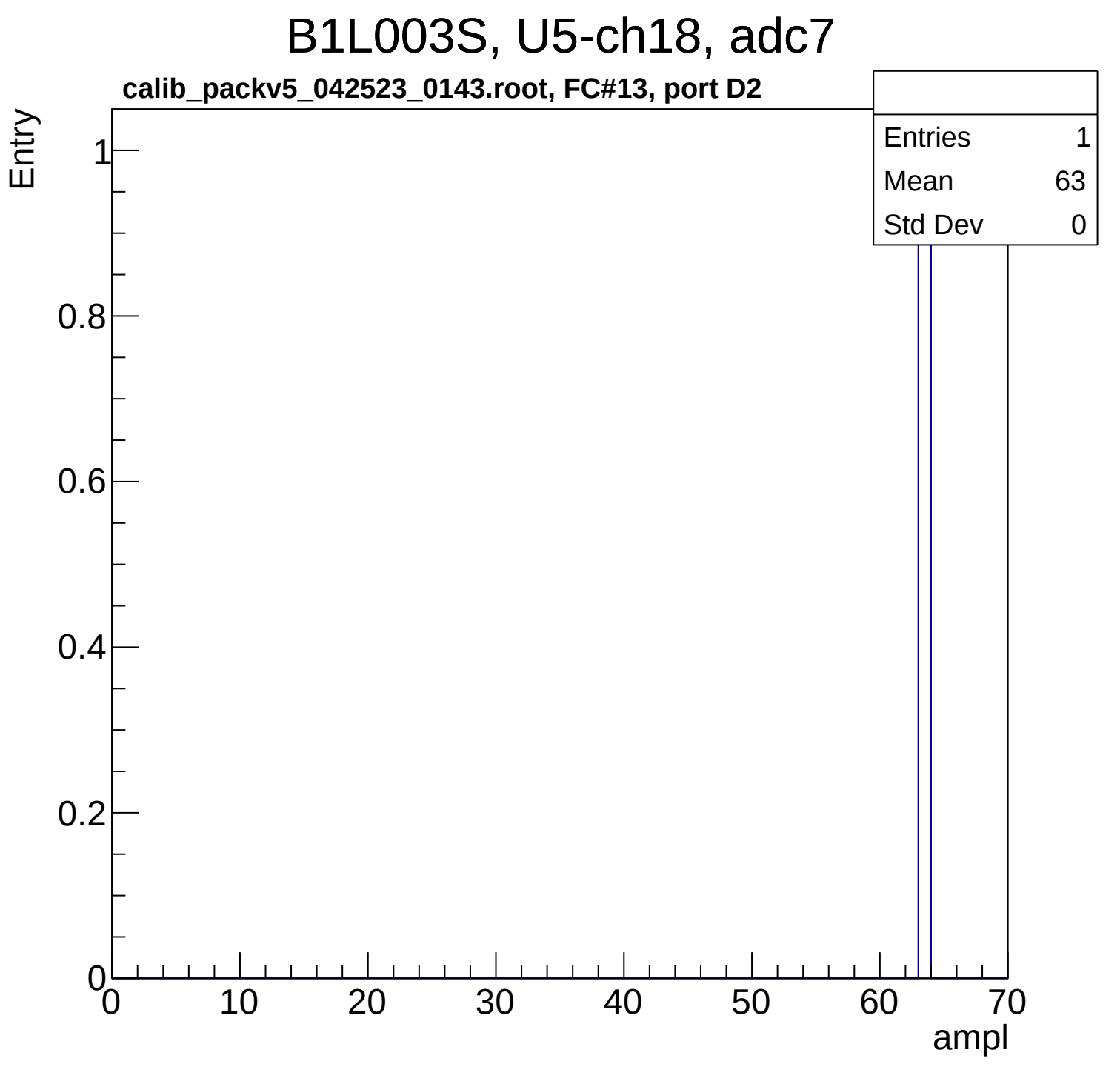
20
30
40
50
60
70


B1L003S, U5-ch18, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
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10
20
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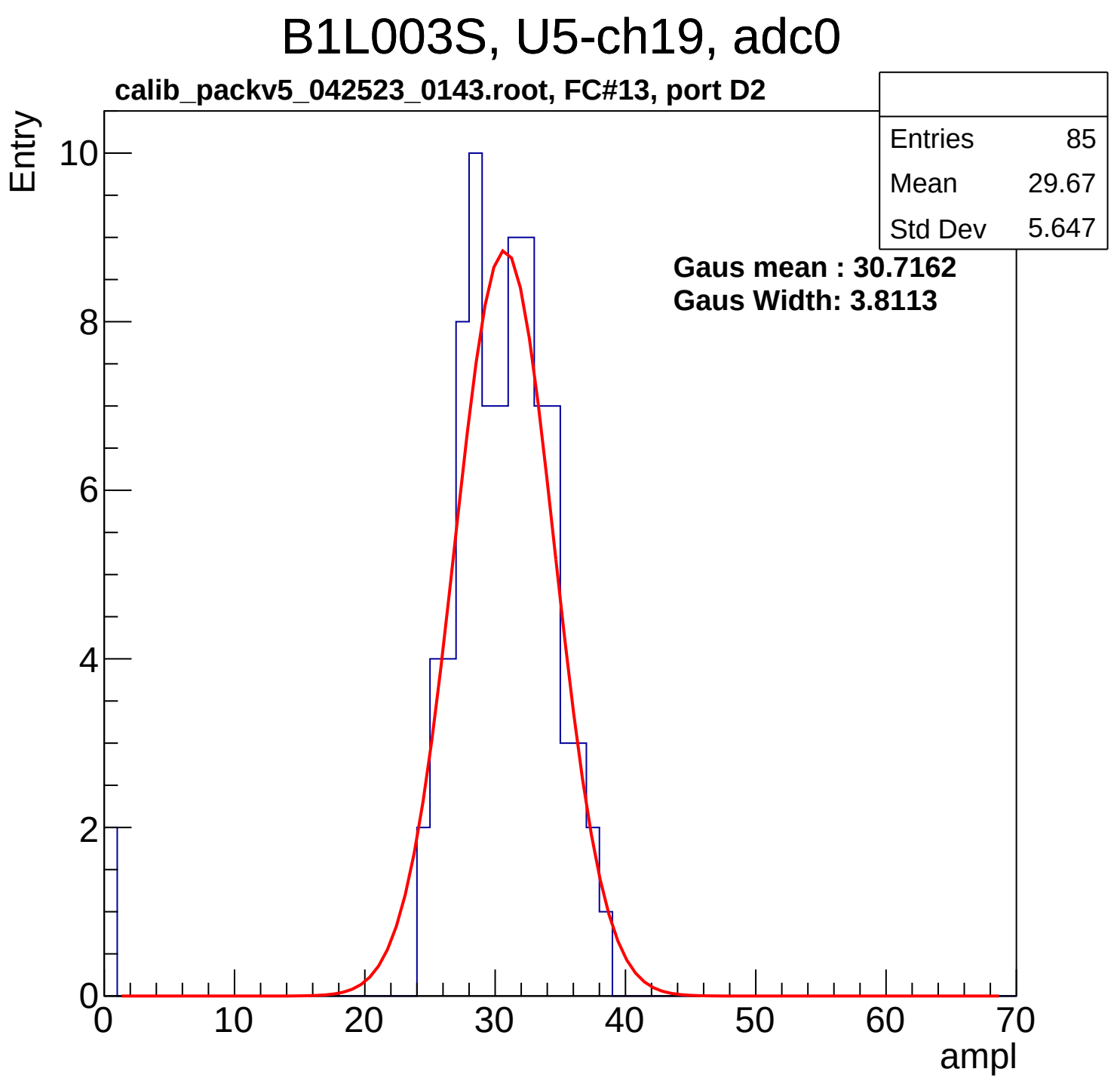
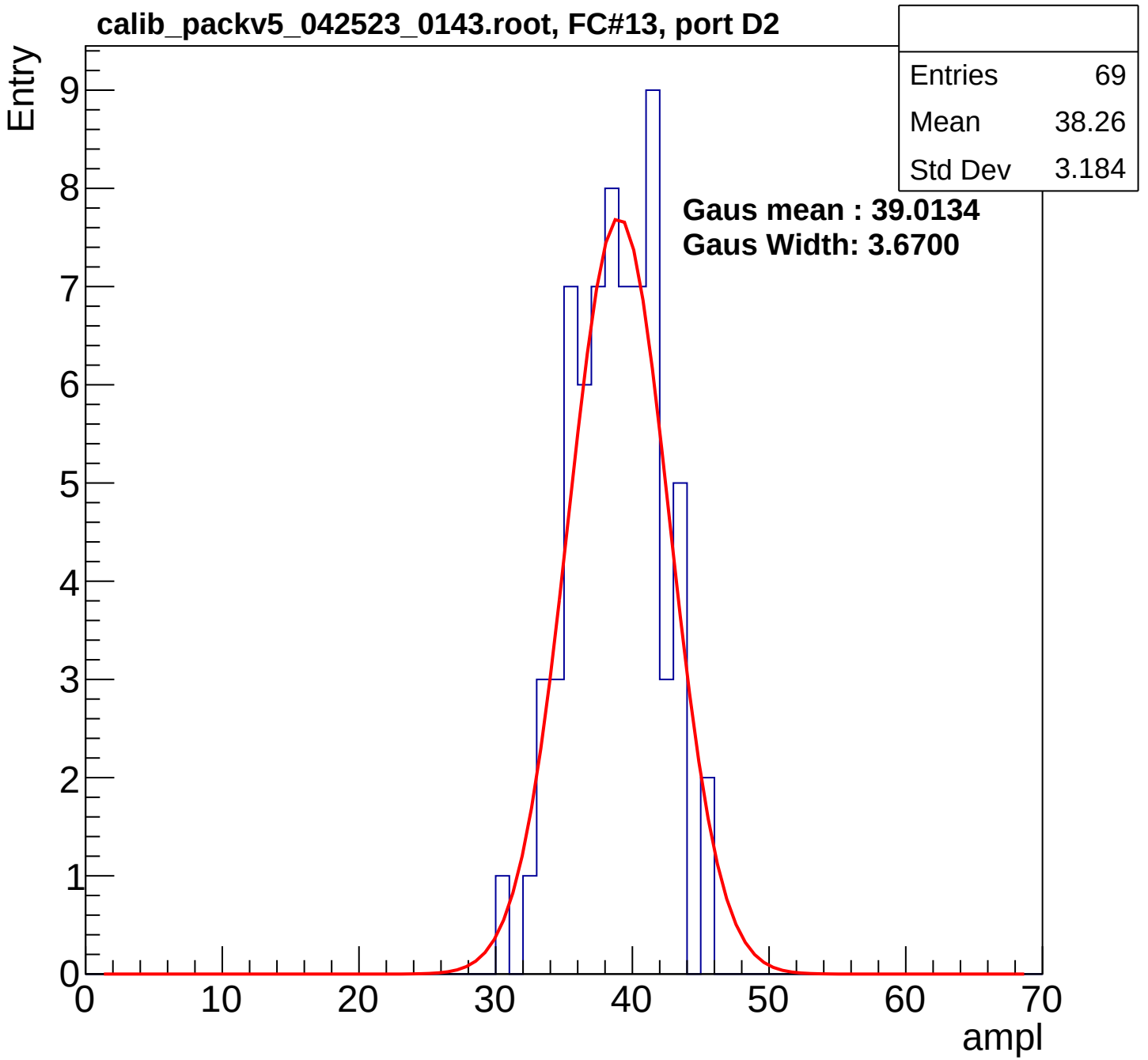
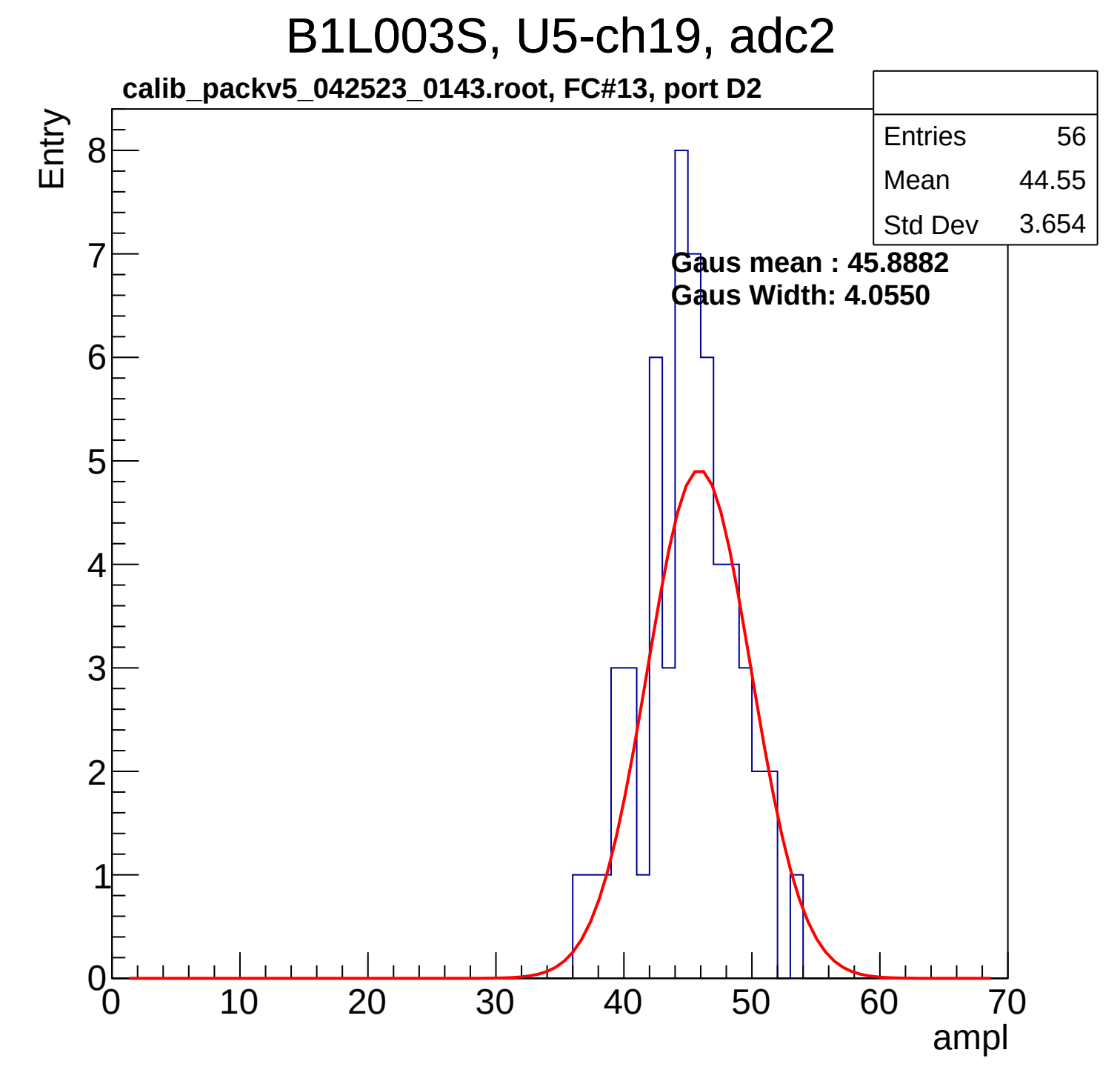
50
60
70
ampl


B1L003S, U5-ch19, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 29.67
Std Dev 5.647
Gaus mean : 30.7162
Gaus Width: 3.8113
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch19, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 38.26
Std Dev 3.184
Gaus mean : 39.0134
Gaus Width: 3.6700
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch19, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 44.55
Std Dev 3.654
Gaus mean : 45.8882
Gaus Width: 4.0550
Entry
ampl
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1
2
3
4
5
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7
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0
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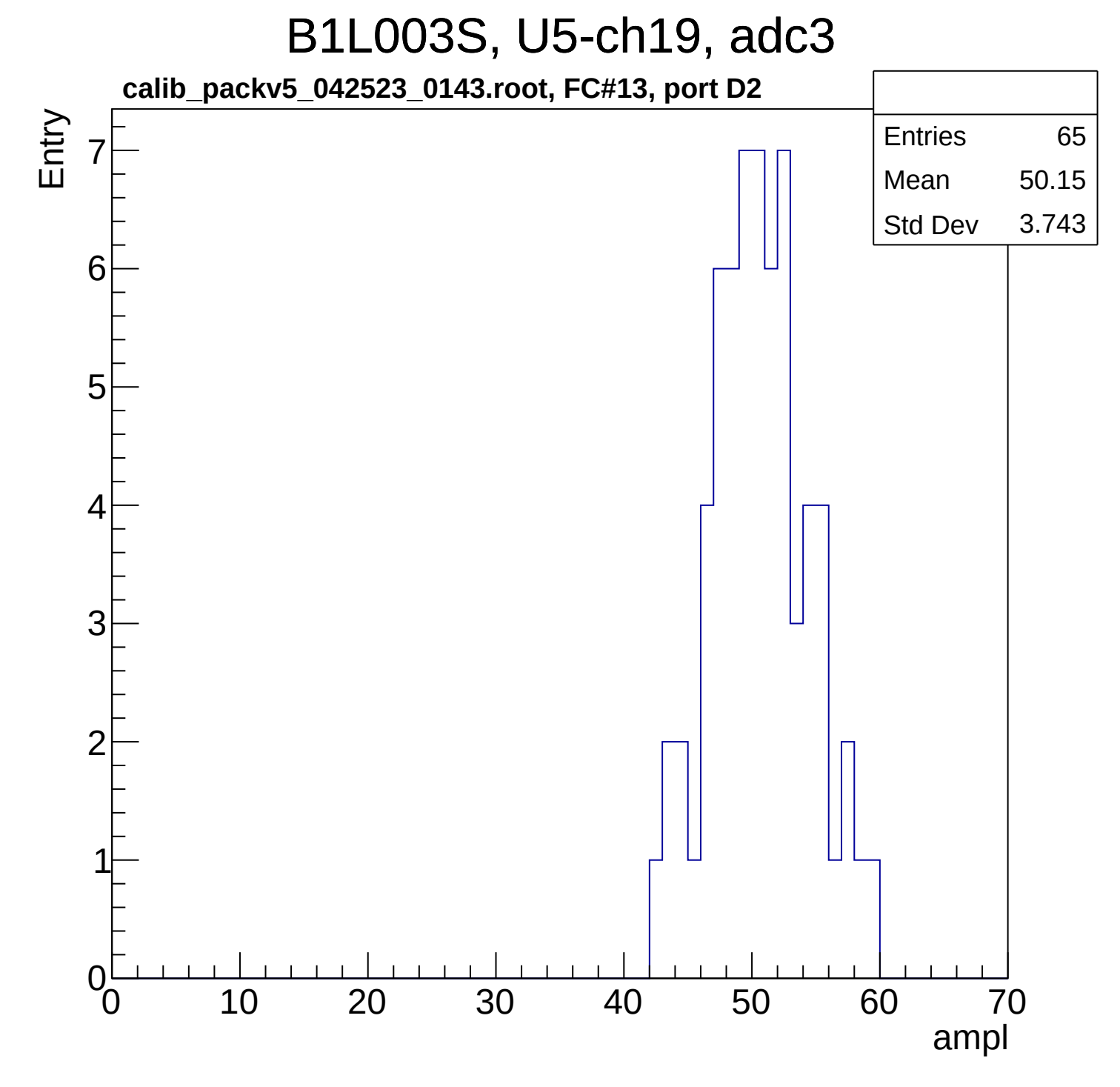
B1L003S, U5-ch19, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.15
Std Dev 3.743
Entry
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2
3
4
5
6
7
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10
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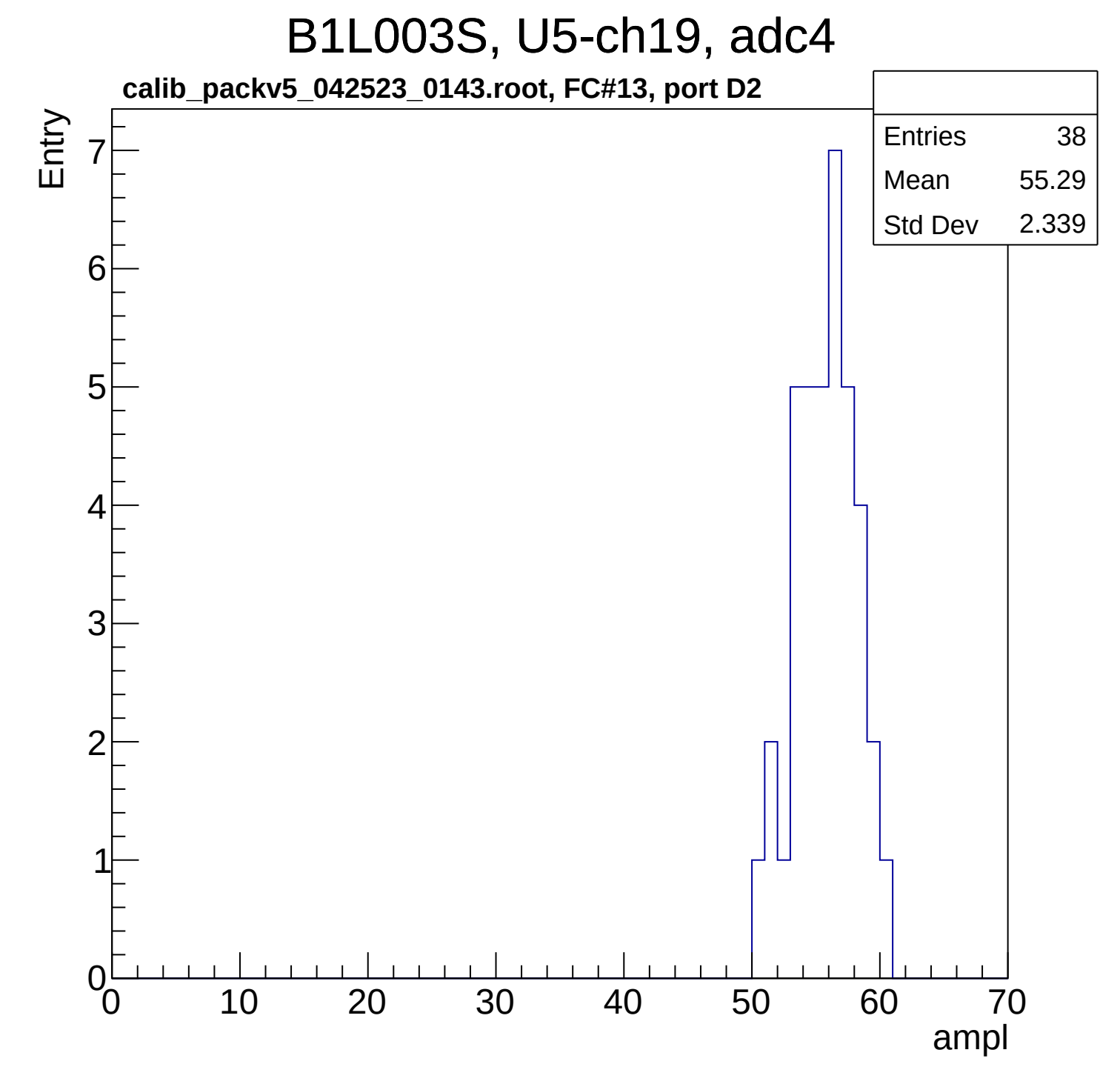
70
ampl


B1L003S, U5-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 55.29
Std Dev 2.339
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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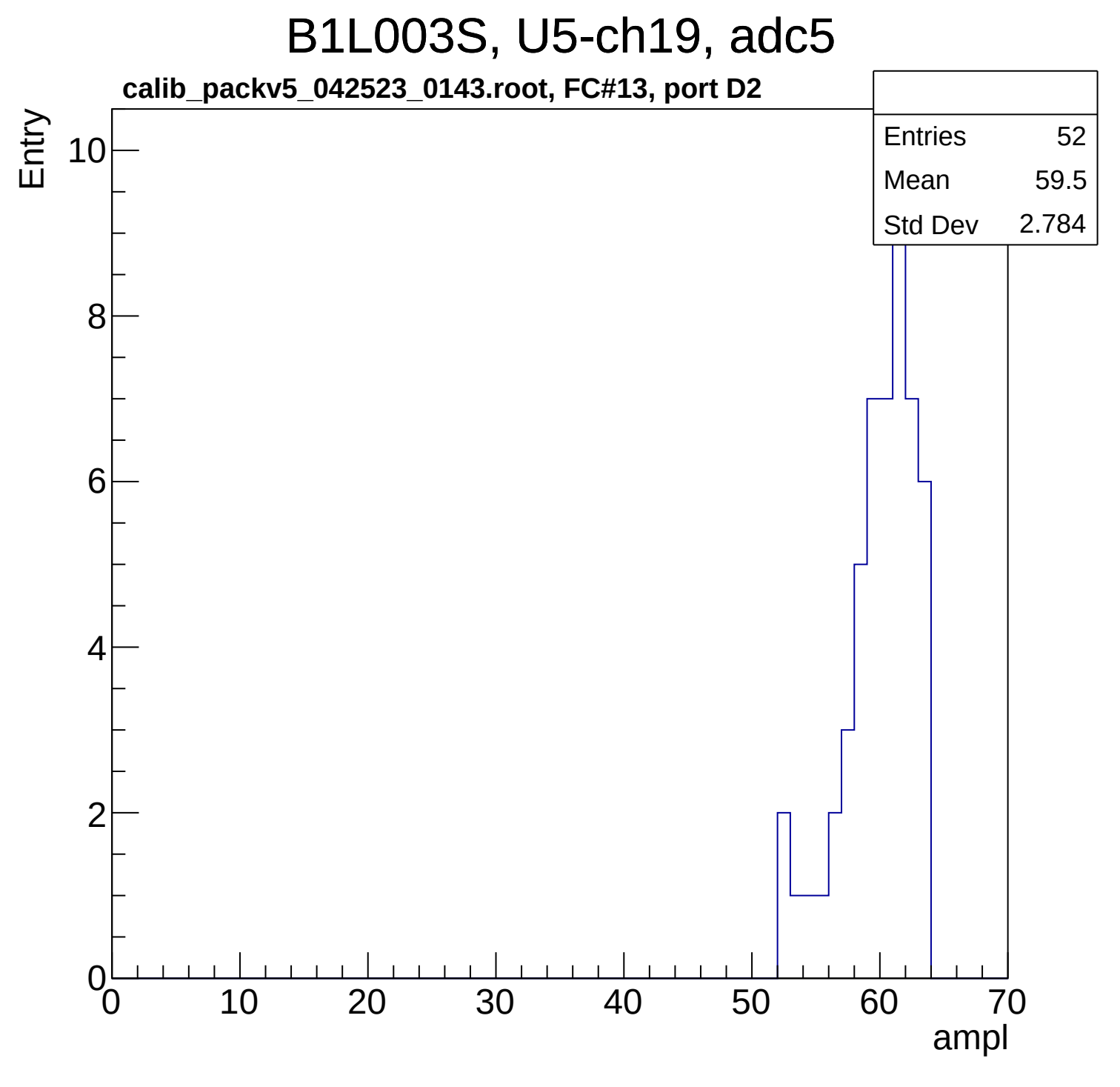
B1L003S, U5-ch19, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.5
Std Dev 2.784
Entry
0
2
4
6
8
10
0
10
20
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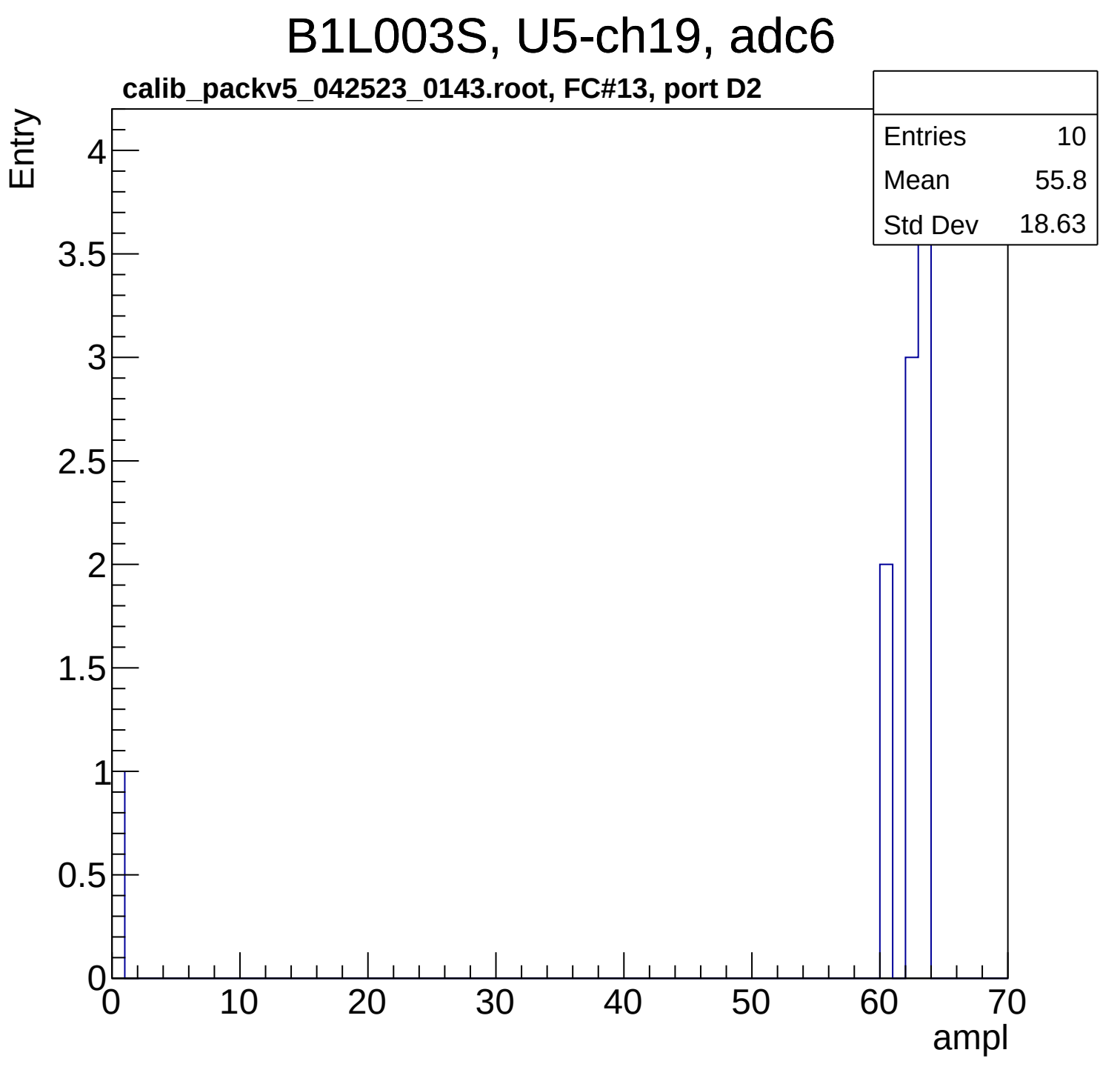
50
60
70
ampl


B1L003S, U5-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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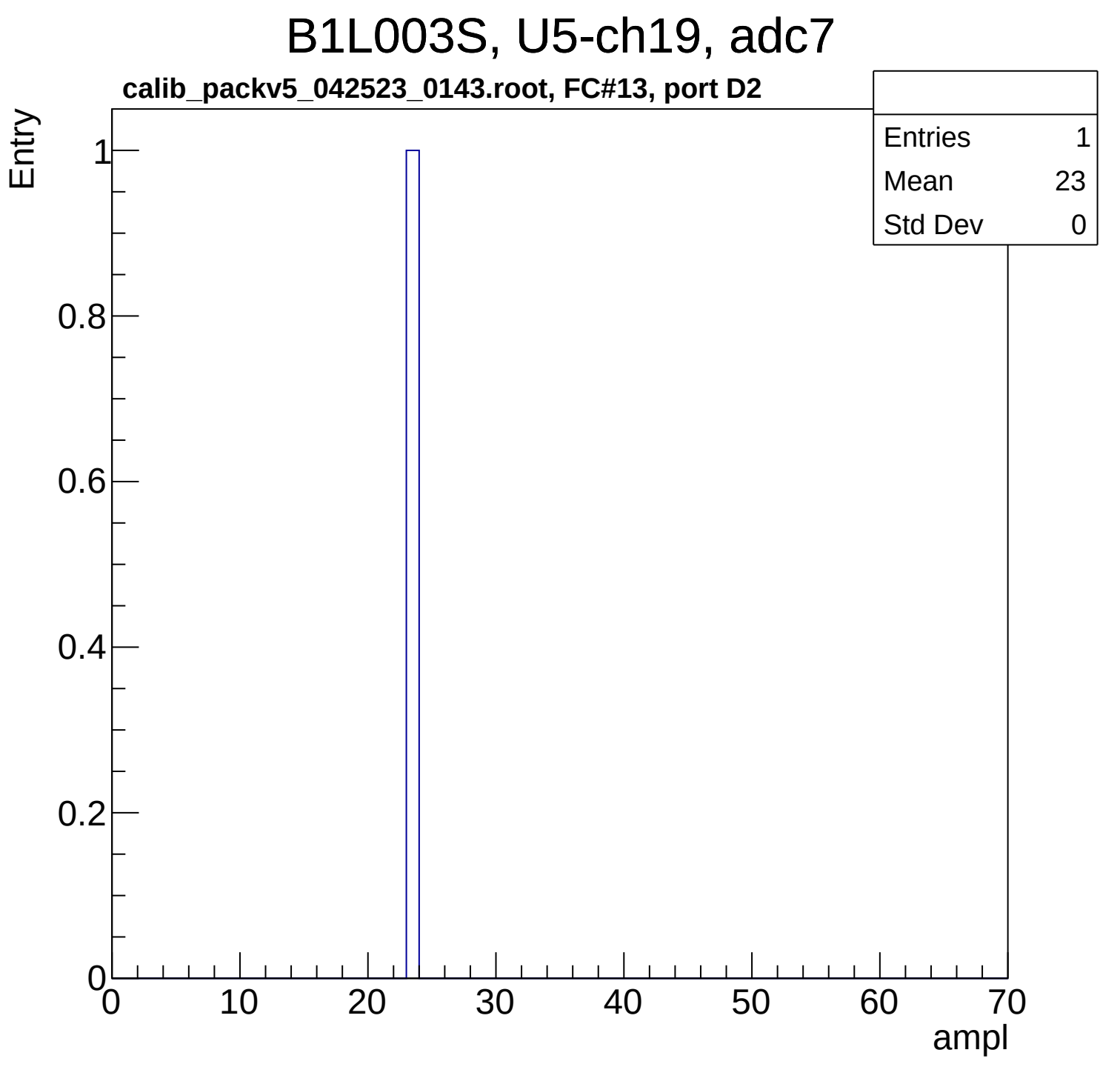
B1L003S, U5-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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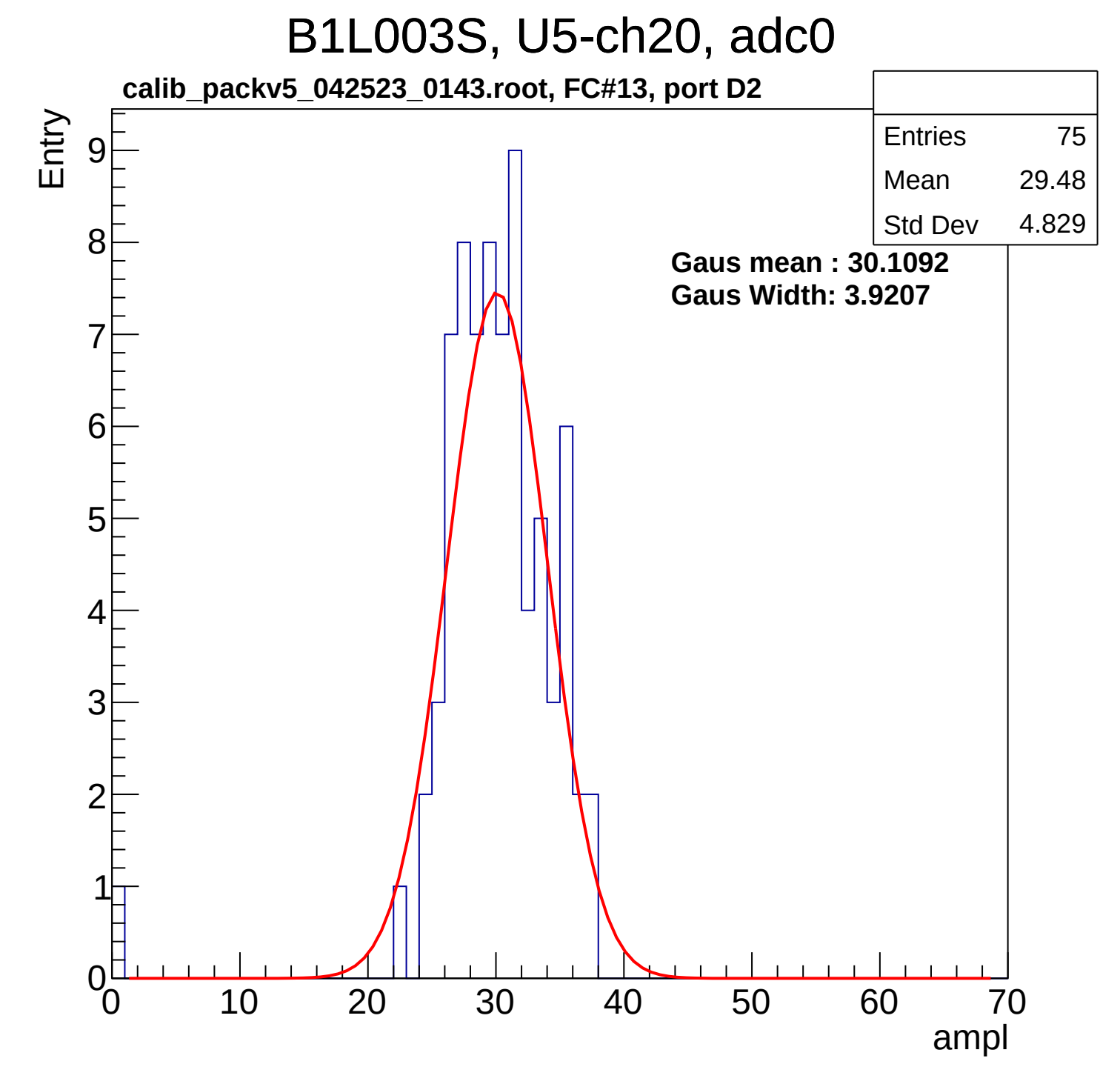
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch20, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.48
Std Dev 4.829
Gaus mean : 30.1092
Gaus Width: 3.9207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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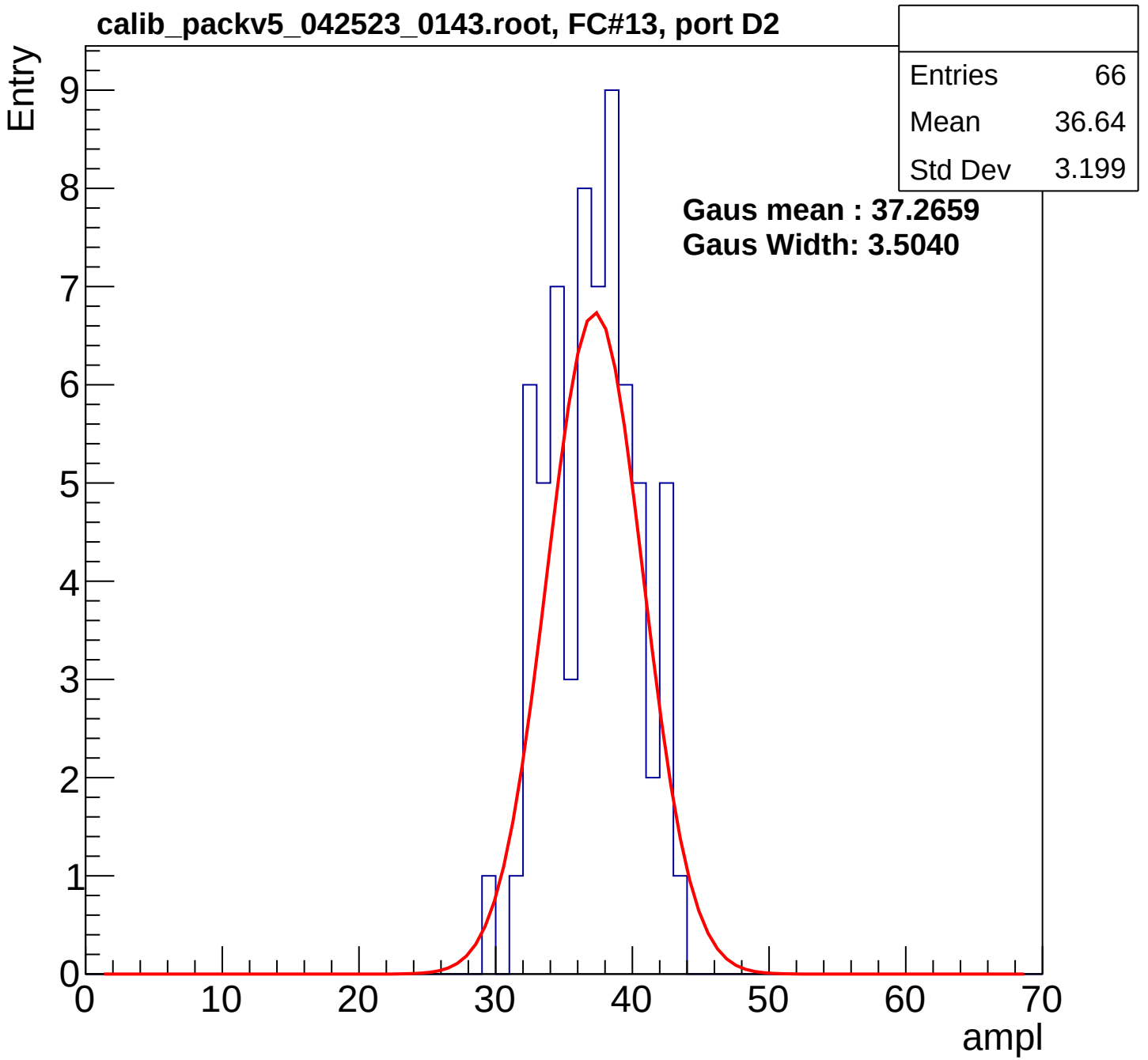
B1L003S, U5-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 36.64
Std Dev 3.199
Gaus mean : 37.2659
Gaus Width: 3.5040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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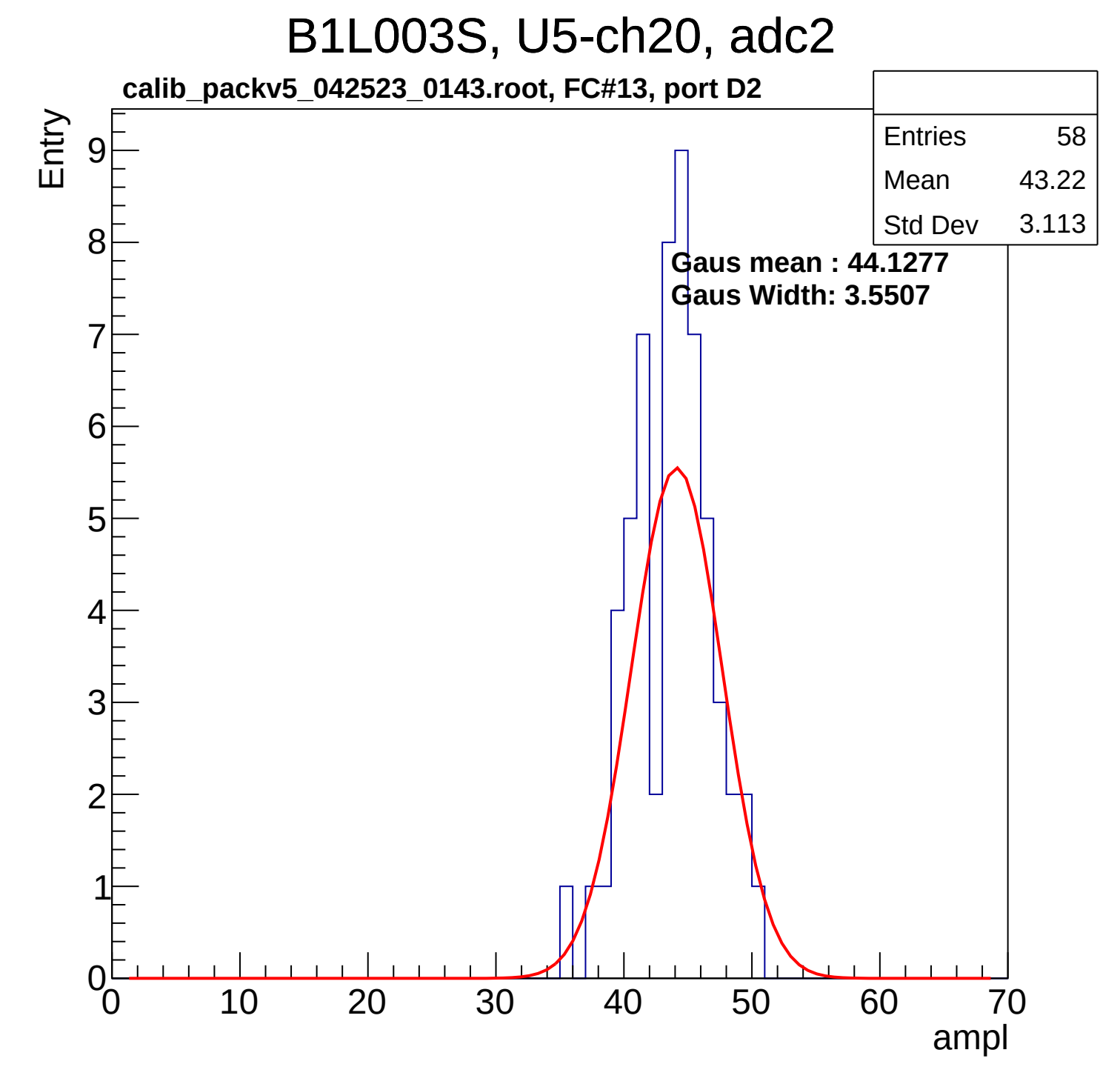
B1L003S, U5-ch20, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.22
Std Dev 3.113
Gaus mean : 44.1277
Gaus Width: 3.5507
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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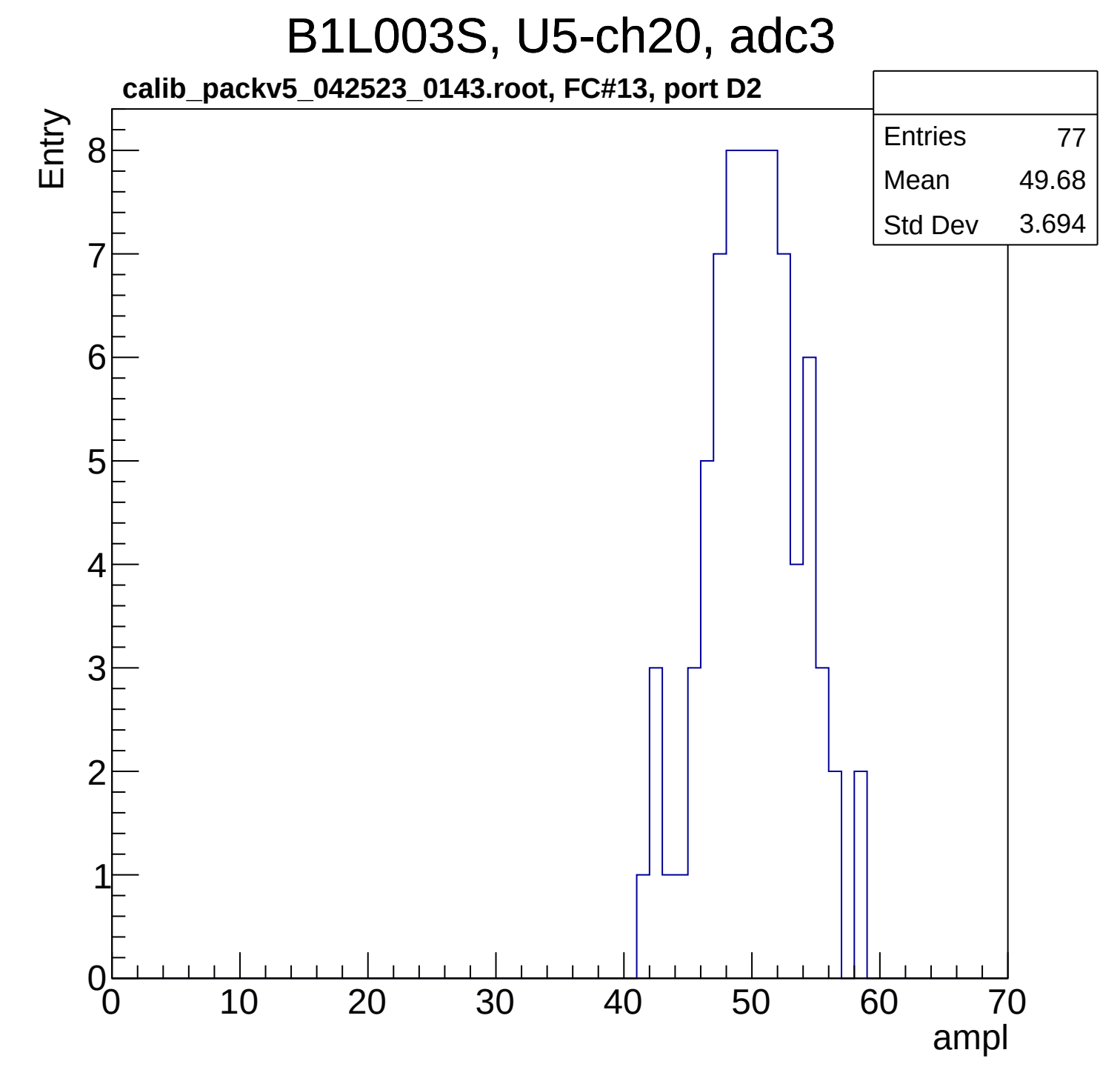
B1L003S, U5-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 49.68
Std Dev 3.694
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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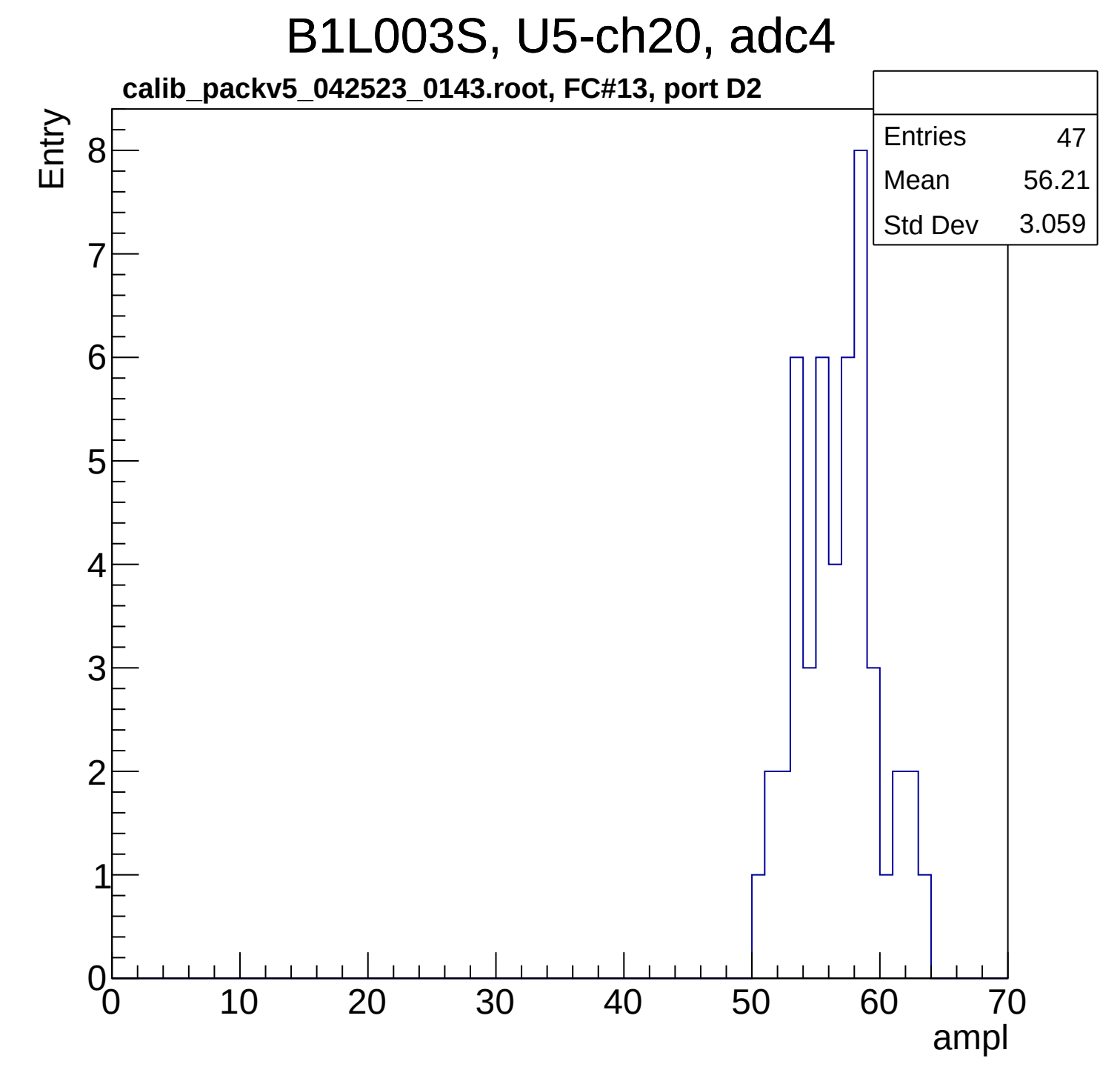
B1L003S, U5-ch20, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 56.21
Std Dev 3.059
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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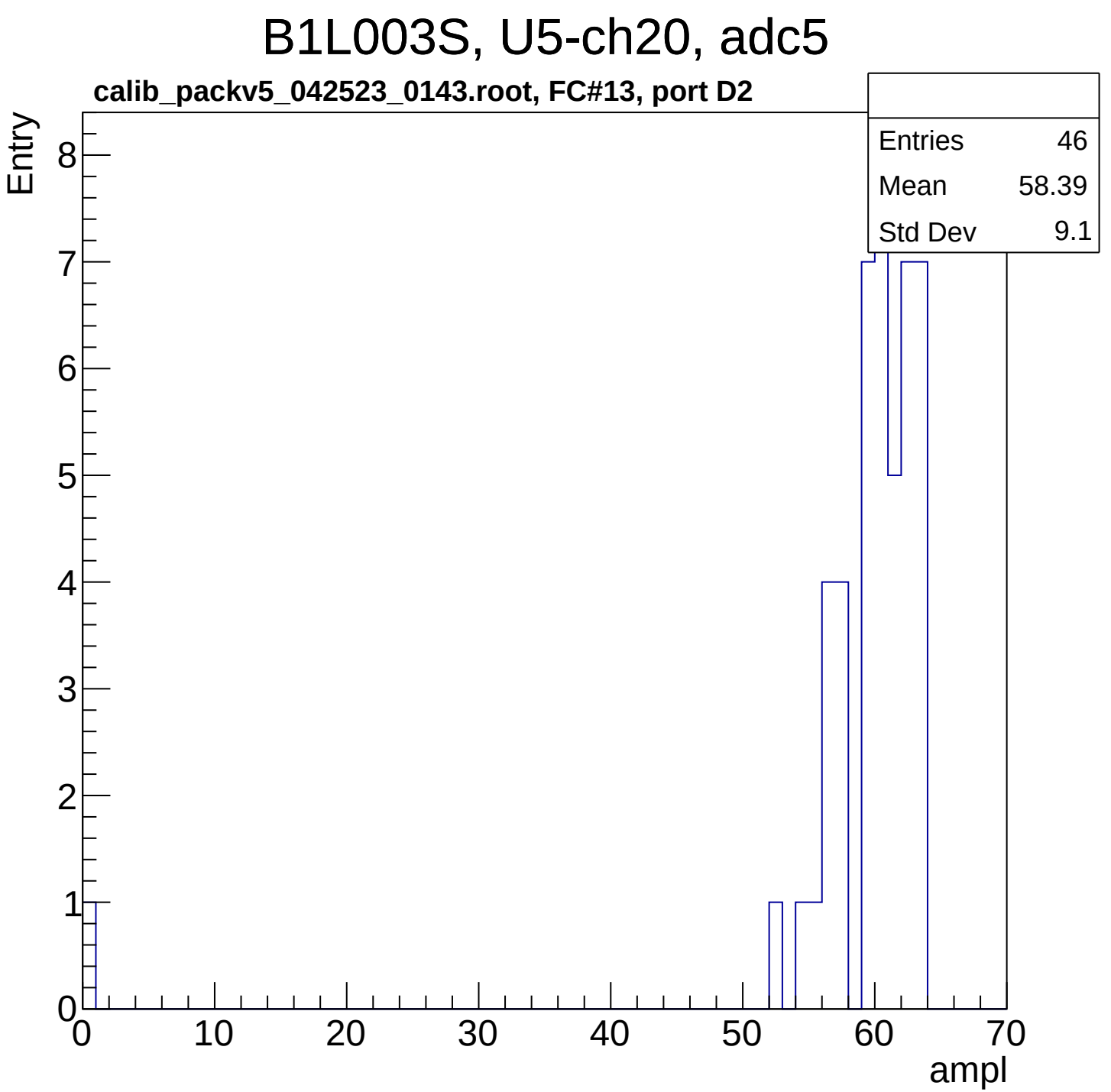
B1L003S, U5-ch20, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.39
Std Dev 9.1
Entry
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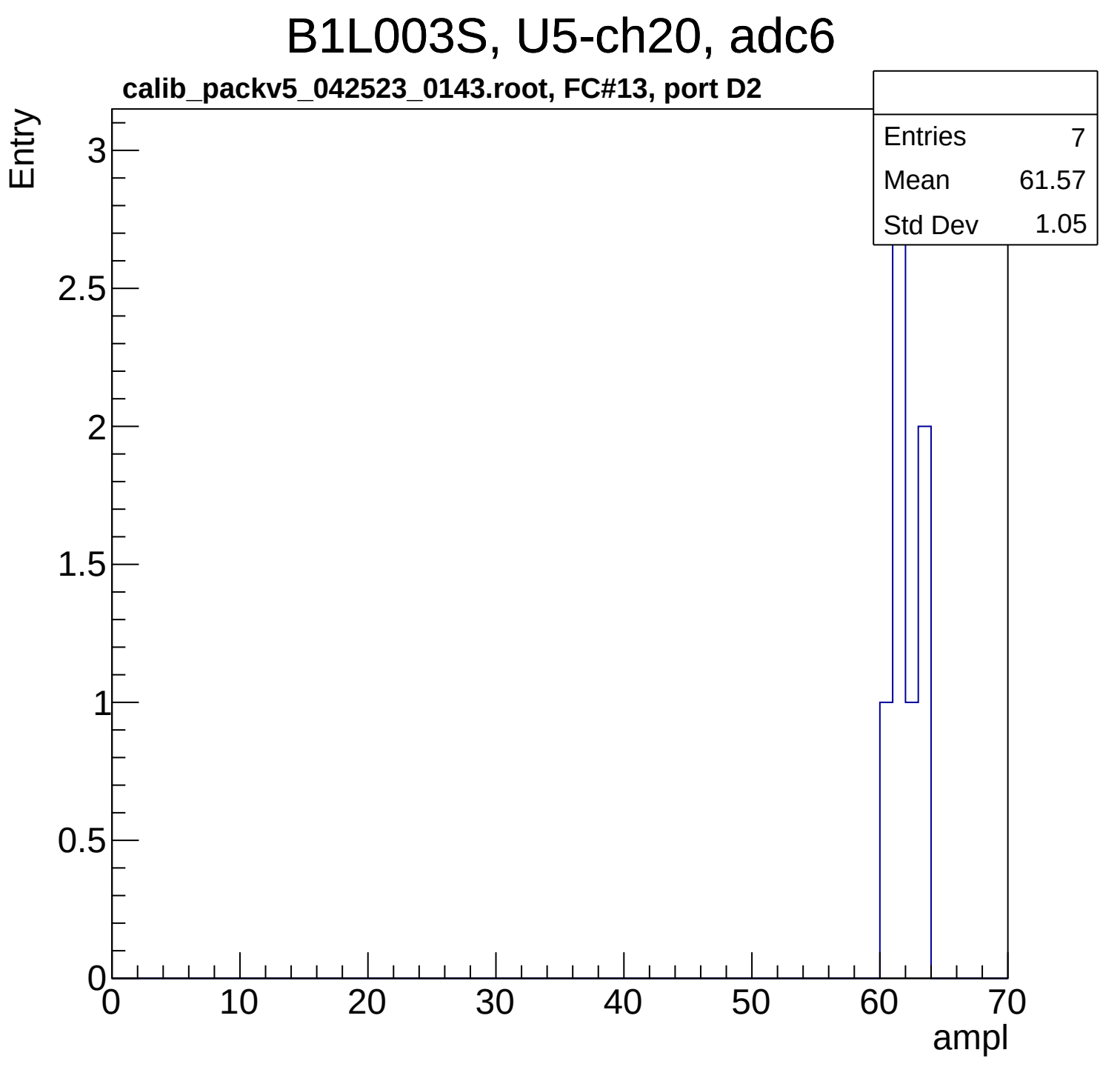
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.57
Std Dev 1.05
Entry
ampl
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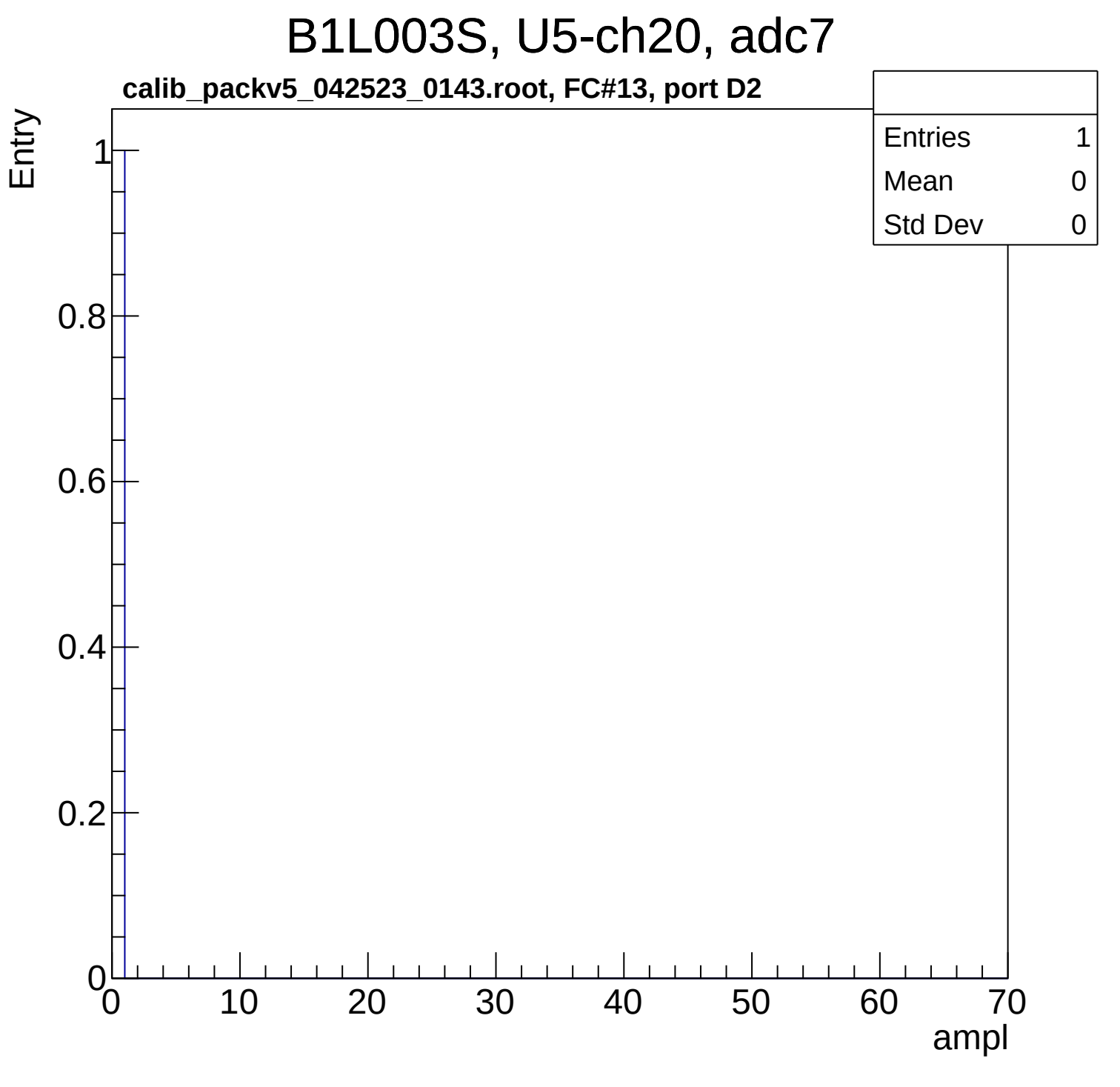
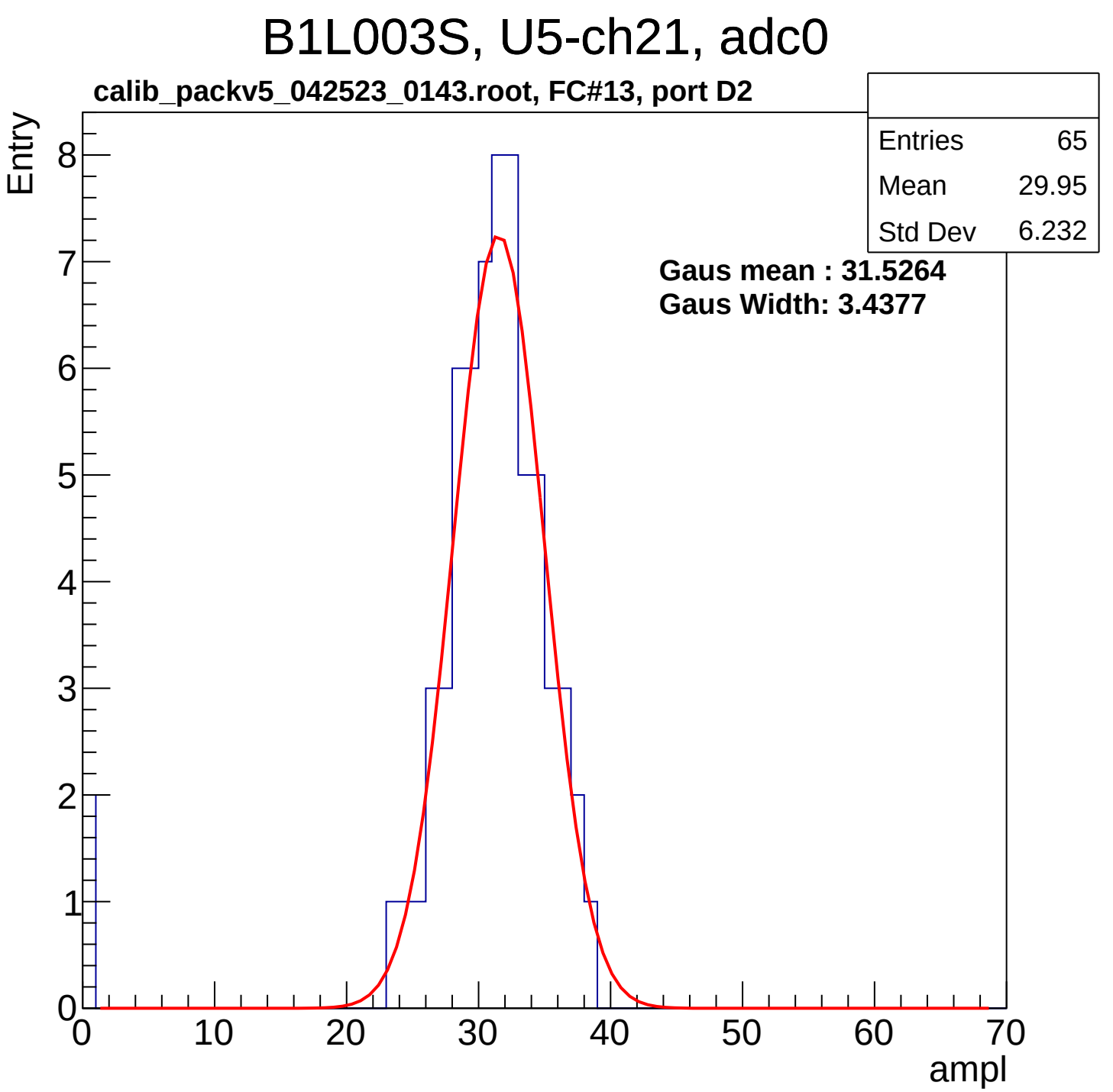
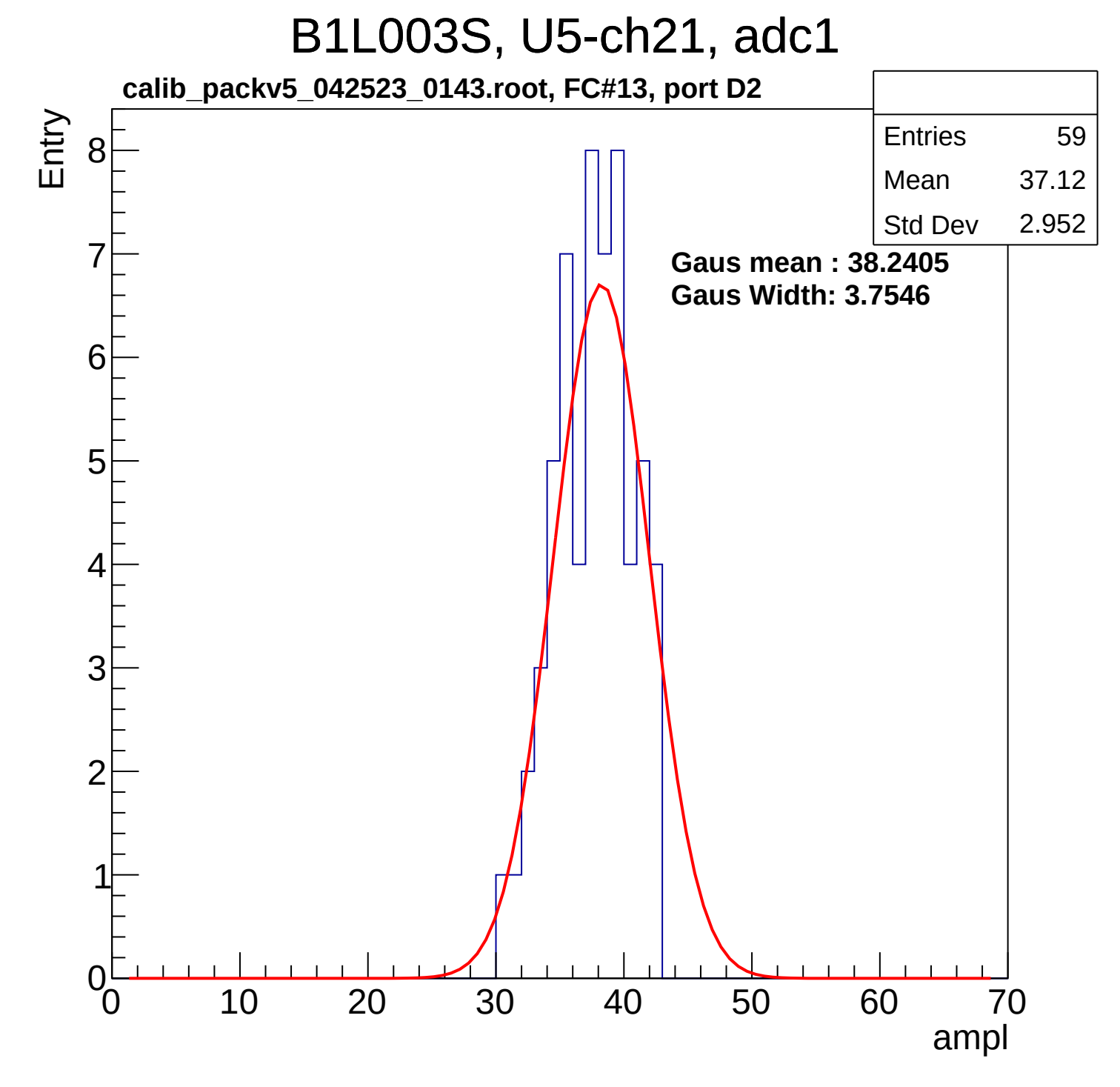
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch21, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.95
Std Dev 6.232
Gaus mean : 31.5264
Gaus Width: 3.4377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch21, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.12
Std Dev 2.952
Gaus mean : 38.2405
Gaus Width: 3.7546
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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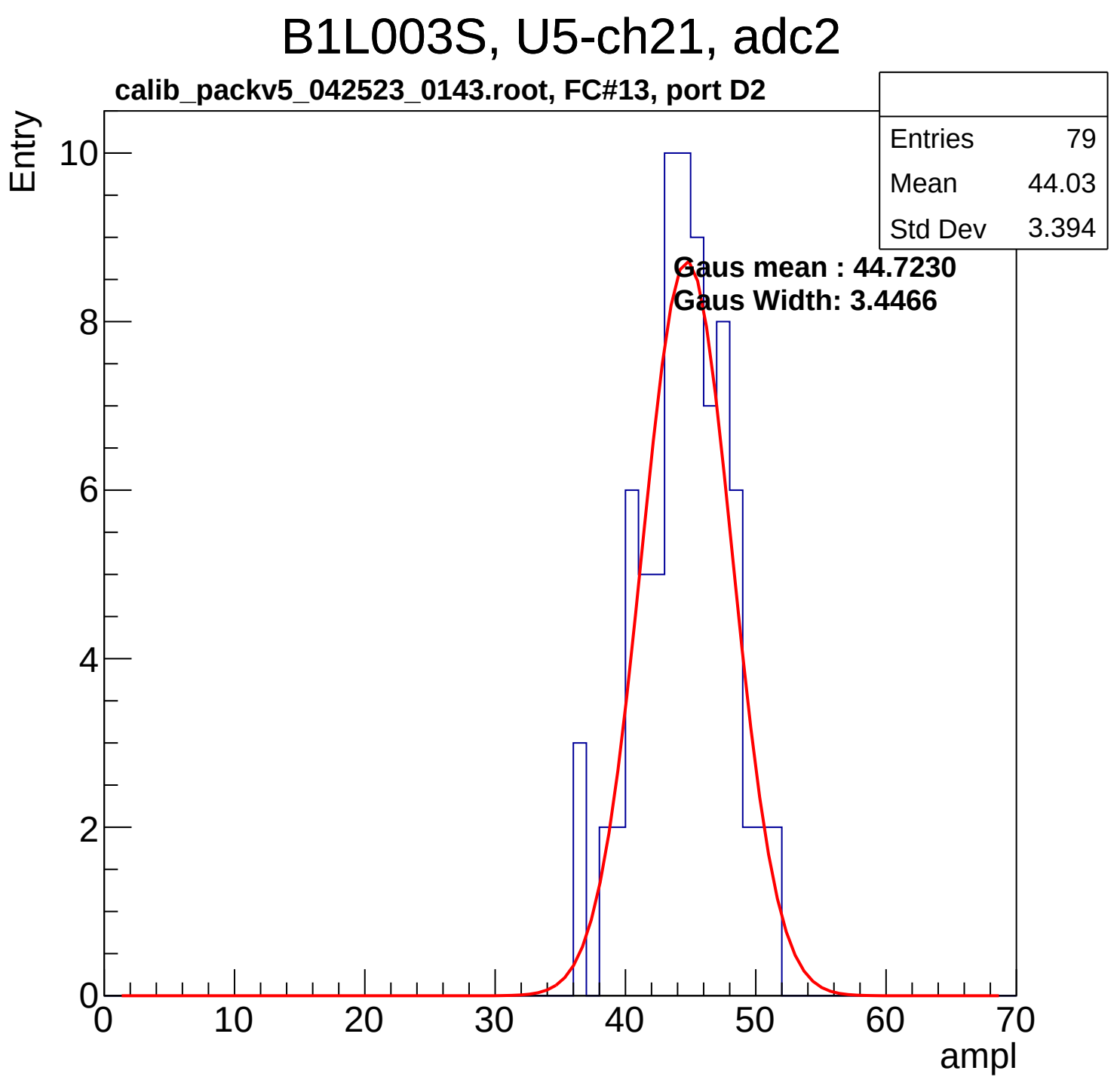
B1L003S, U5-ch21, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 44.03
Std Dev 3.394
Gaus mean : 44.7230
Gaus Width: 3.4466
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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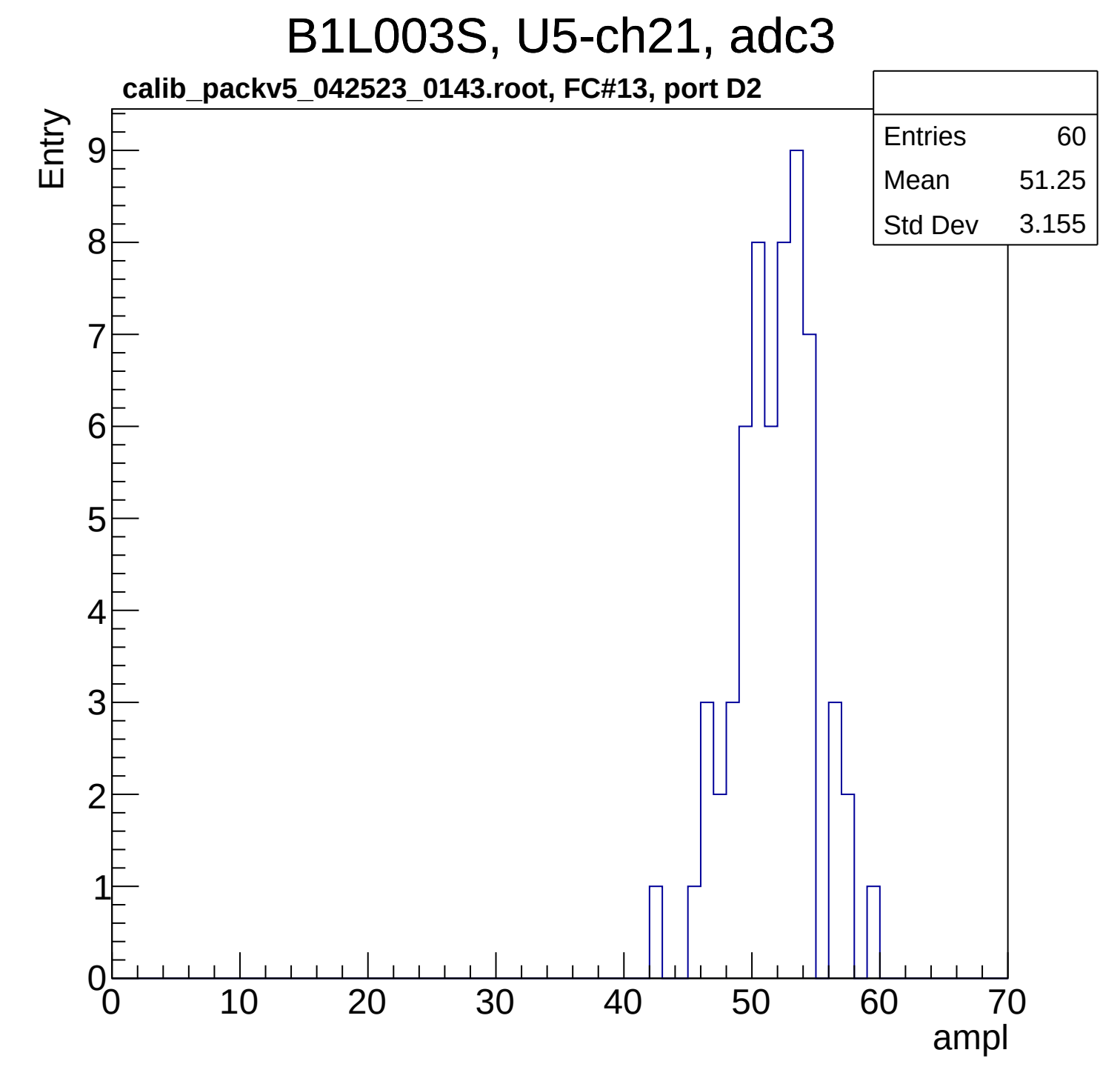
B1L003S, U5-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 51.25
Std Dev 3.155
Entry
ampl
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3
4
5
6
7
8
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0
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60
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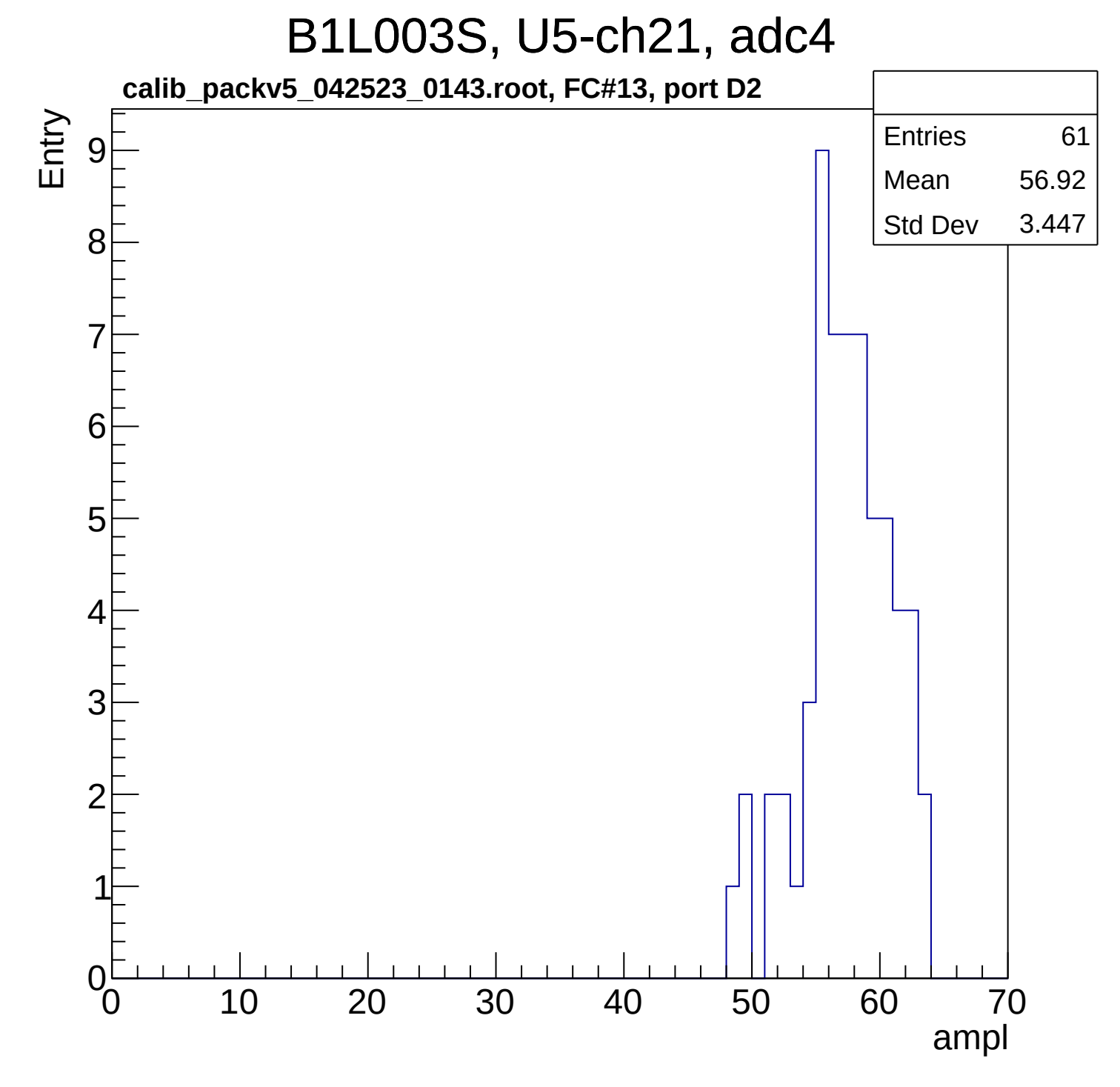
B1L003S, U5-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 56.92
Std Dev 3.447
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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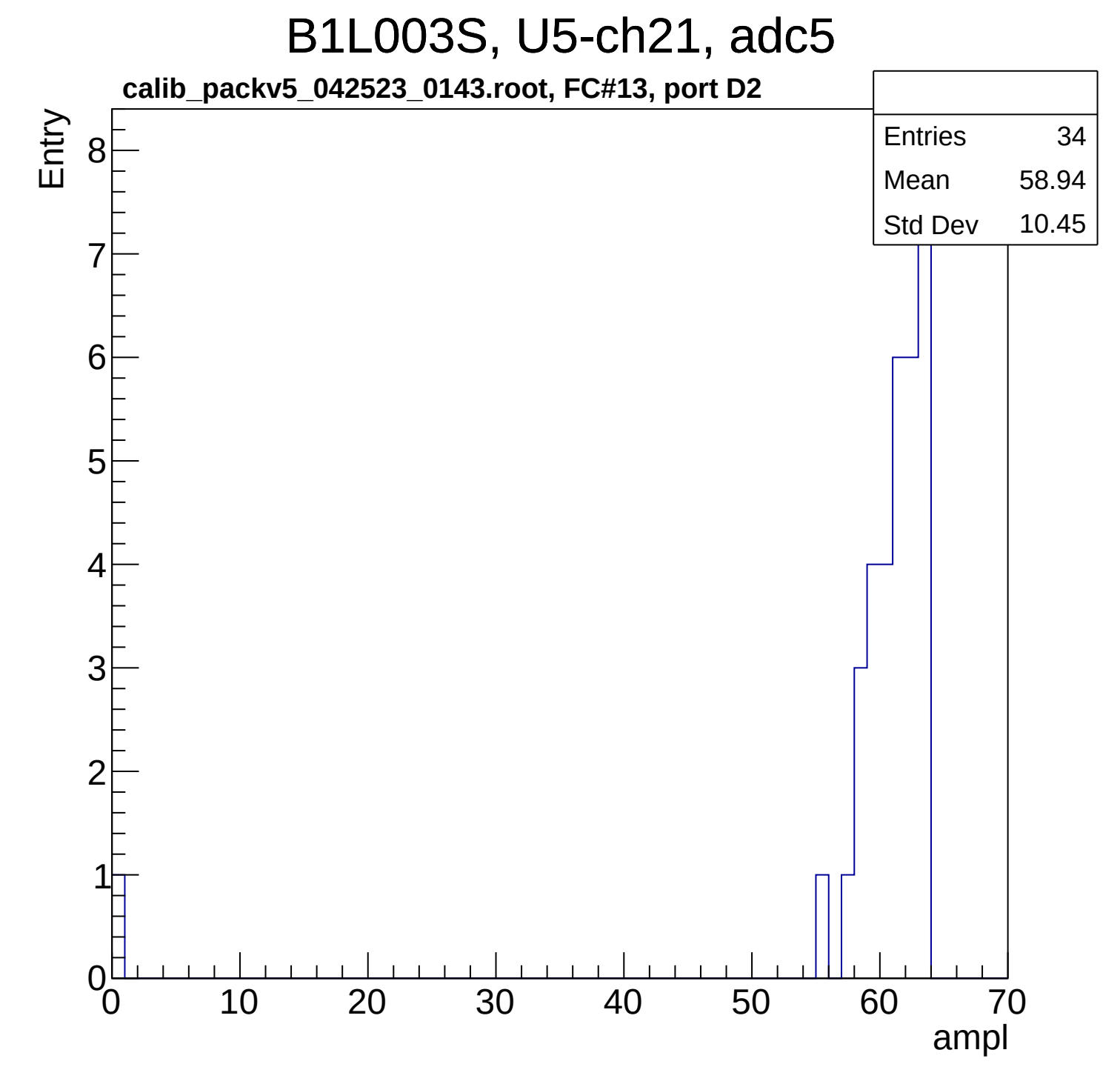
B1L003S, U5-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 58.94
Std Dev 10.45
Entry
ampl
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0
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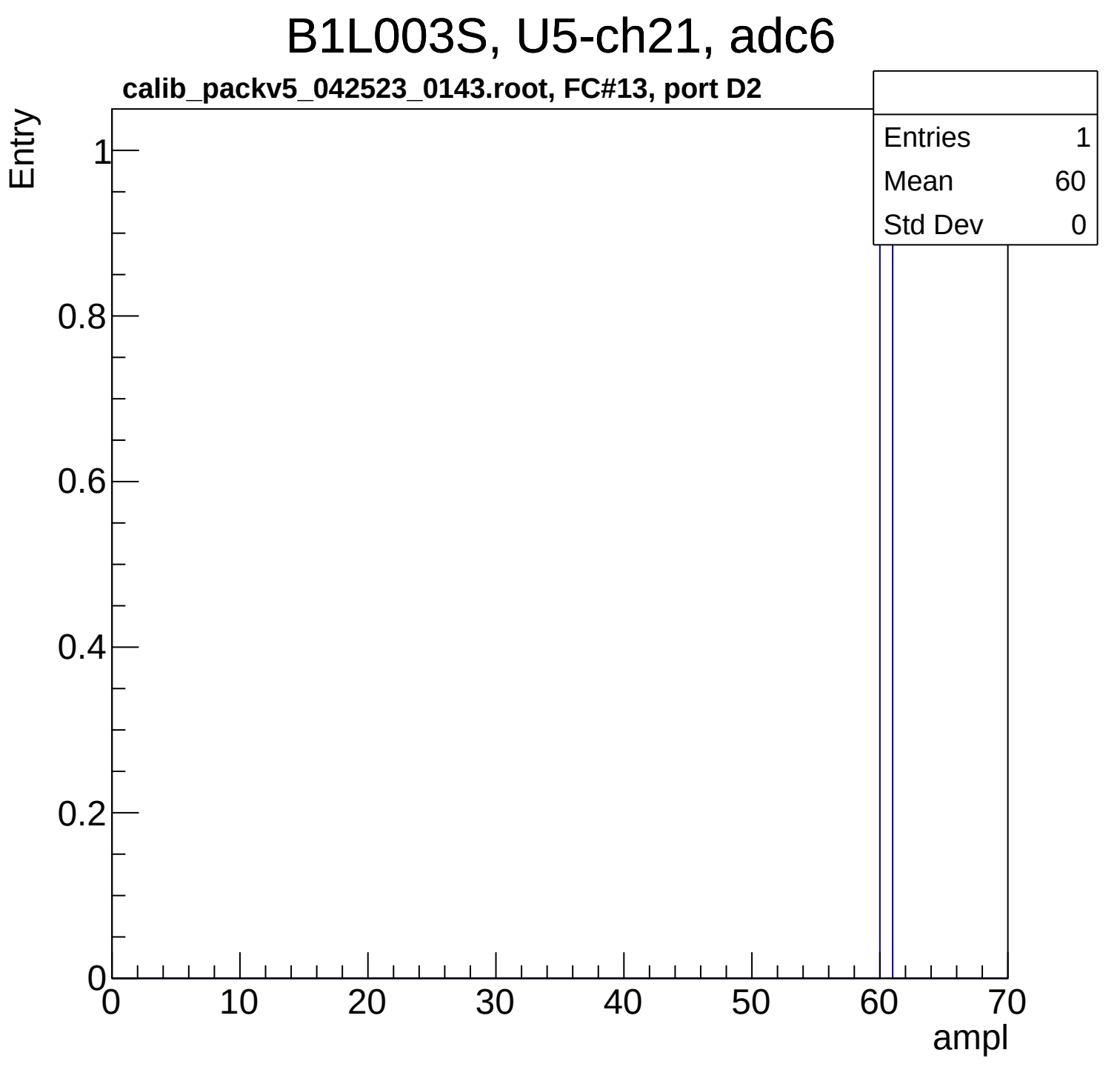
B1L003S, U5-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
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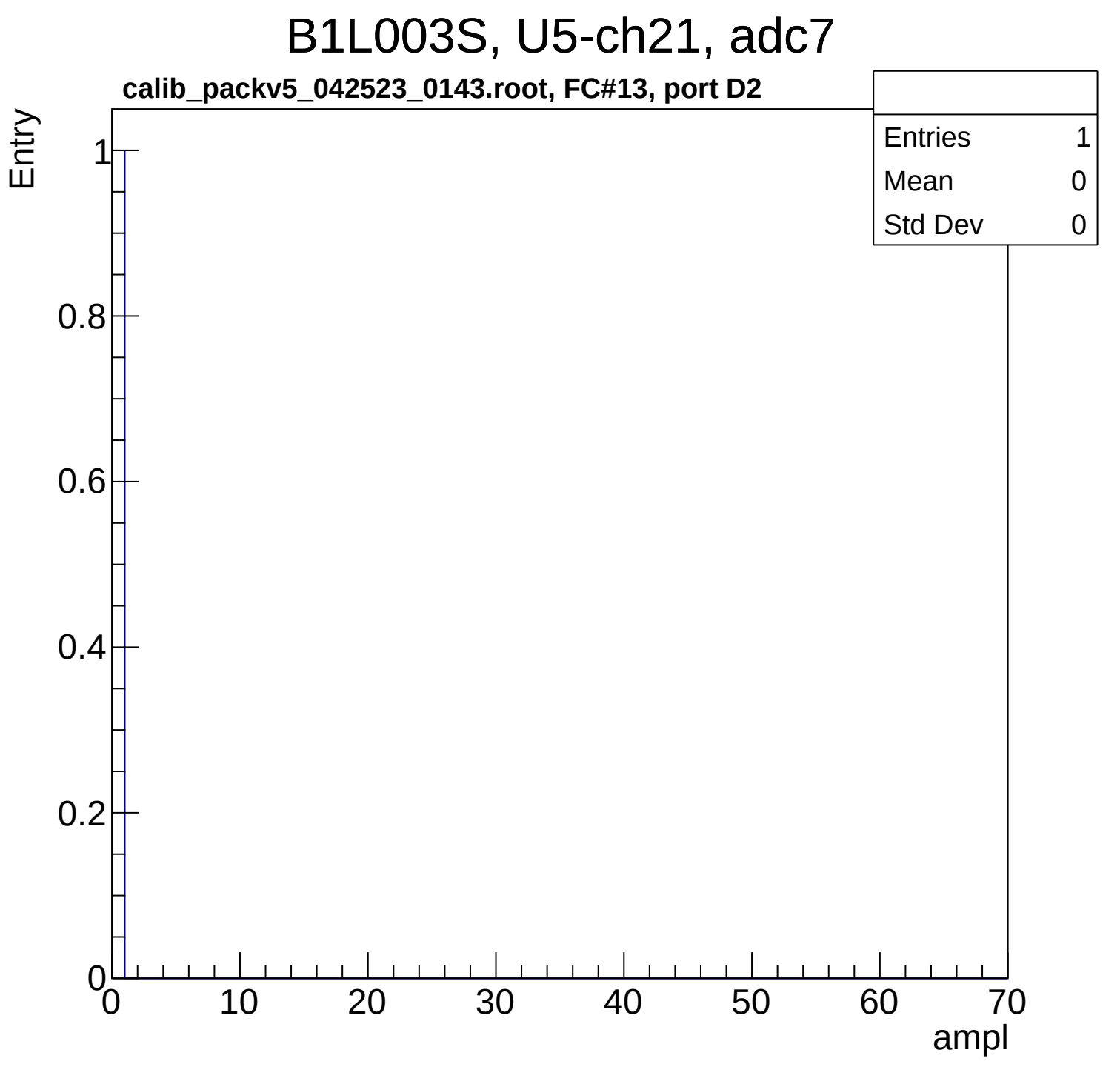
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch21, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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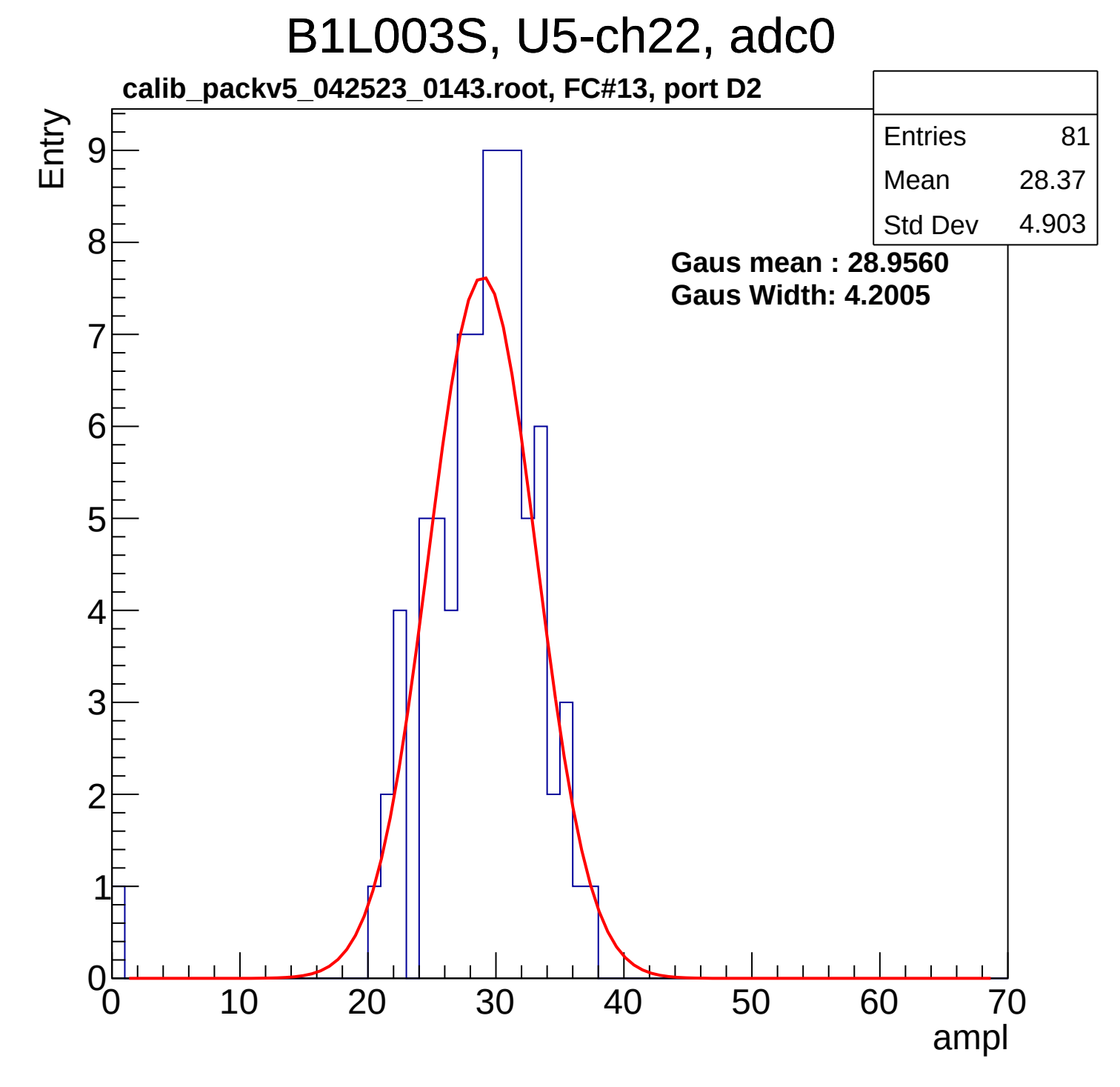
50
60
70
ampl


B1L003S, U5-ch22, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.37
Std Dev 4.903
Gaus mean : 28.9560
Gaus Width: 4.2005
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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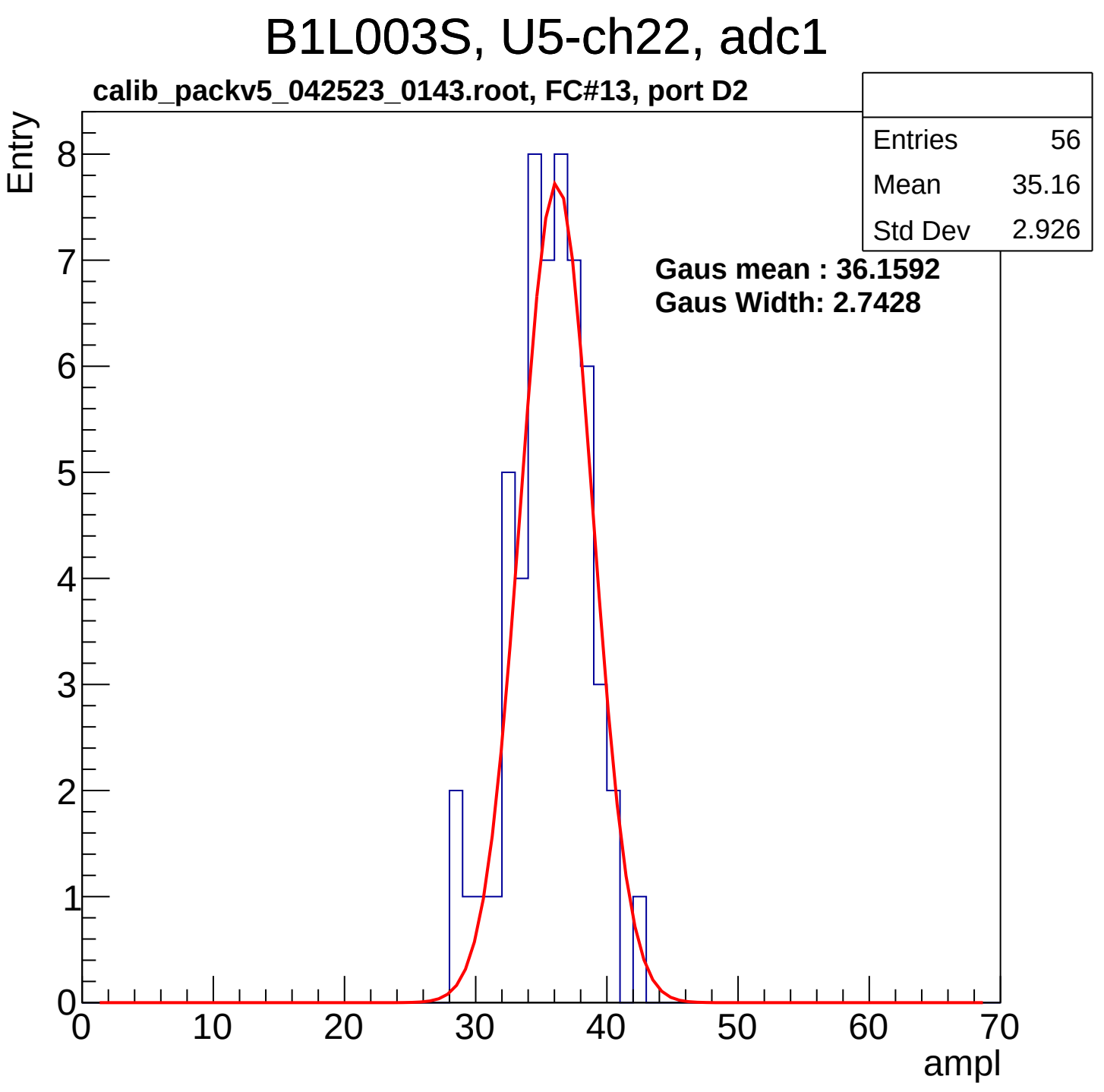
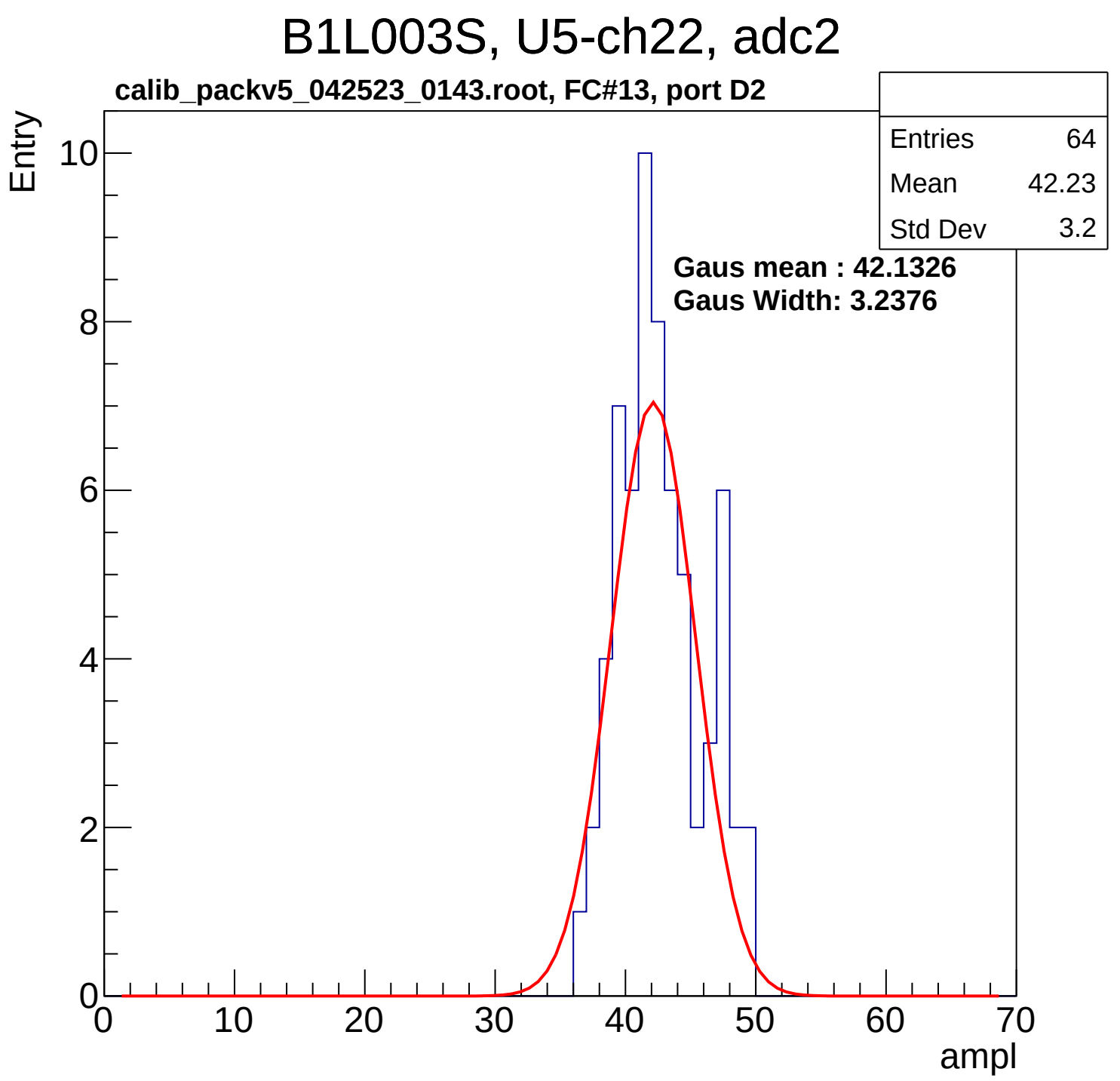

B1L003S, U5-ch22, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 35.16
Std Dev 2.926
Gaus mean : 36.1592
Gaus Width: 2.7428
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch22, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.23
Std Dev 3.2
Gaus mean : 42.1326
Gaus Width: 3.2376
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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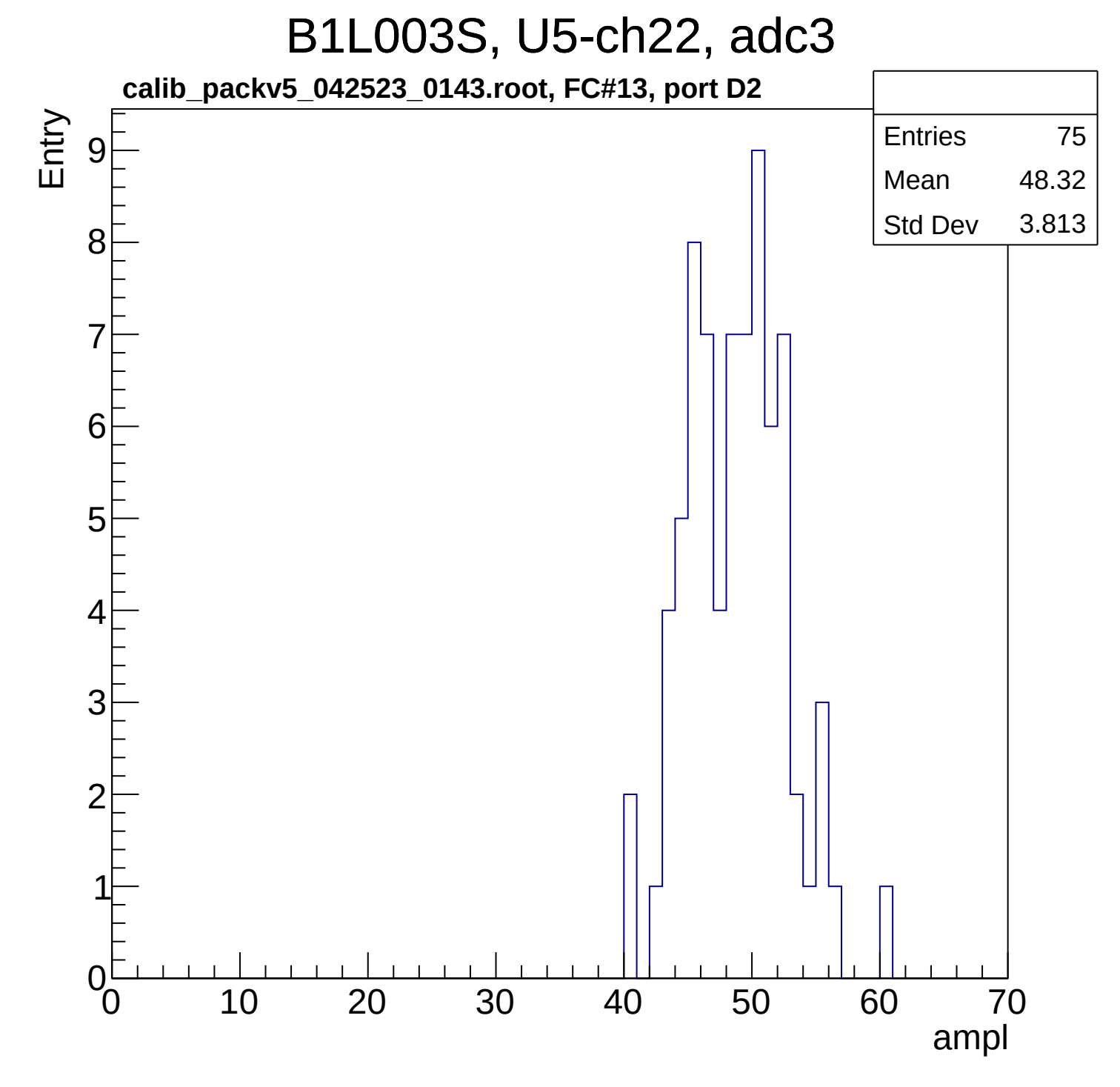
B1L003S, U5-ch22, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 48.32
Std Dev 3.813
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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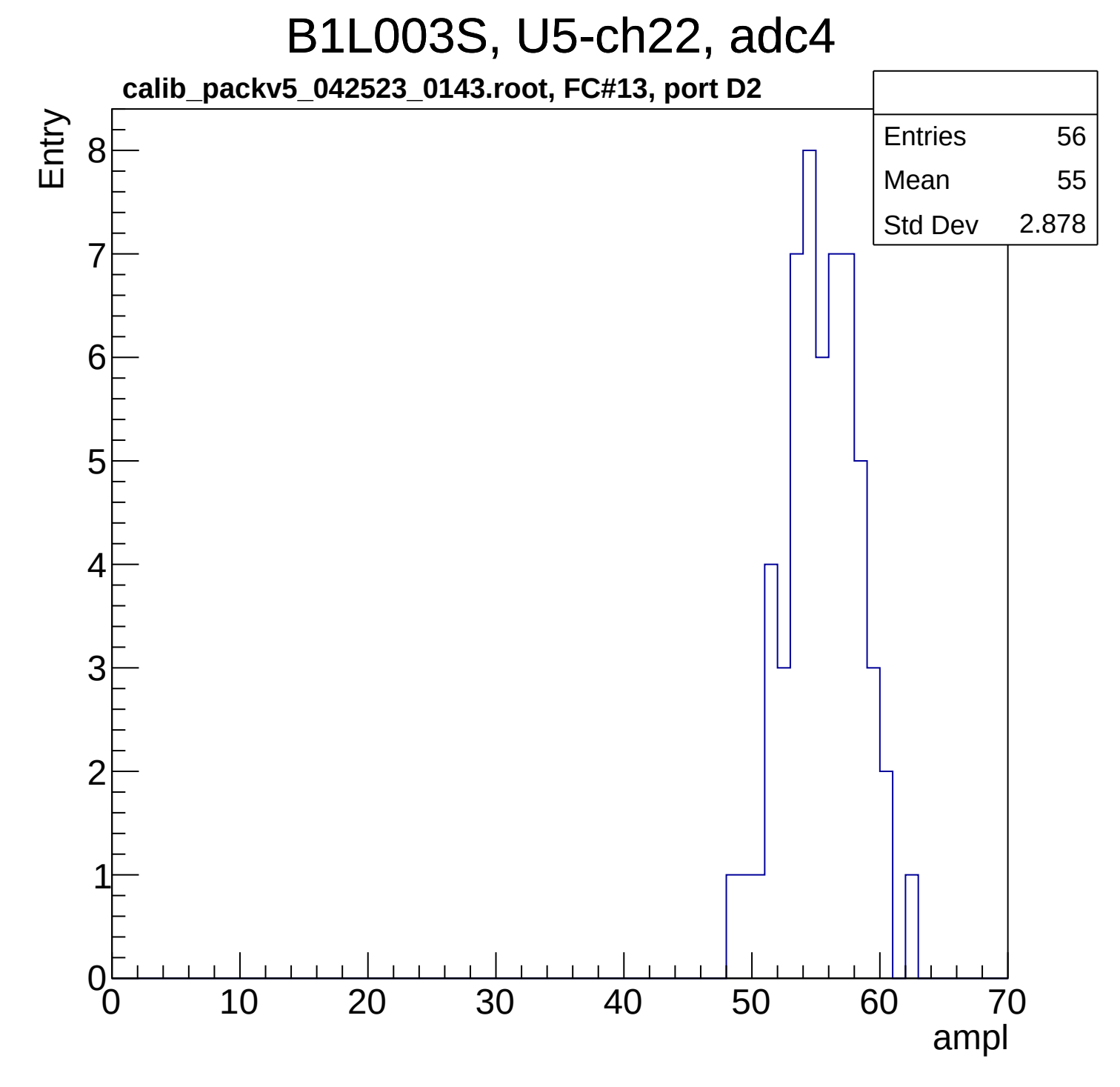
B1L003S, U5-ch22, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55
Std Dev 2.878
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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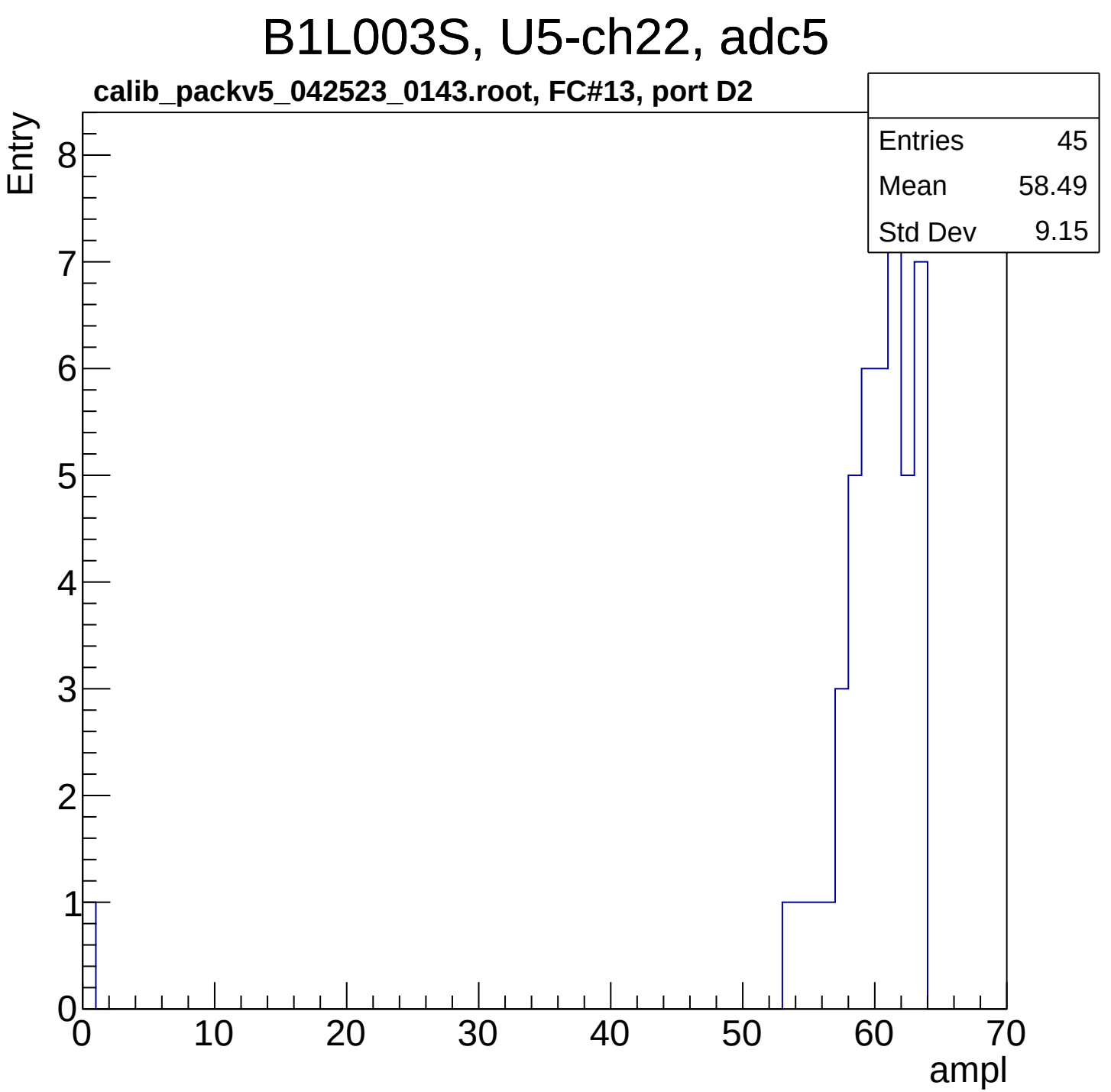
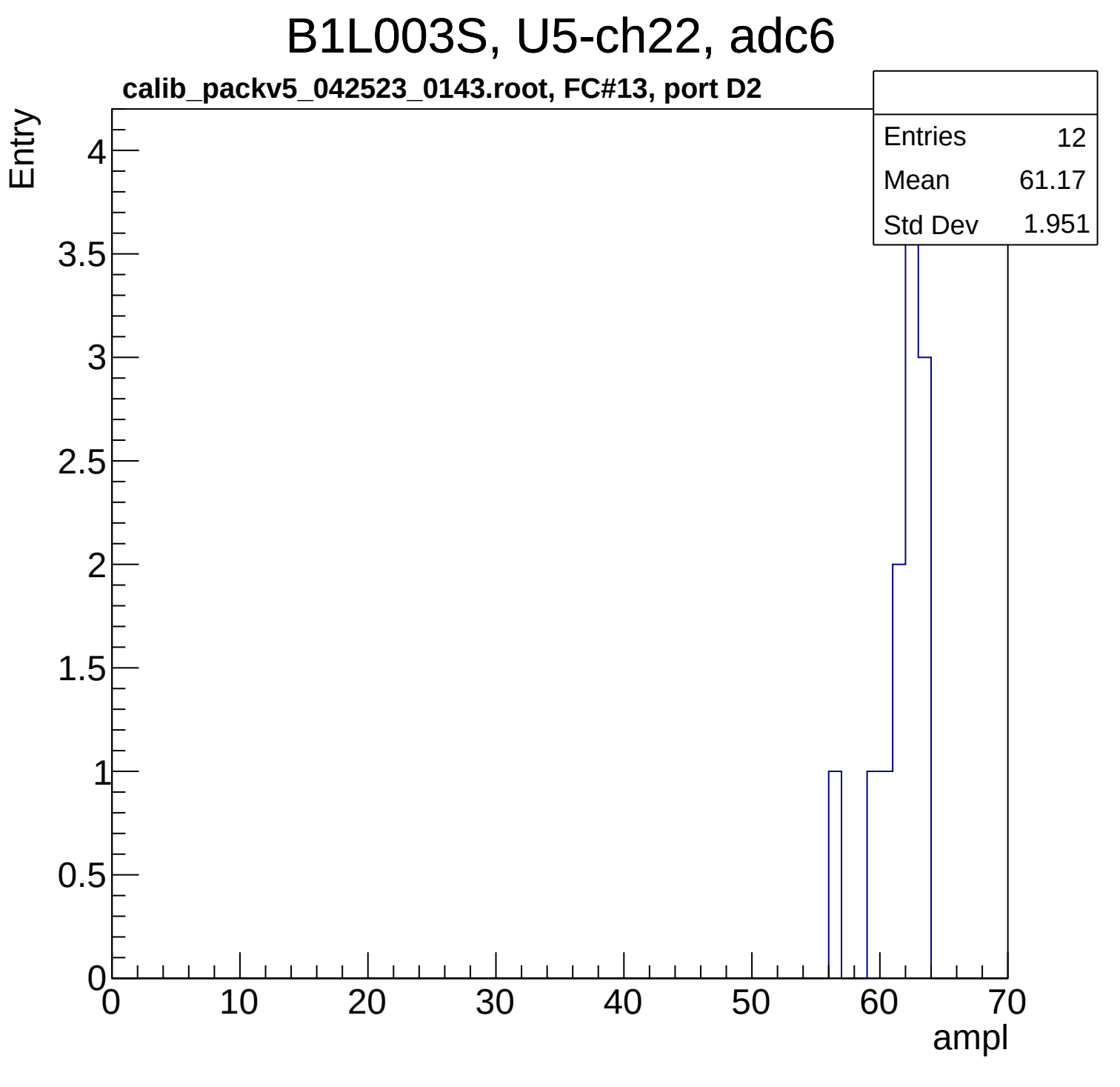

B1L003S, U5-ch22, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.49
Std Dev 9.15
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch22, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.17
Std Dev 1.951
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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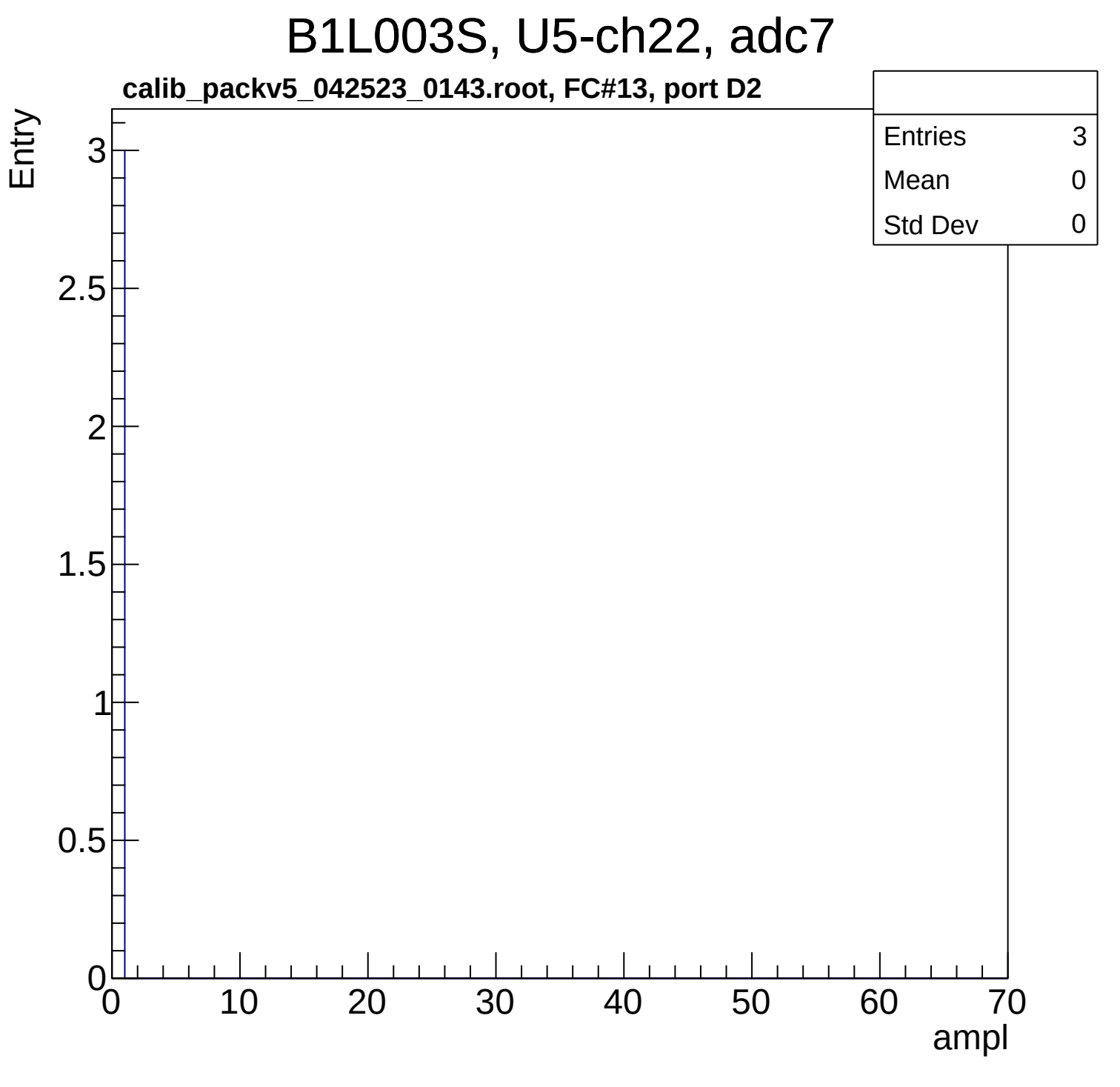
B1L003S, U5-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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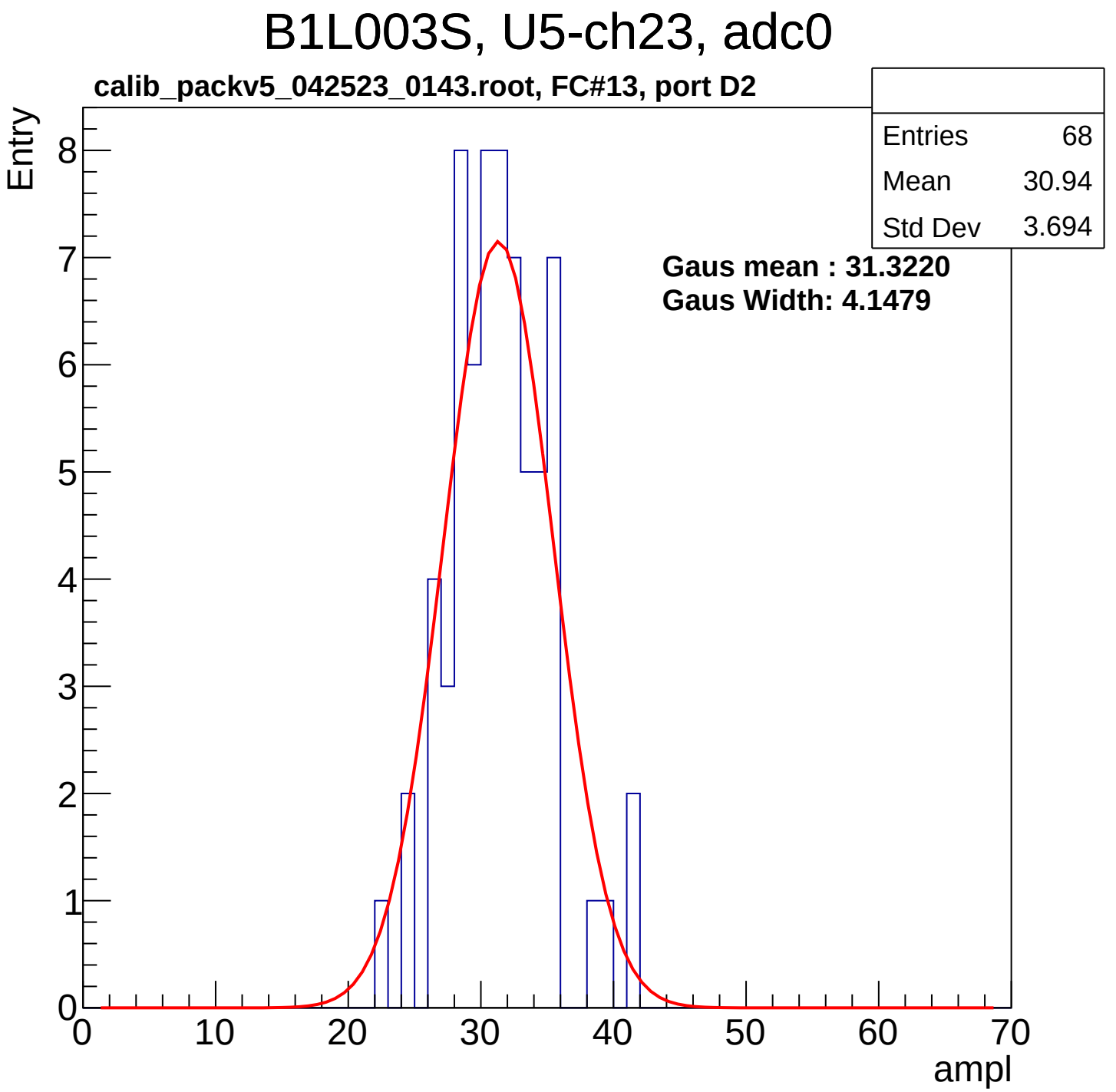
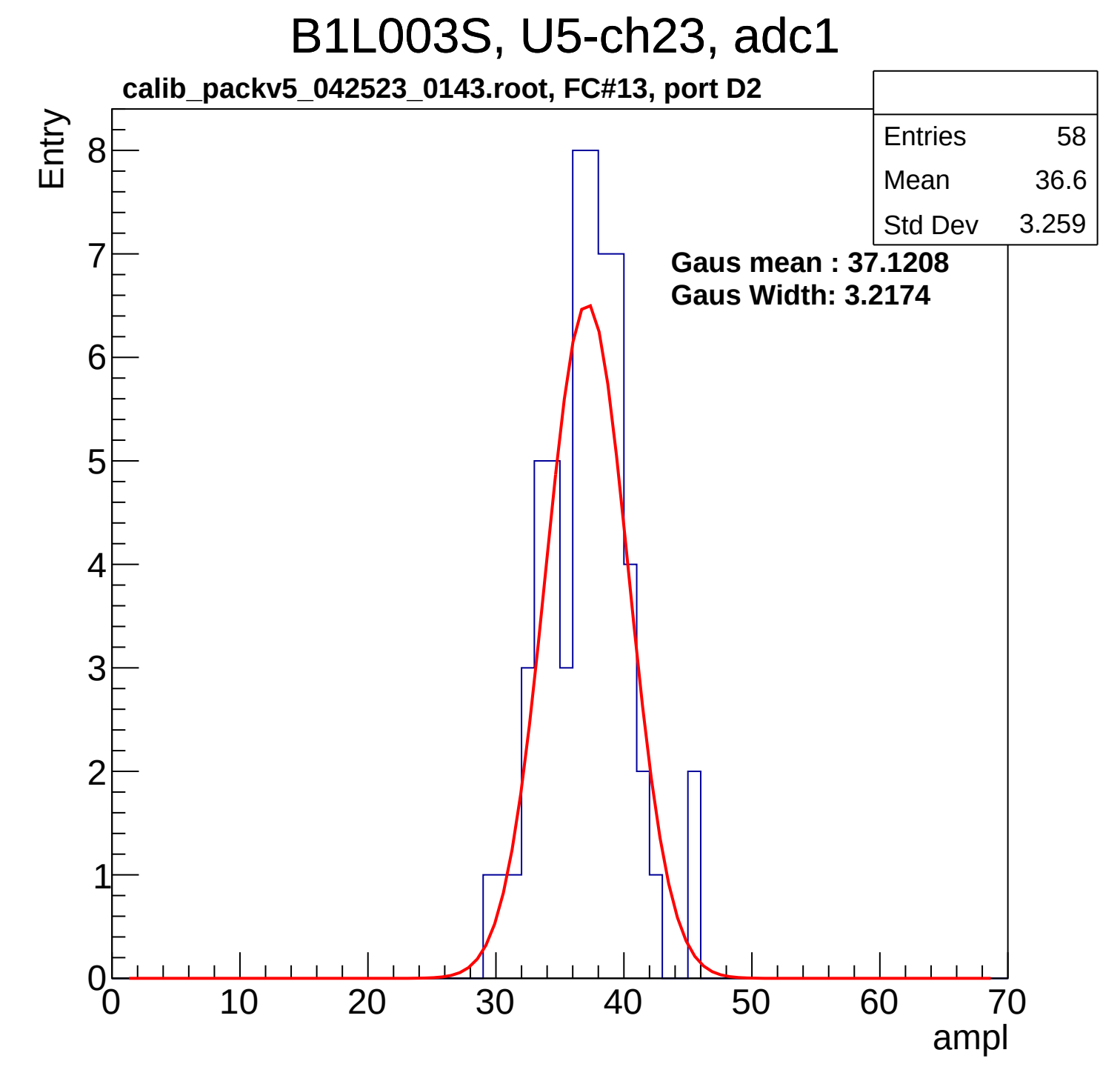
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch23, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.94
Std Dev 3.694
Gaus mean : 31.3220
Gaus Width: 4.1479
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch23, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.6
Std Dev 3.259
Gaus mean : 37.1208
Gaus Width: 3.2174
Entry
ampl
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1
2
3
4
5
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7
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0
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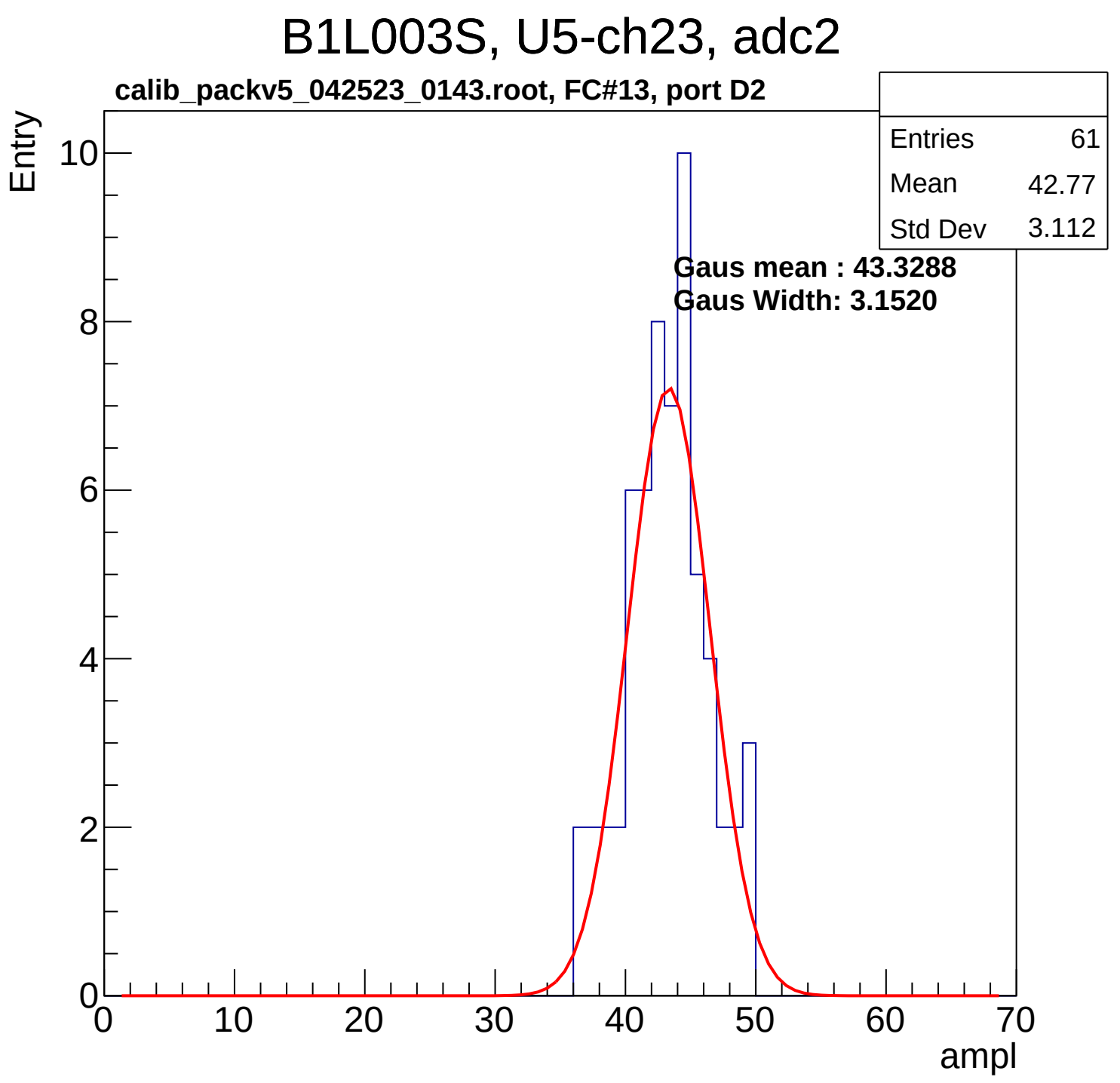
B1L003S, U5-ch23, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.77
Std Dev 3.112
Gaus mean : 43.3288
Gaus Width: 3.1520
Entry
ampl
0
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4
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8
10
0
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60
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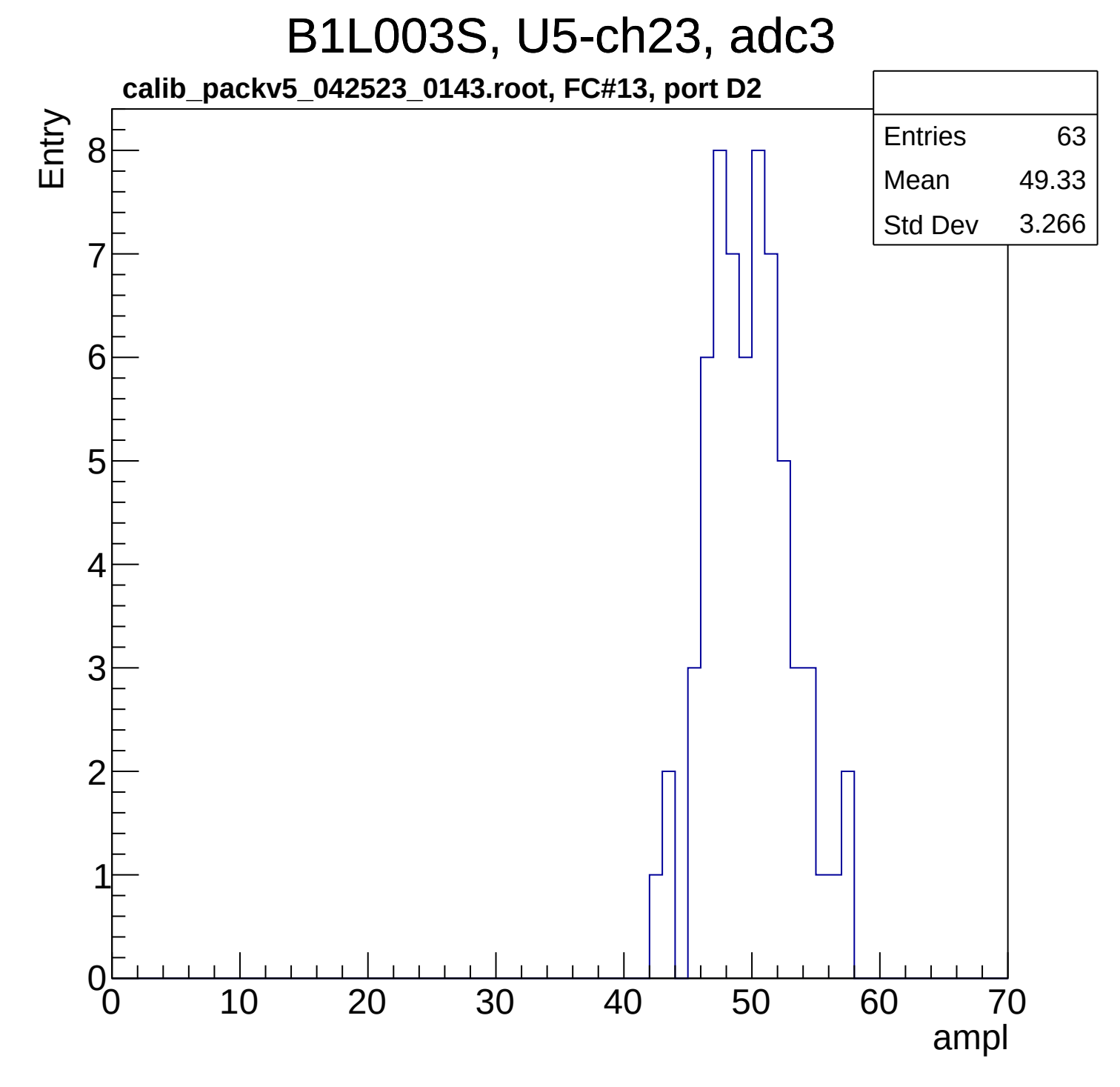
B1L003S, U5-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.33
Std Dev 3.266
Entry
ampl
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1
2
3
4
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0
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40
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60
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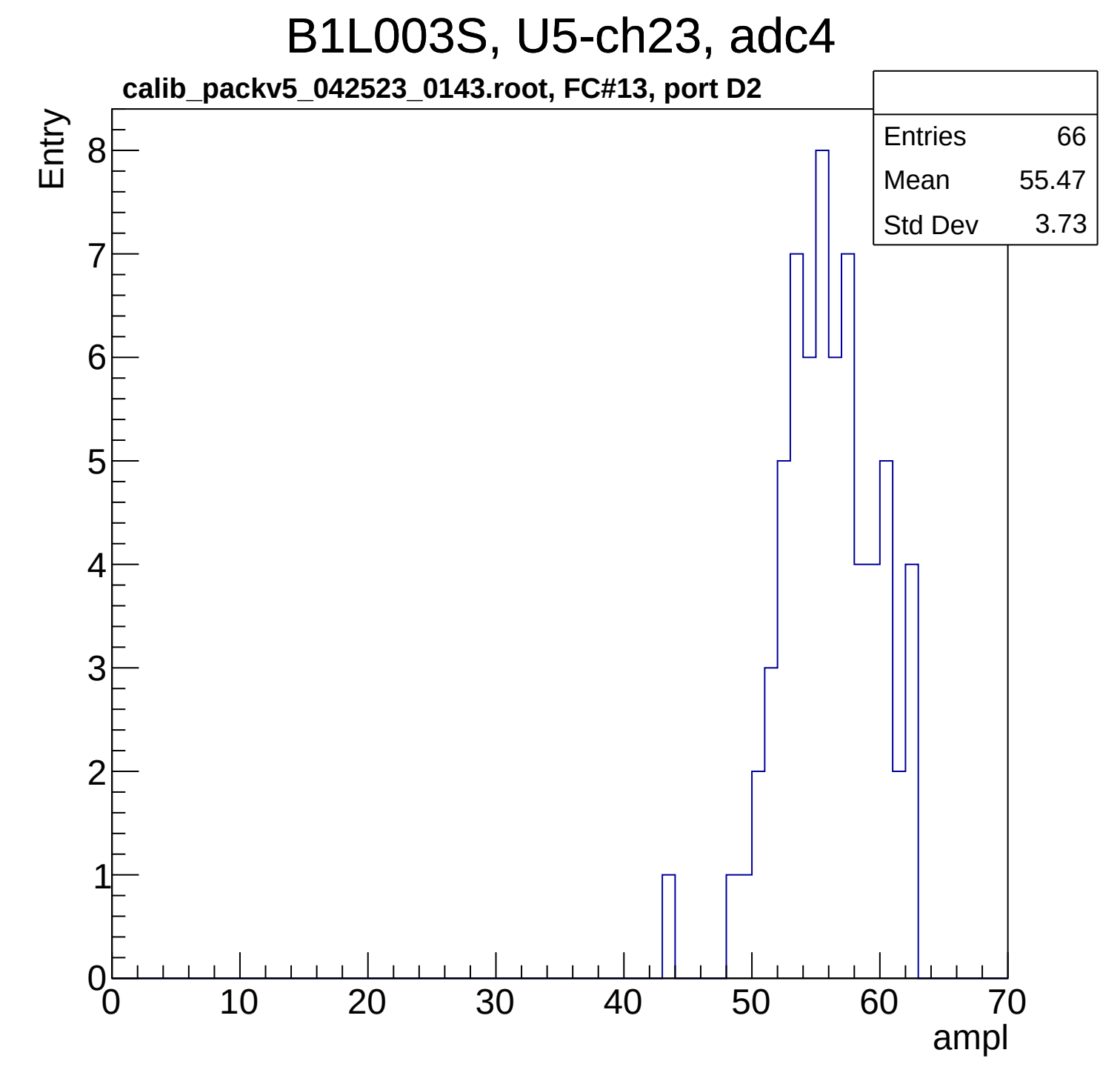
B1L003S, U5-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.47
Std Dev 3.73
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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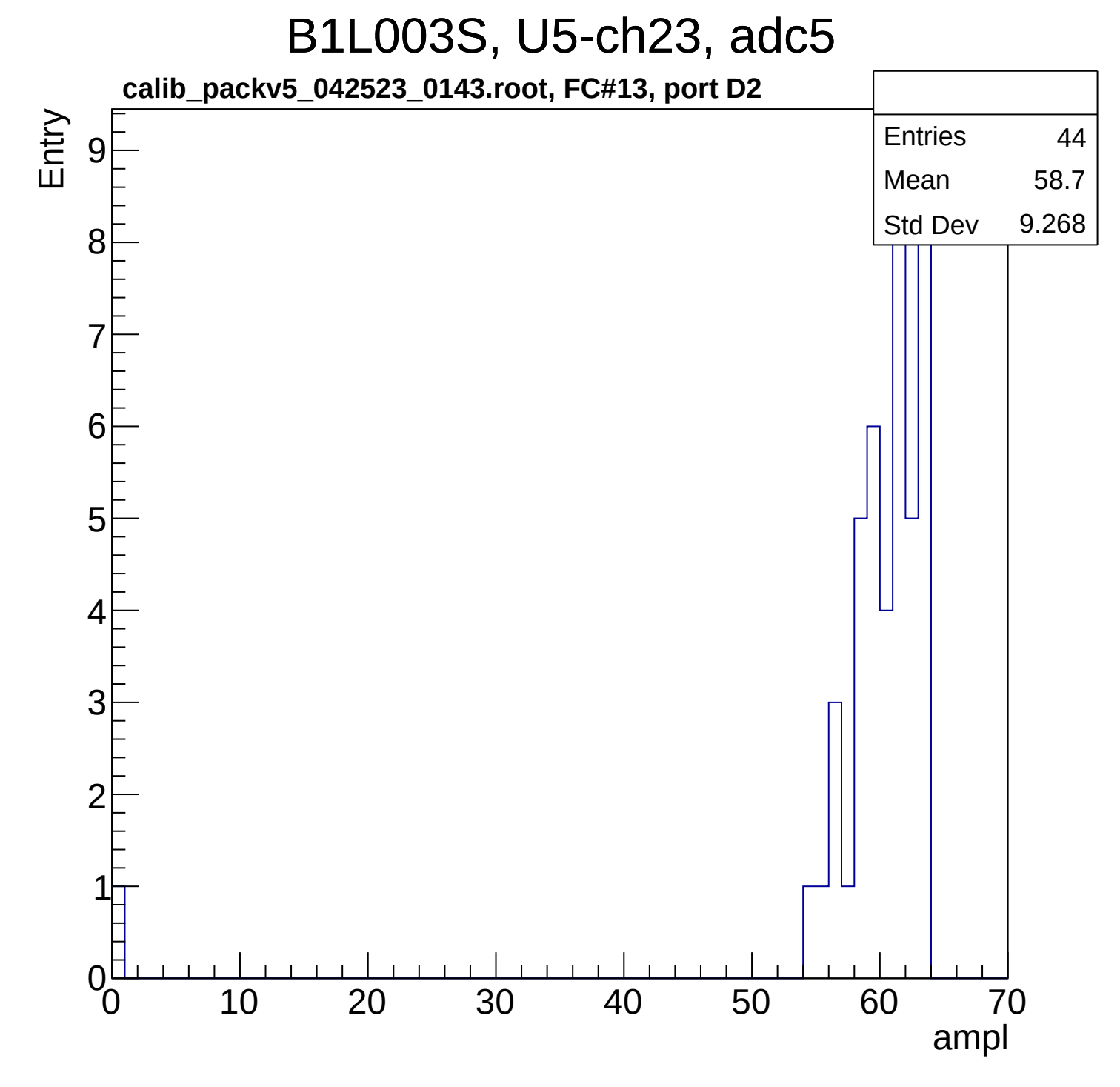
B1L003S, U5-ch23, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.7
Std Dev 9.268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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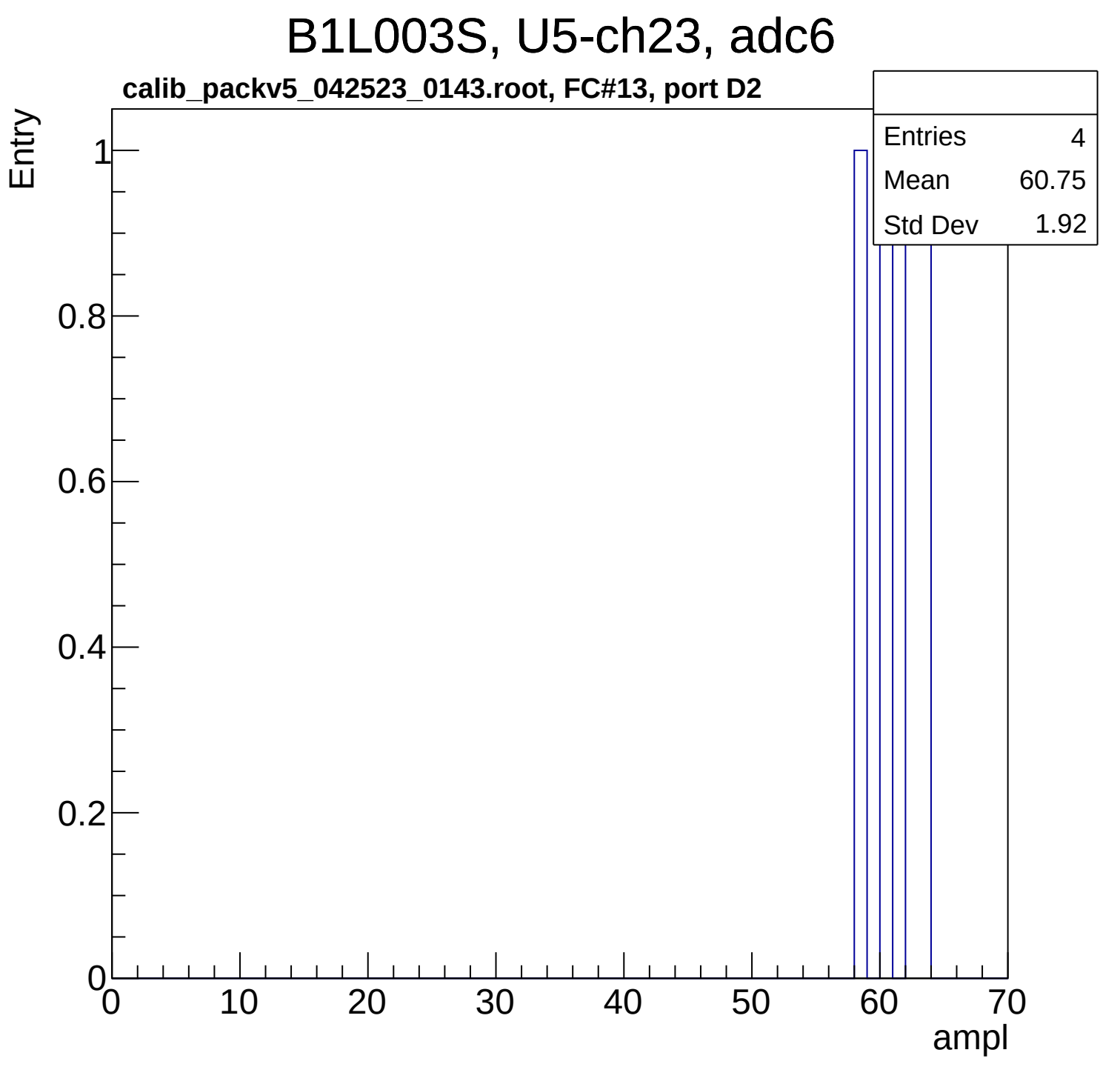
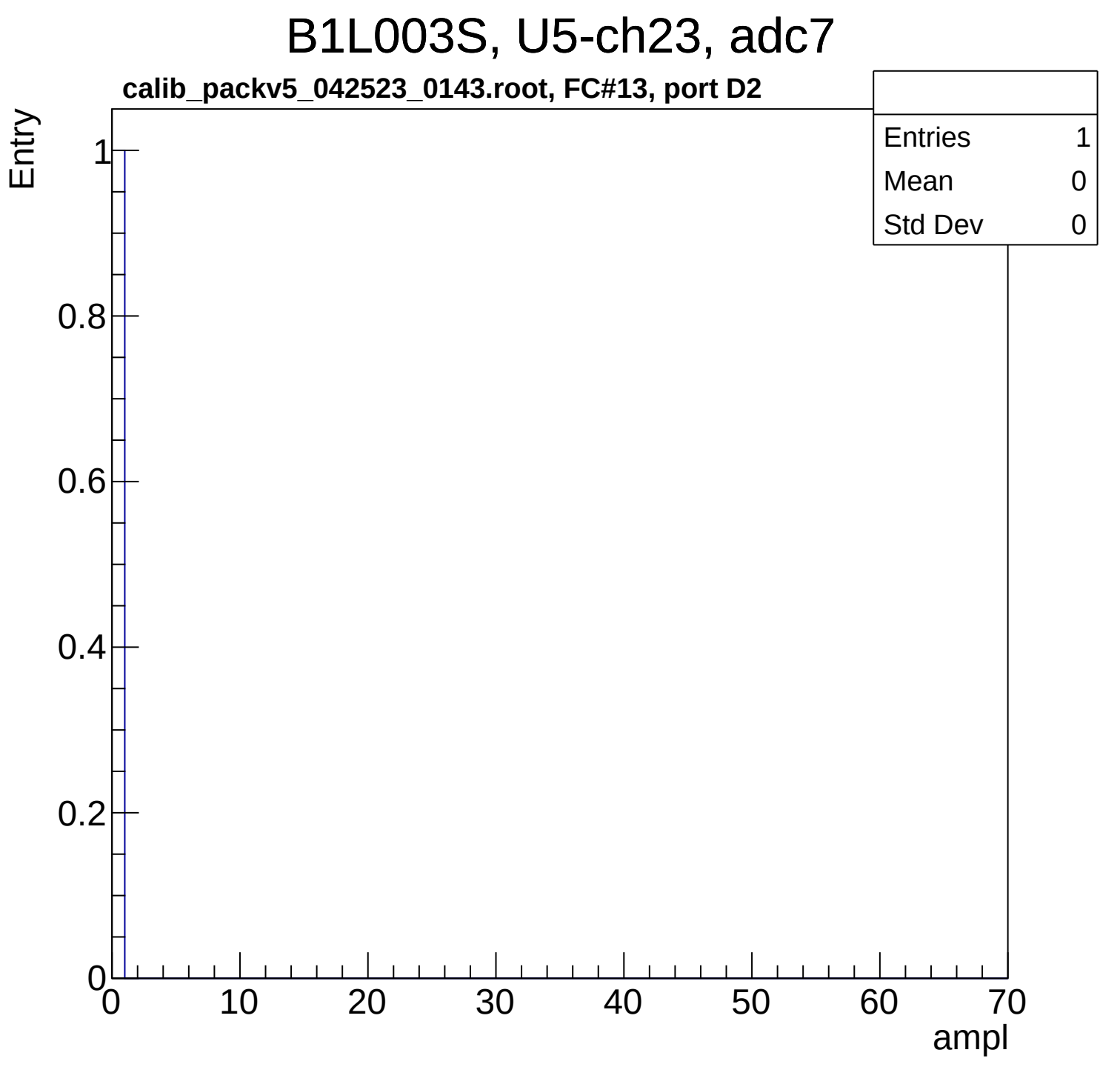
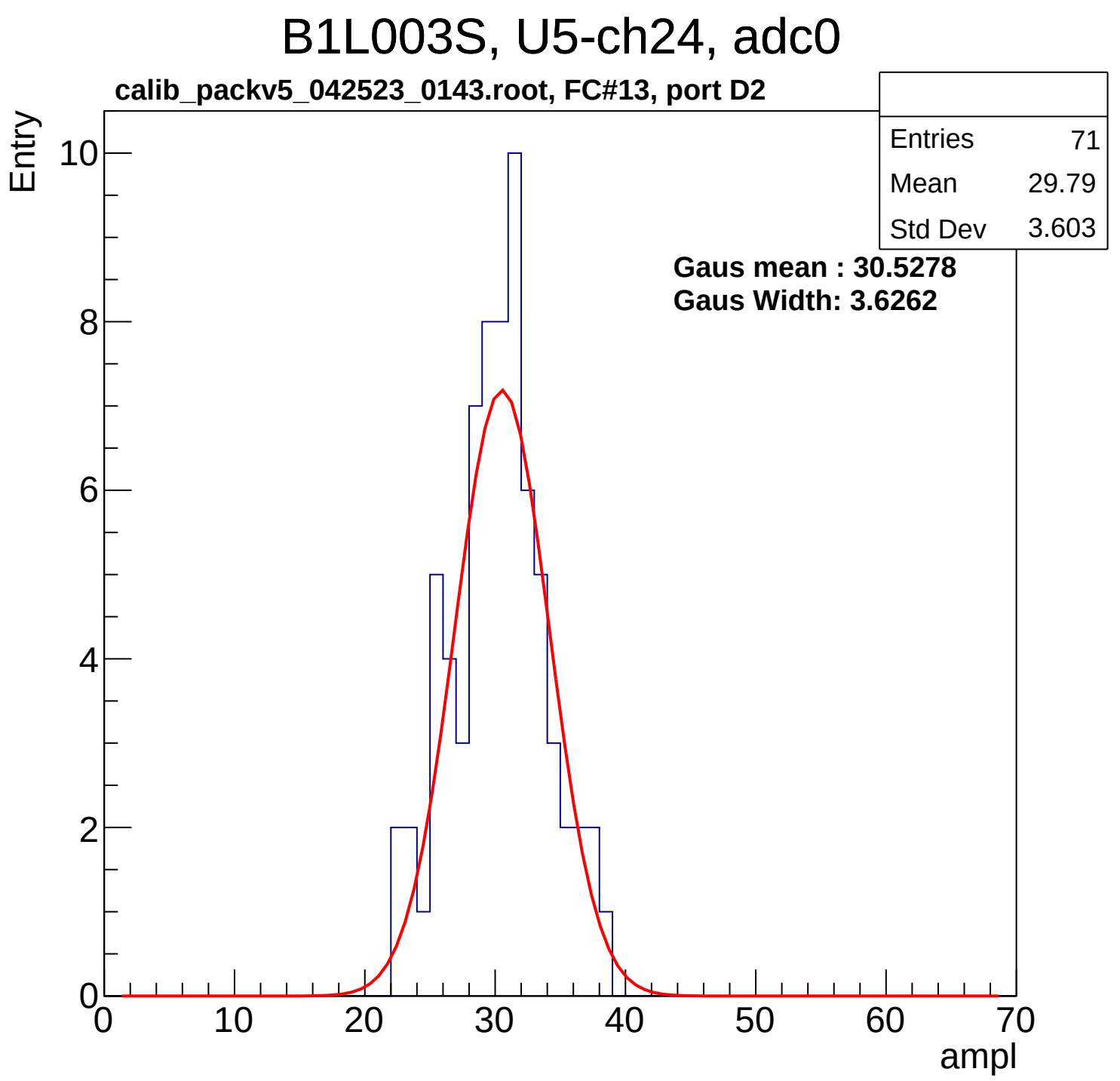
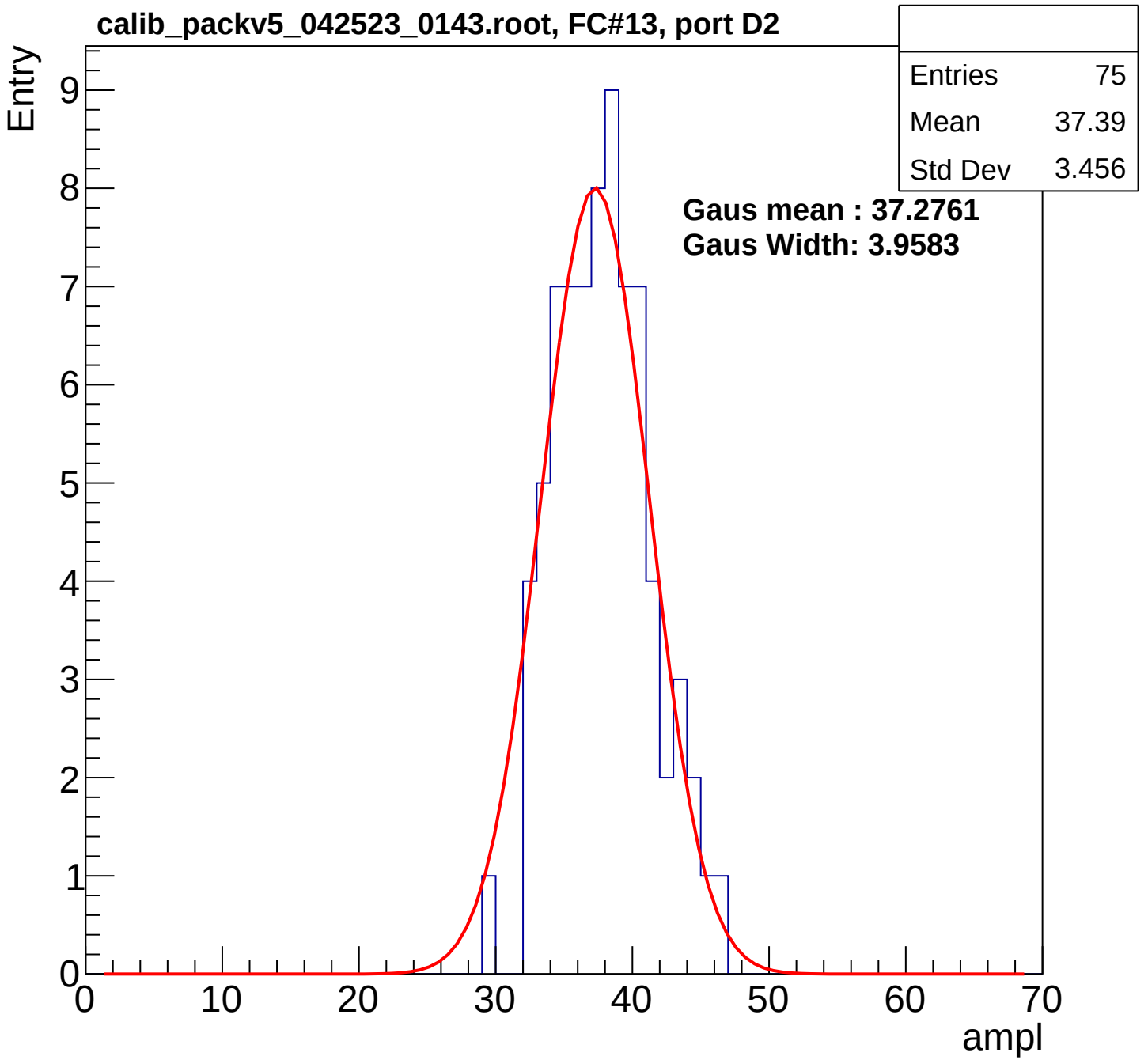
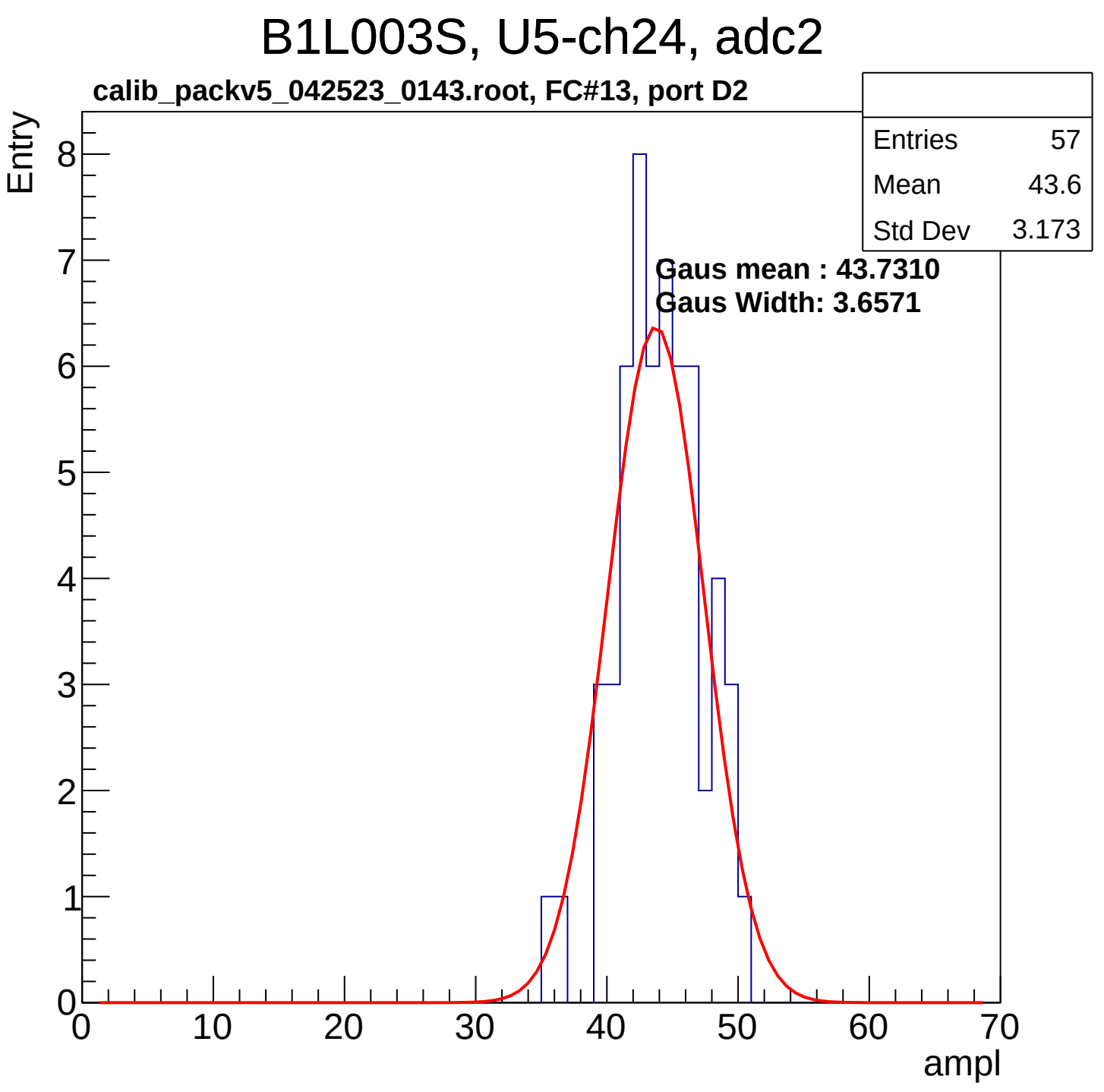
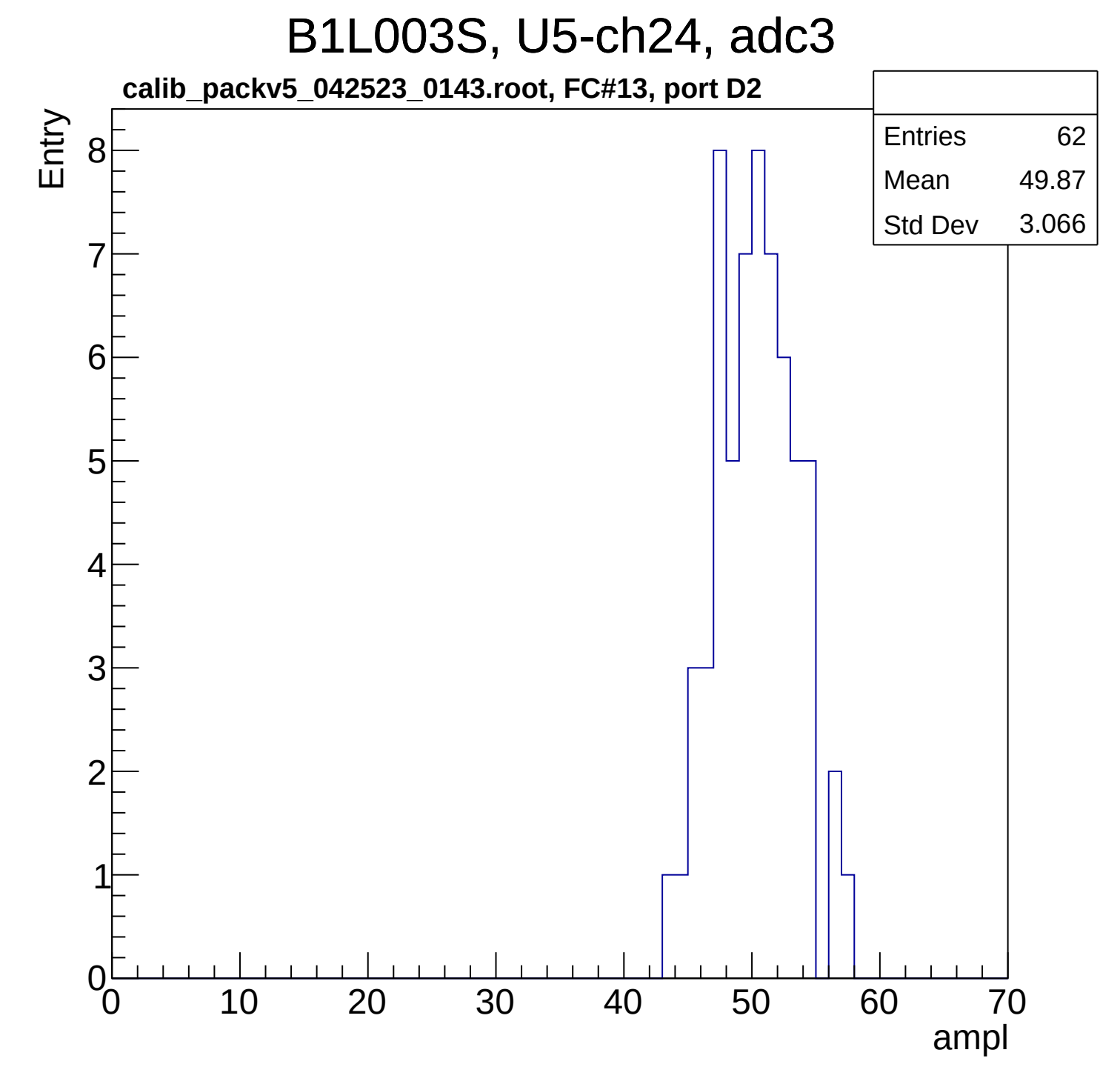

B1L003S, U5-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 60.75
Std Dev 1.92
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

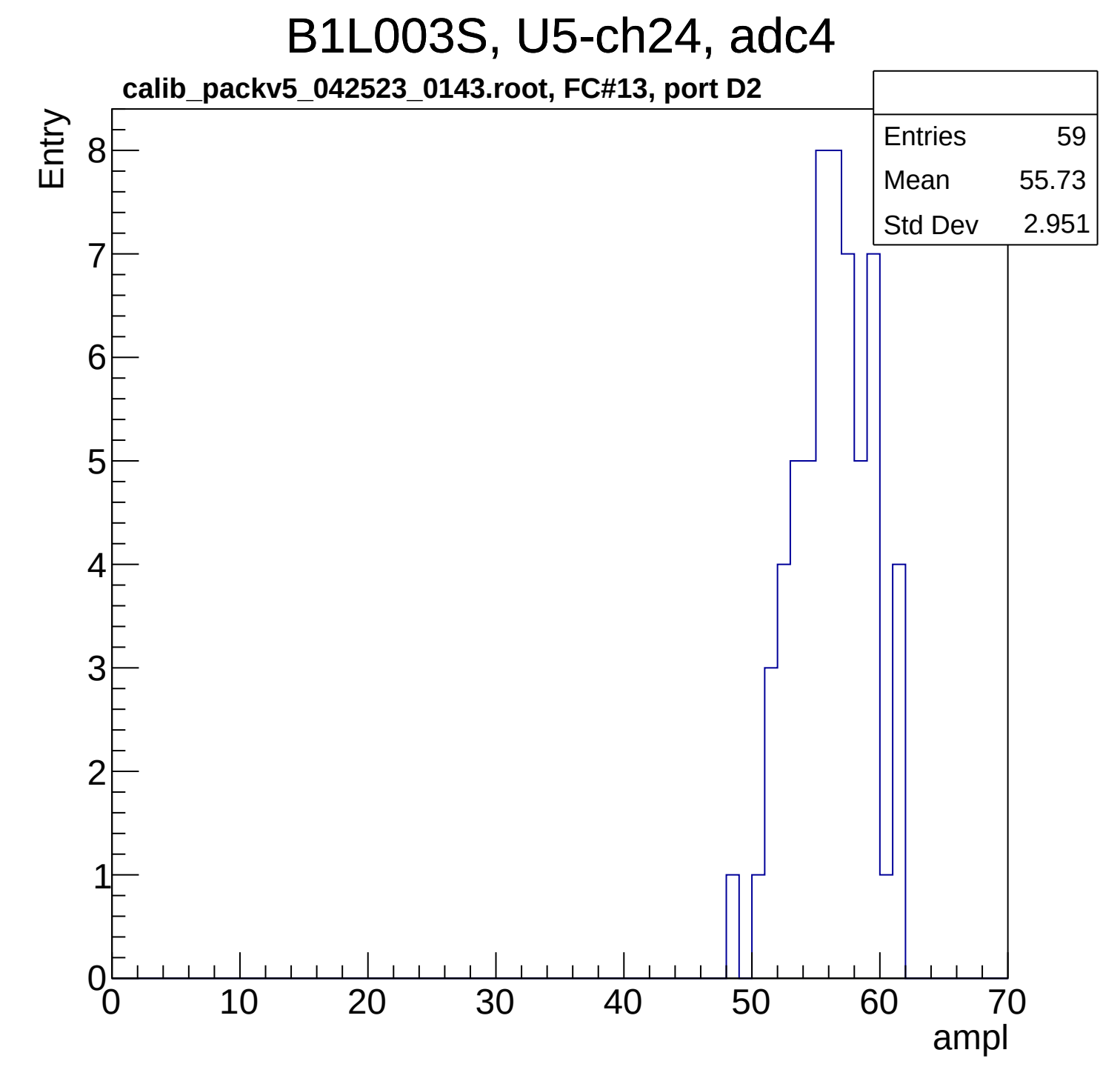

B1L003S, U5-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch24, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.79
Std Dev 3.603
Gaus mean : 30.5278
Gaus Width: 3.6262
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.39
Std Dev 3.456
Gaus mean : 37.2761
Gaus Width: 3.9583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.6
Std Dev 3.173
Gaus mean : 43.7310
Gaus Width: 3.6571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 49.87
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch24, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.73
Std Dev 2.951
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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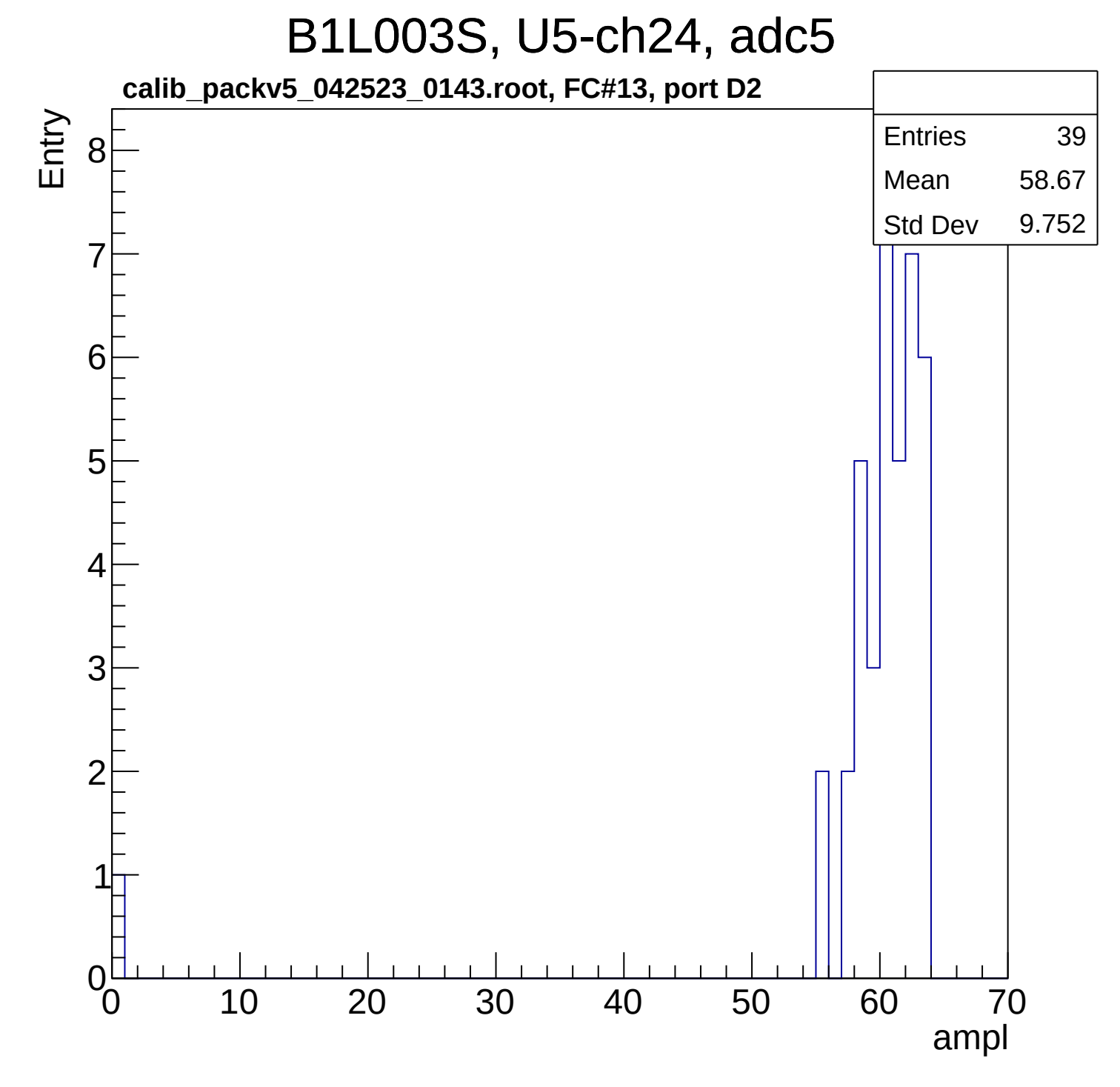
B1L003S, U5-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.67
Std Dev 9.752
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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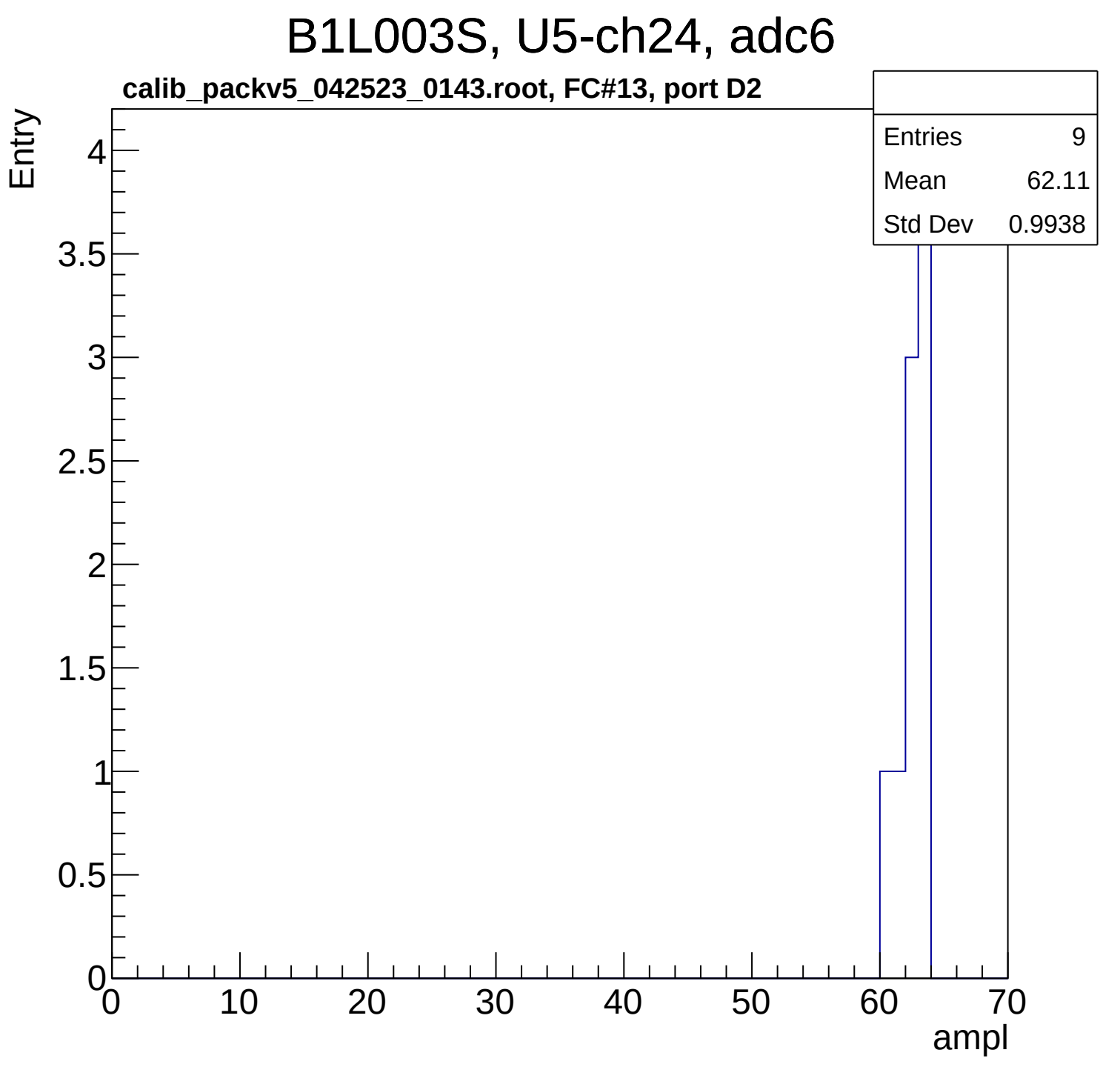
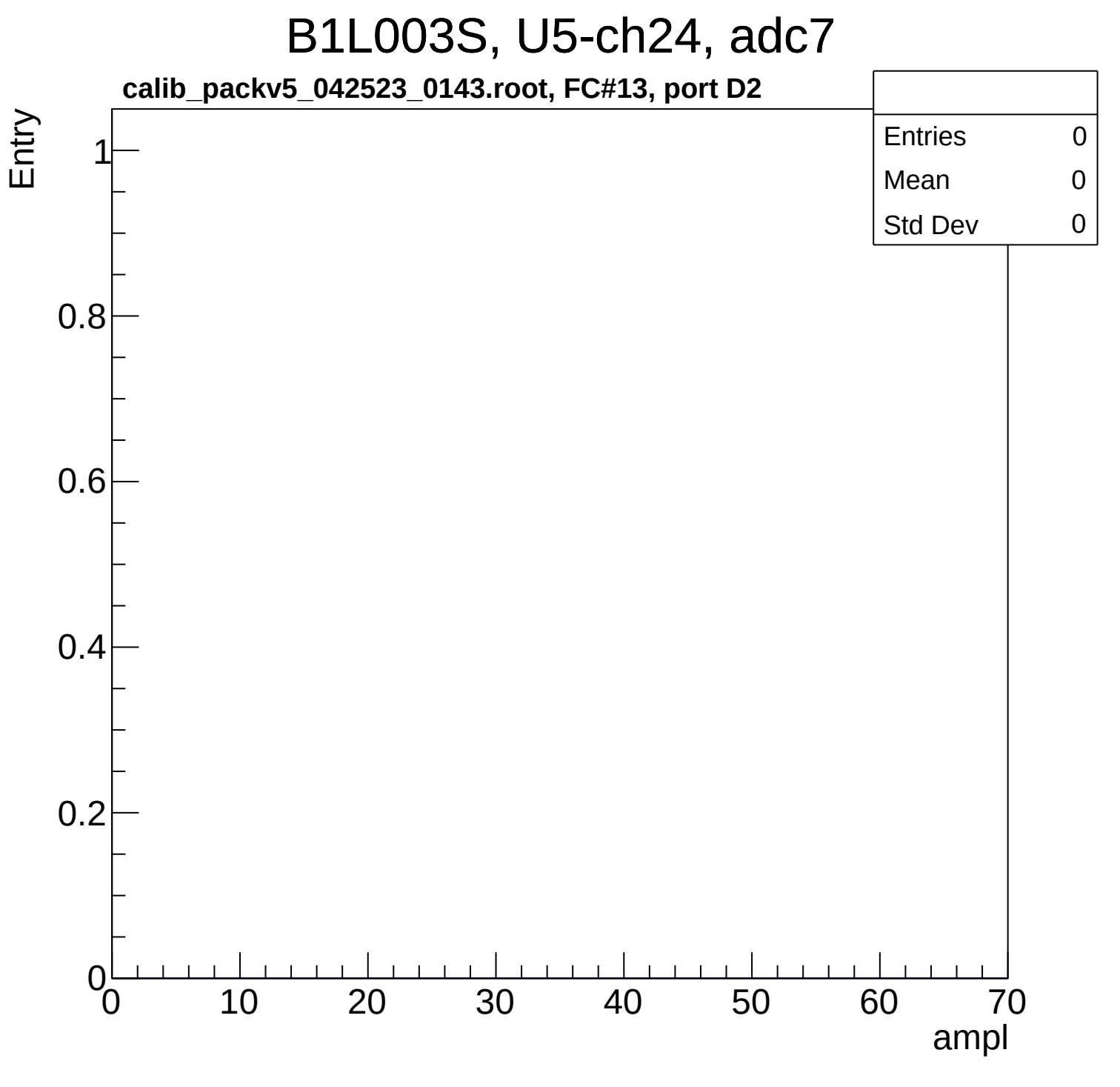
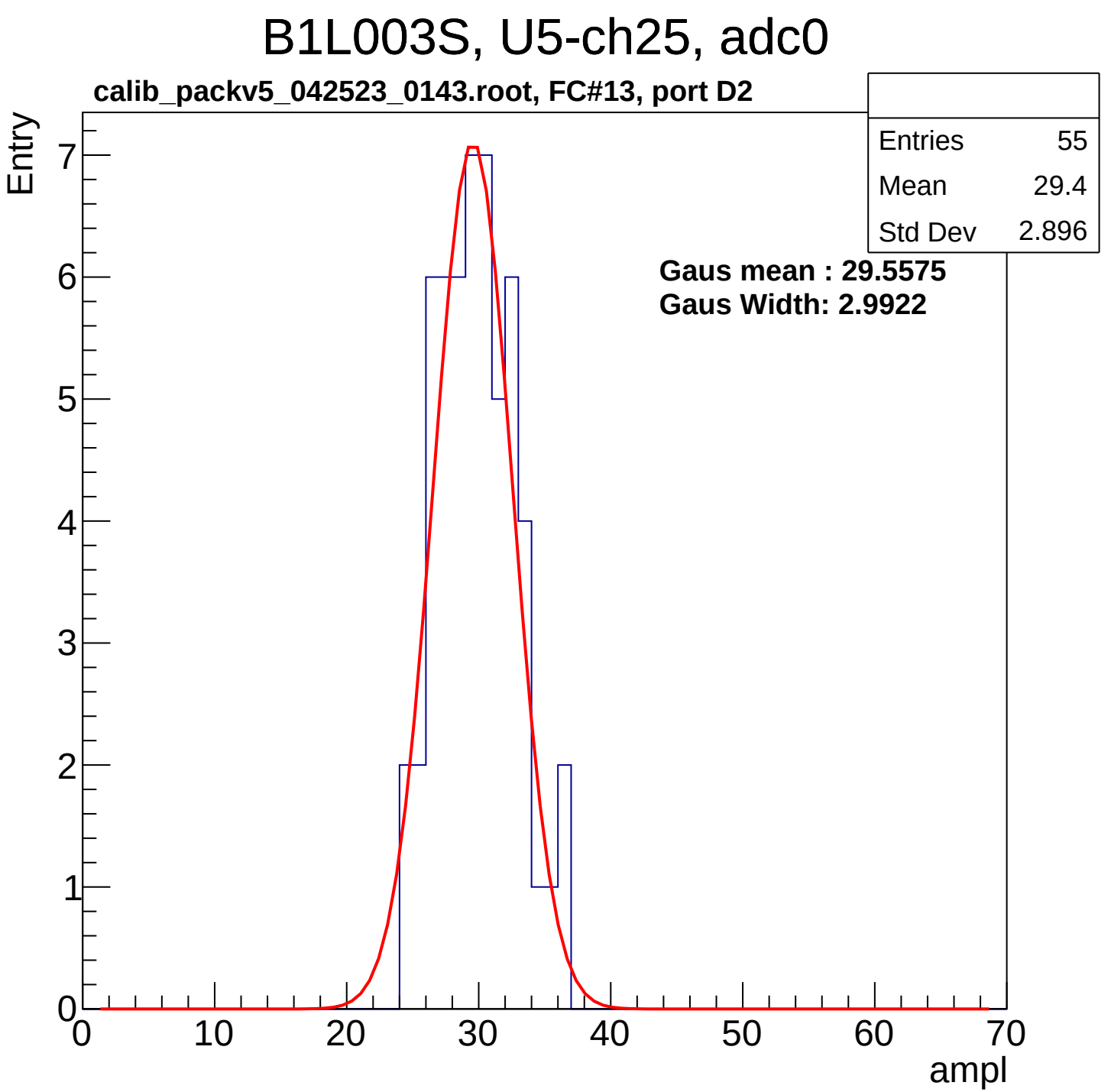
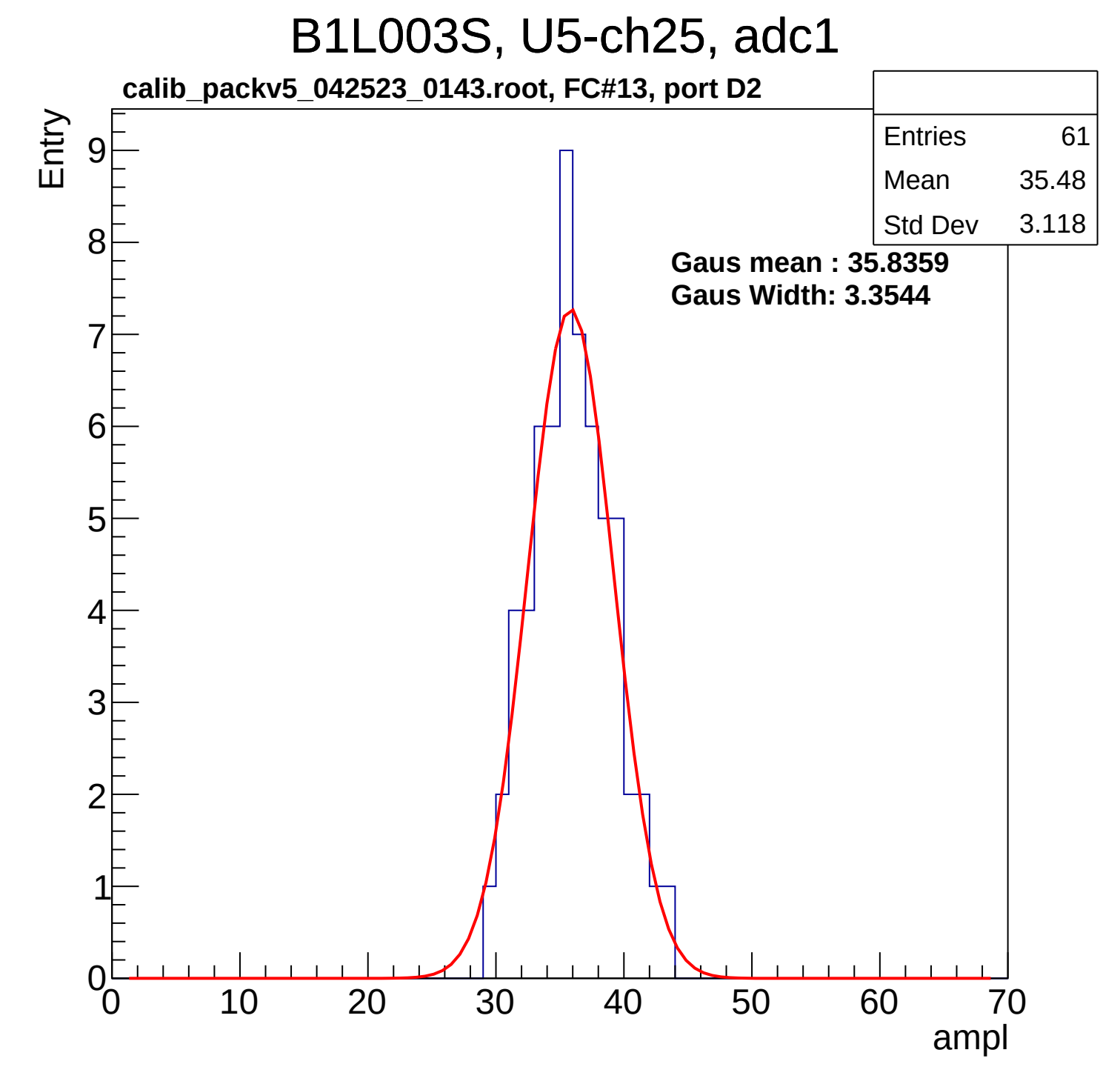
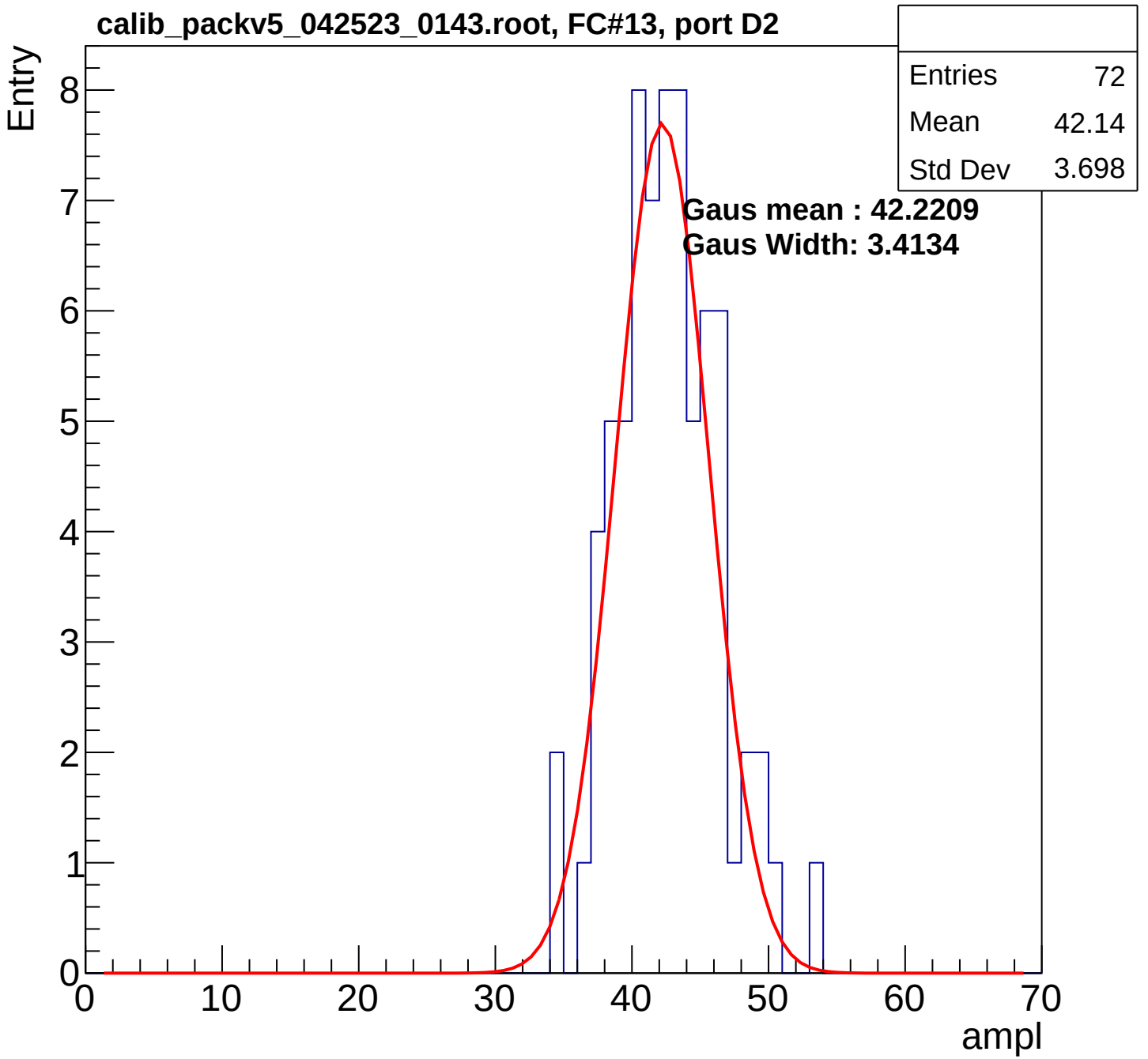
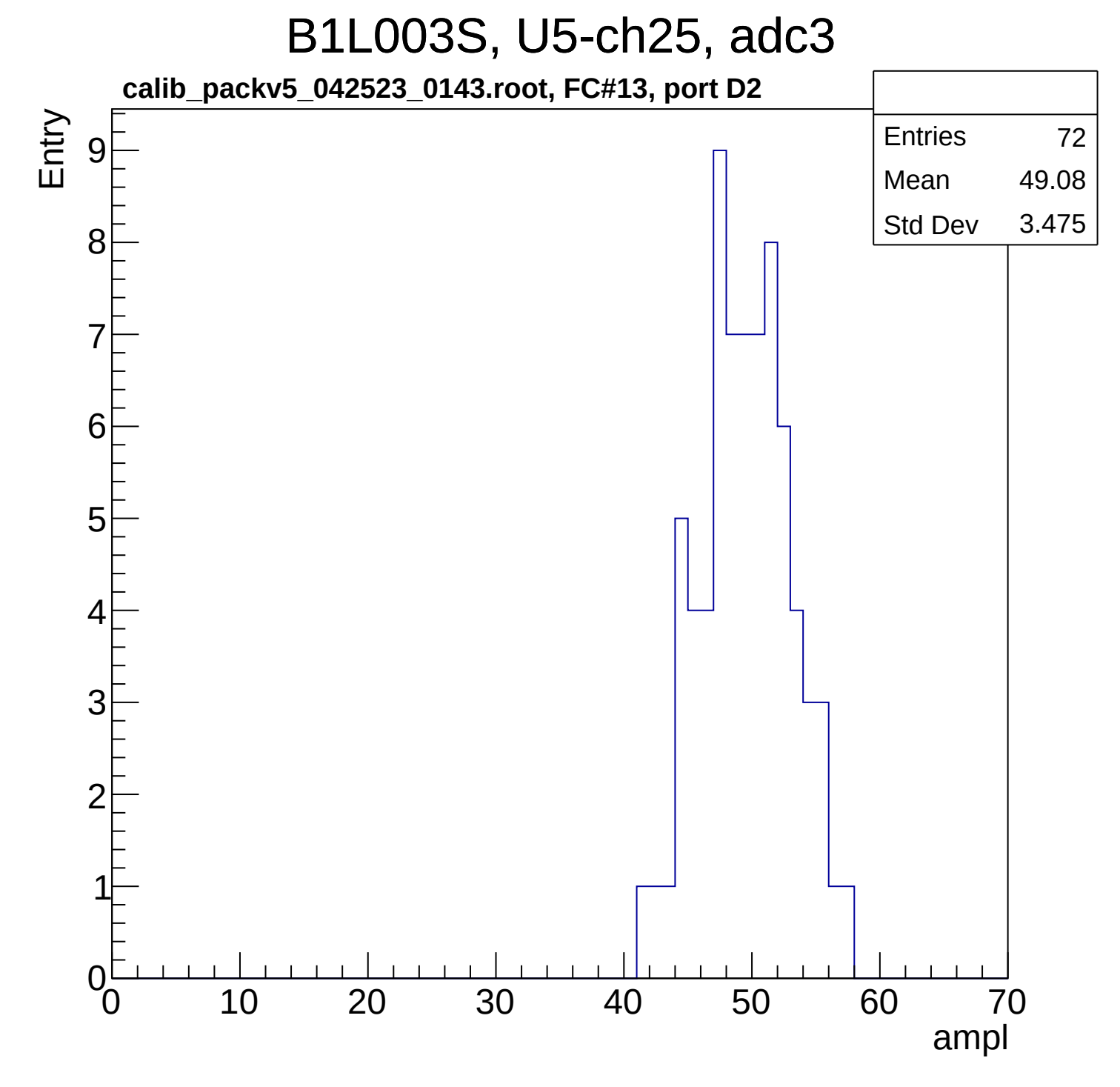

B1L003S, U5-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.4
Std Dev 2.896
Gaus mean : 29.5575
Gaus Width: 2.9922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.48
Std Dev 3.118
Gaus mean : 35.8359
Gaus Width: 3.3544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.14
Std Dev 3.698
Gaus mean : 42.2209
Gaus Width: 3.4134
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.08
Std Dev 3.475
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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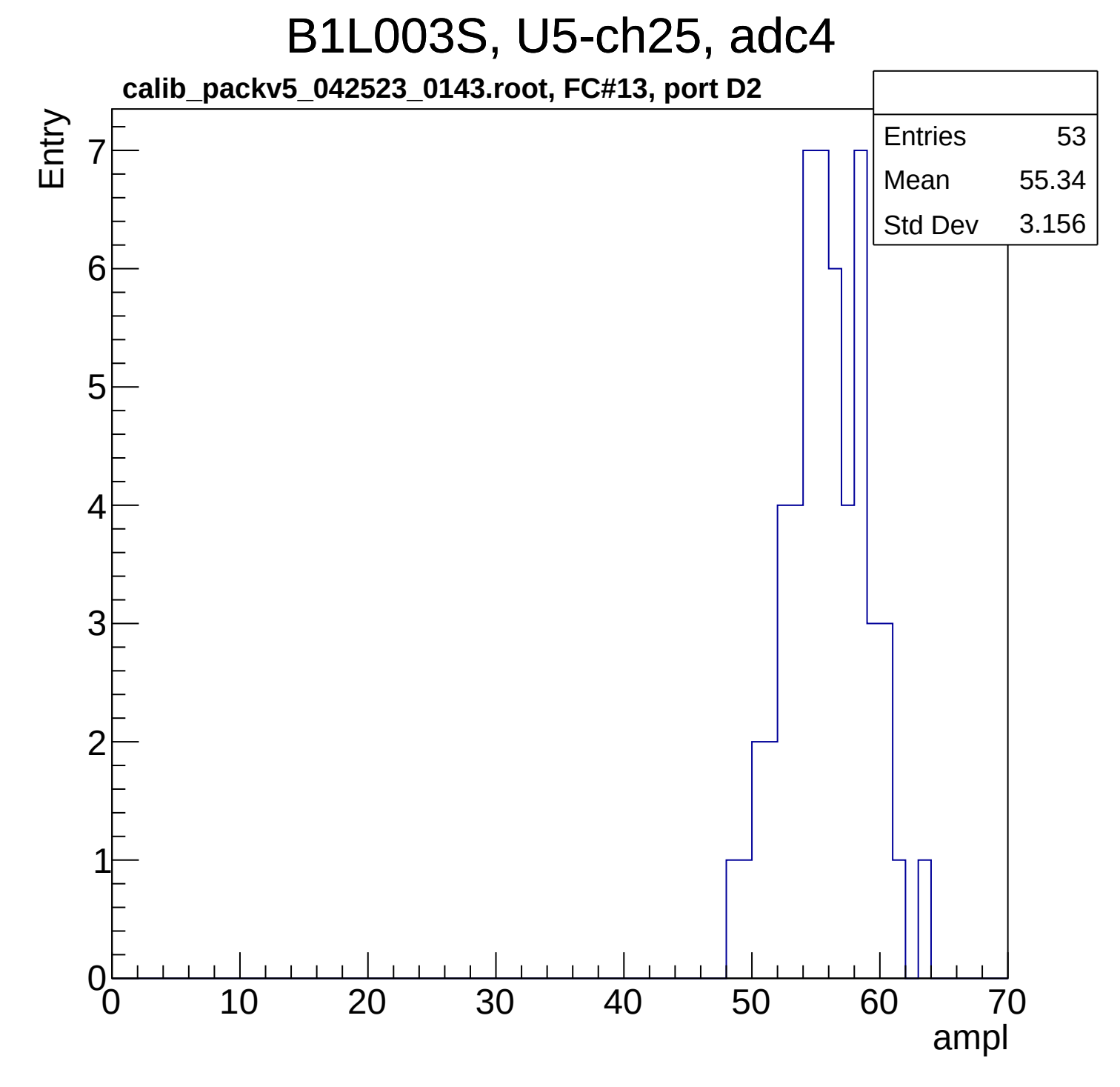
B1L003S, U5-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.34
Std Dev 3.156
Entry
ampl
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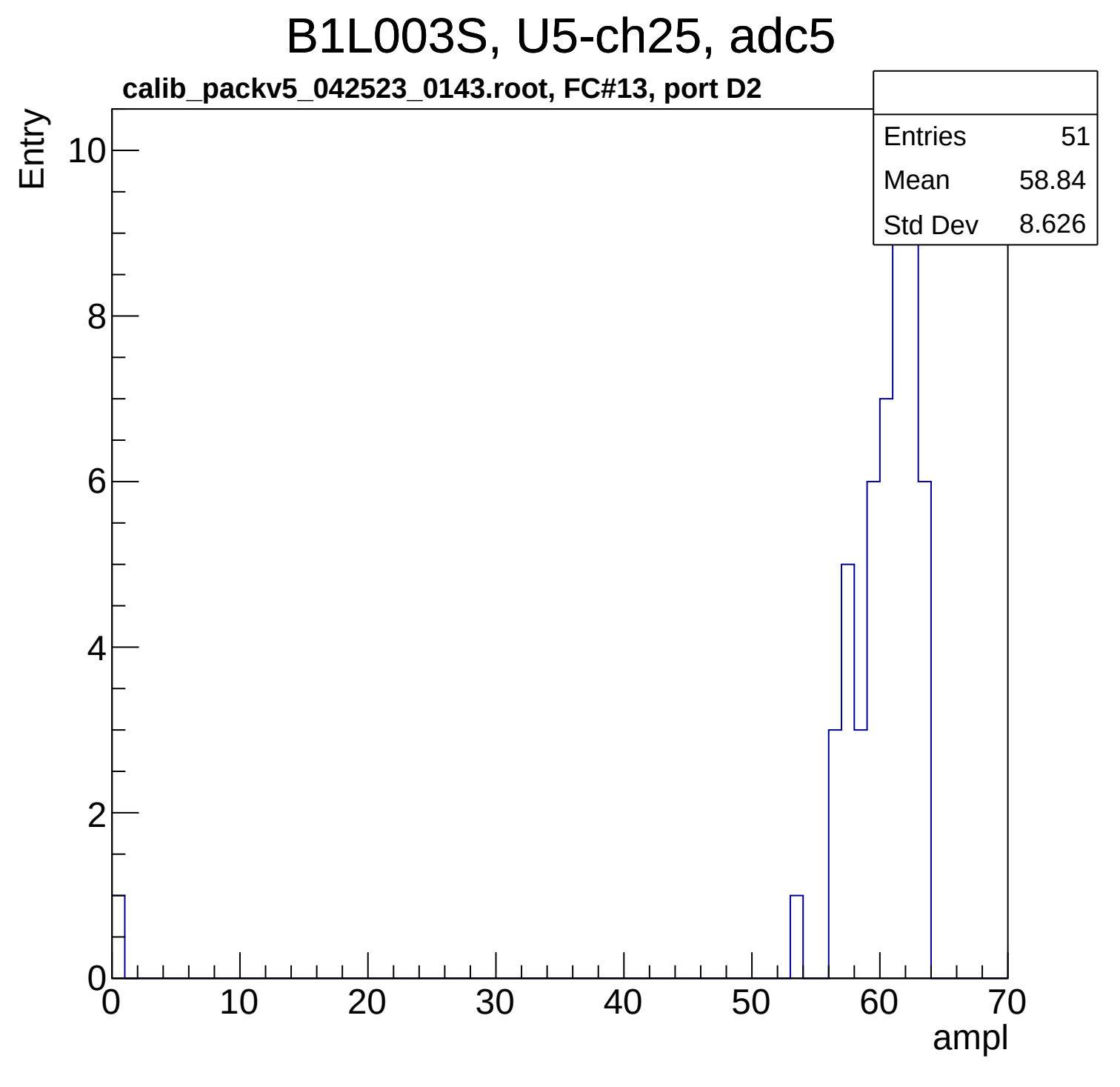
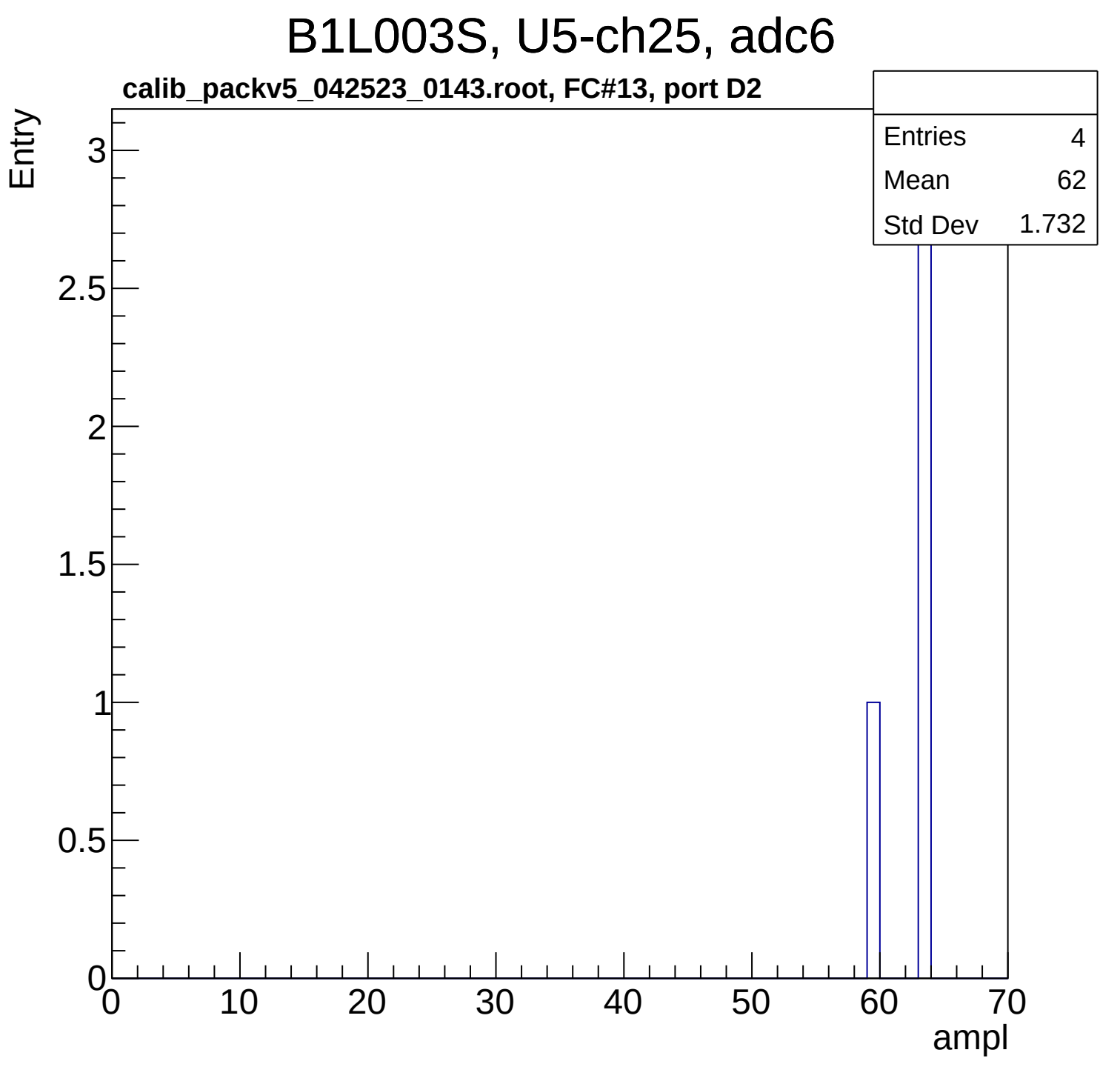
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.84
Std Dev 8.626
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
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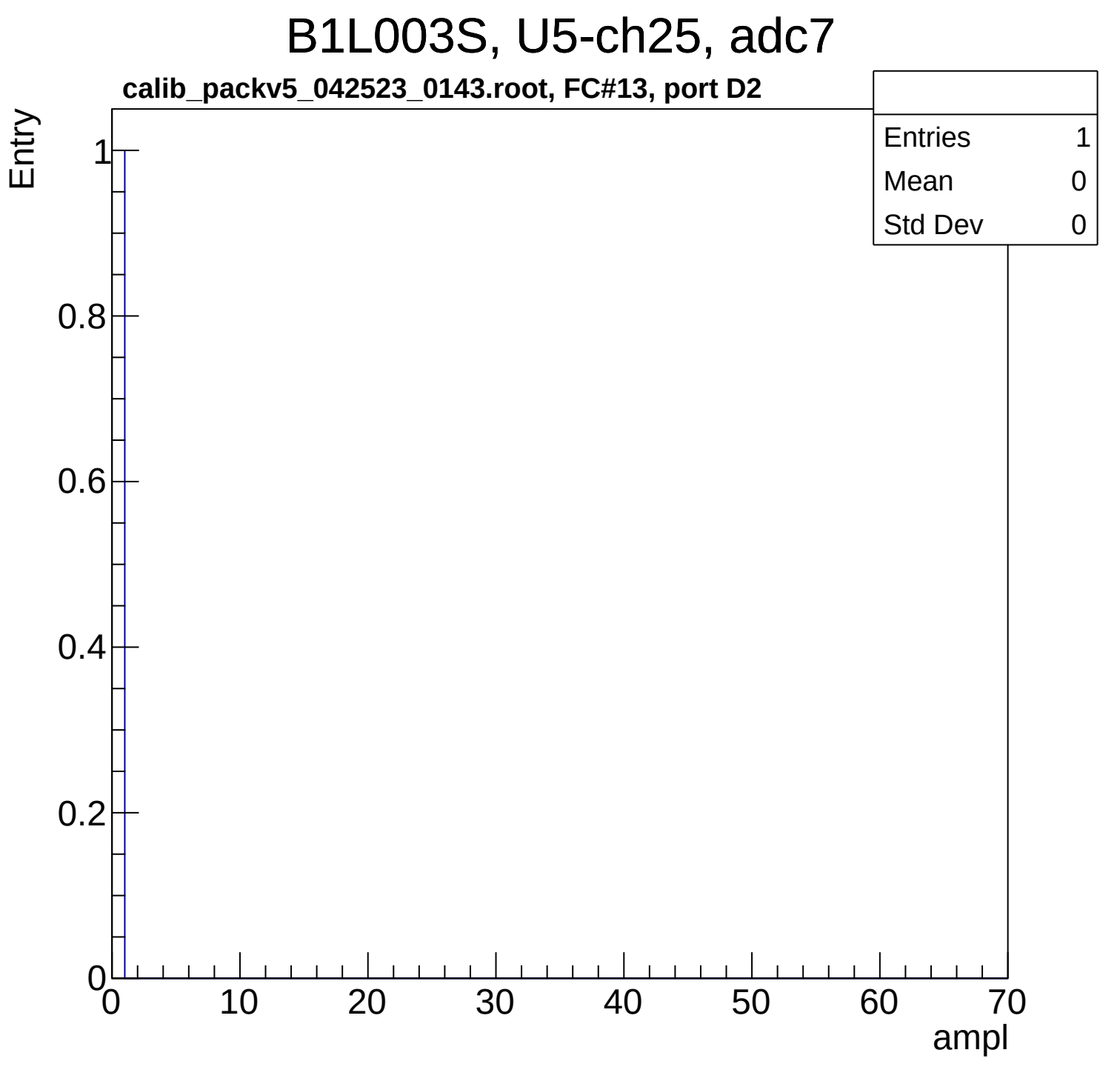
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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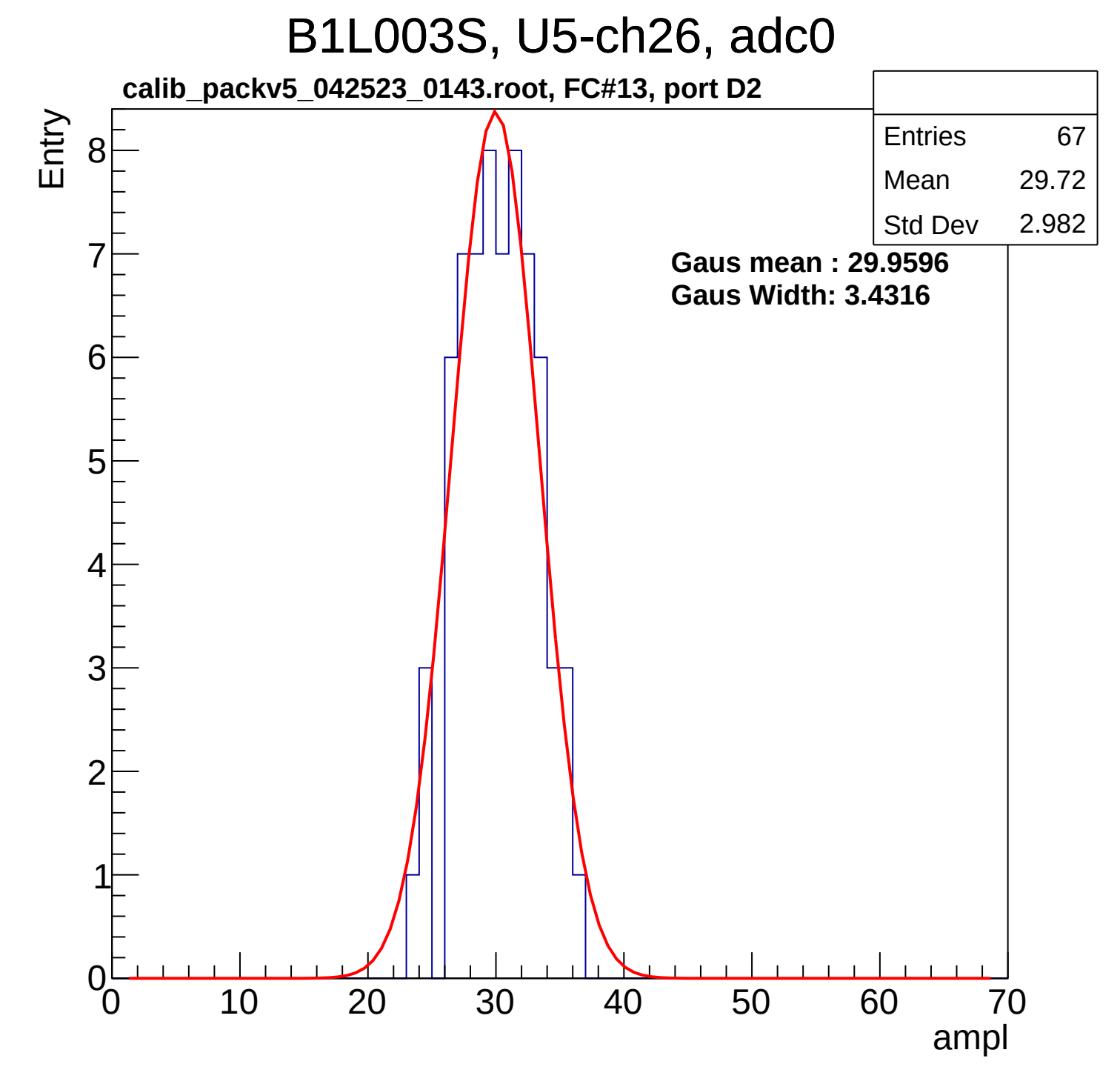
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.72
Std Dev 2.982
Gaus mean : 29.9596
Gaus Width: 3.4316
Entry
ampl
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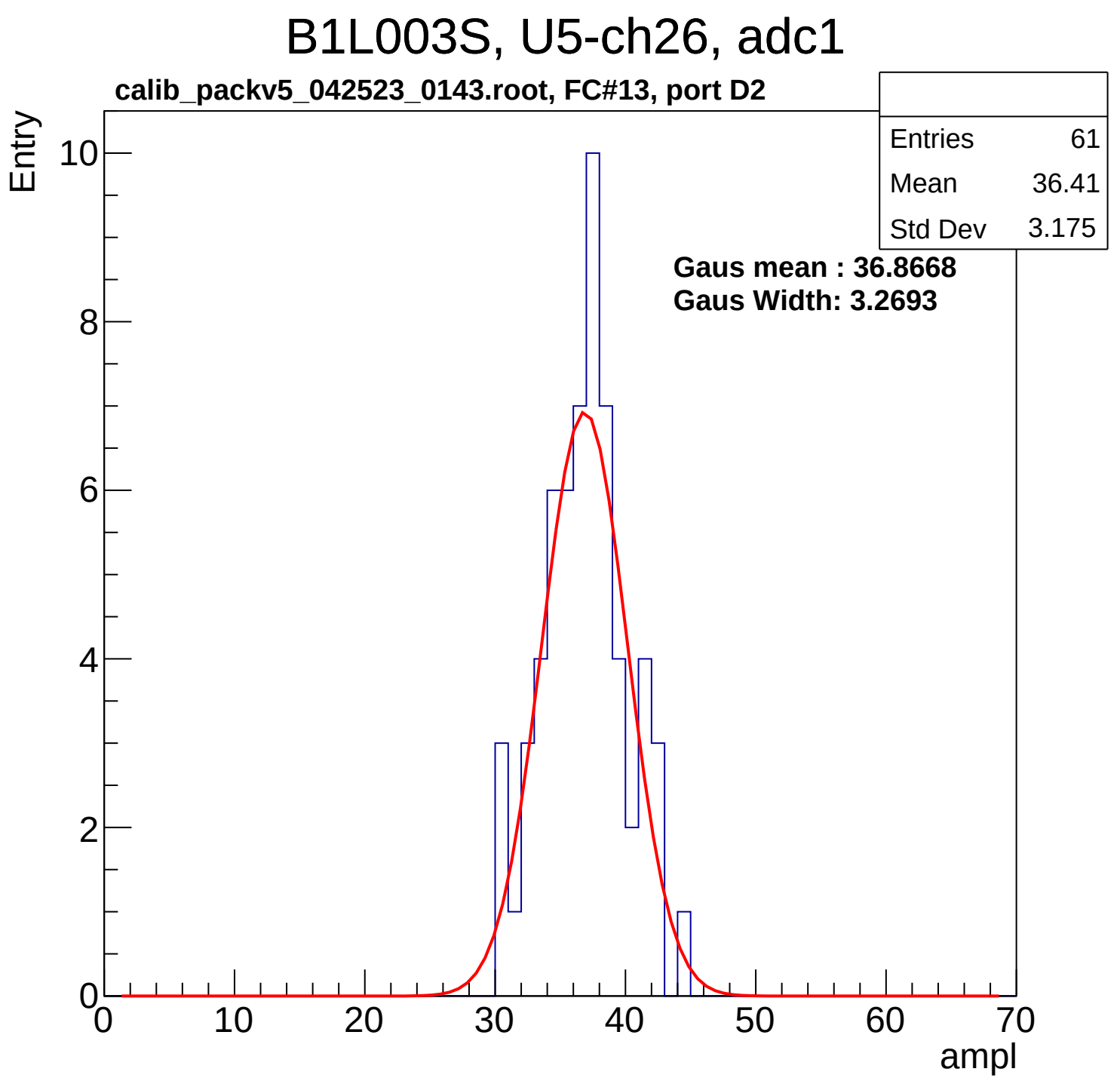
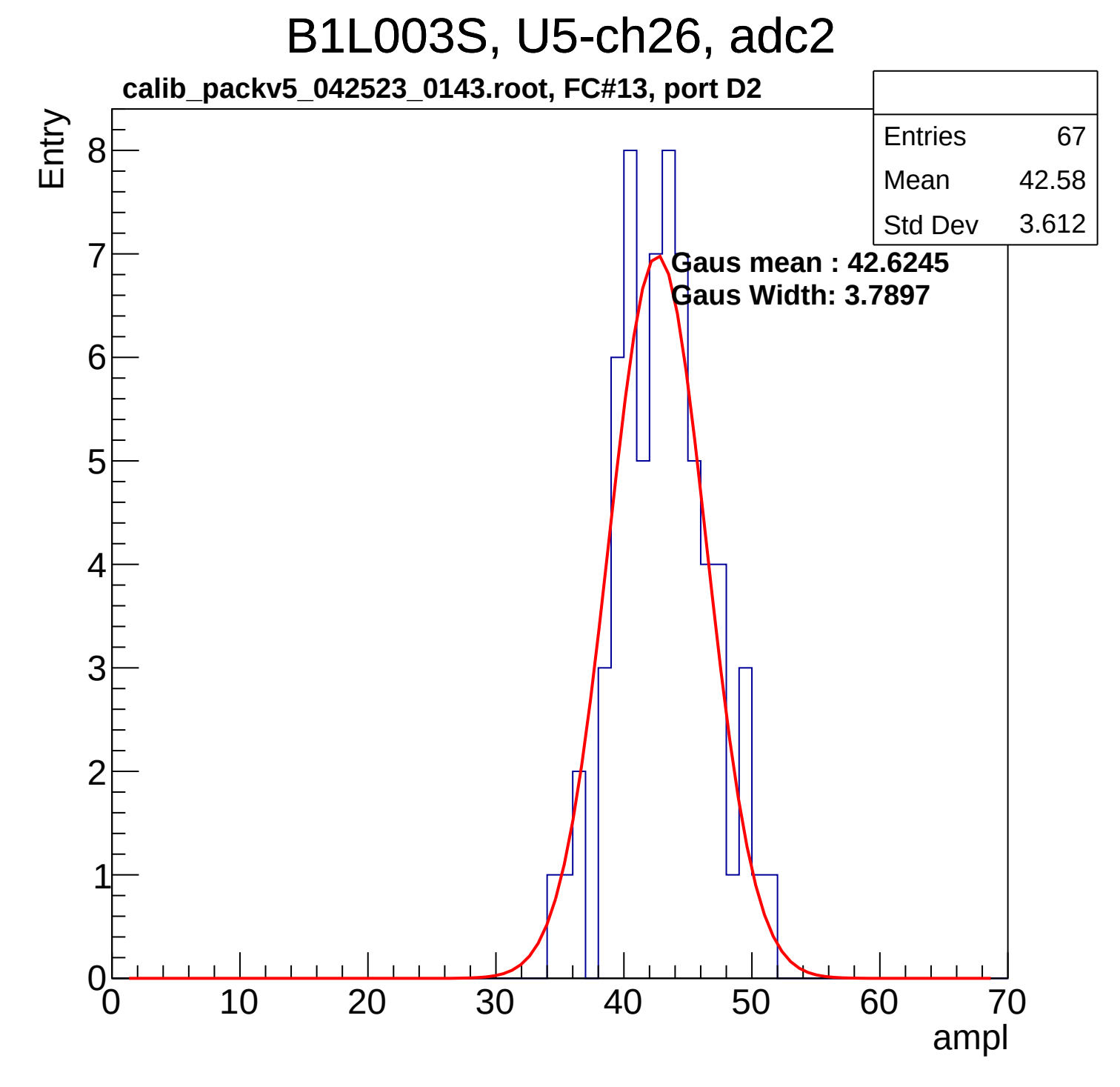
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.41
Std Dev 3.175
Gaus mean : 36.8668
Gaus Width: 3.2693
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.58
Std Dev 3.612
Gaus mean : 42.6245
Gaus Width: 3.7897
Entry
ampl
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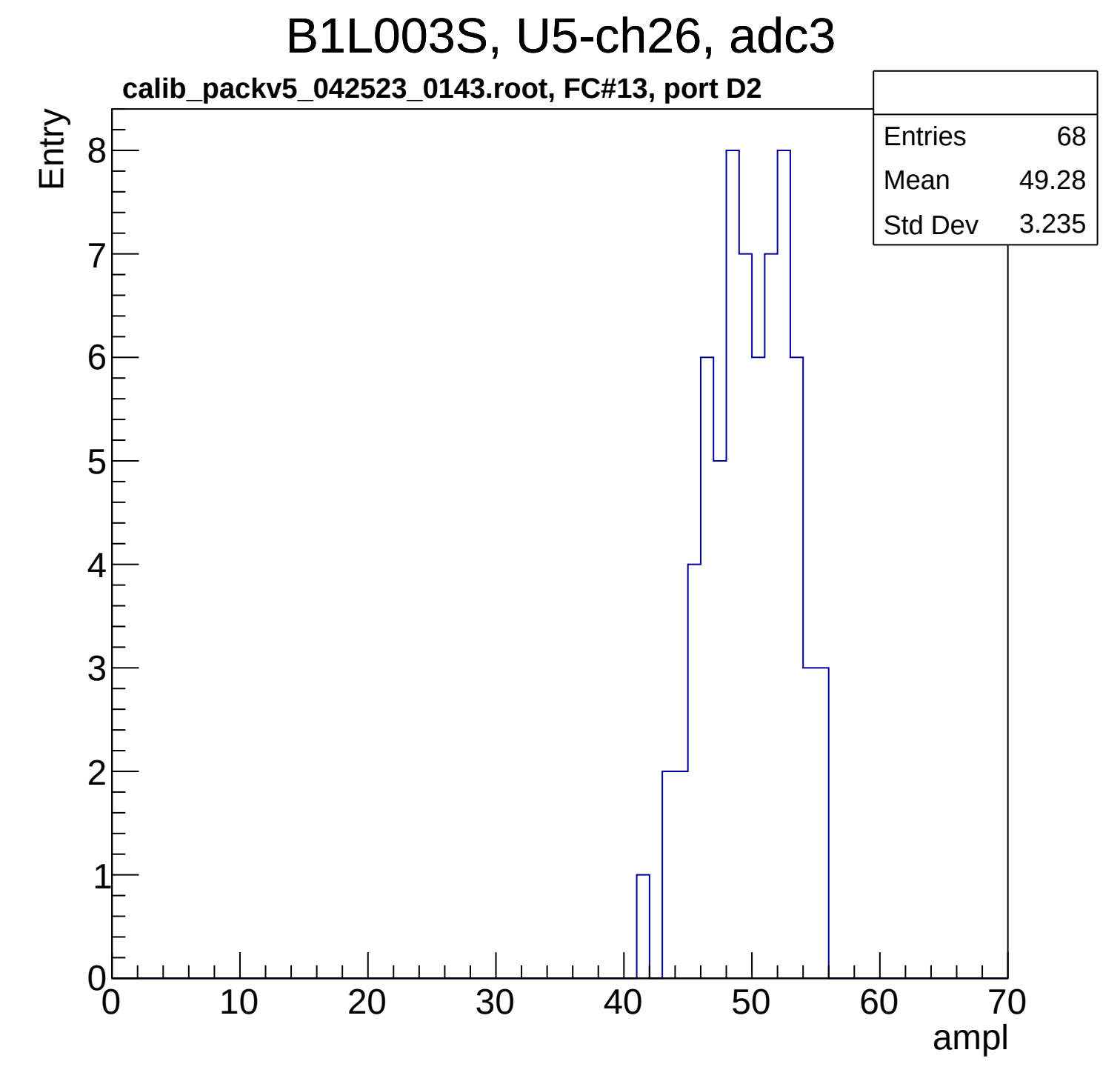
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.28
Std Dev 3.235
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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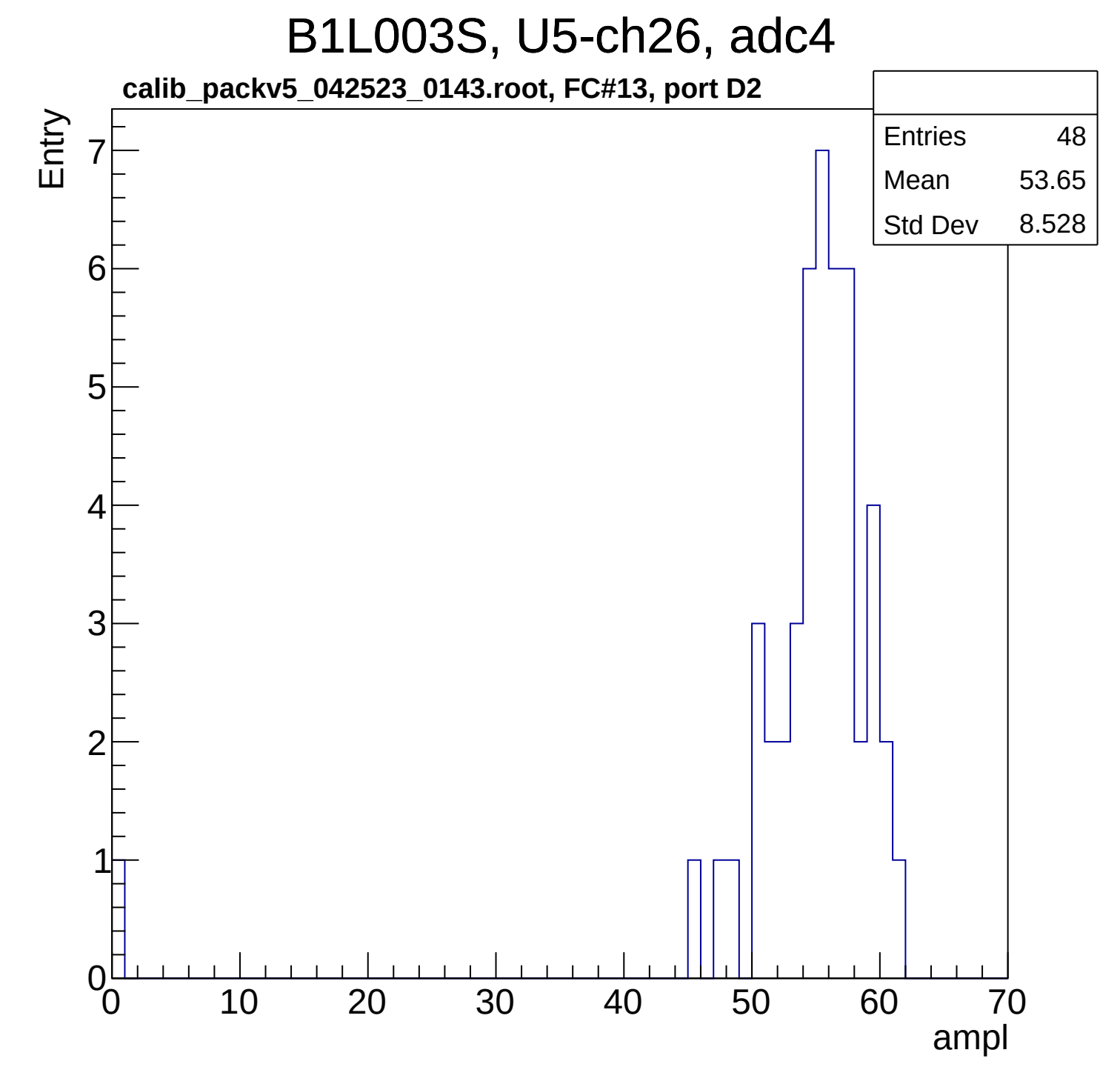
B1L003S, U5-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 53.65
Std Dev 8.528
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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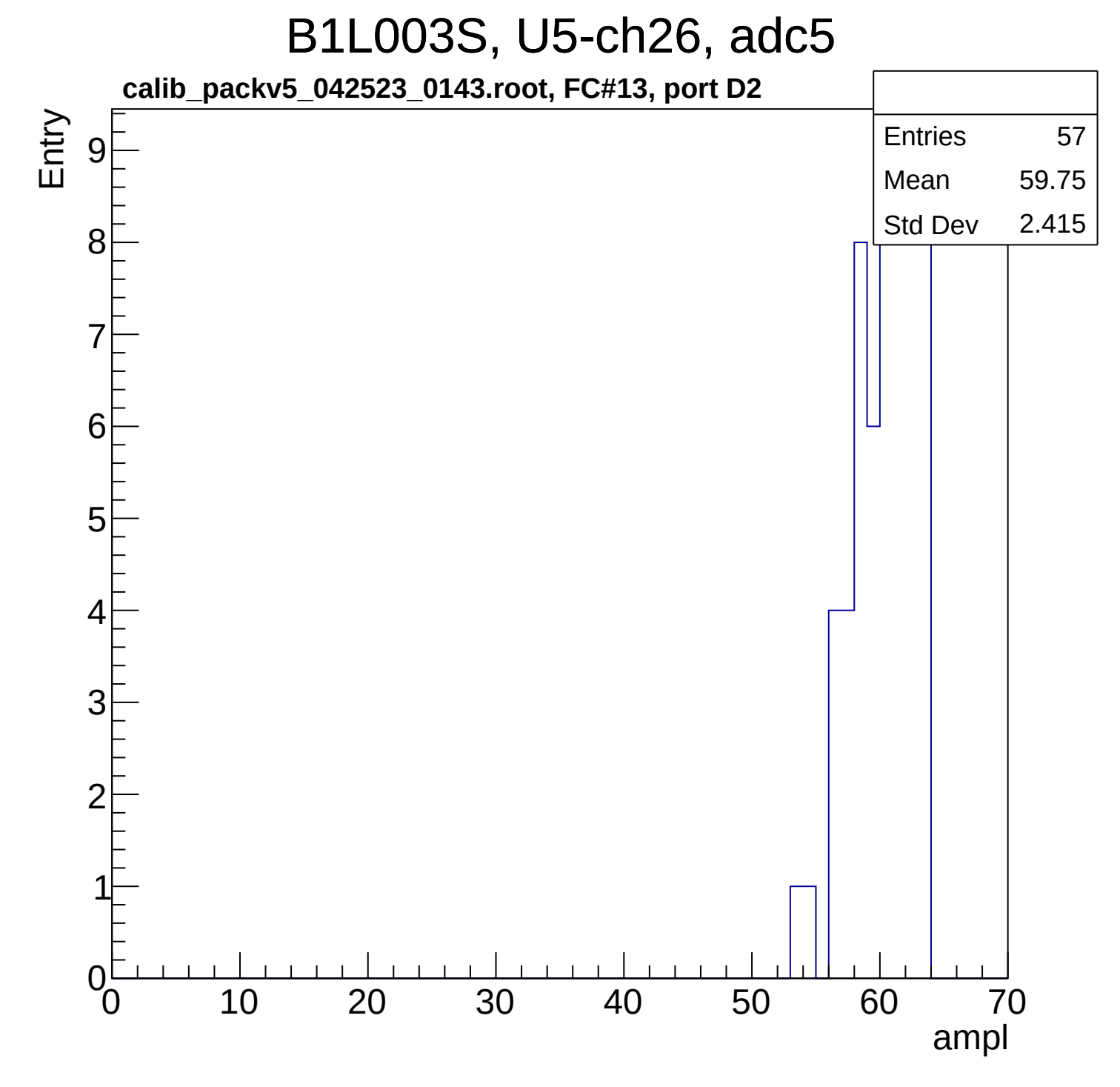
B1L003S, U5-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 59.75
Std Dev 2.415
Entry
ampl
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3
4
5
6
7
8
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0
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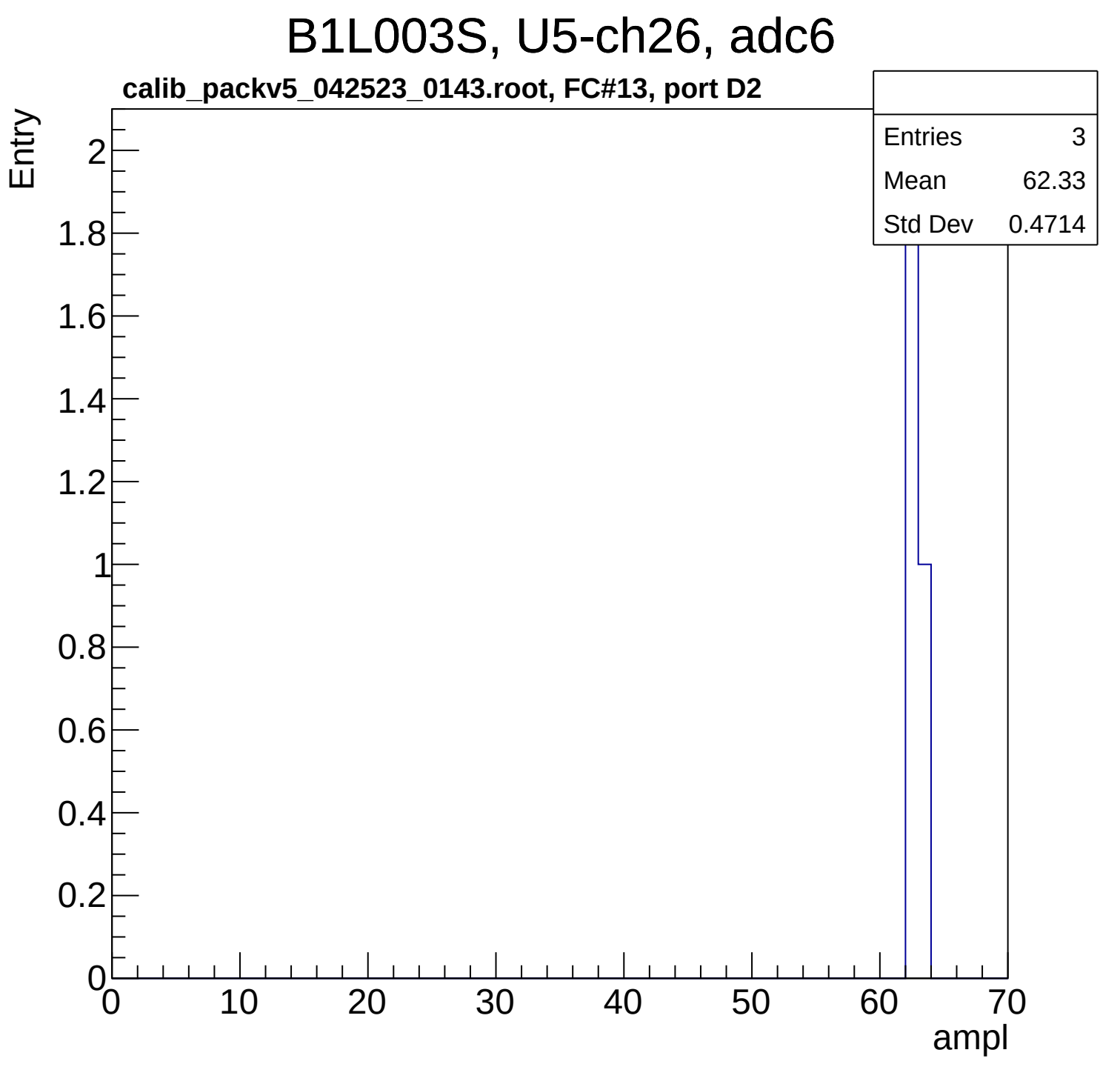
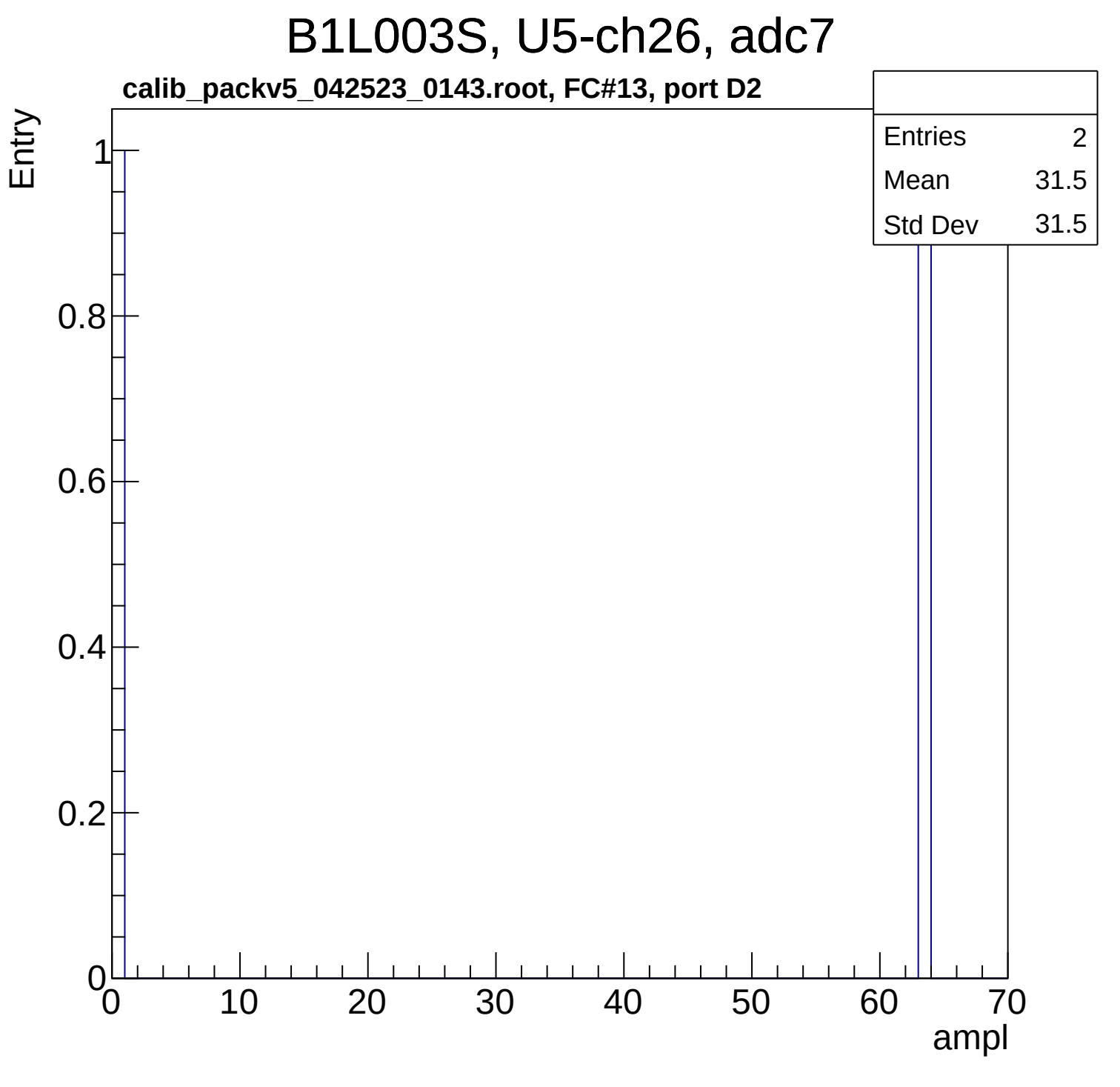
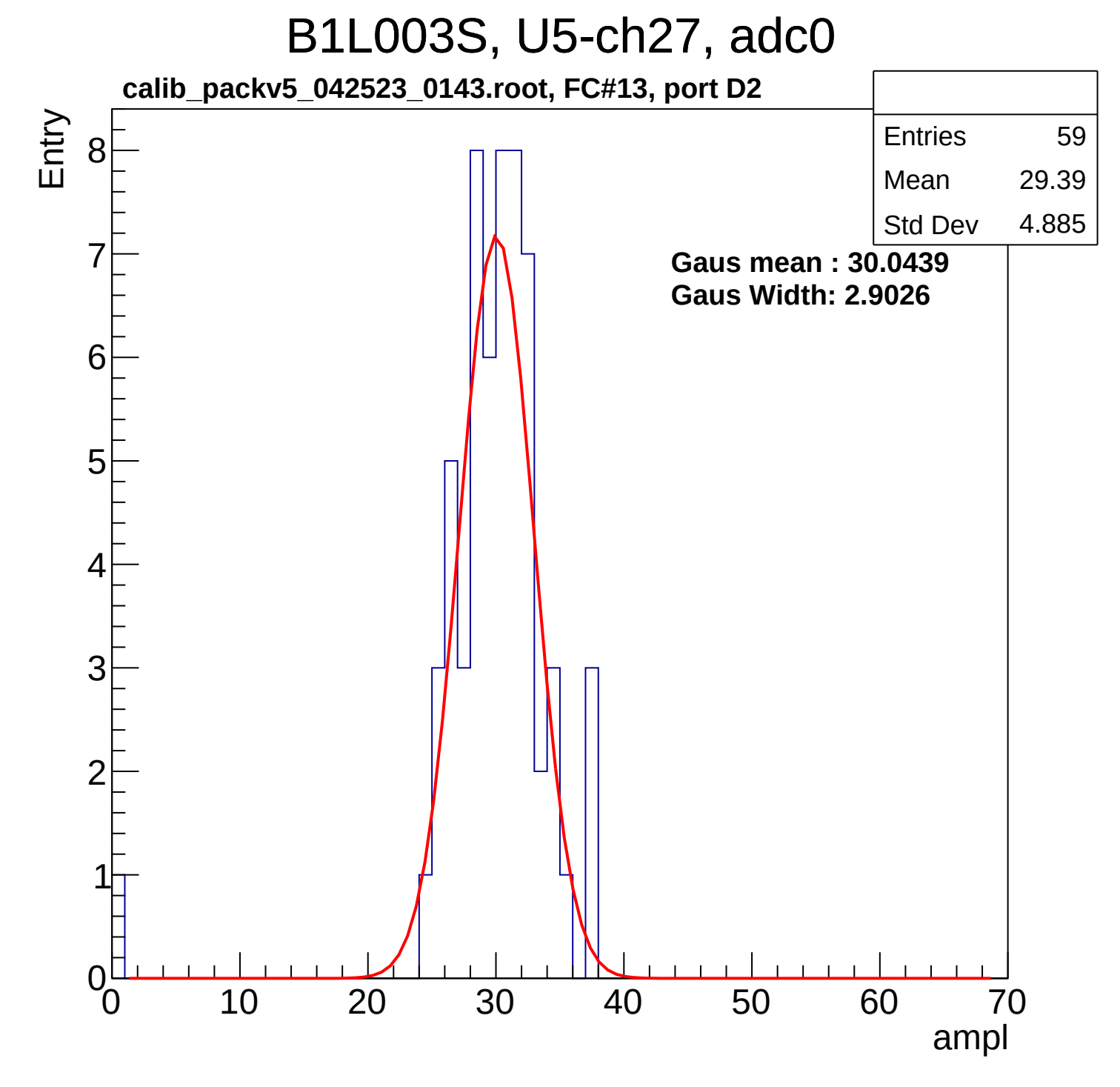

B1L003S, U5-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch27, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.39
Std Dev 4.885
Gaus mean : 30.0439
Gaus Width: 2.9026
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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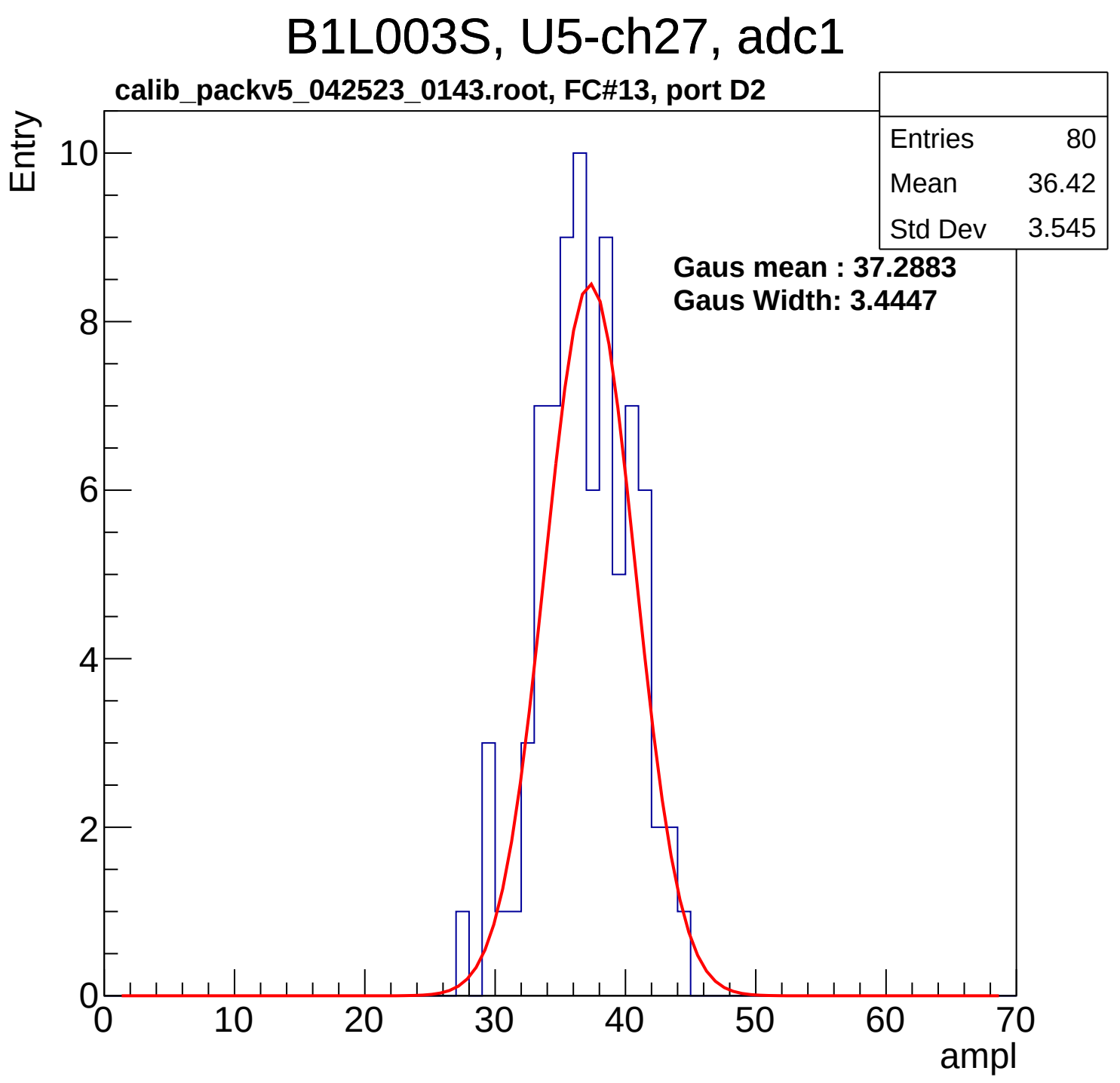
60
70


B1L003S, U5-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 36.42
Std Dev 3.545
Gaus mean : 37.2883
Gaus Width: 3.4447
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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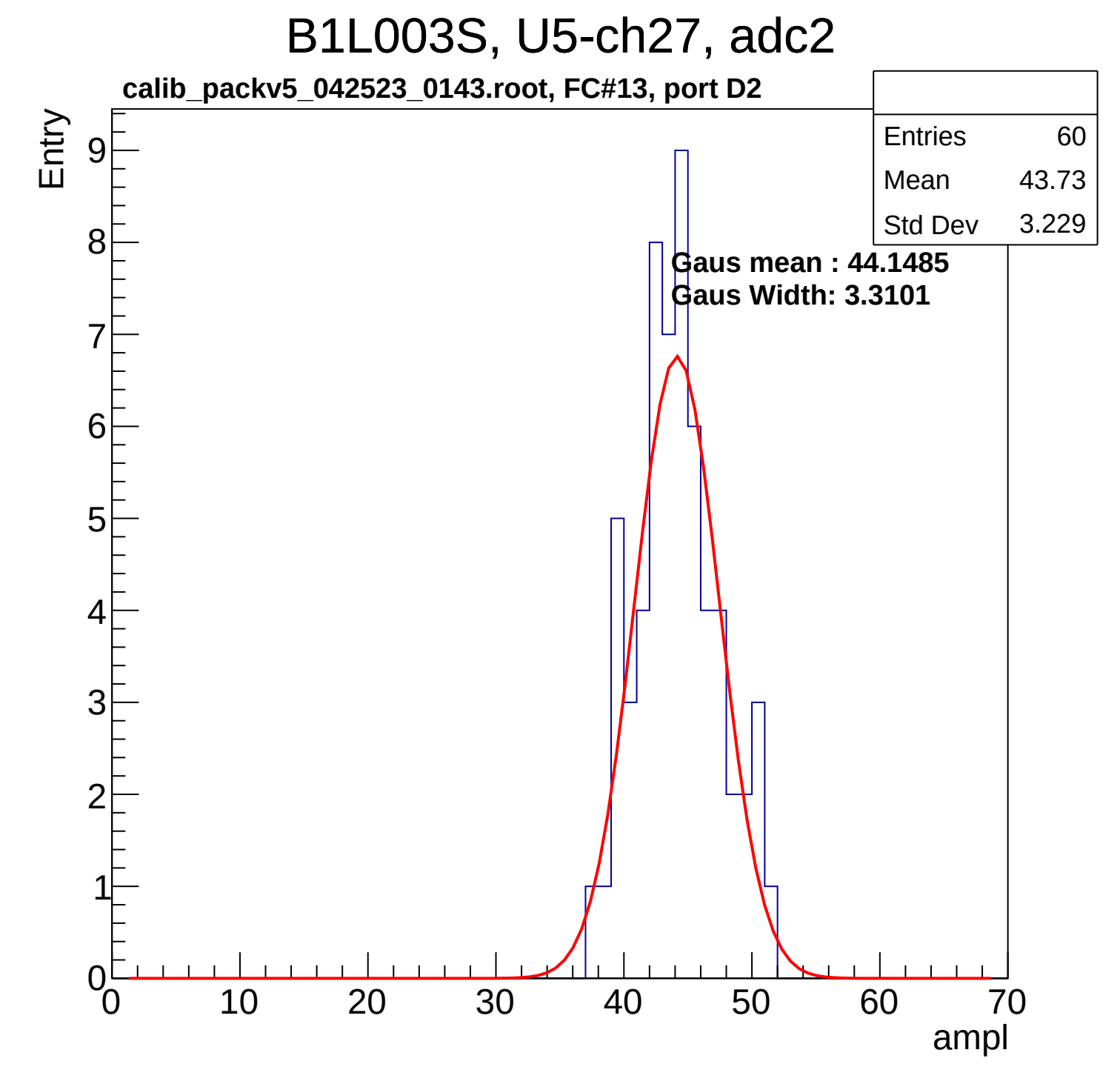
B1L003S, U5-ch27, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.73
Std Dev 3.229
Gaus mean : 44.1485
Gaus Width: 3.3101
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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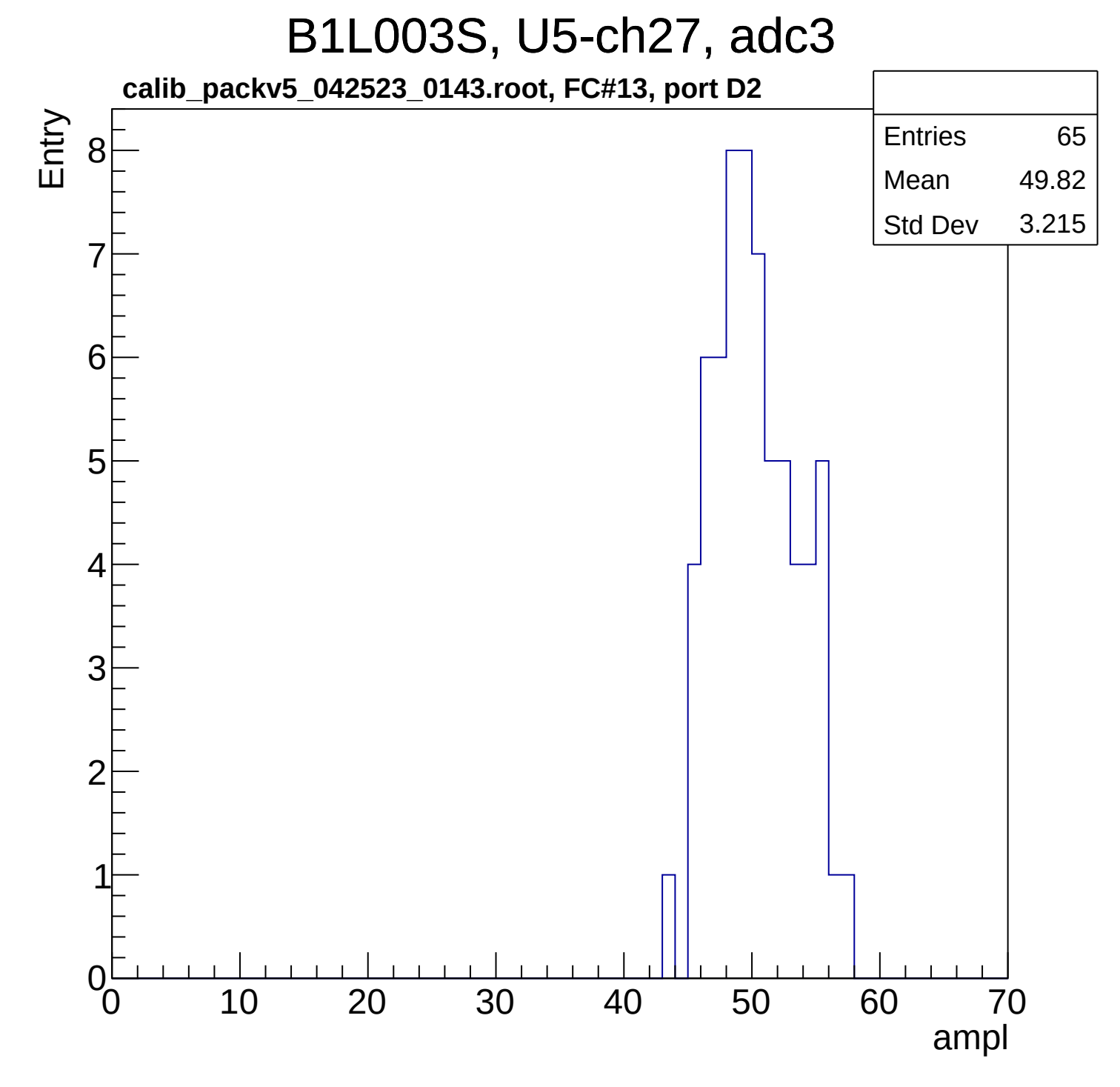
B1L003S, U5-ch27, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.82
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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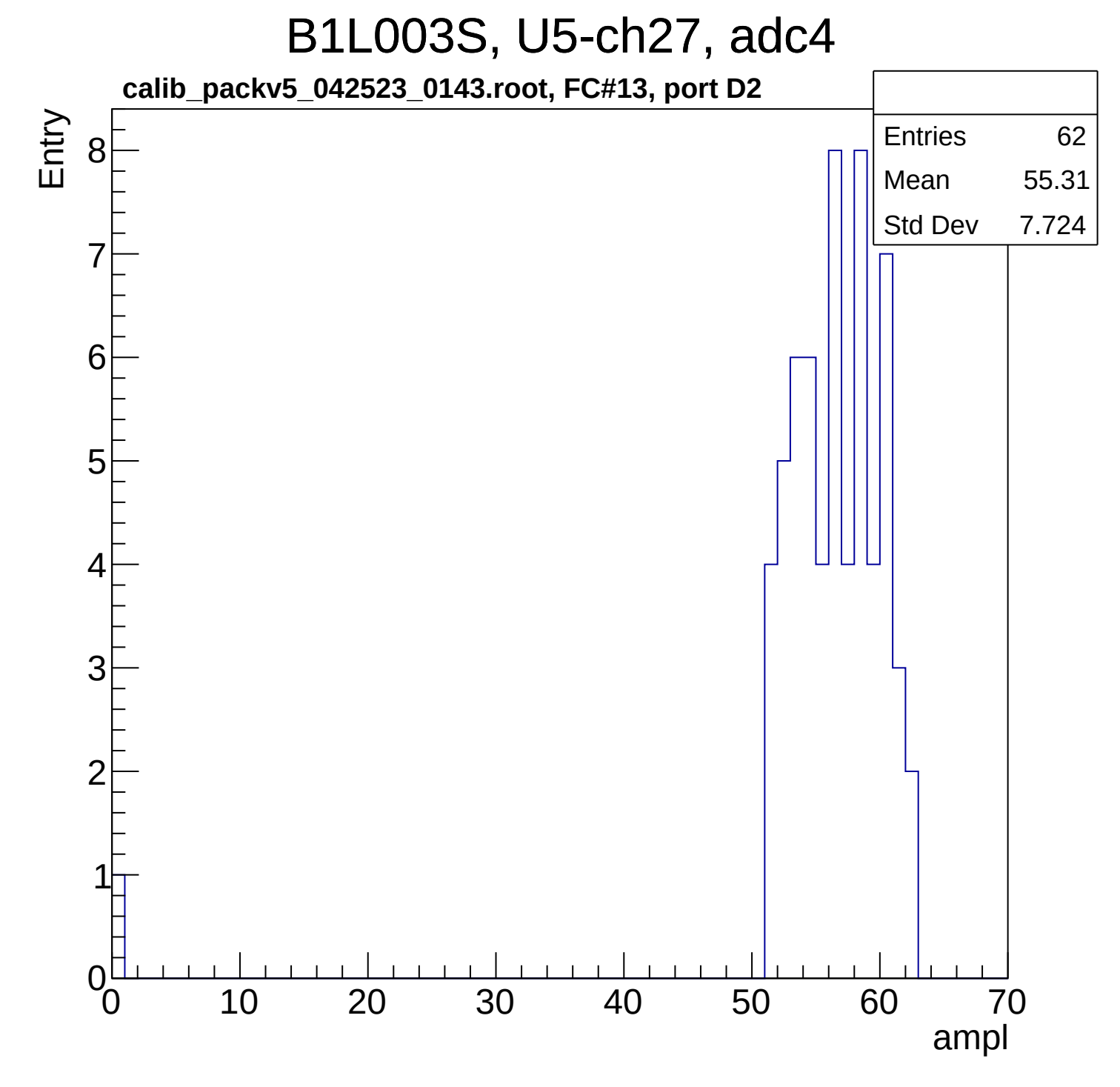
B1L003S, U5-ch27, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.31
Std Dev 7.724
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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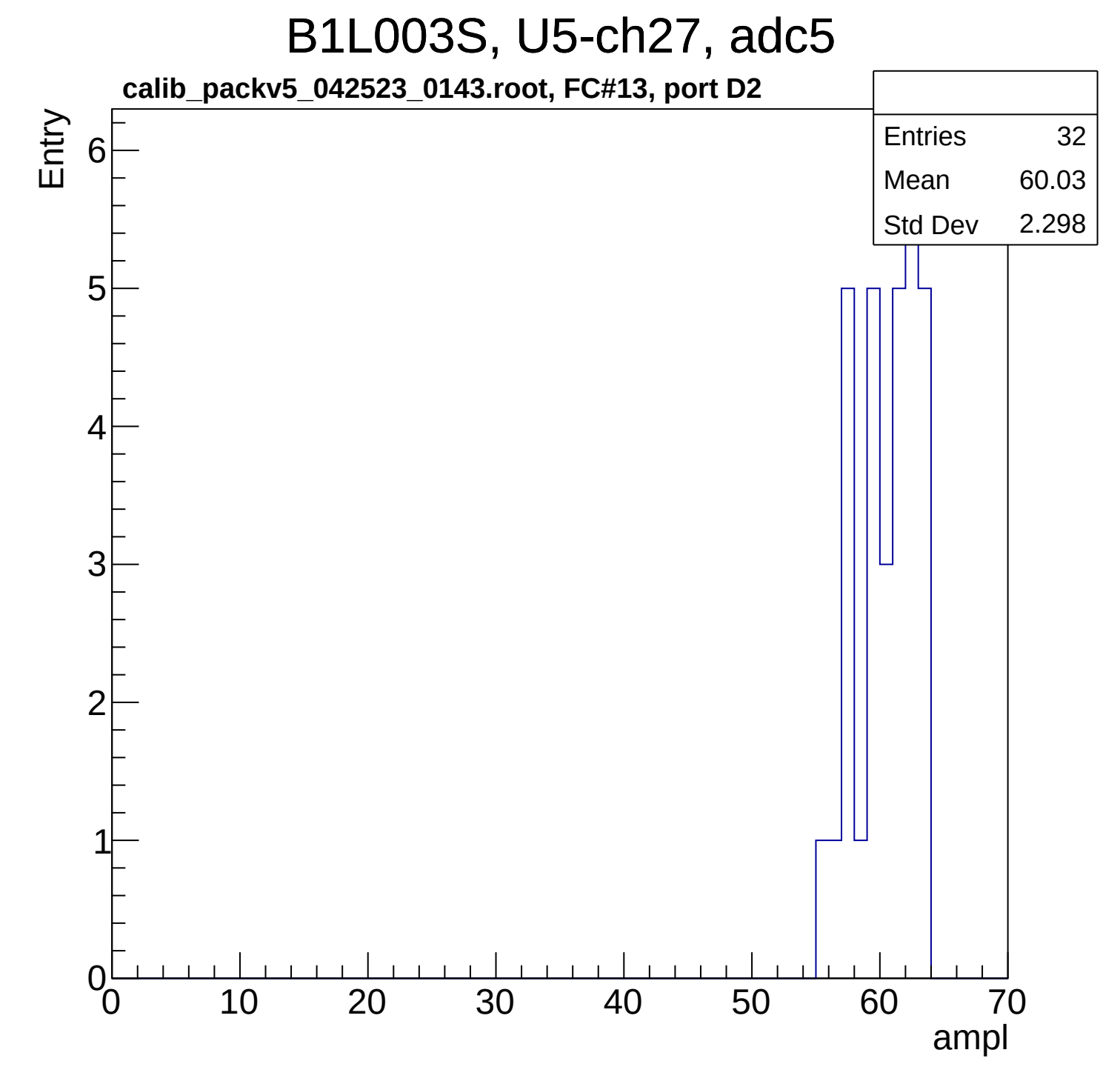
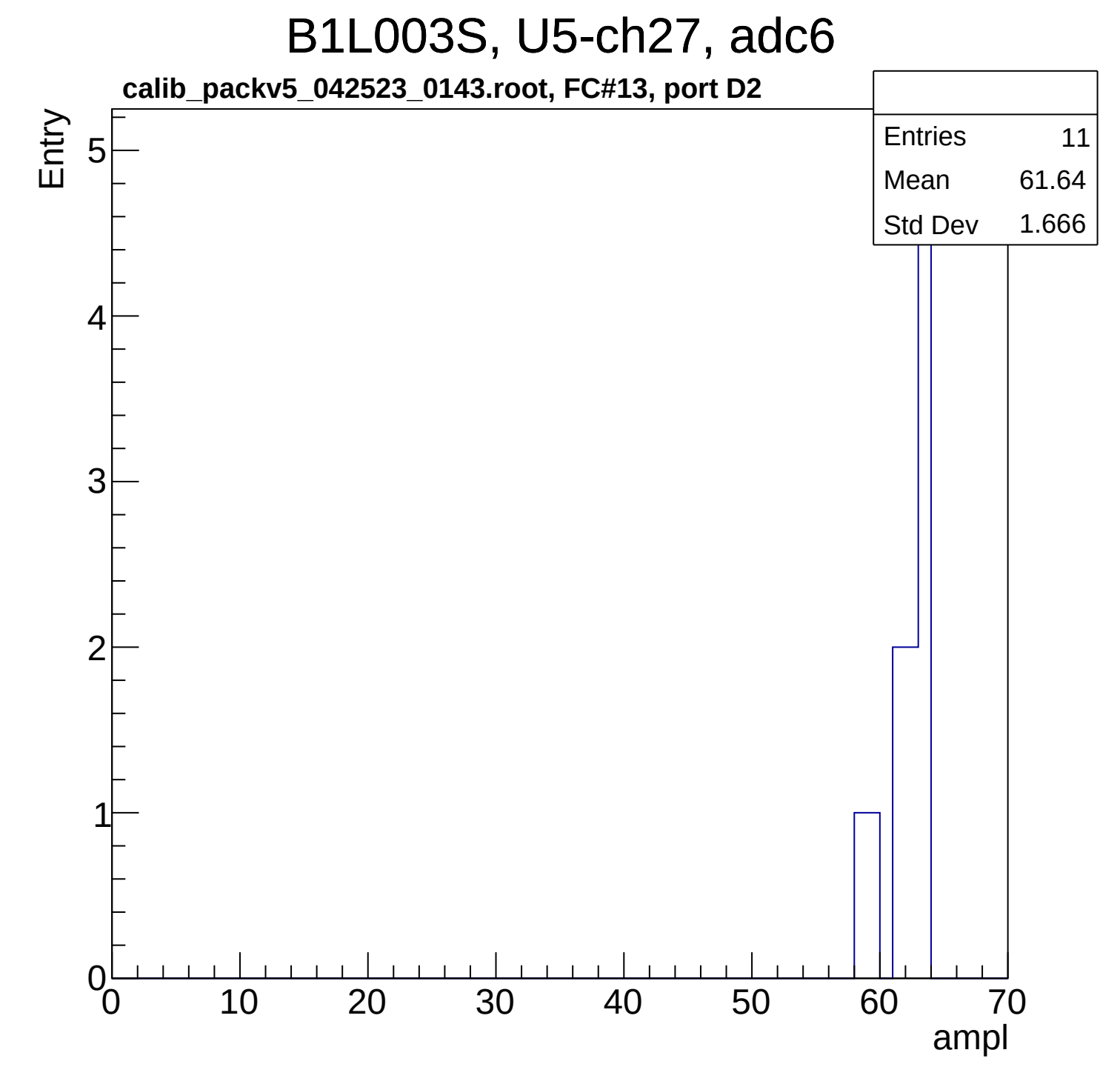
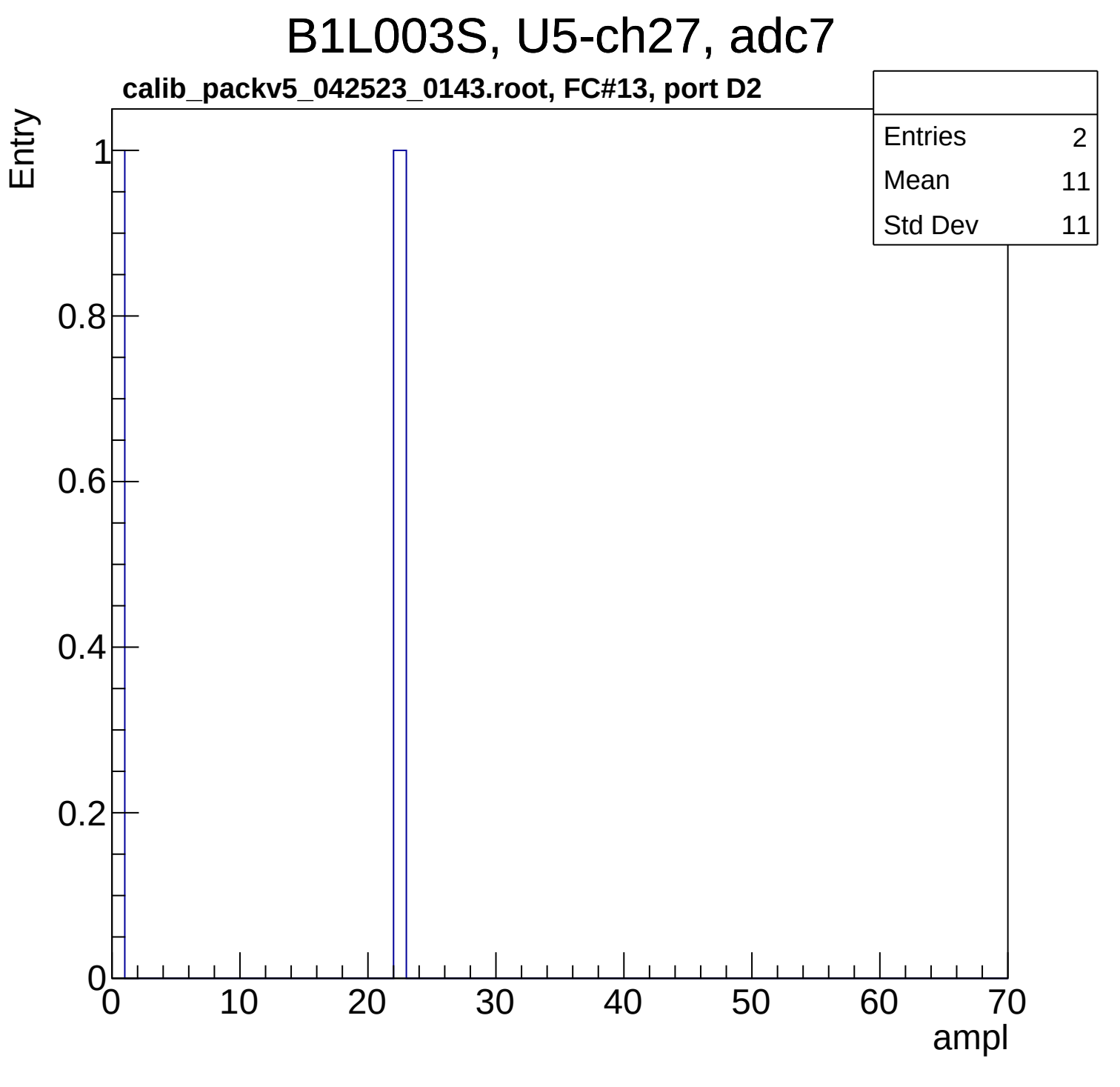

B1L003S, U5-ch27, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 60.03
Std Dev 2.298
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch27, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.64
Std Dev 1.666
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U5-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
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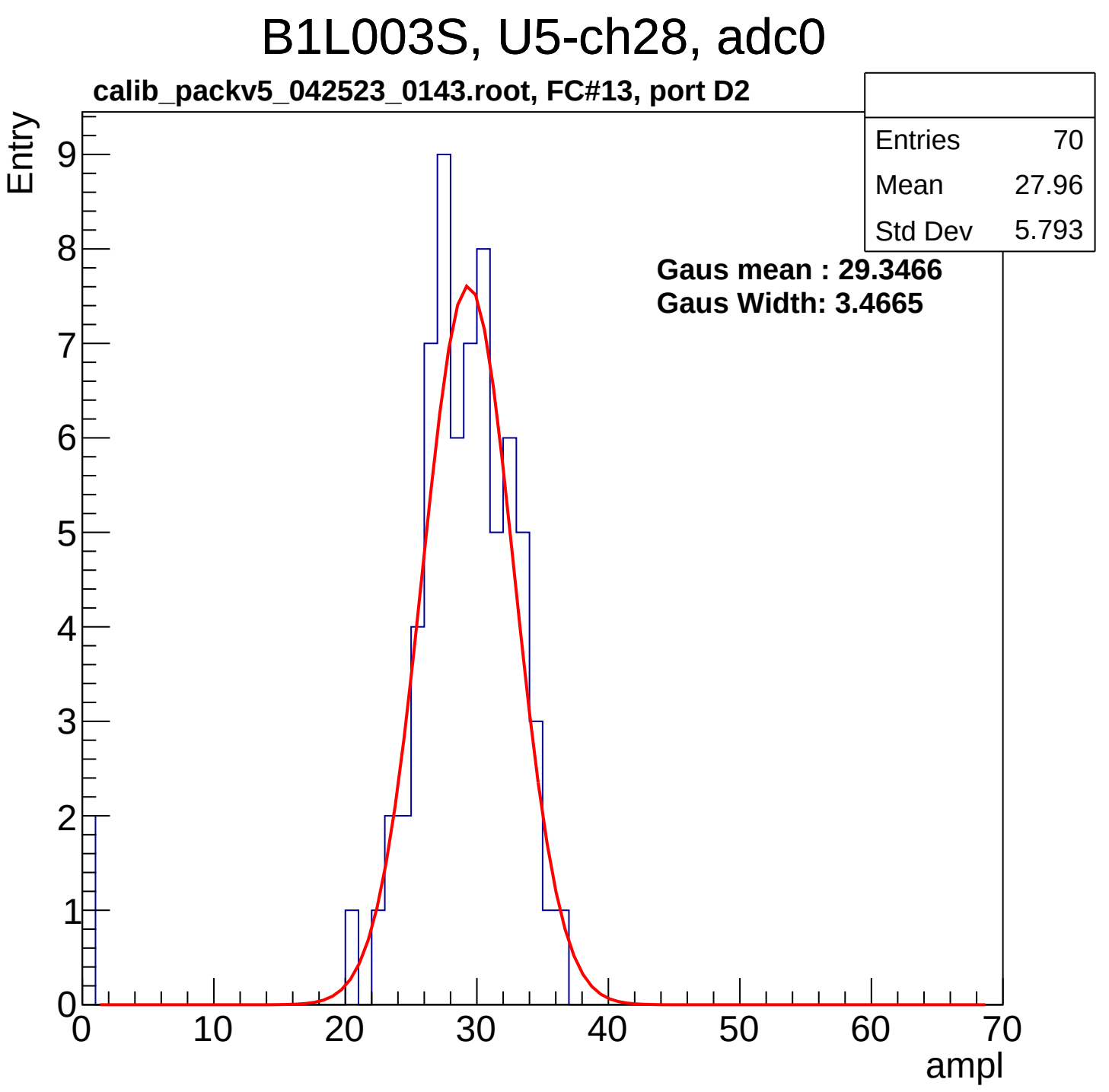
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch28, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.96
Std Dev 5.793
Gaus mean : 29.3466
Gaus Width: 3.4665
Entry
ampl
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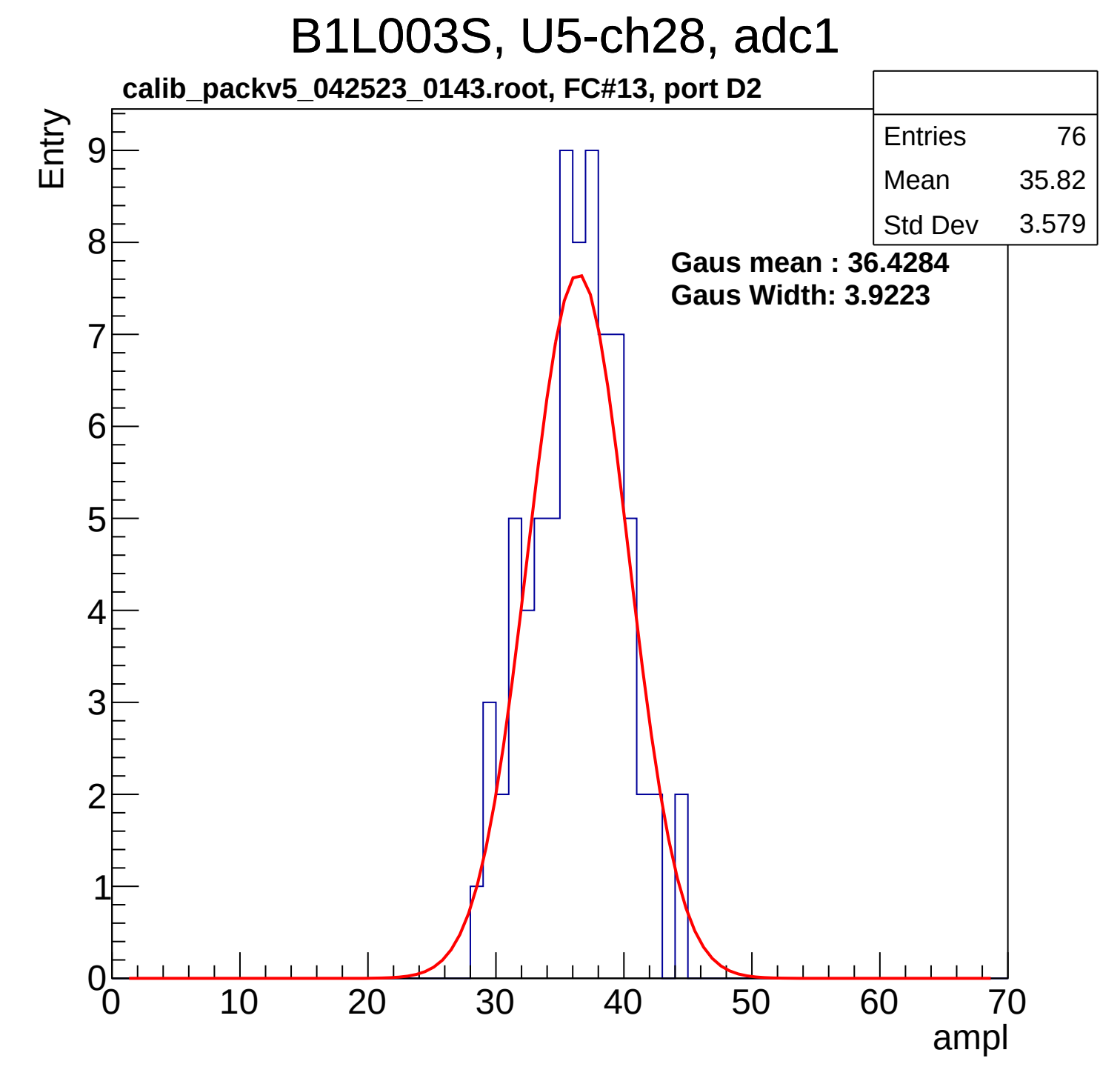
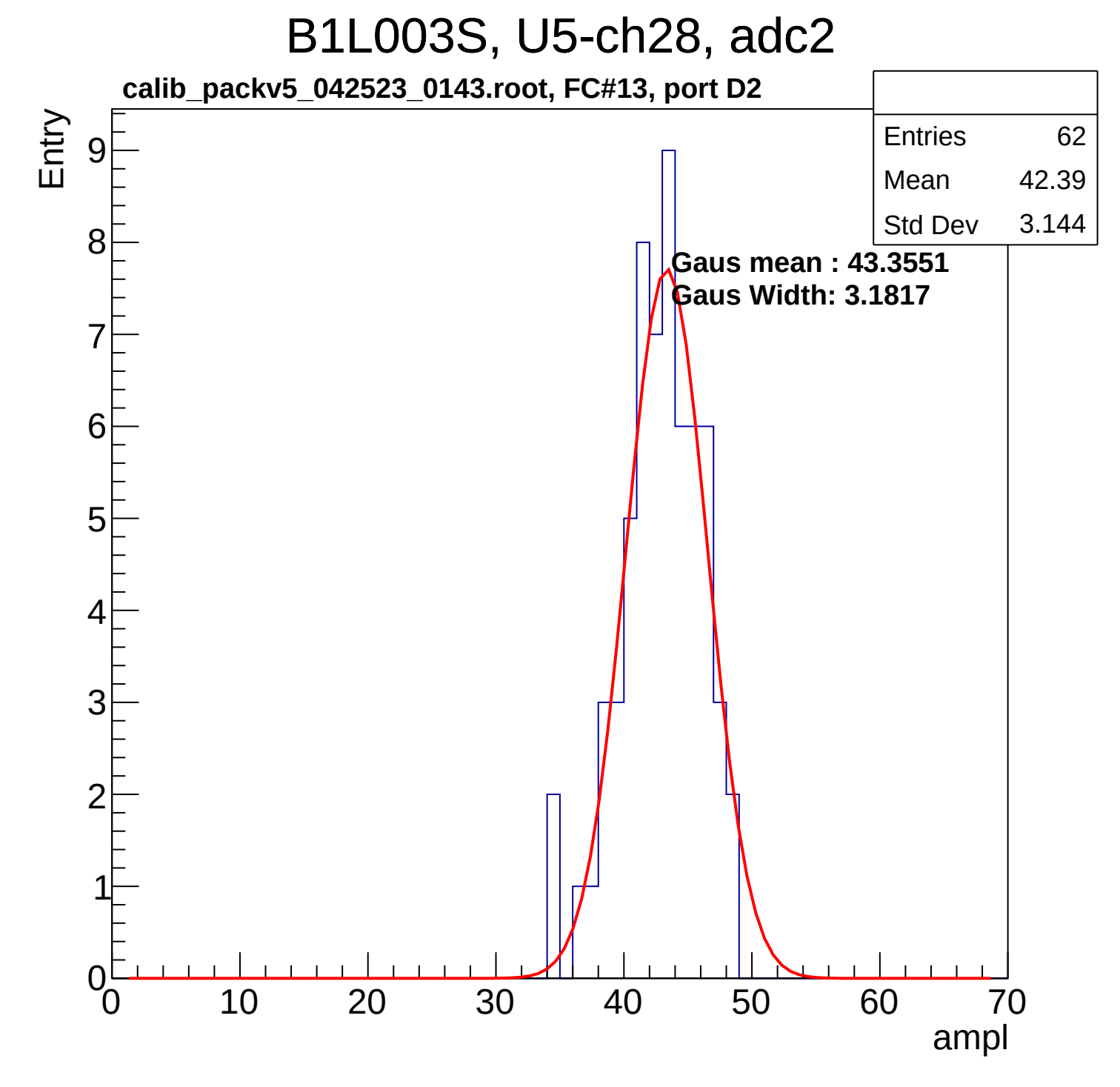
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.82
Std Dev 3.579
Gaus mean : 36.4284
Gaus Width: 3.9223
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.39
Std Dev 3.144
Gaus mean : 43.3551
Gaus Width: 3.1817
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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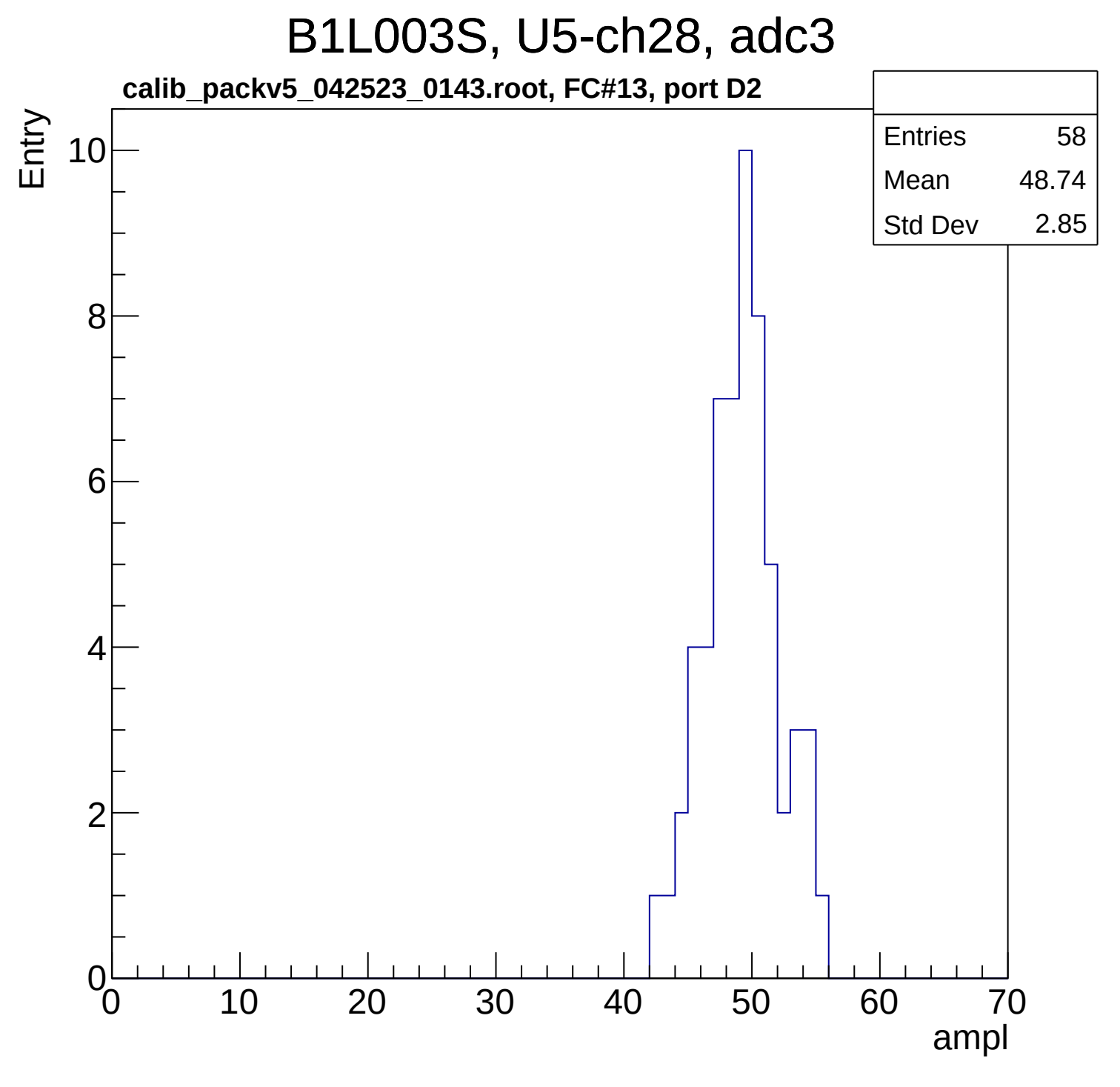
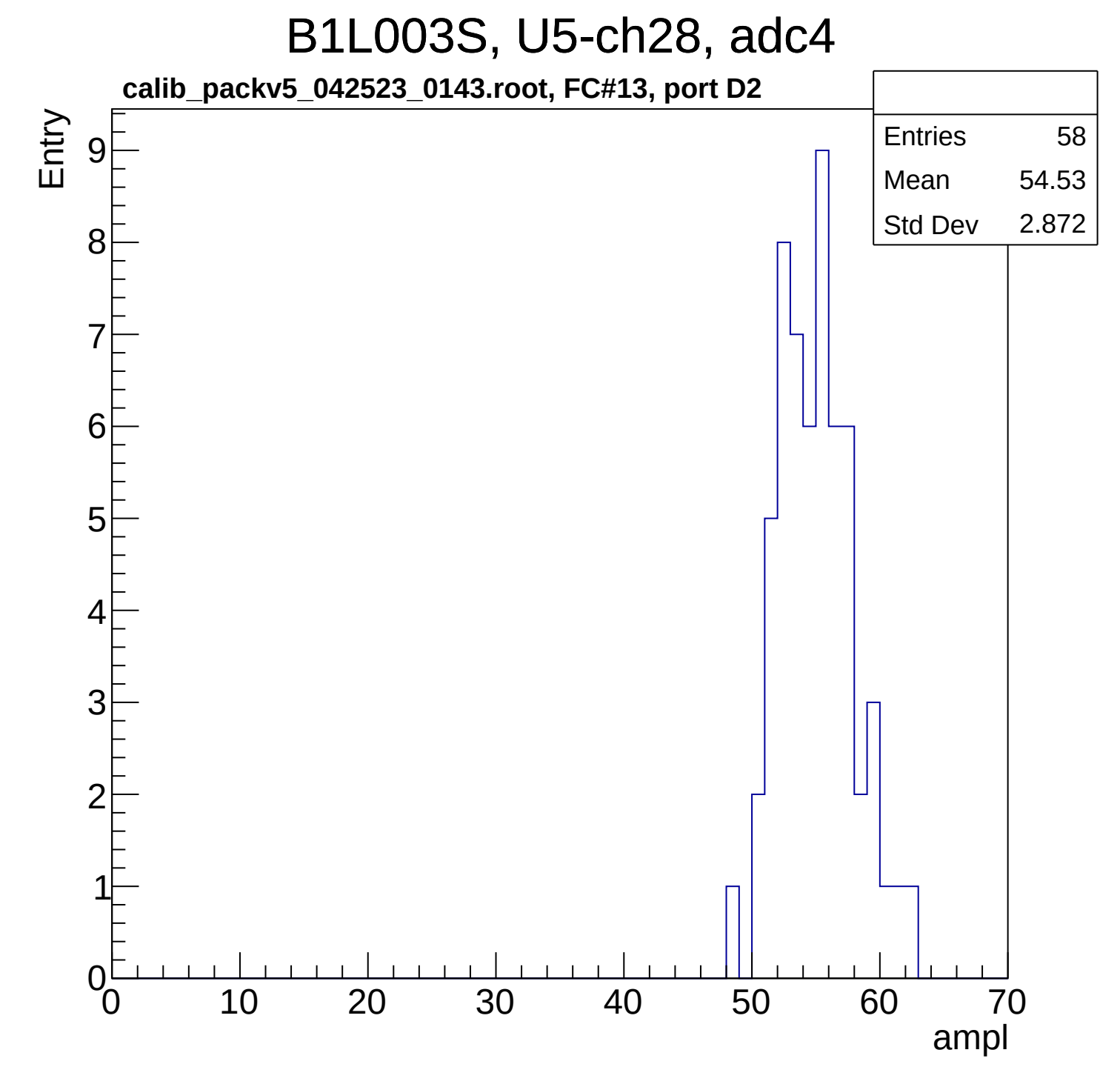

B1L003S, U5-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.74
Std Dev 2.85
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.53
Std Dev 2.872
Entry
ampl
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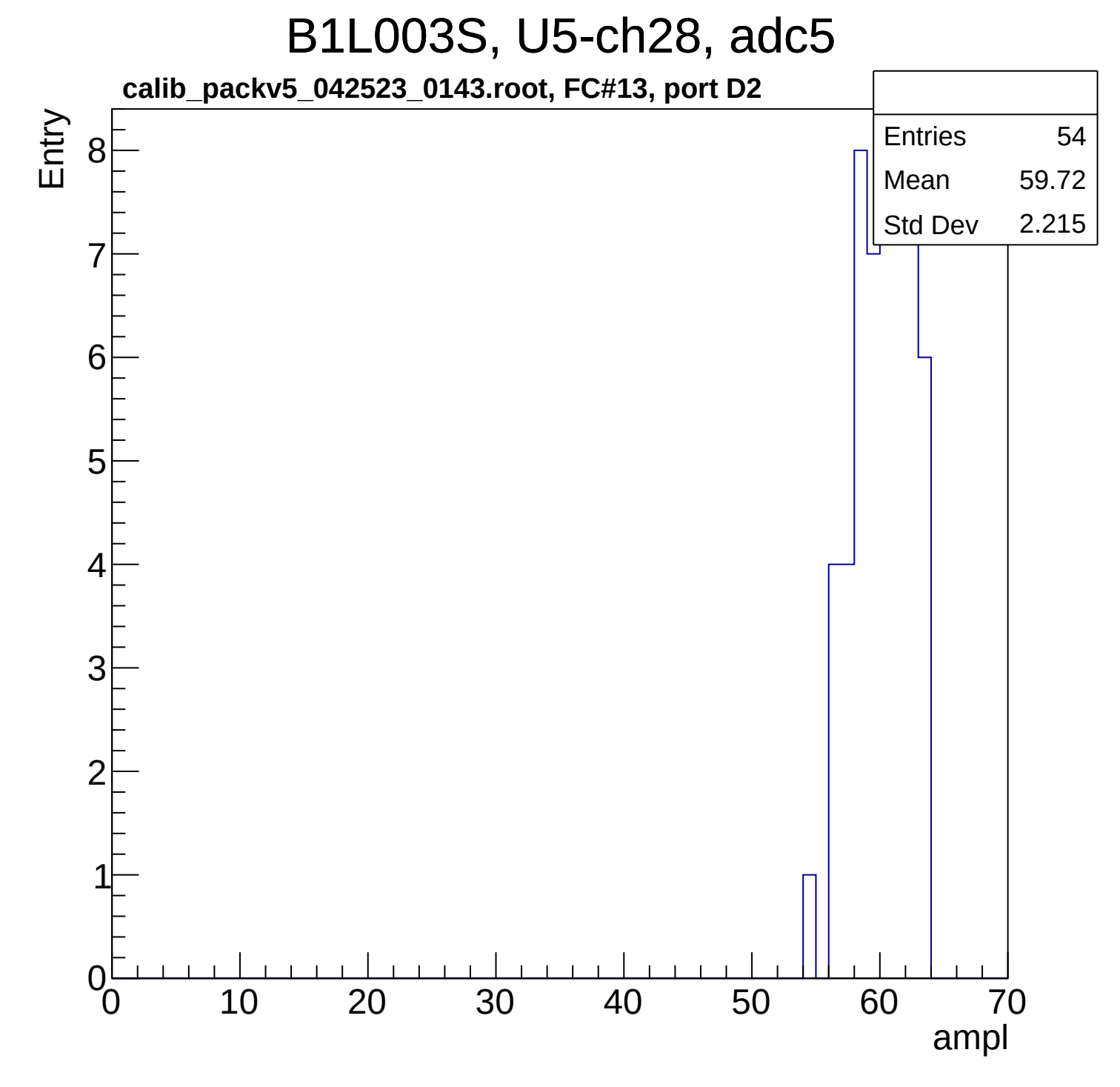
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch28, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.72
Std Dev 2.215
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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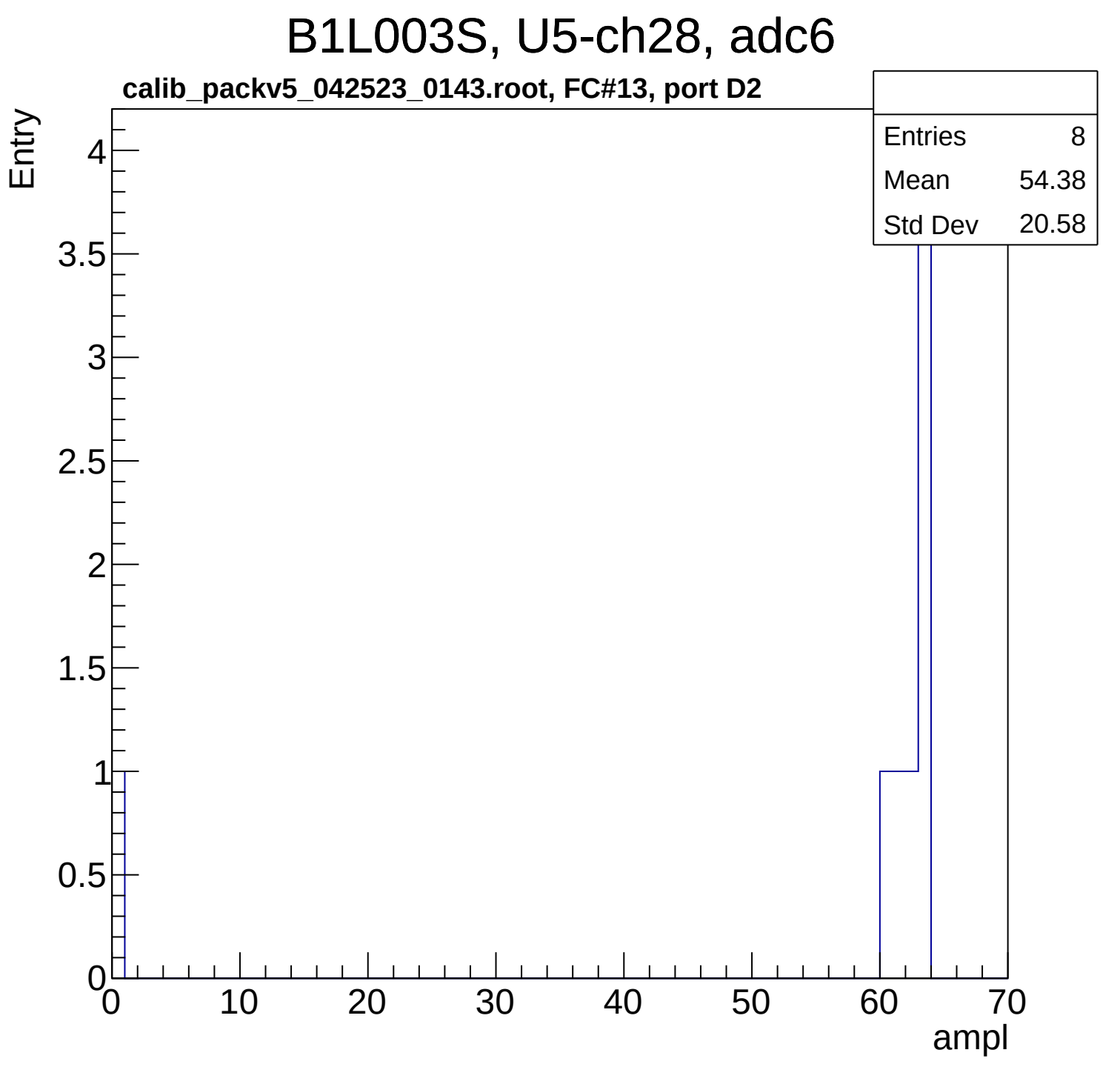
B1L003S, U5-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.38
Std Dev 20.58
Entry
ampl
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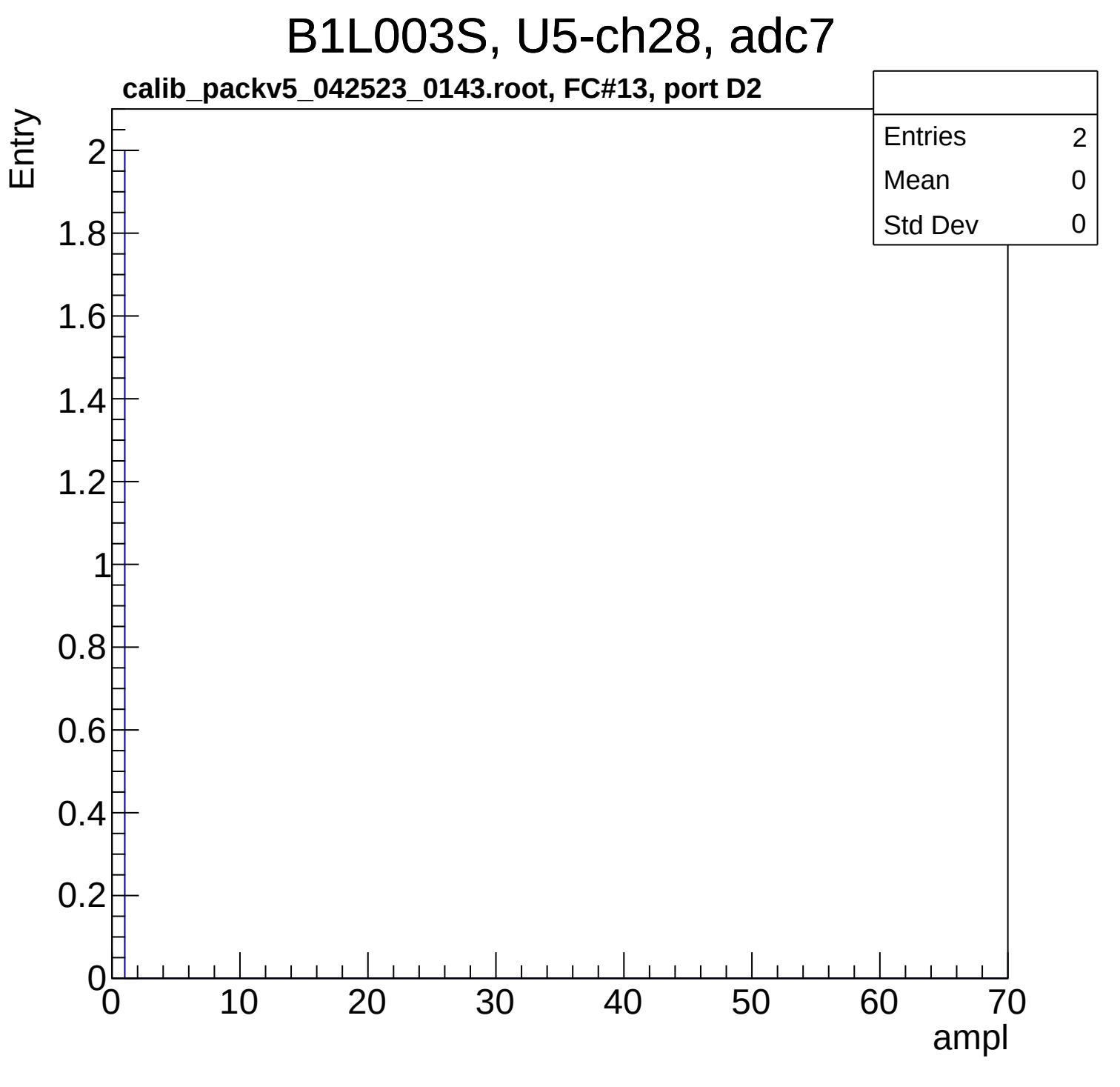
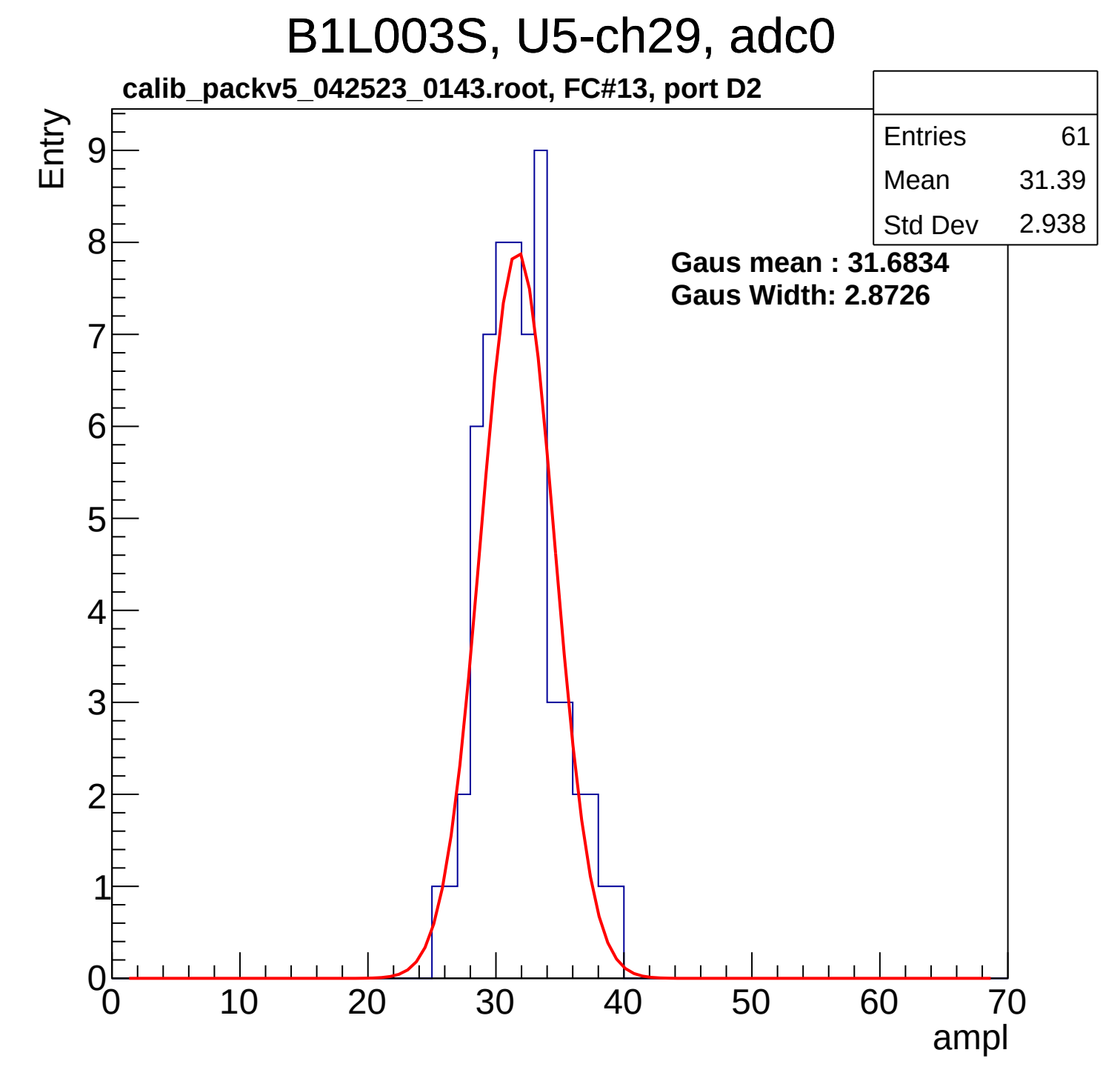
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 31.39
Std Dev 2.938
Gaus mean : 31.6834
Gaus Width: 2.8726
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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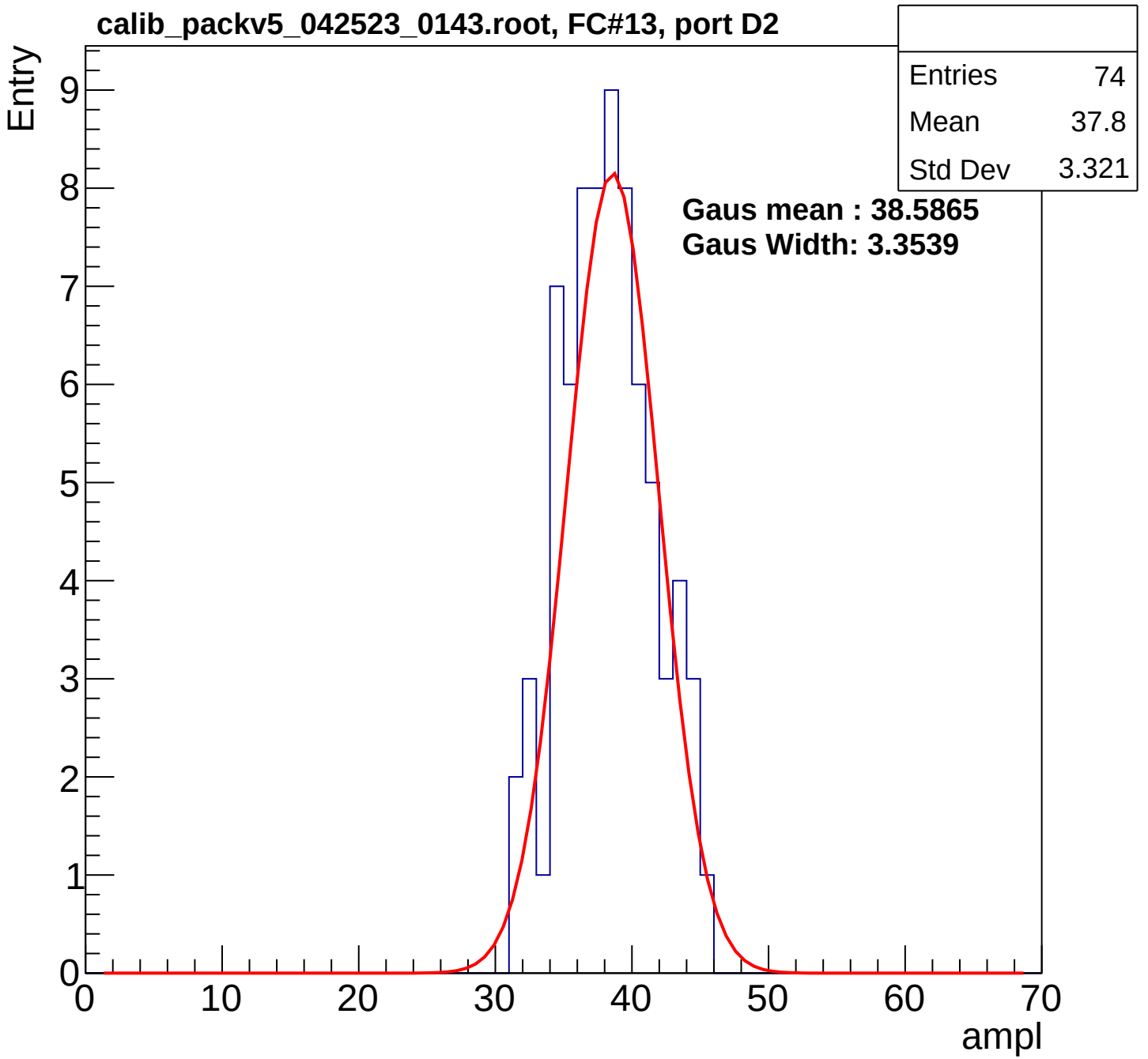
60
70


B1L003S, U5-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 37.8
Std Dev 3.321
Gaus mean : 38.5865
Gaus Width: 3.3539
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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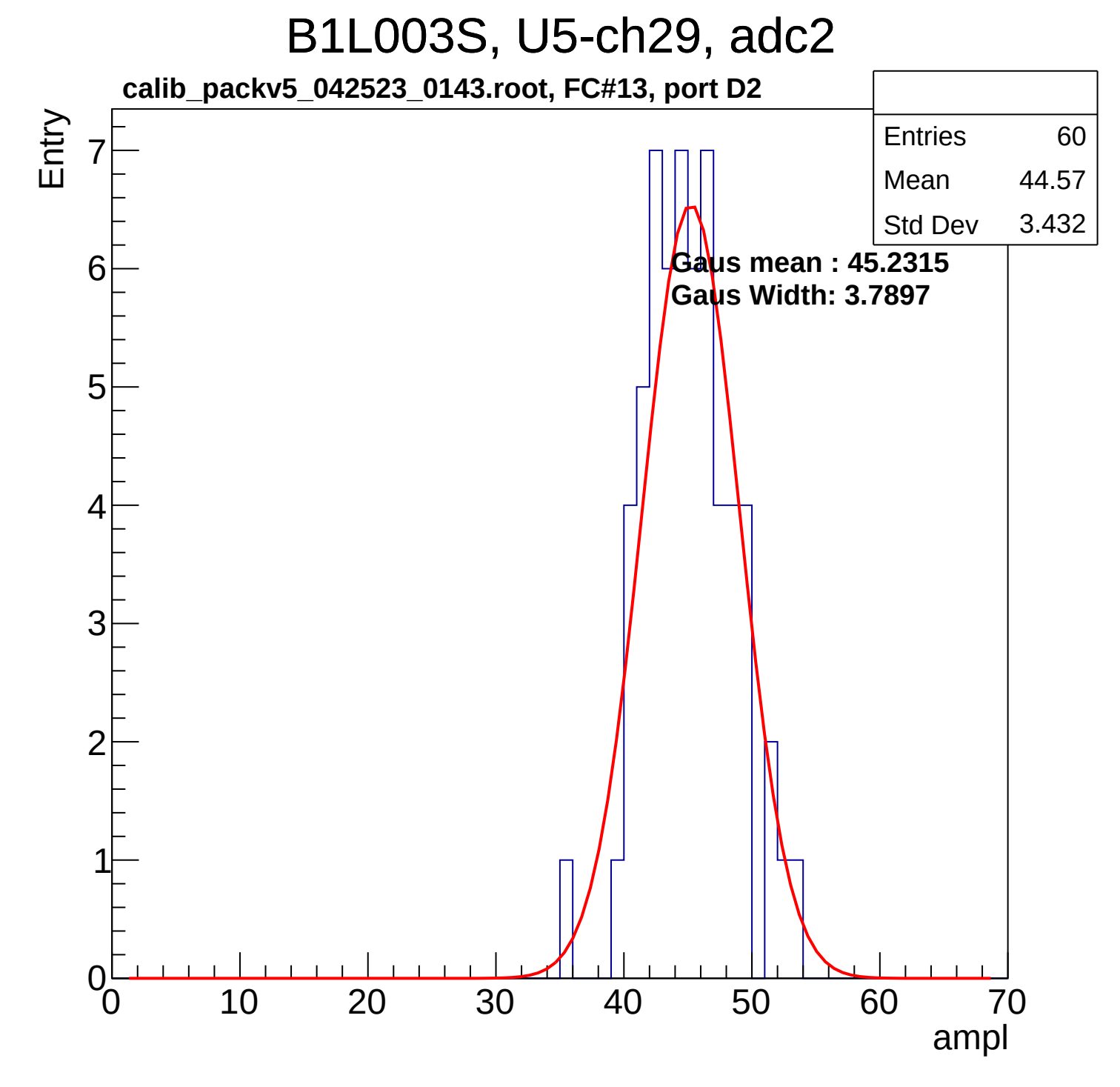
B1L003S, U5-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 44.57
Std Dev 3.432
Gaus mean : 45.2315
Gaus Width: 3.7897
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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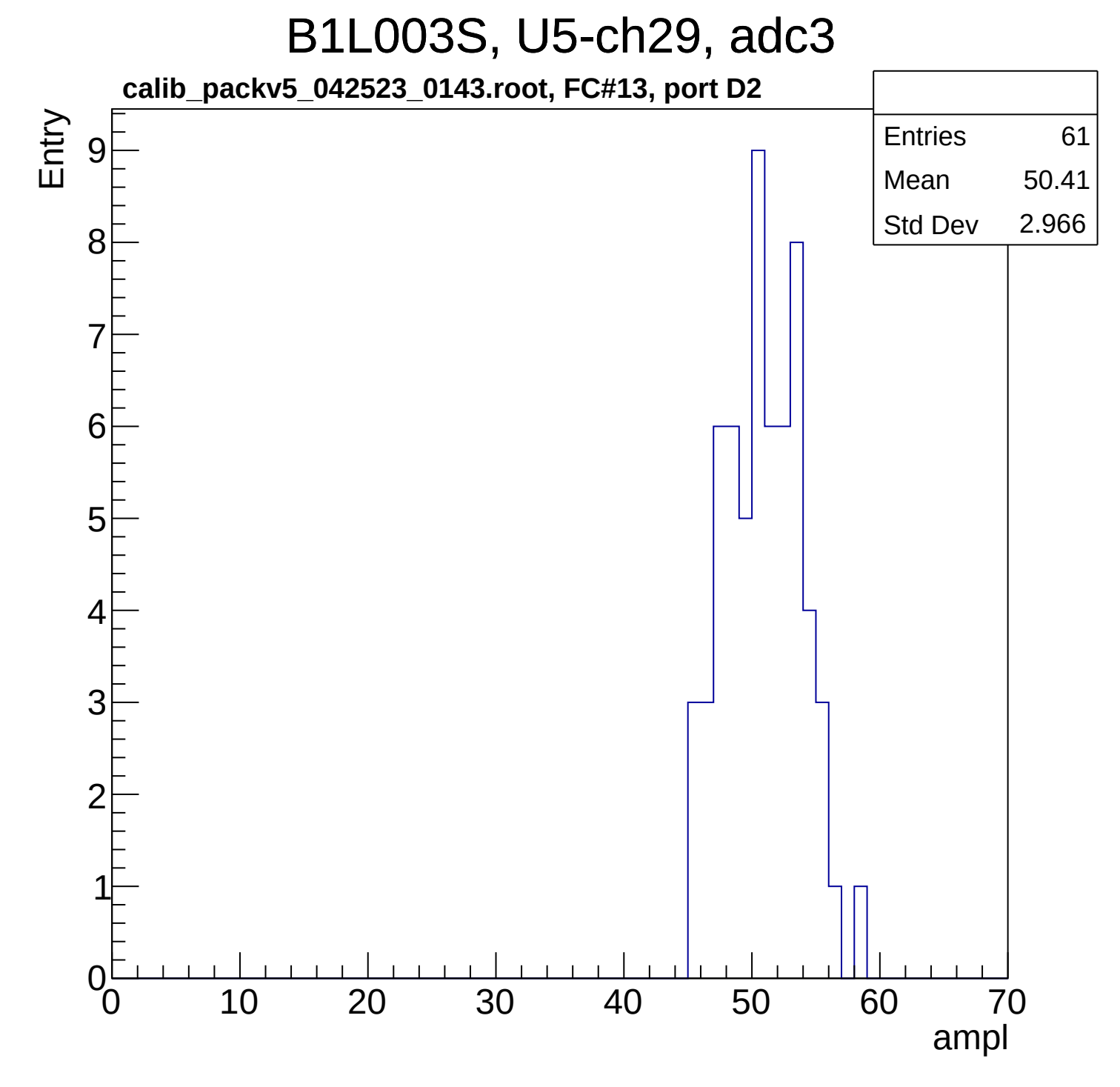
B1L003S, U5-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.41
Std Dev 2.966
Entry
ampl
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0
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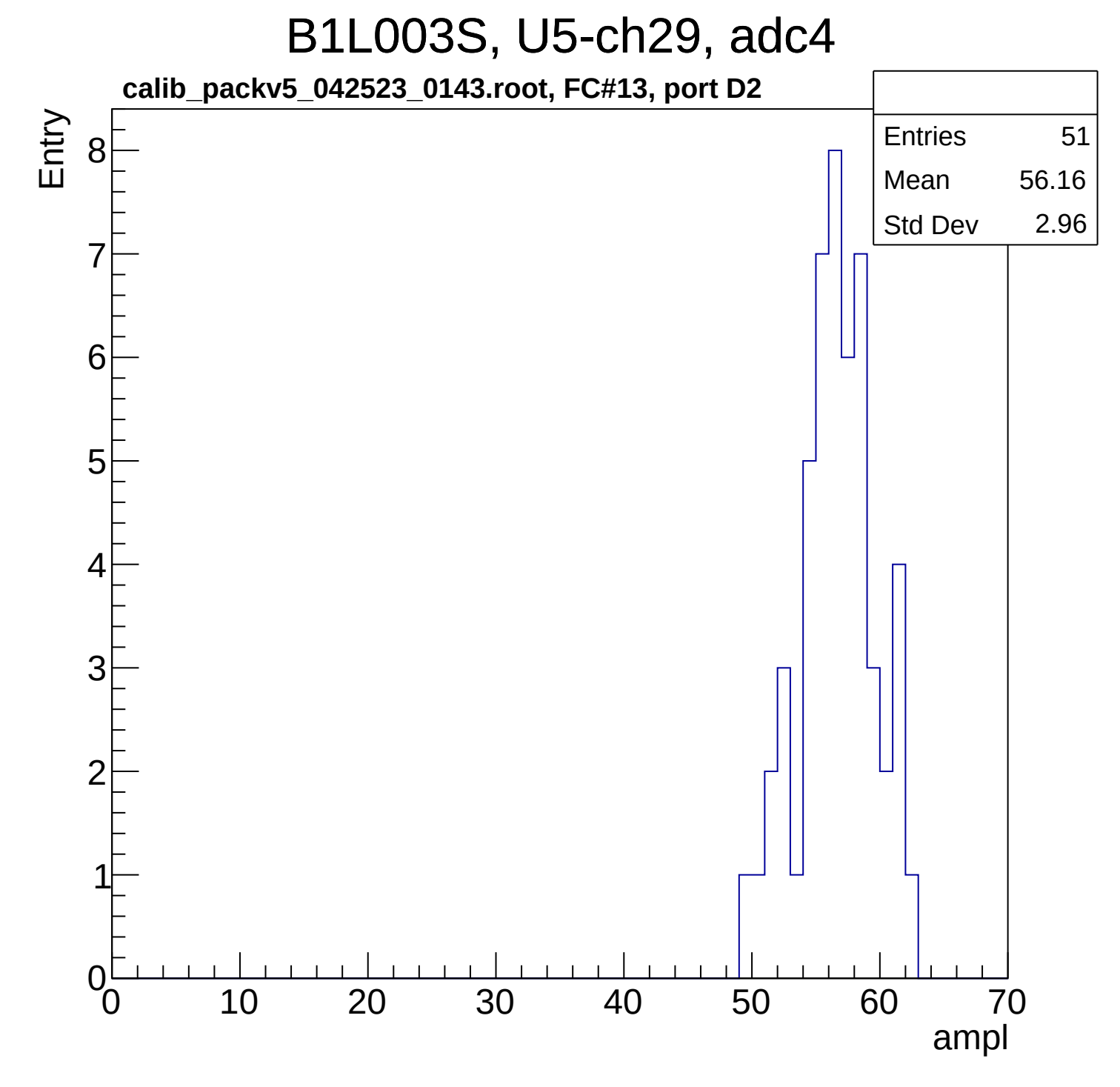
B1L003S, U5-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 56.16
Std Dev 2.96
Entry
ampl
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4
5
6
7
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0
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20
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60
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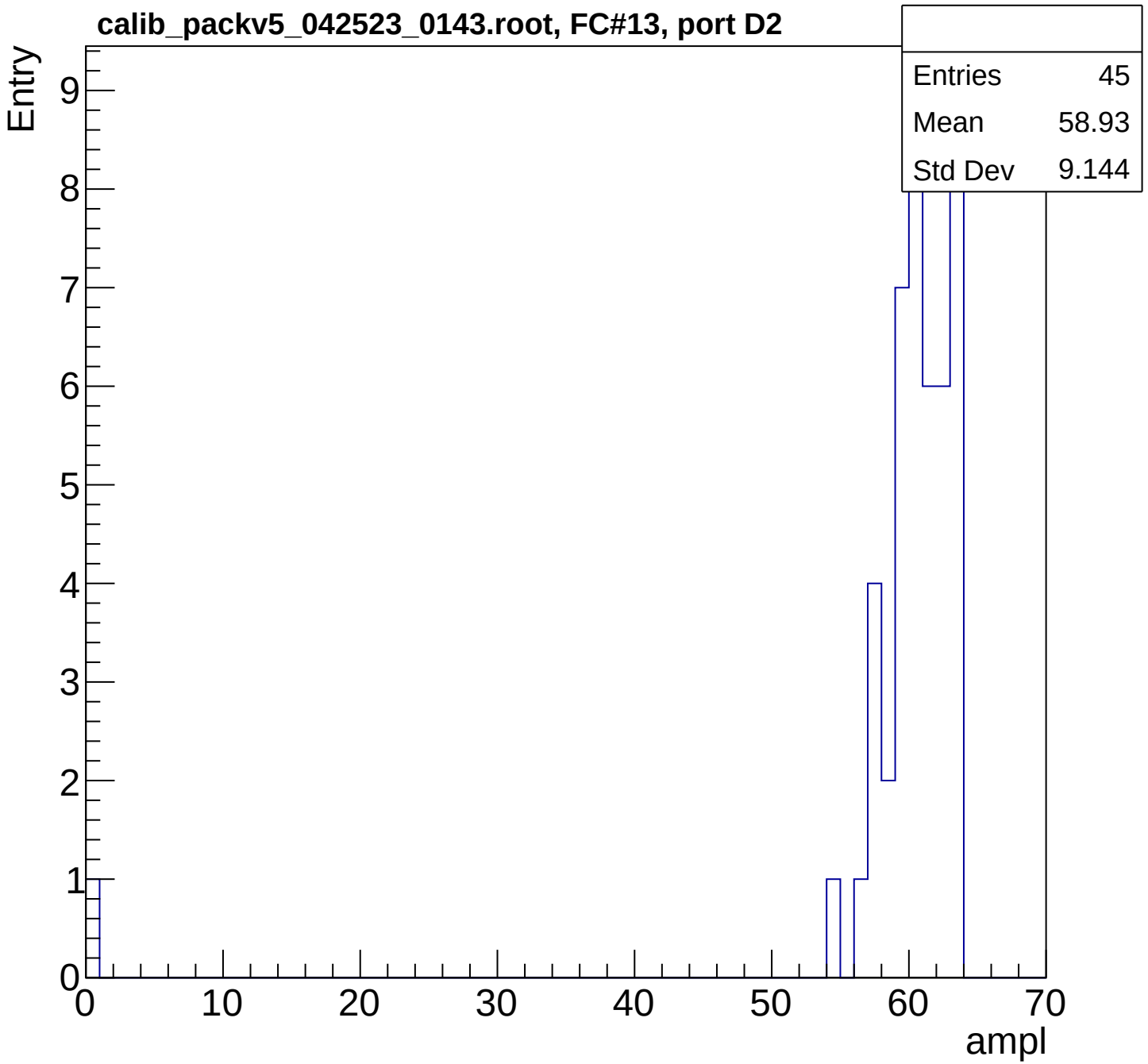
B1L003S, U5-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.93
Std Dev 9.144
Entry
ampl
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2
3
4
5
6
7
8
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0
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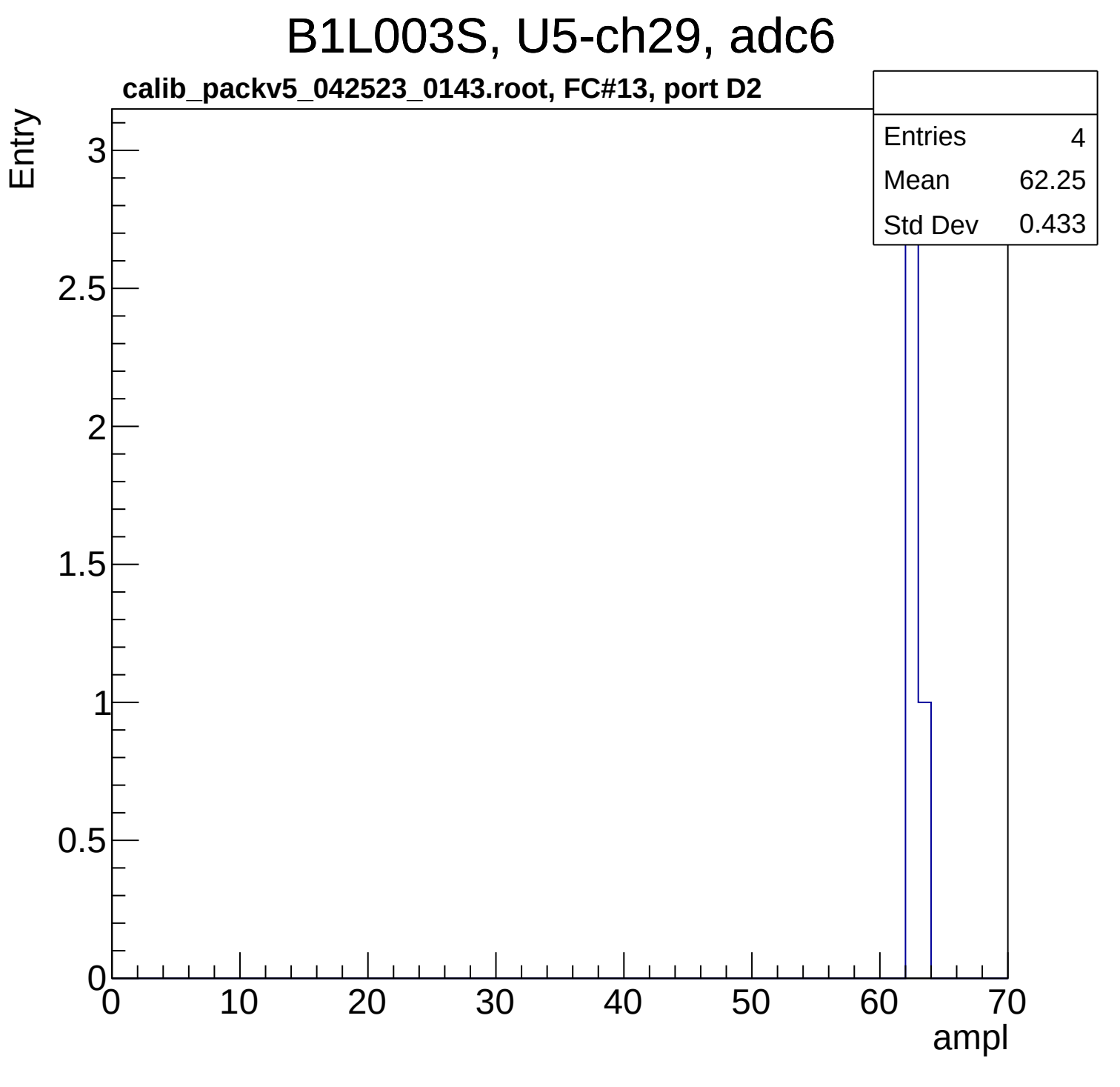
B1L003S, U5-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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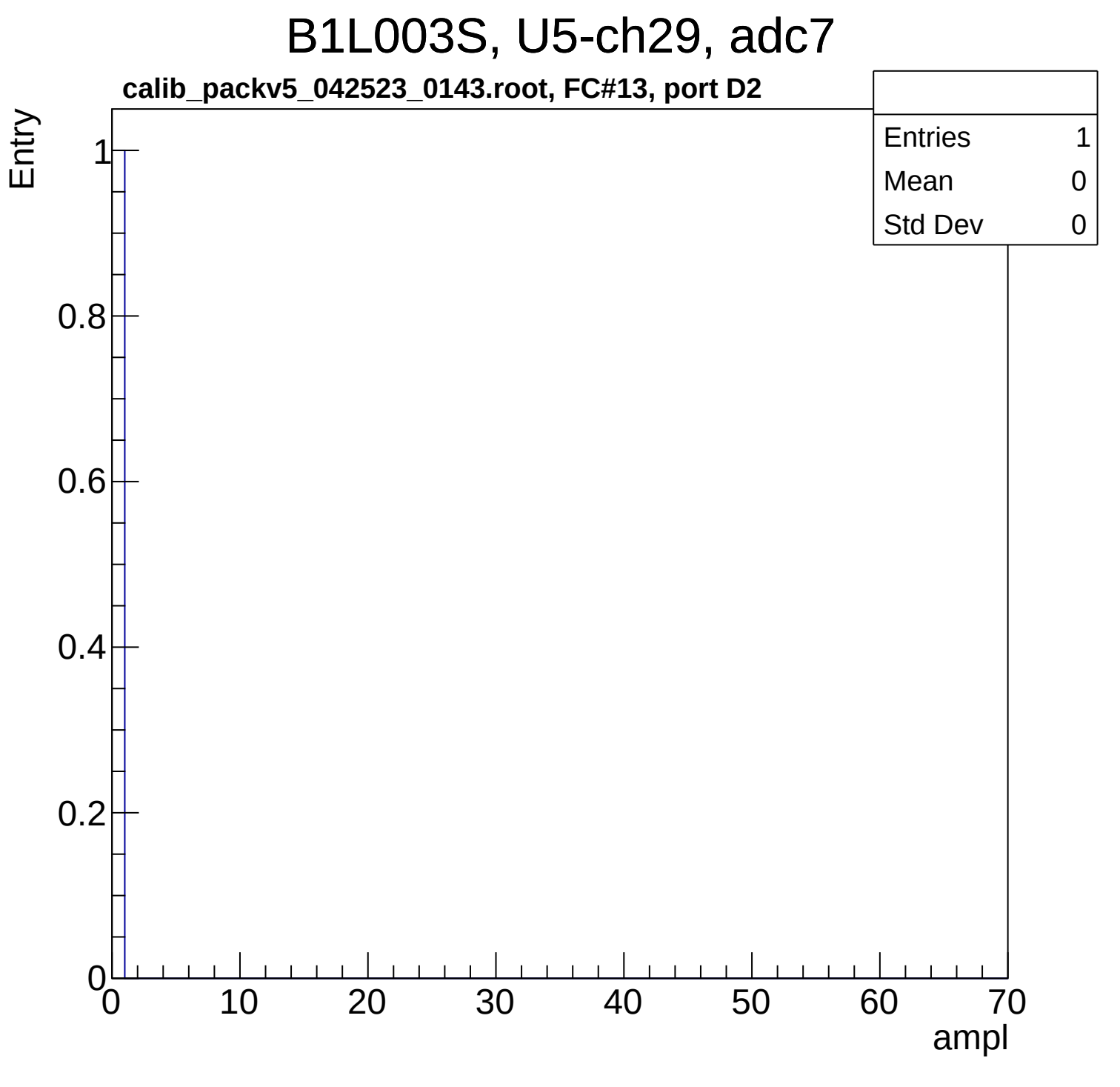
B1L003S, U5-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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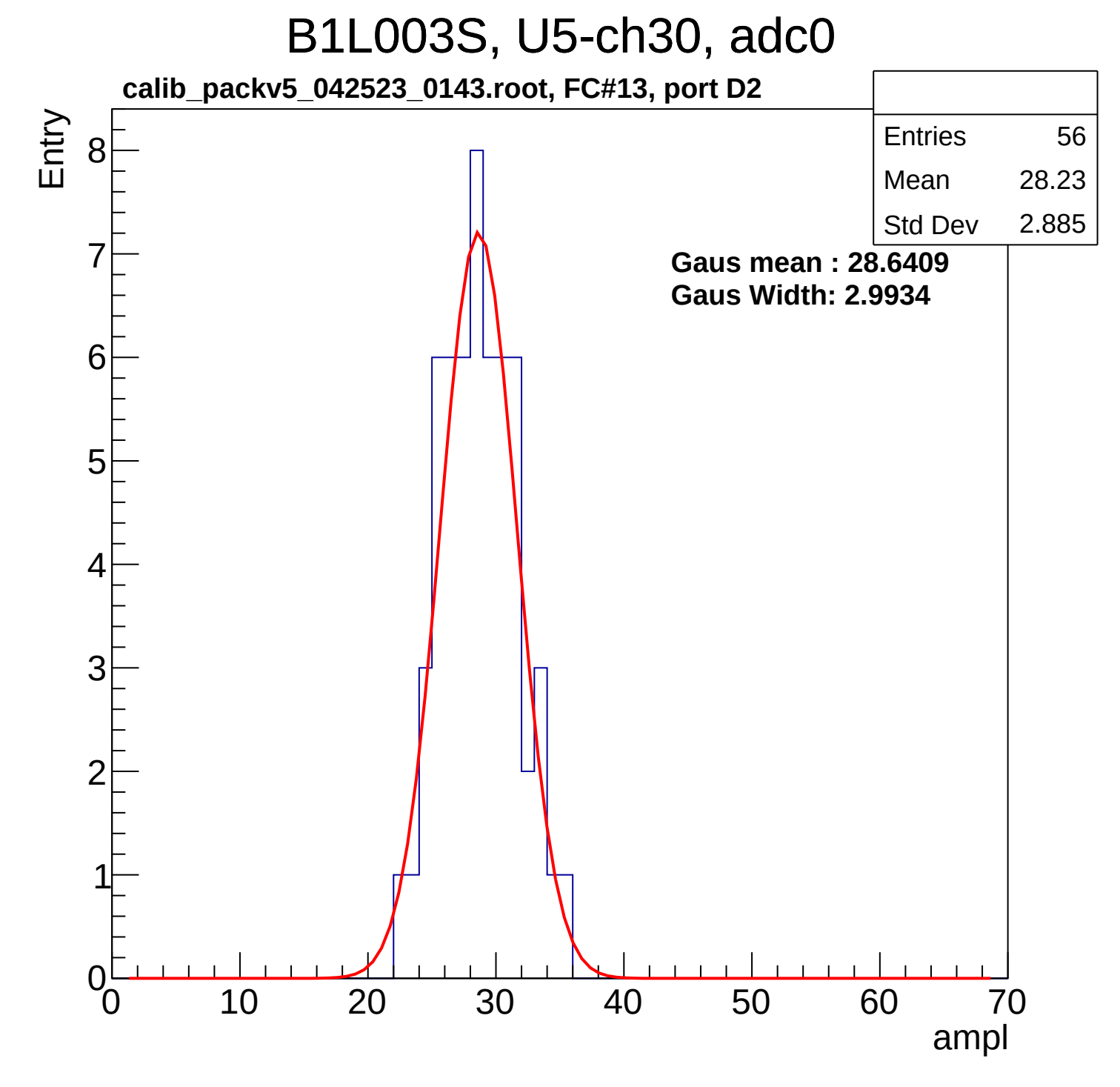
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch30, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 28.23
Std Dev 2.885
Gaus mean : 28.6409
Gaus Width: 2.9934
Entry
ampl
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2
3
4
5
6
7
8
0
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20
30
40
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60
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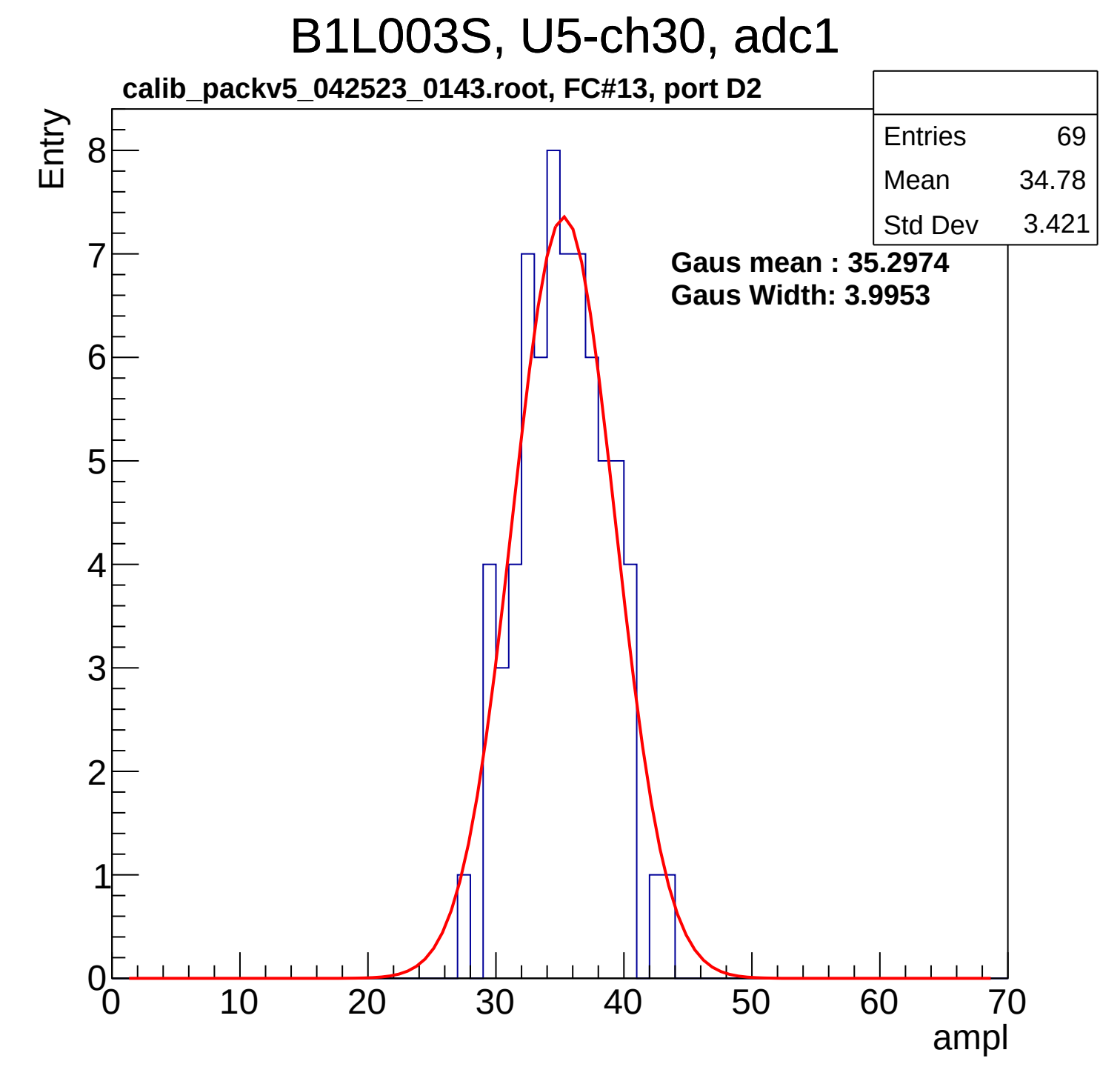
B1L003S, U5-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 34.78
Std Dev 3.421
Gaus mean : 35.2974
Gaus Width: 3.9953
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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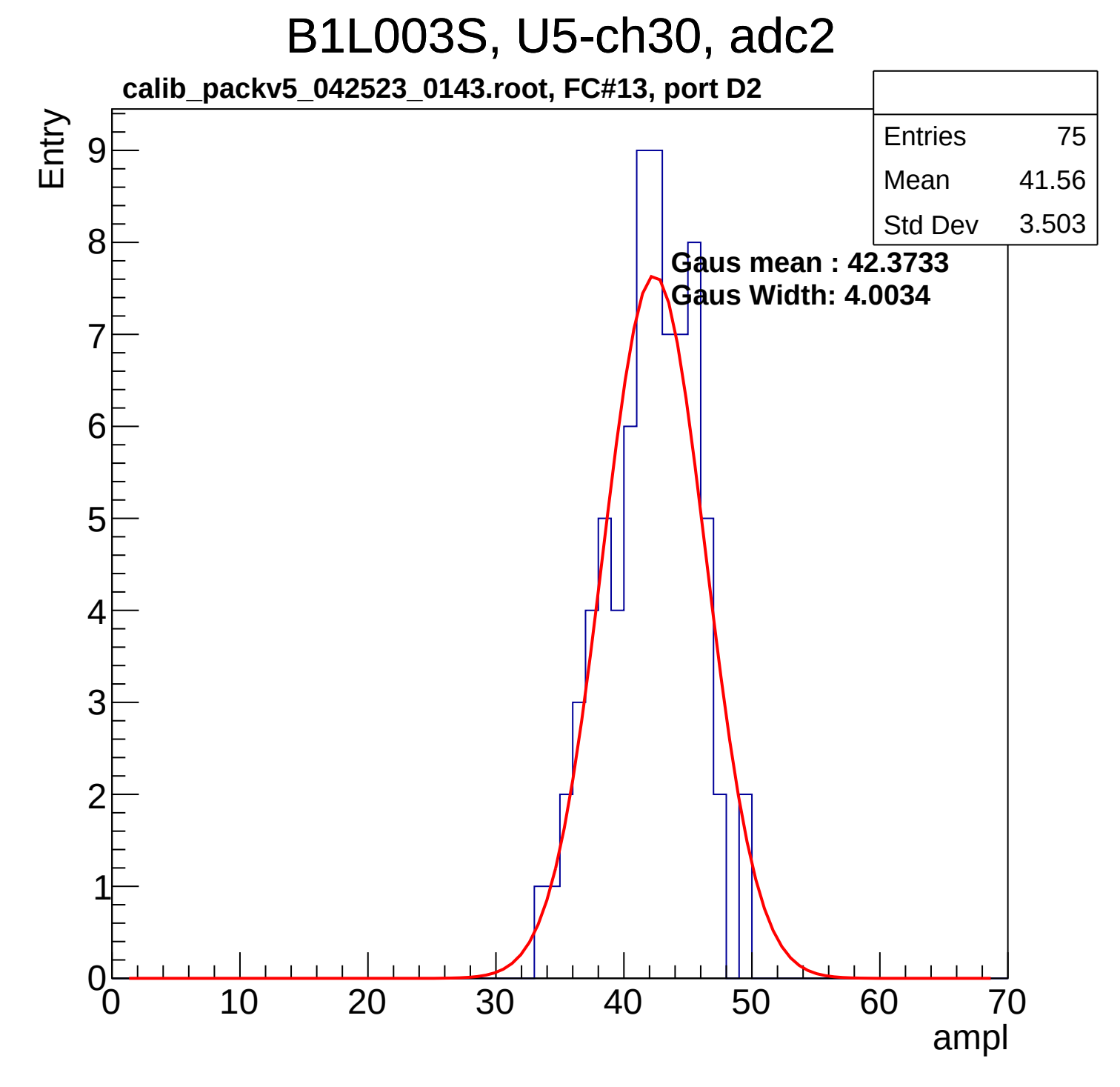
B1L003S, U5-ch30, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 41.56
Std Dev 3.503
Gaus mean : 42.3733
Gaus Width: 4.0034
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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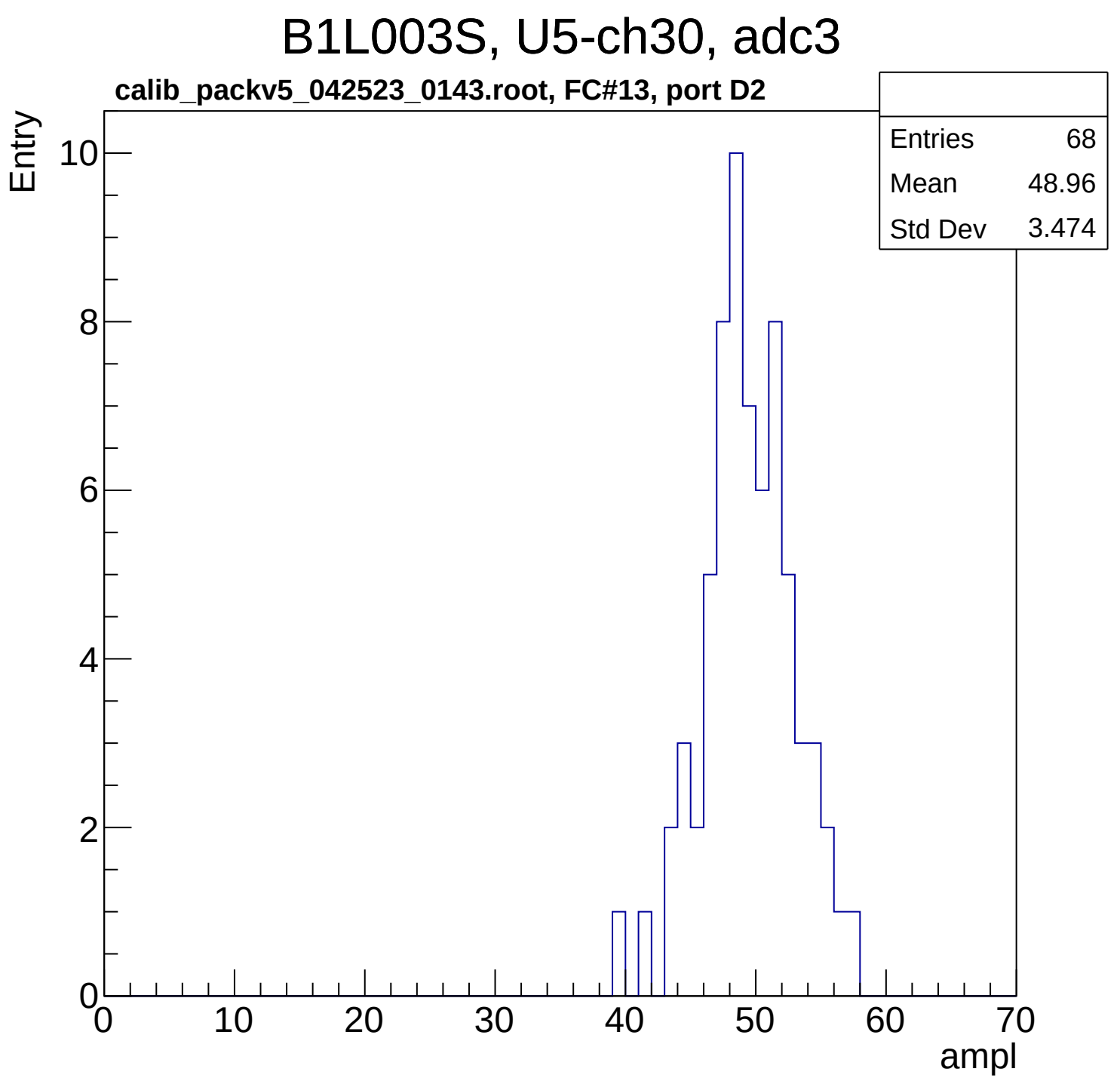
B1L003S, U5-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.96
Std Dev 3.474
Entry
ampl
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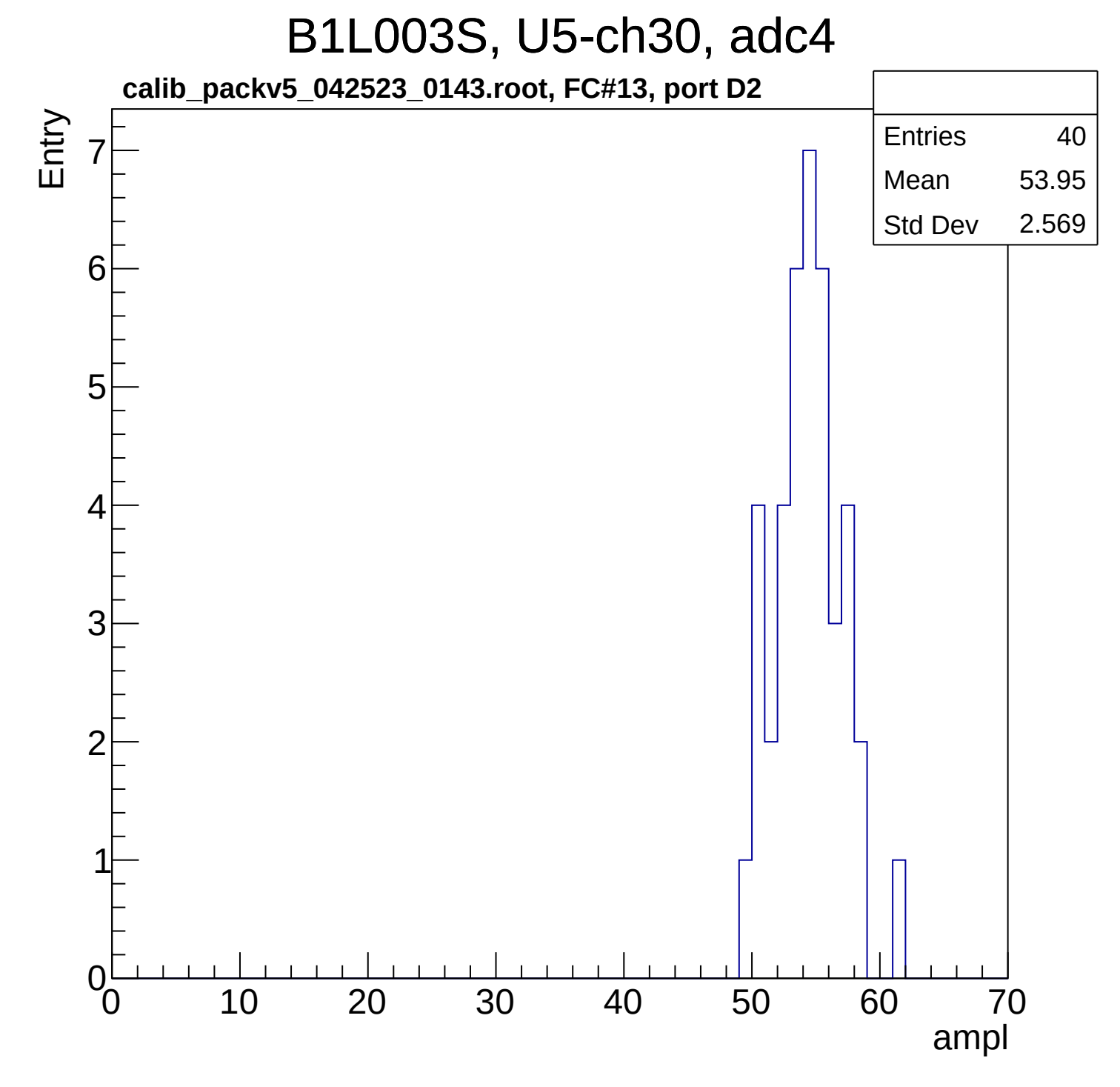
B1L003S, U5-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 53.95
Std Dev 2.569
Entry
ampl
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4
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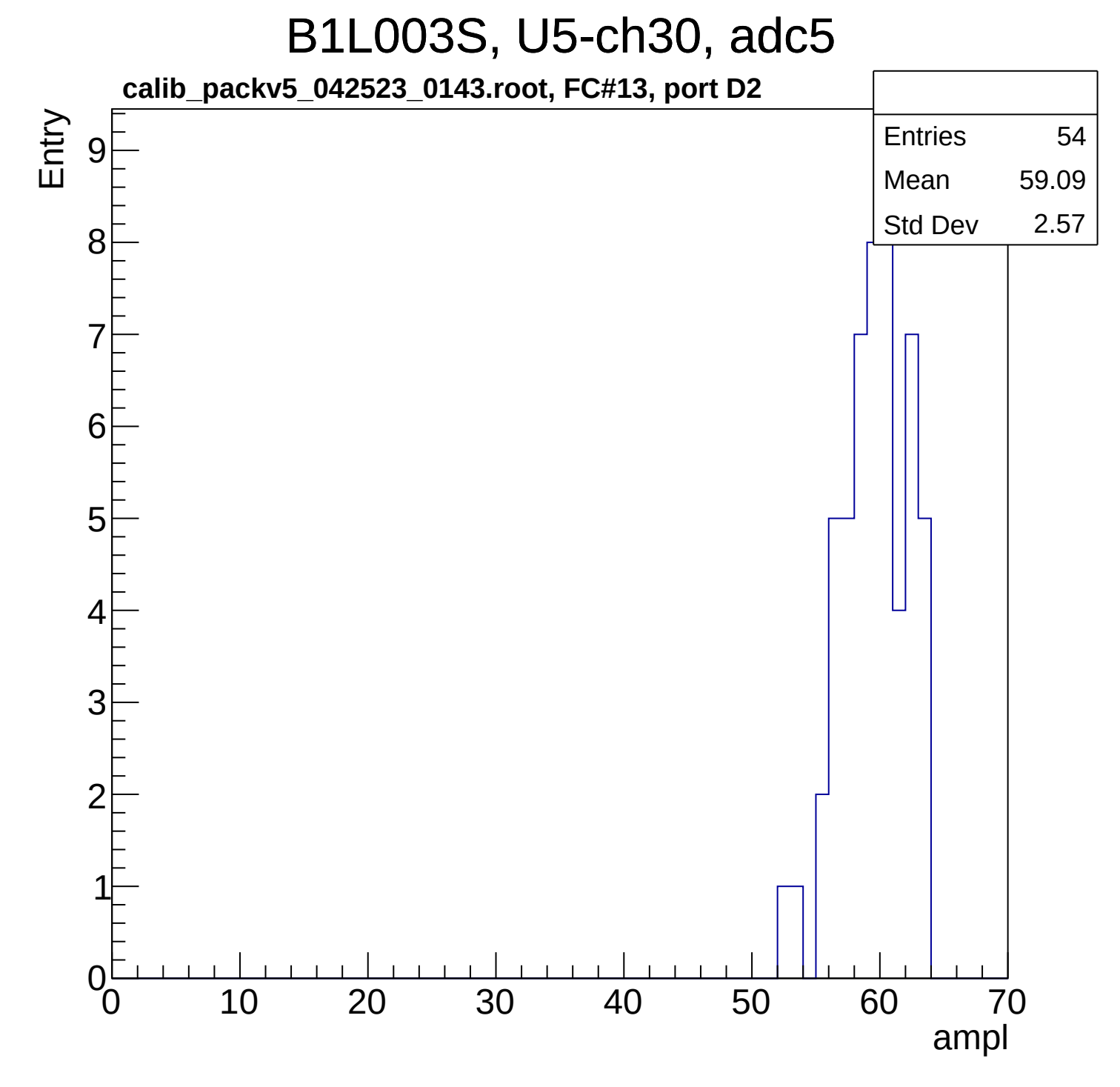
B1L003S, U5-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.09
Std Dev 2.57
Entry
ampl
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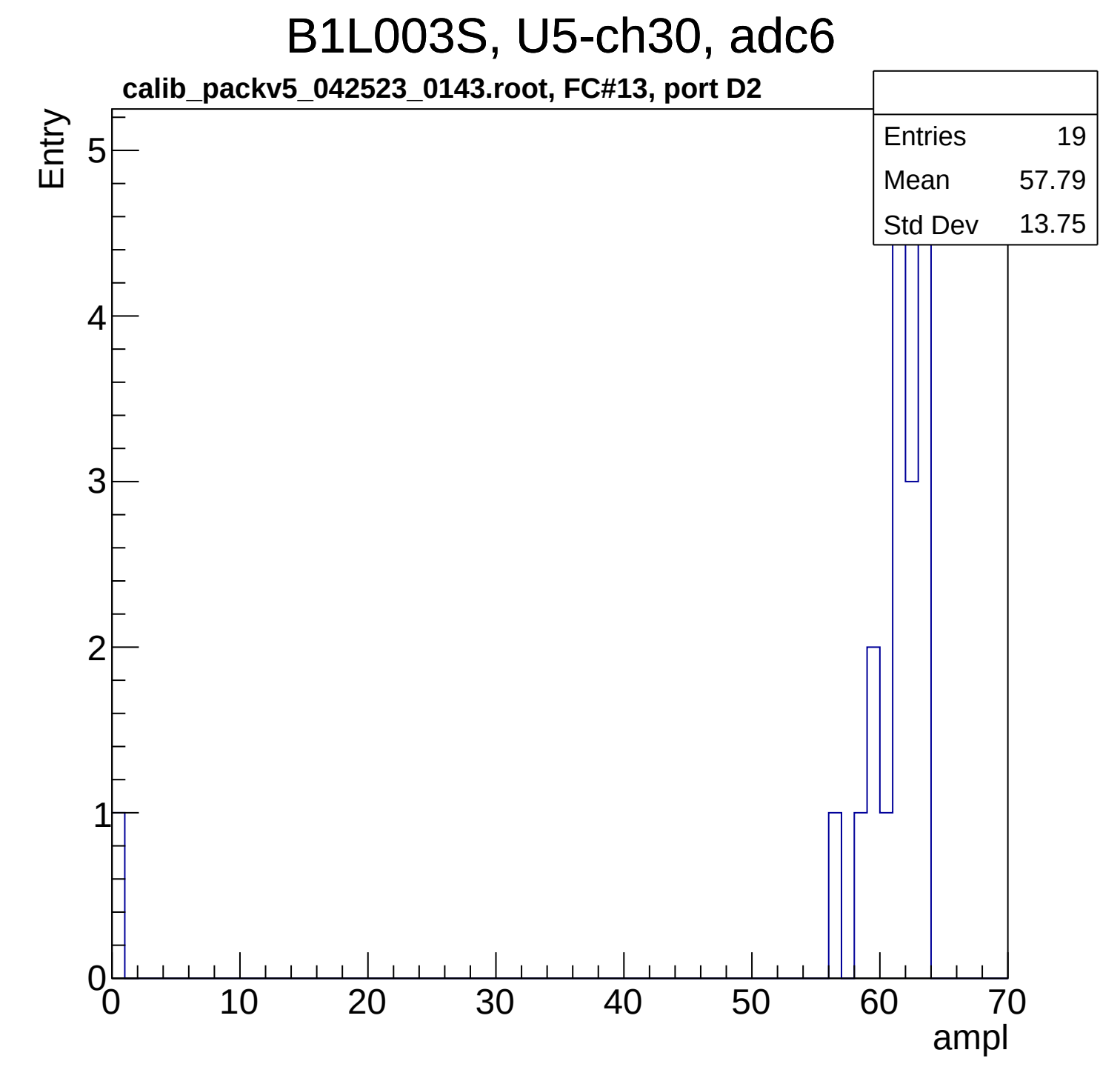
B1L003S, U5-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 57.79
Std Dev 13.75
Entry
0
1
2
3
4
5
0
10
20
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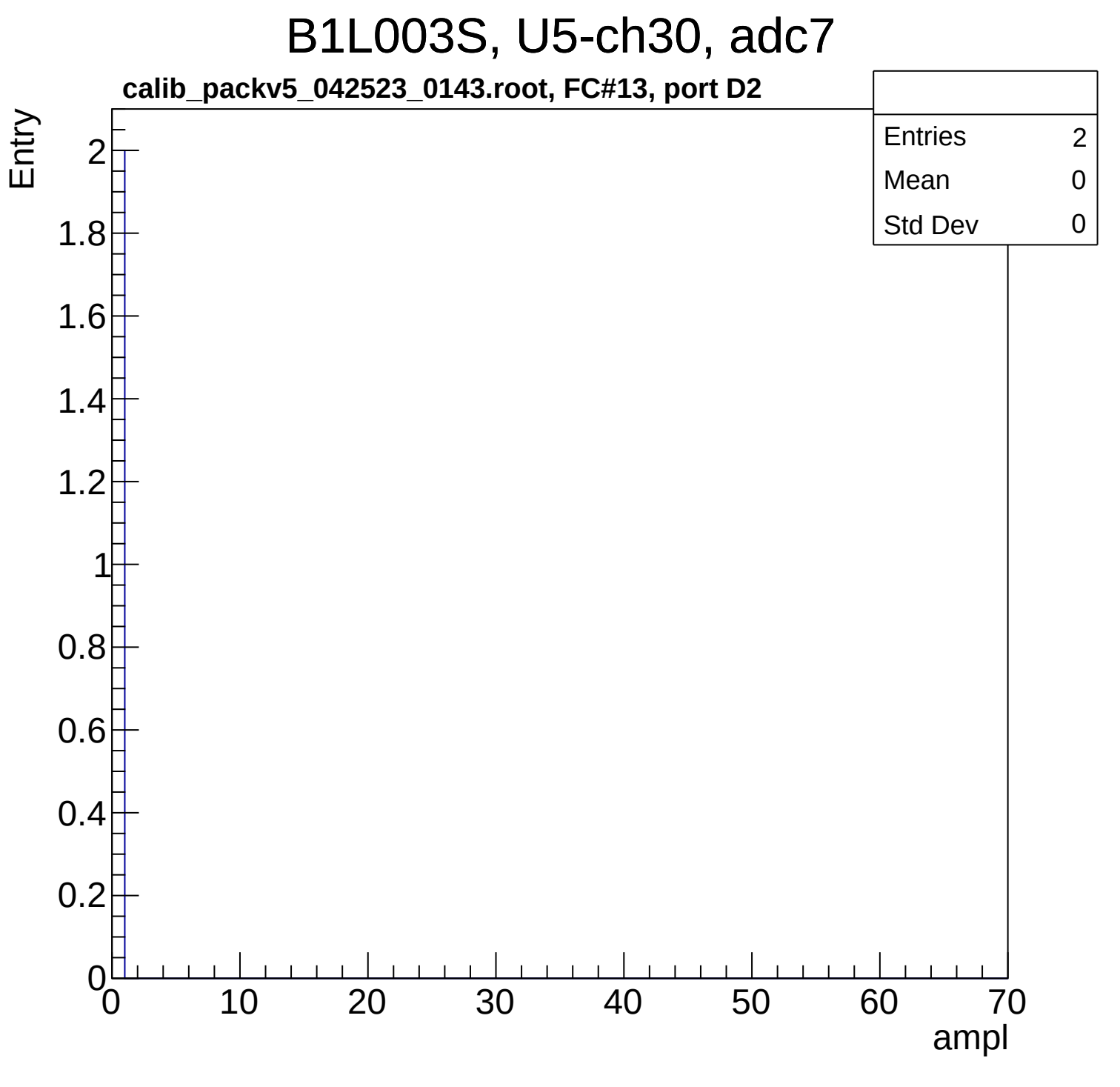
50
60
70
ampl


B1L003S, U5-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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0
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60
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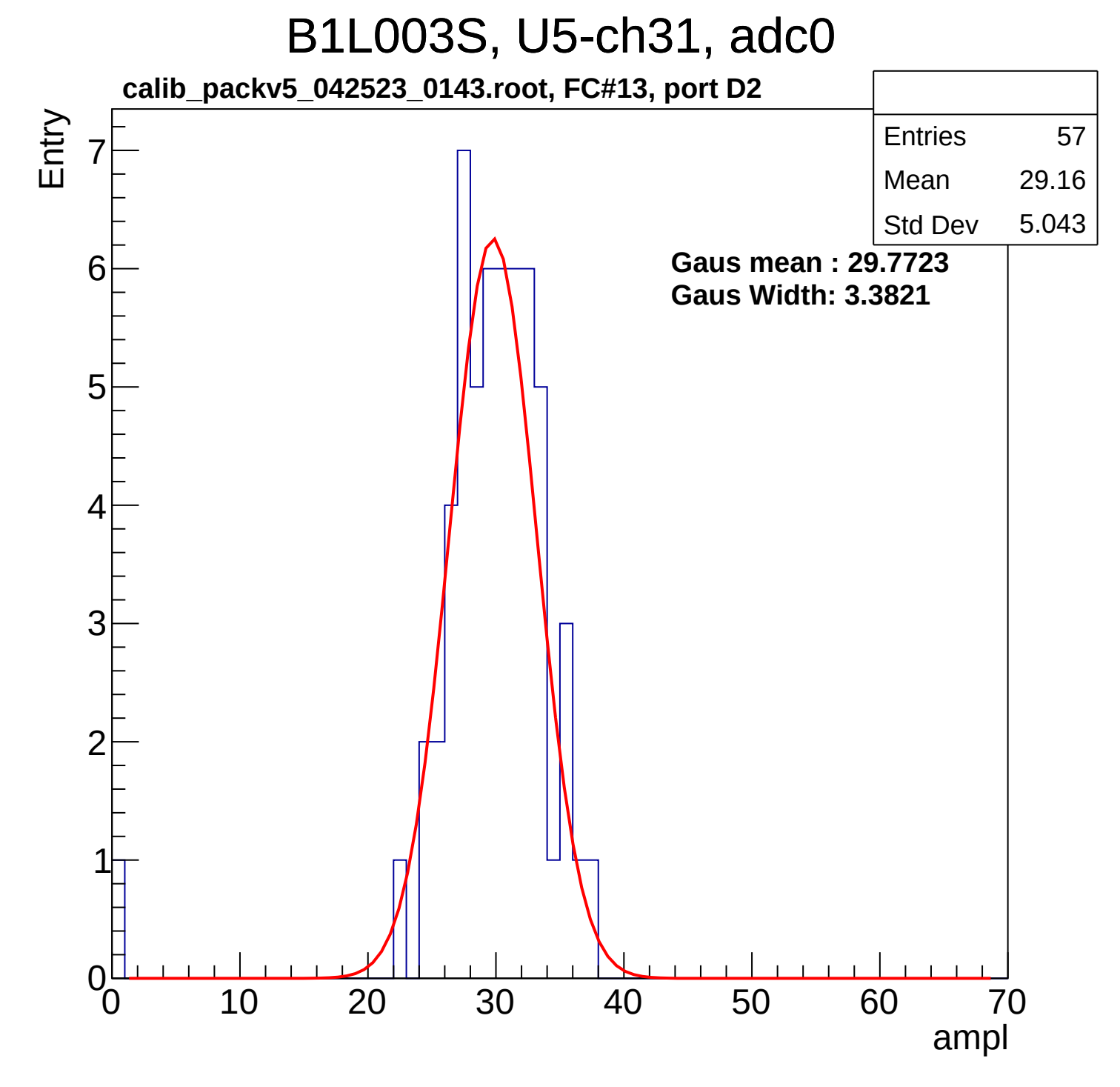
B1L003S, U5-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.16
Std Dev 5.043
Gaus mean : 29.7723
Gaus Width: 3.3821
Entry
ampl
0
1
2
3
4
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6
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0
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60
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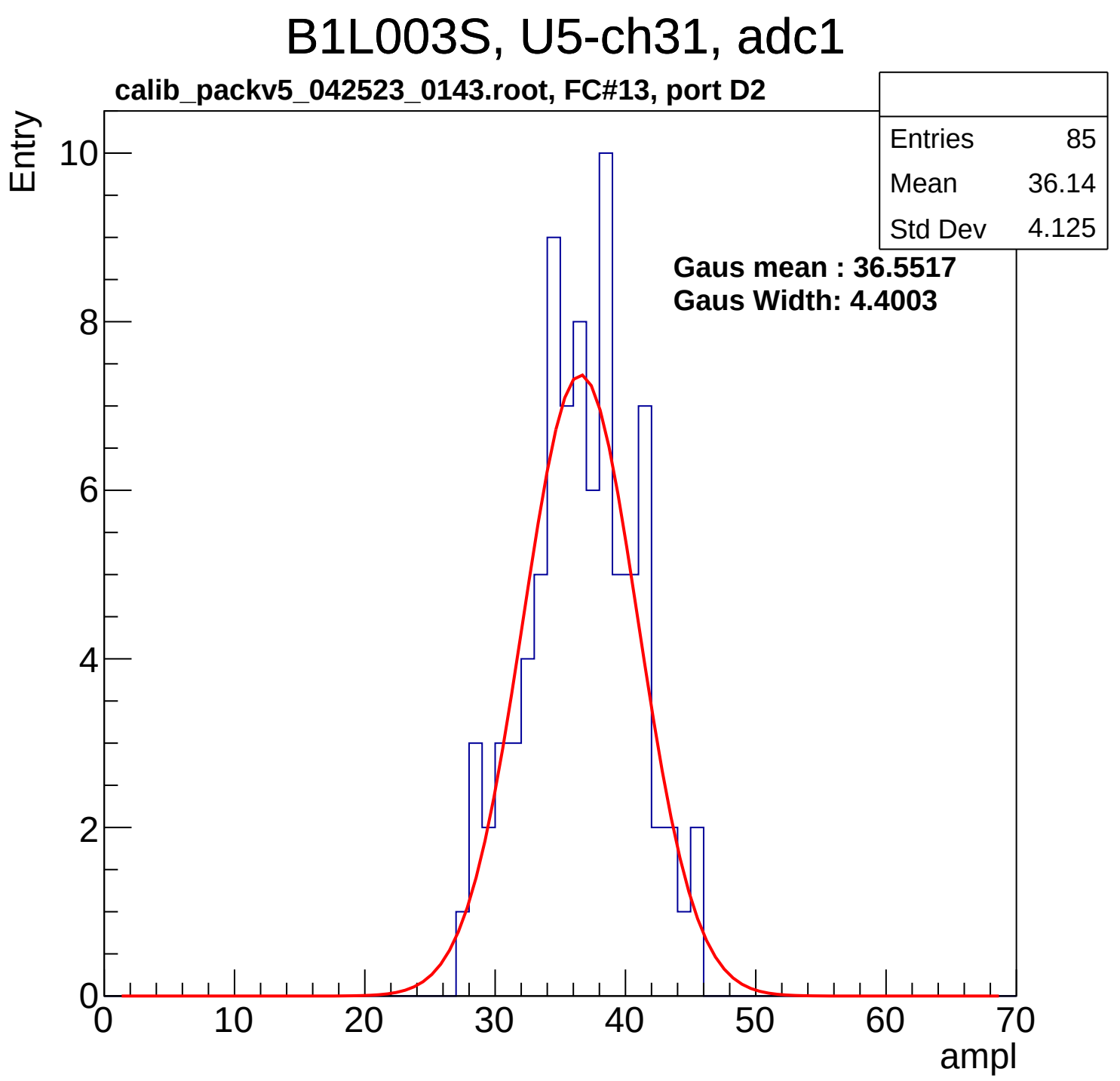
B1L003S, U5-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 36.14
Std Dev 4.125
Gaus mean : 36.5517
Gaus Width: 4.4003
Entry
ampl
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2
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0
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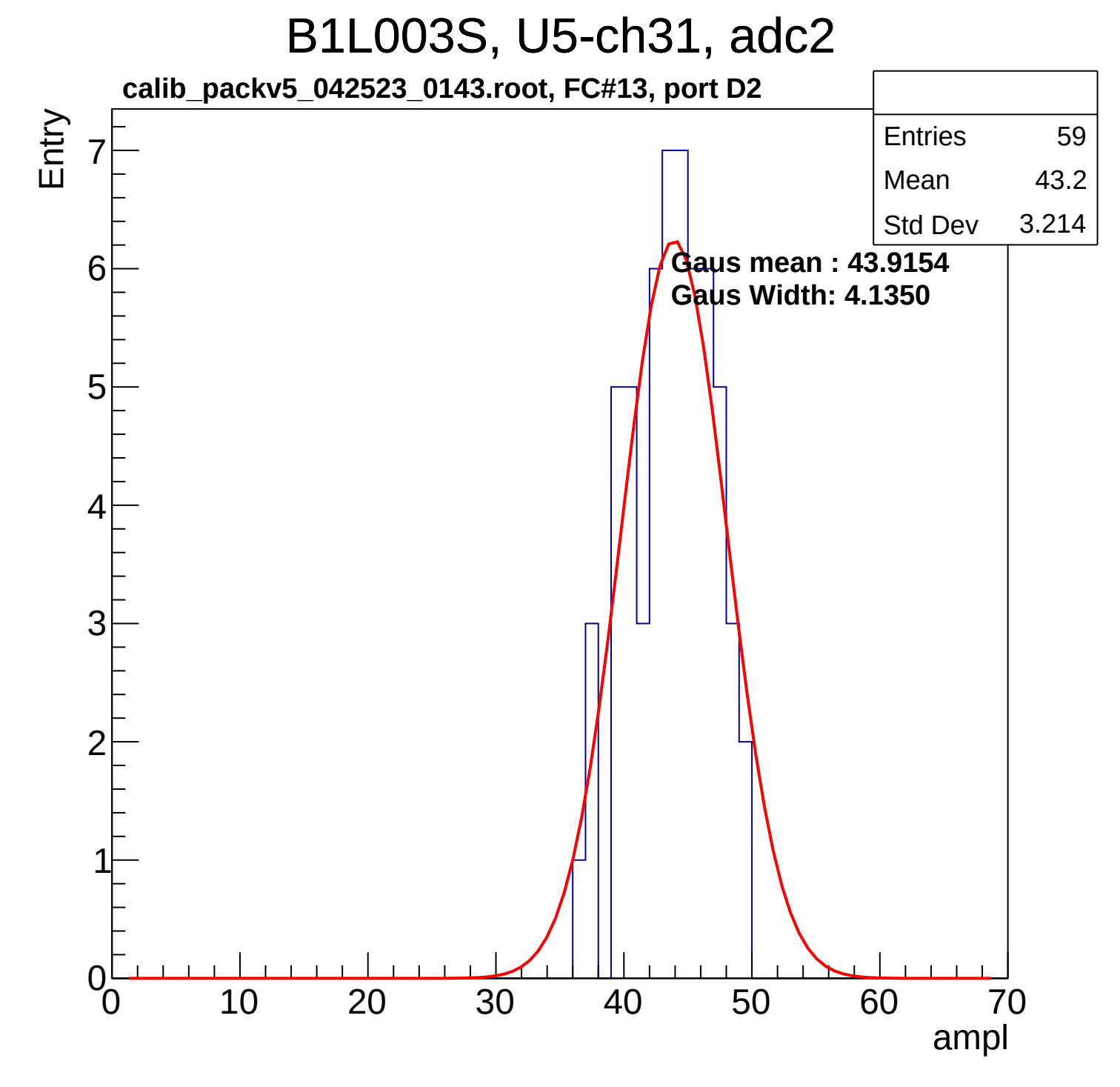
B1L003S, U5-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.2
Std Dev 3.214
Gaus mean : 43.9154
Gaus Width: 4.1350
Entry
ampl
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1
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3
4
5
6
7
0
10
20
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40
50
60
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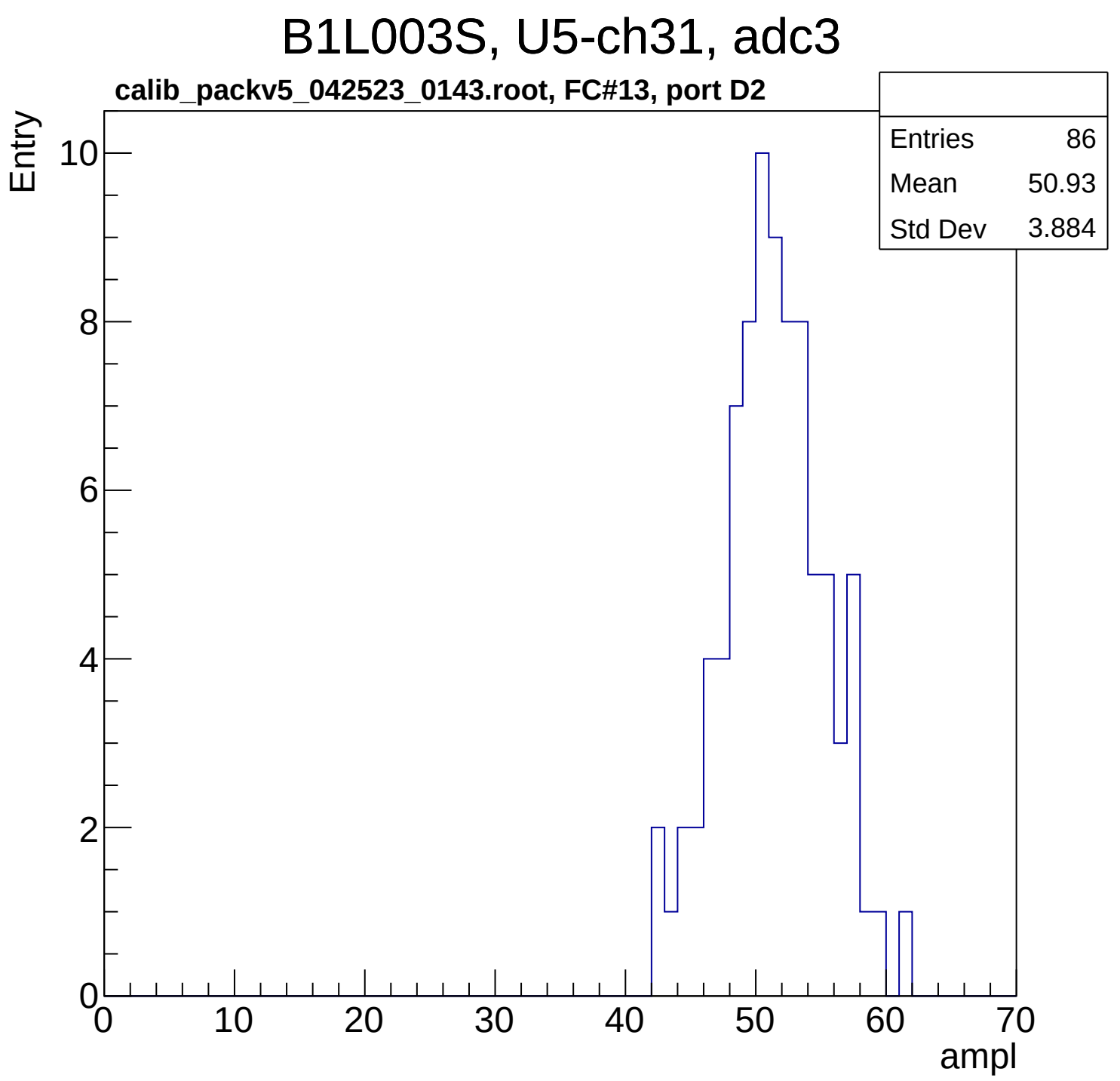
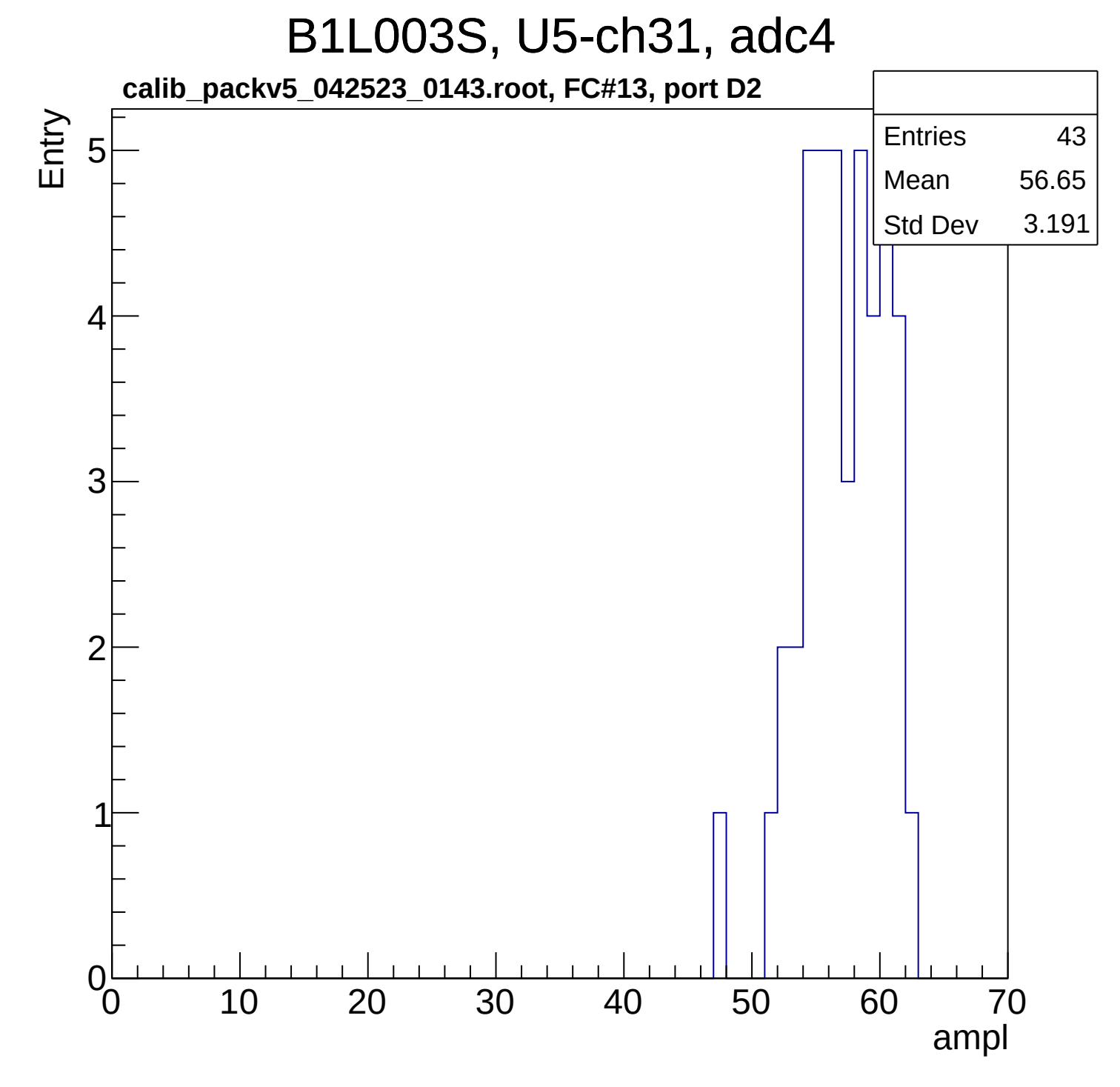

B1L003S, U5-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 50.93
Std Dev 3.884
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 56.65
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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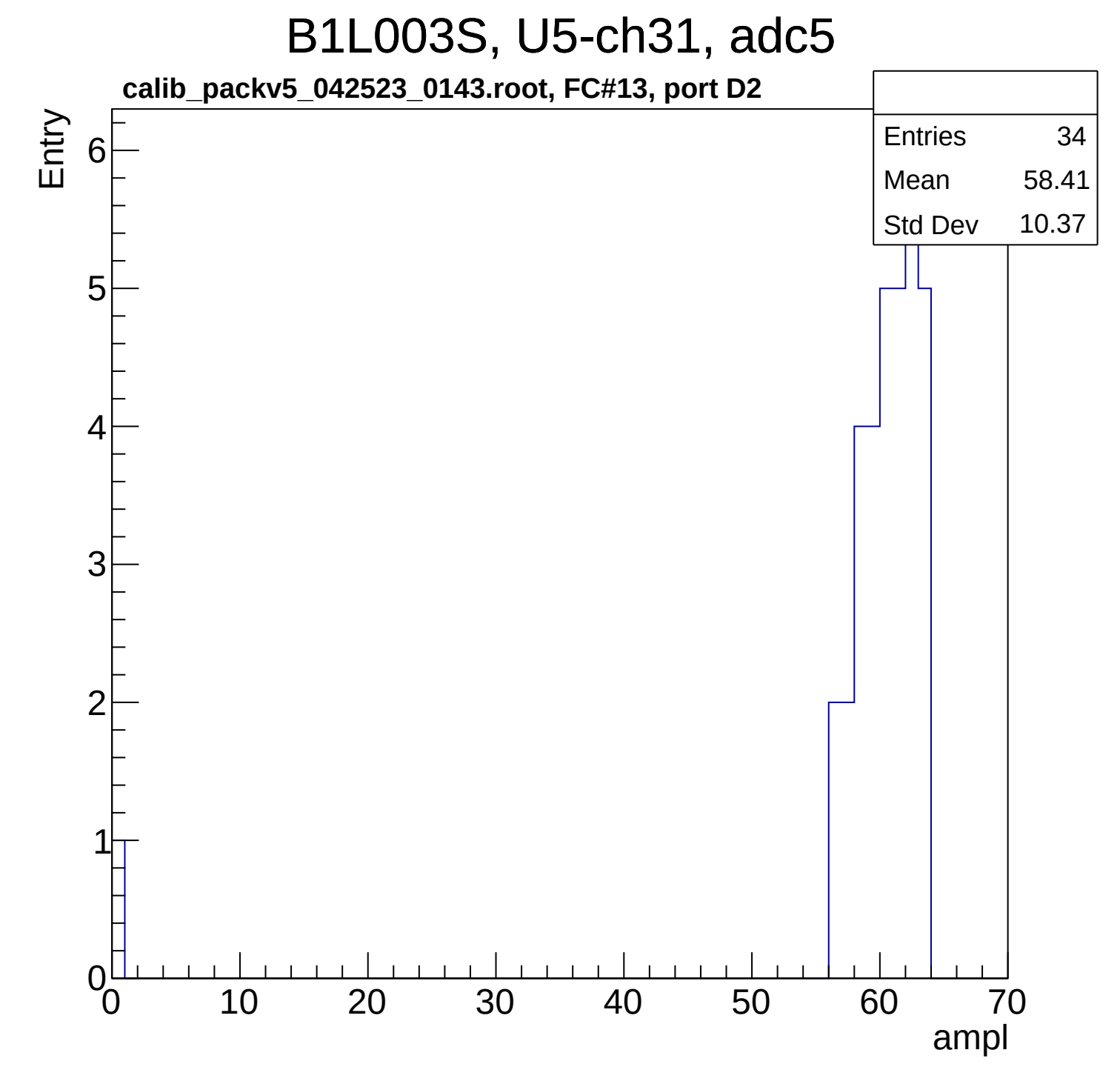
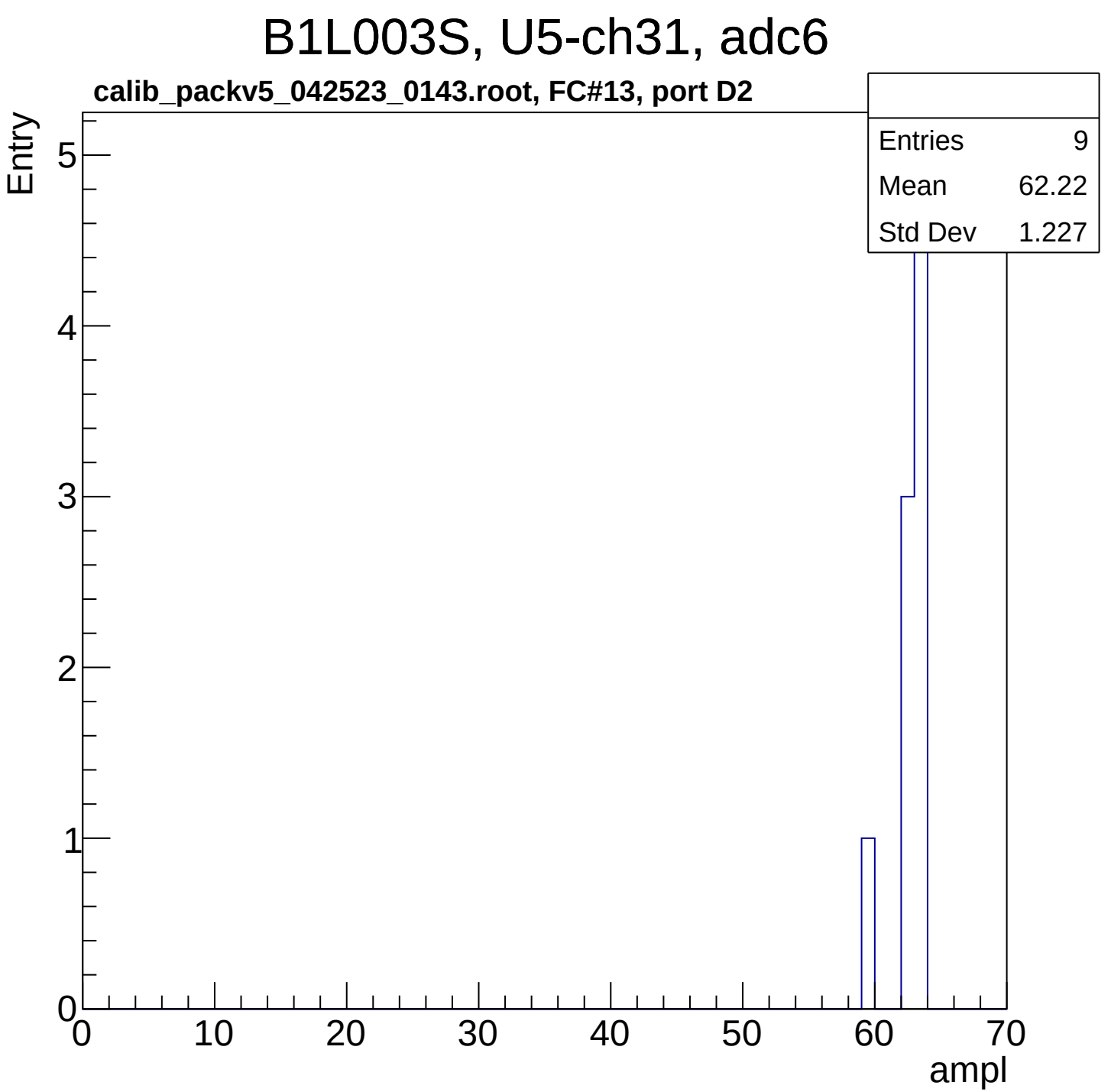

B1L003S, U5-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 58.41
Std Dev 10.37
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.22
Std Dev 1.227
Entry
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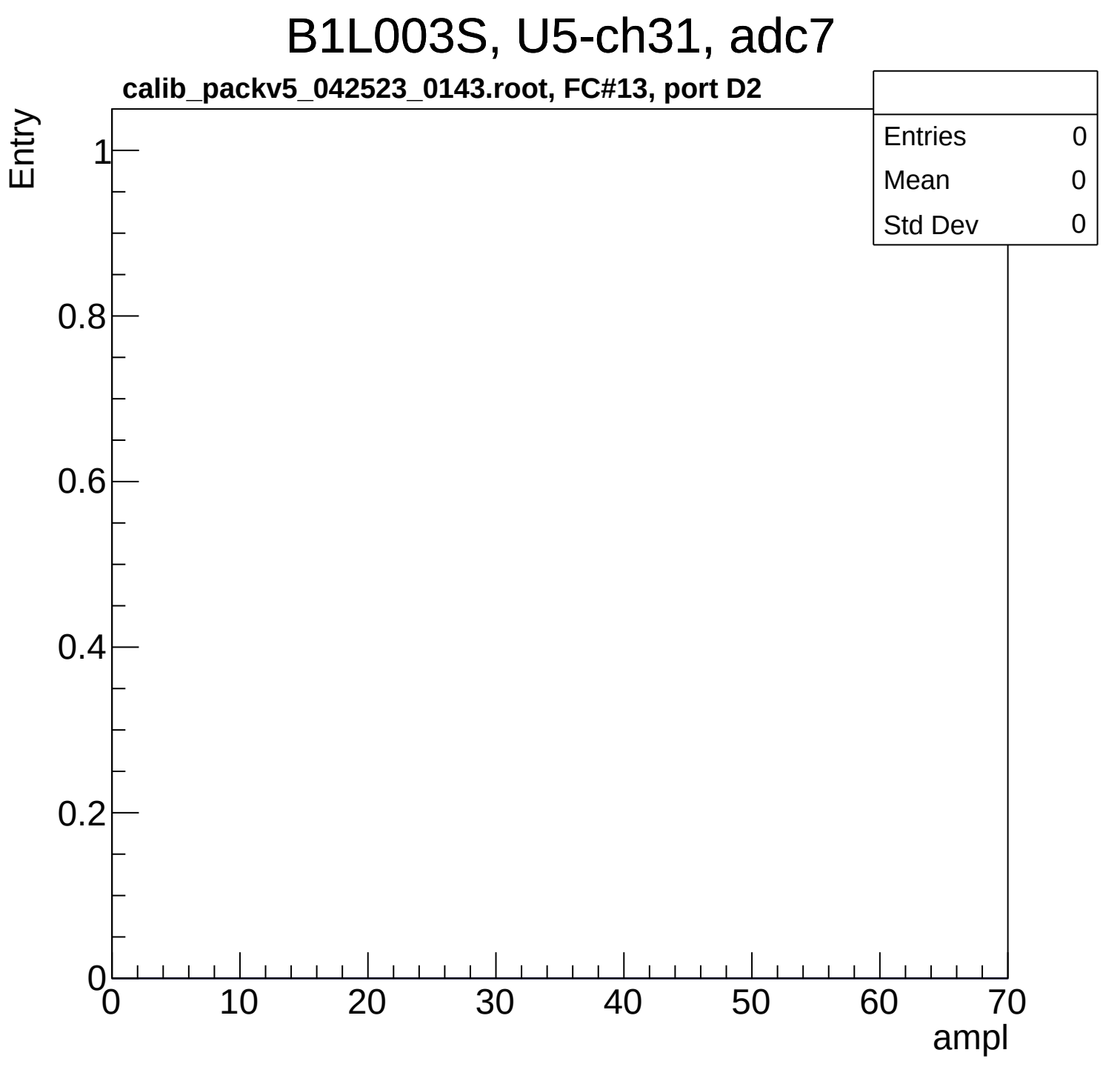
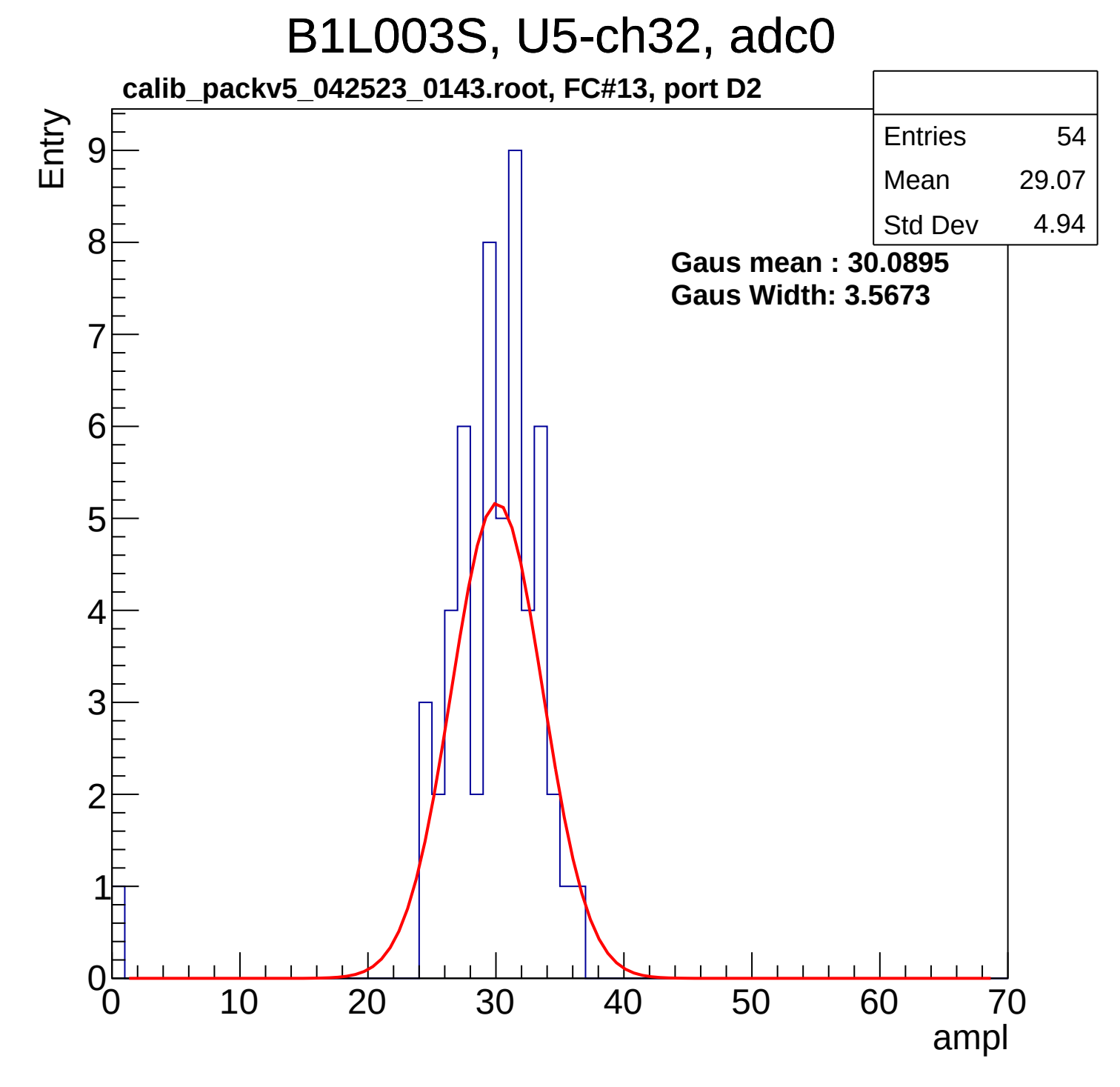
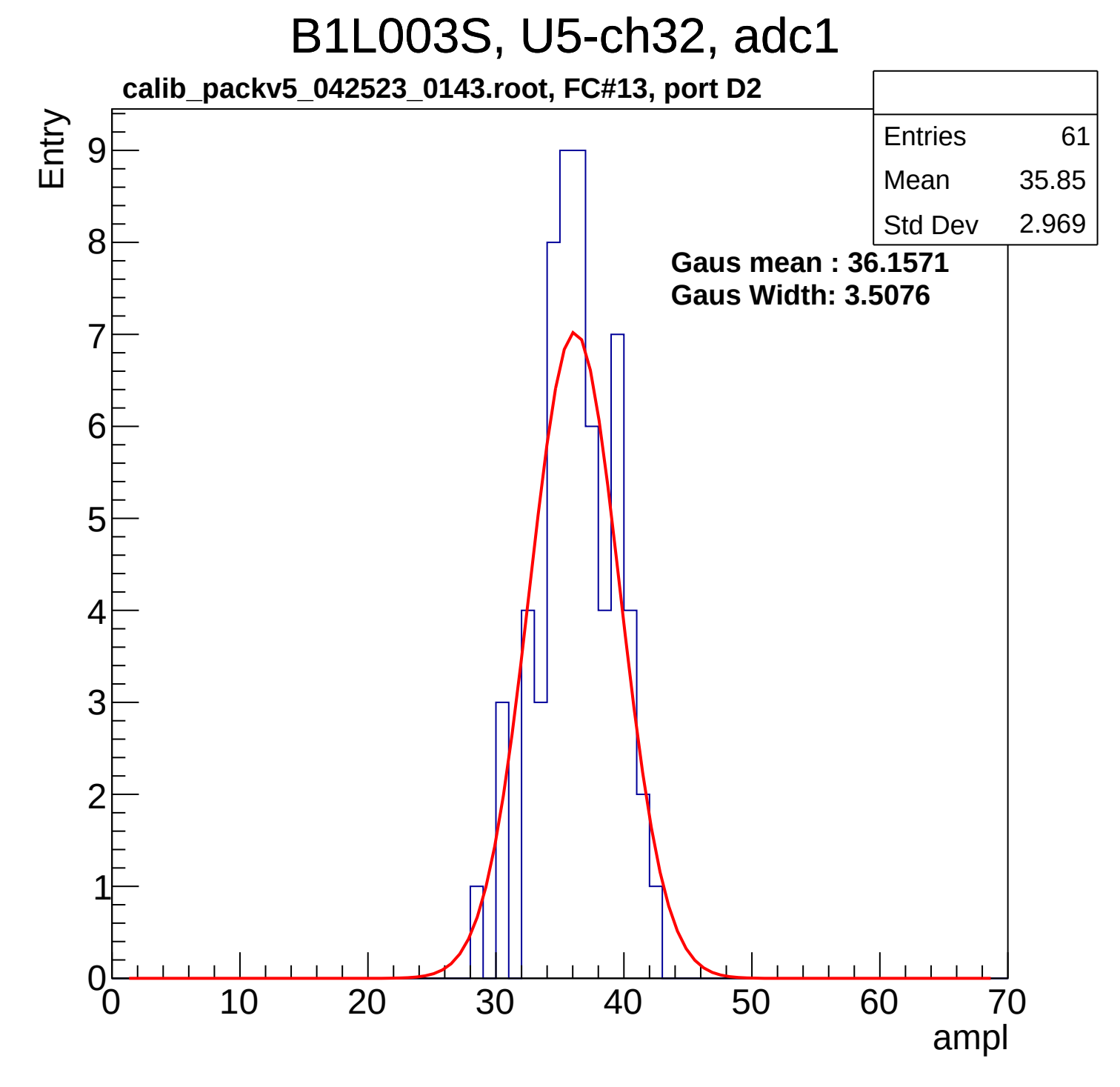
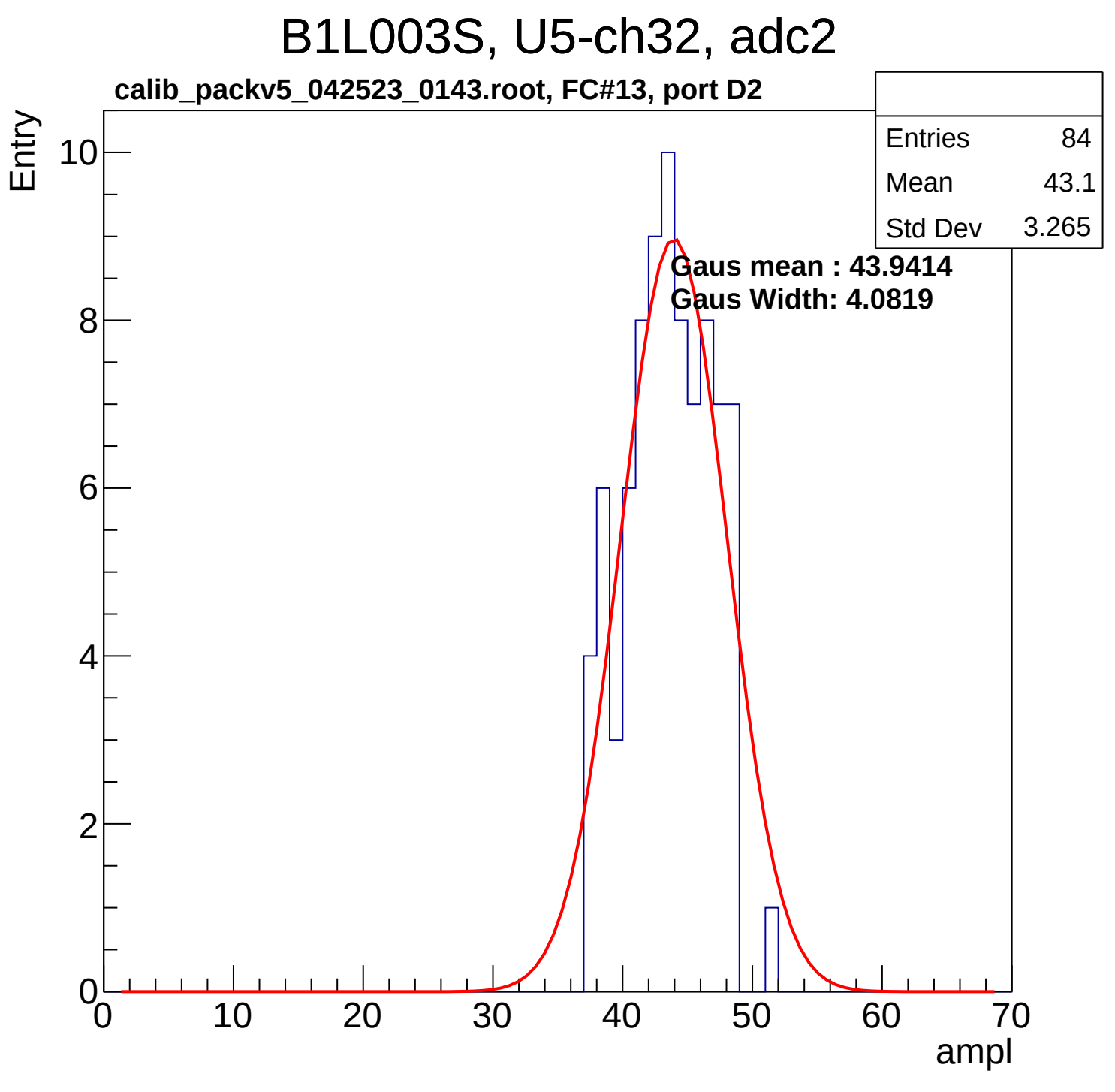
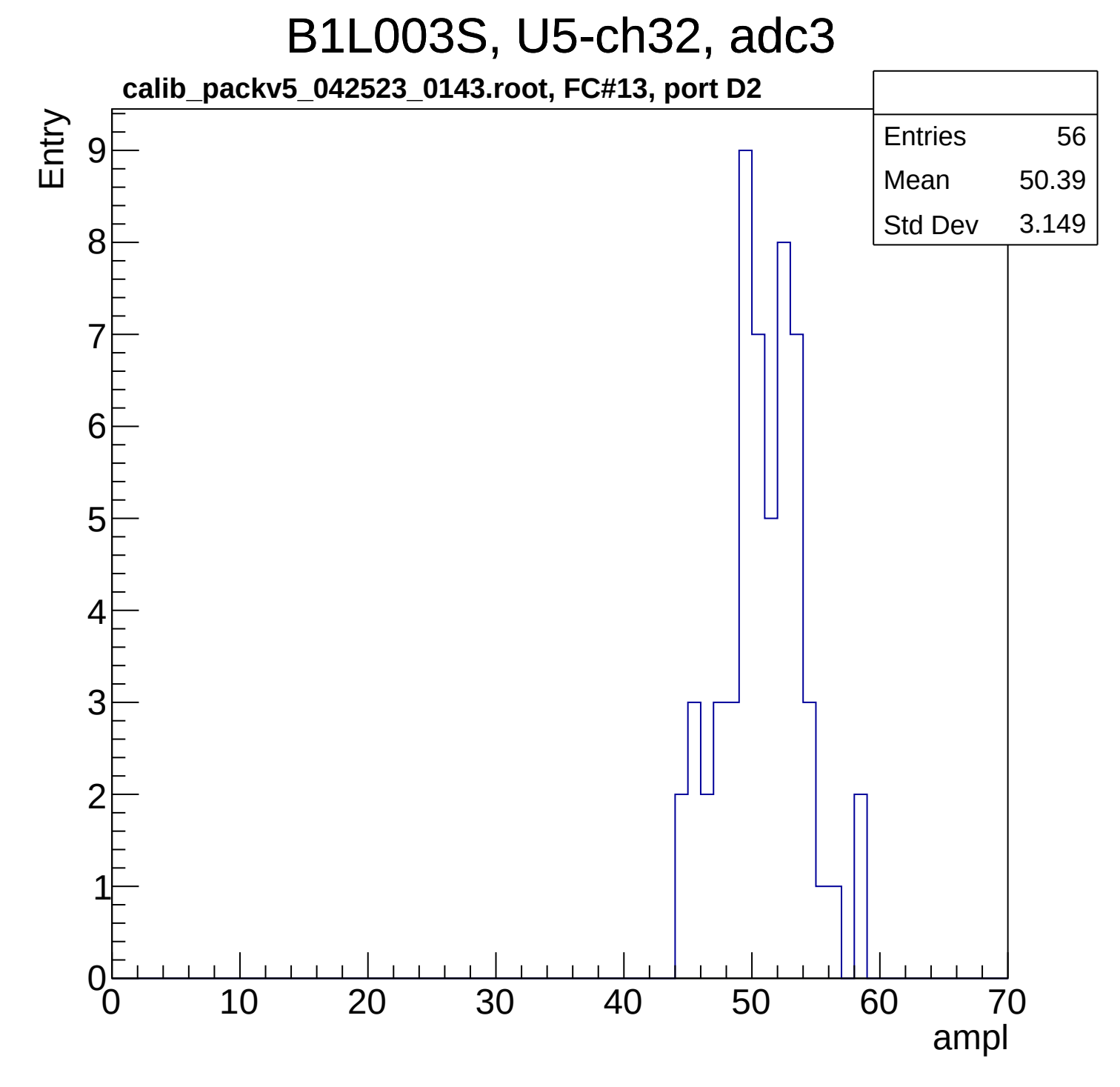
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.07
Std Dev 4.94
Gaus mean : 30.0895
Gaus Width: 3.5673
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.85
Std Dev 2.969
Gaus mean : 36.1571
Gaus Width: 3.5076
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 43.1
Std Dev 3.265
Gaus mean : 43.9414
Gaus Width: 4.0819
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.39
Std Dev 3.149
Entry
ampl
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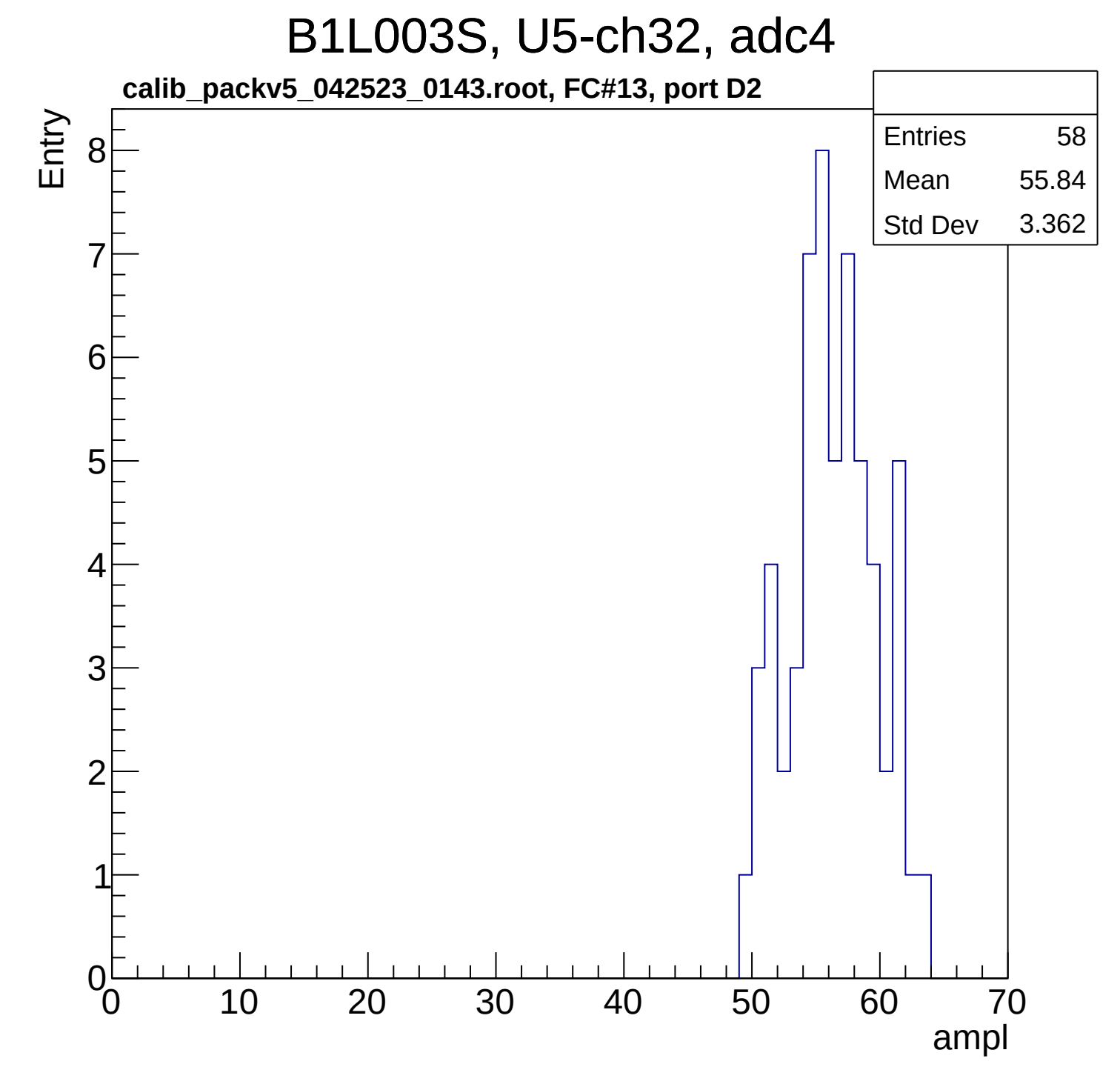
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.84
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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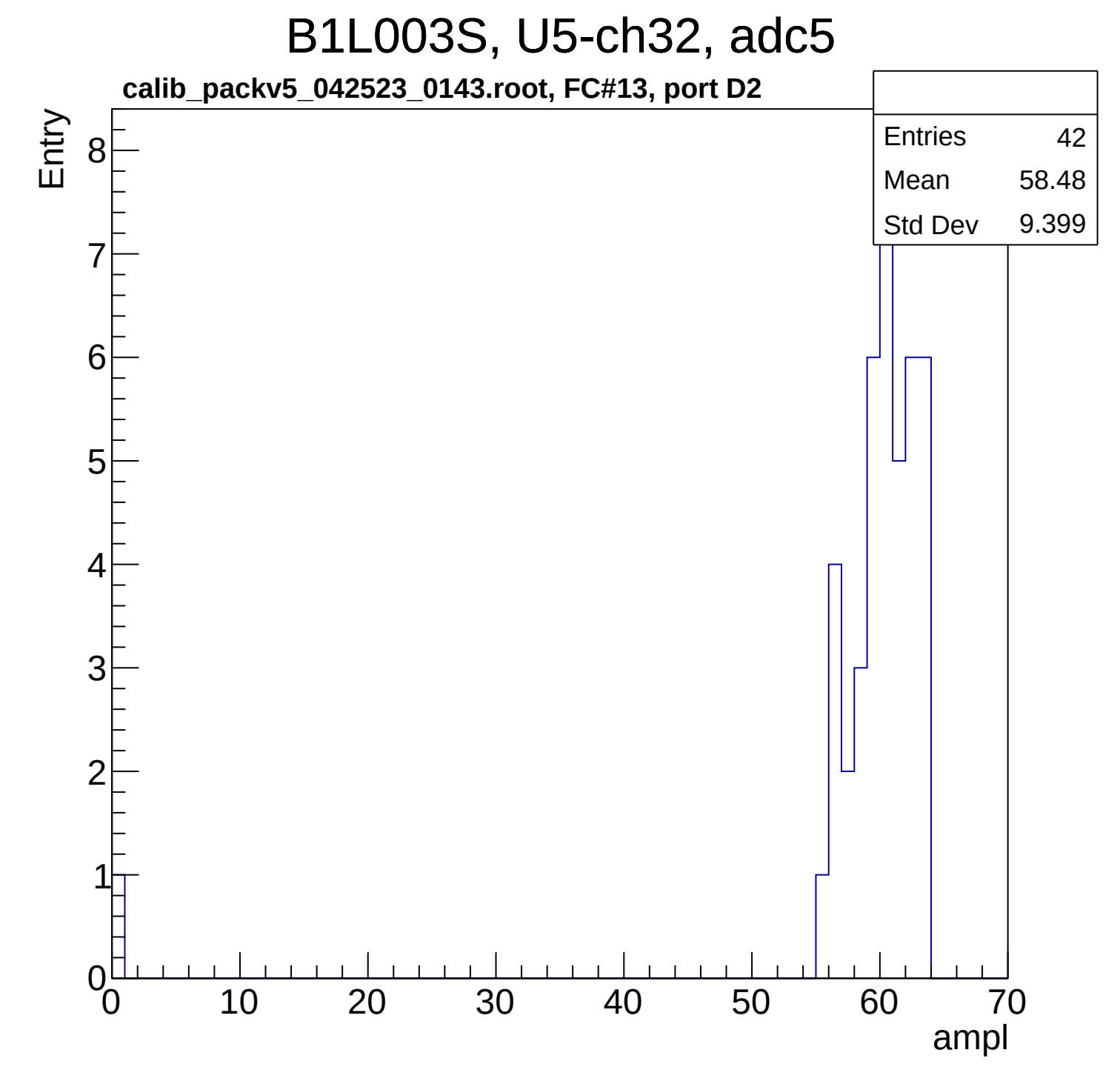
B1L003S, U5-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.48
Std Dev 9.399
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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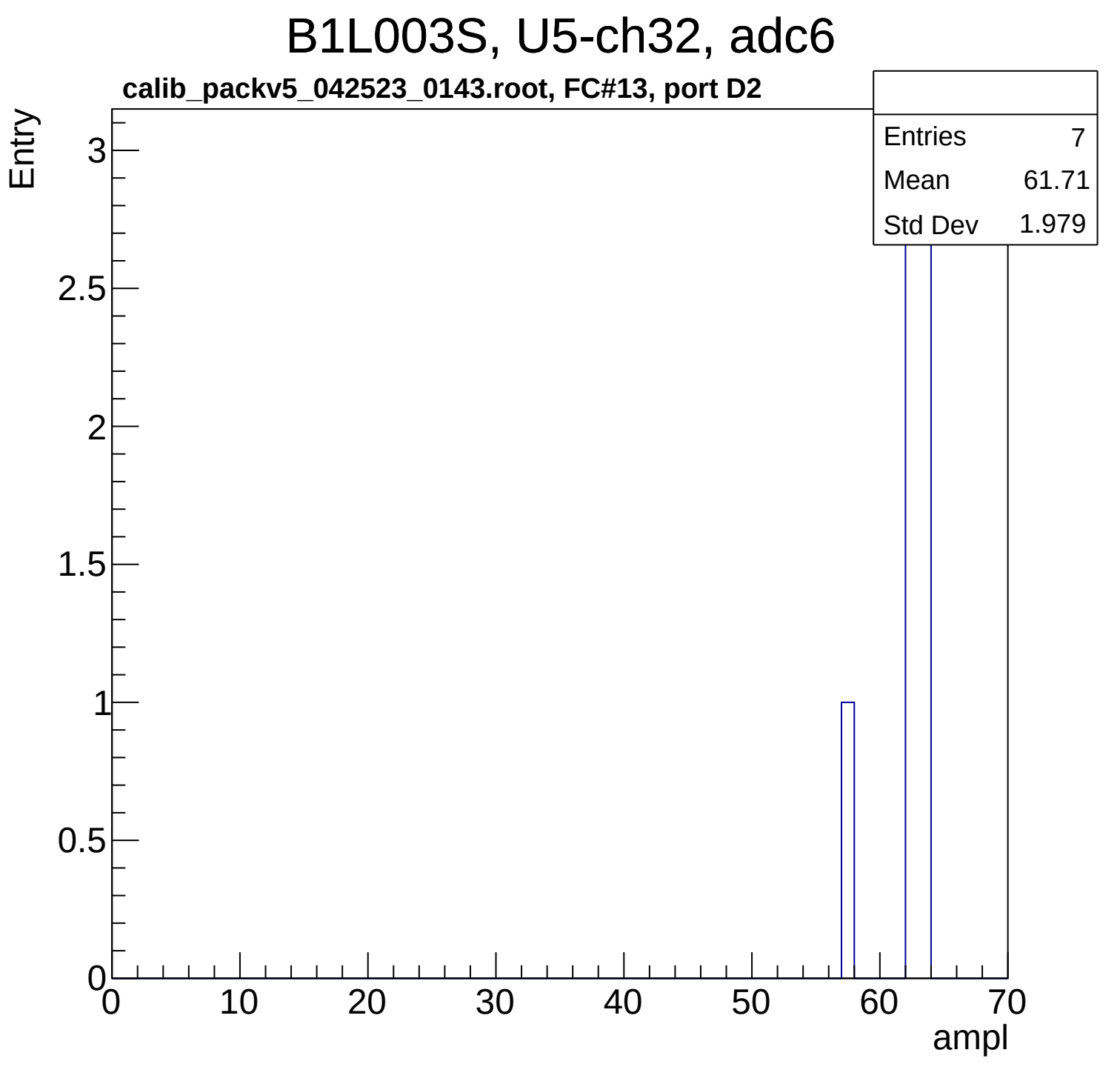
B1L003S, U5-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.71
Std Dev 1.979
Entry
ampl
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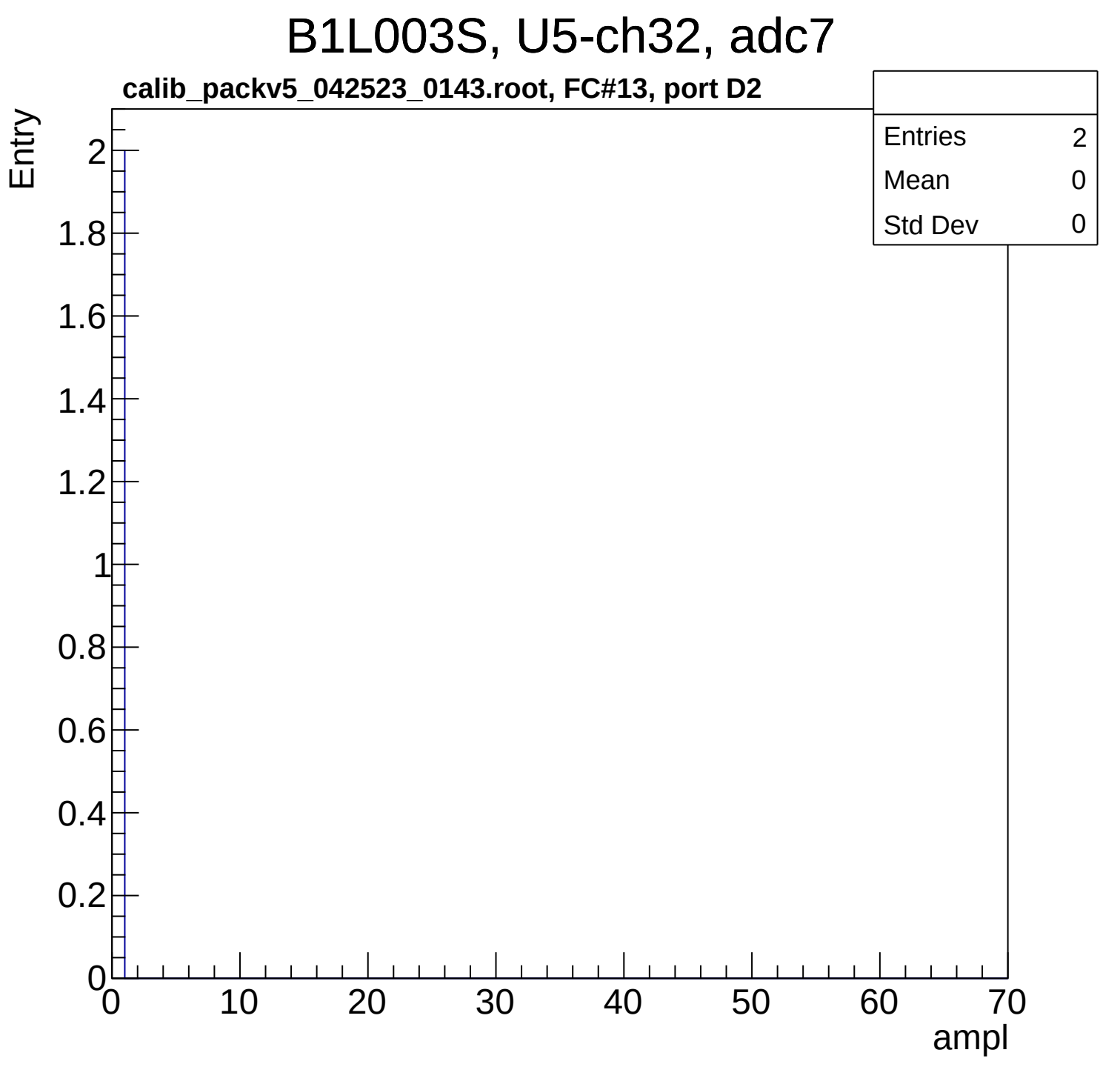
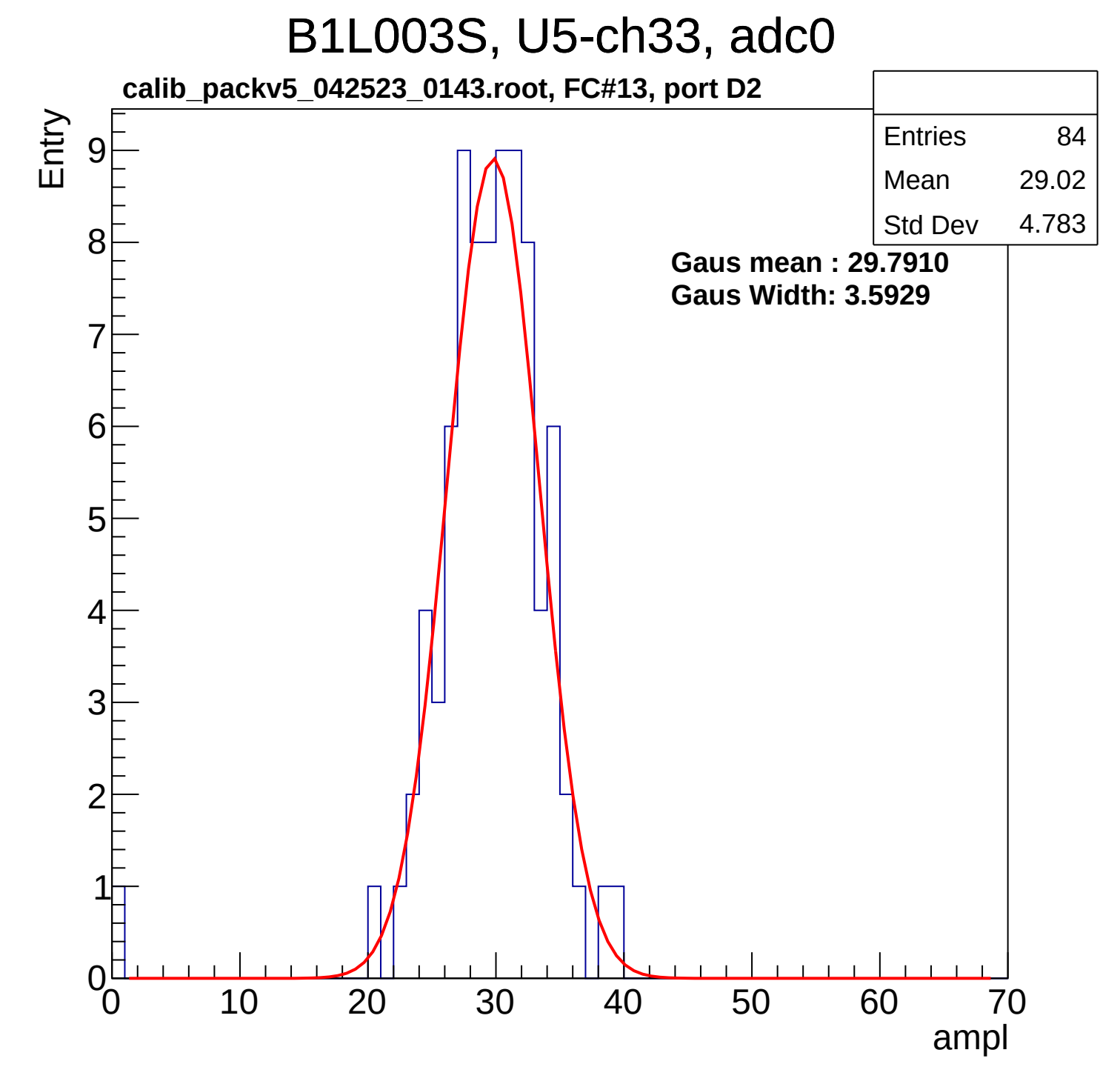
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 29.02
Std Dev 4.783
Gaus mean : 29.7910
Gaus Width: 3.5929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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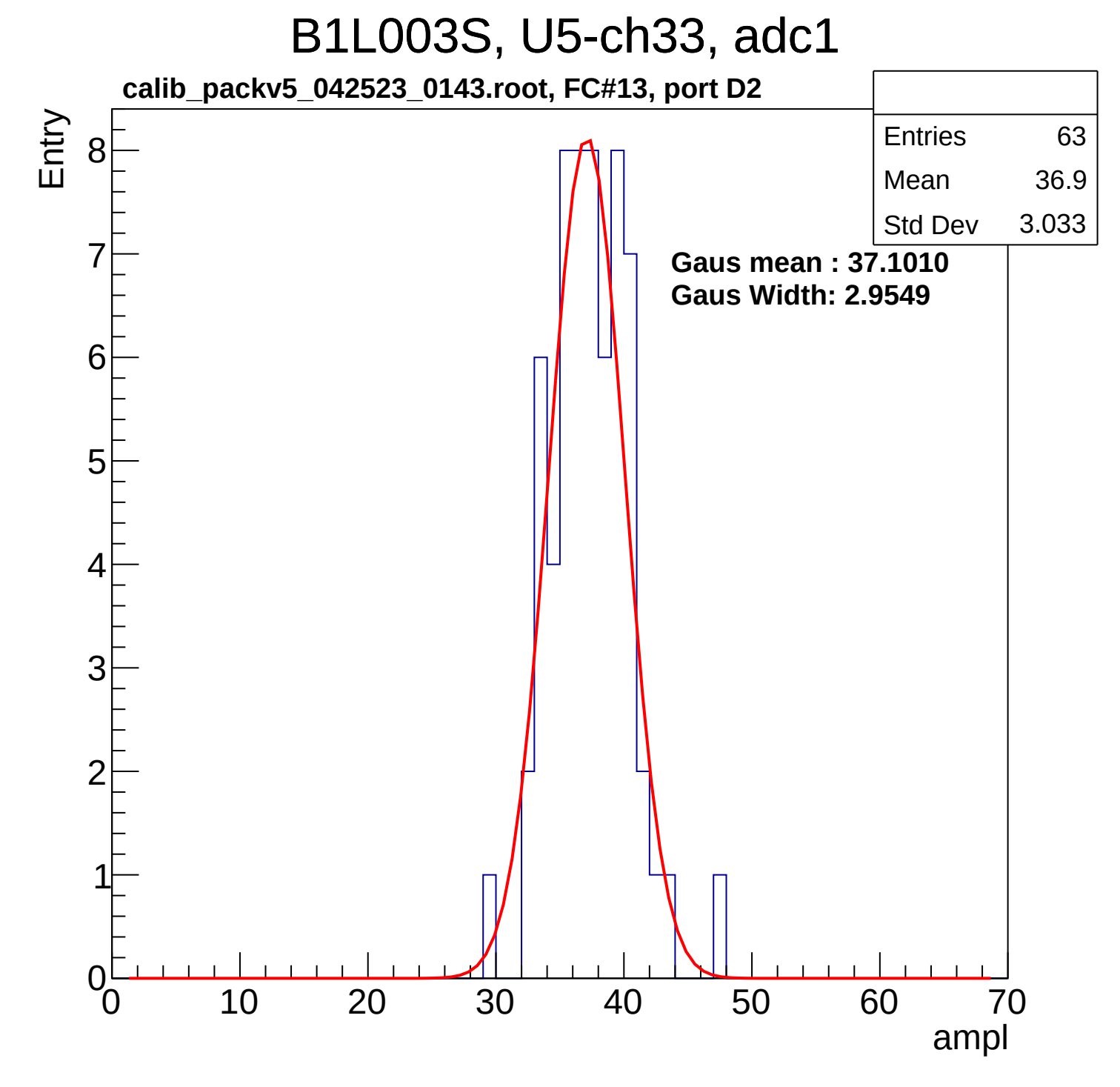
B1L003S, U5-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.9
Std Dev 3.033
Gaus mean : 37.1010
Gaus Width: 2.9549
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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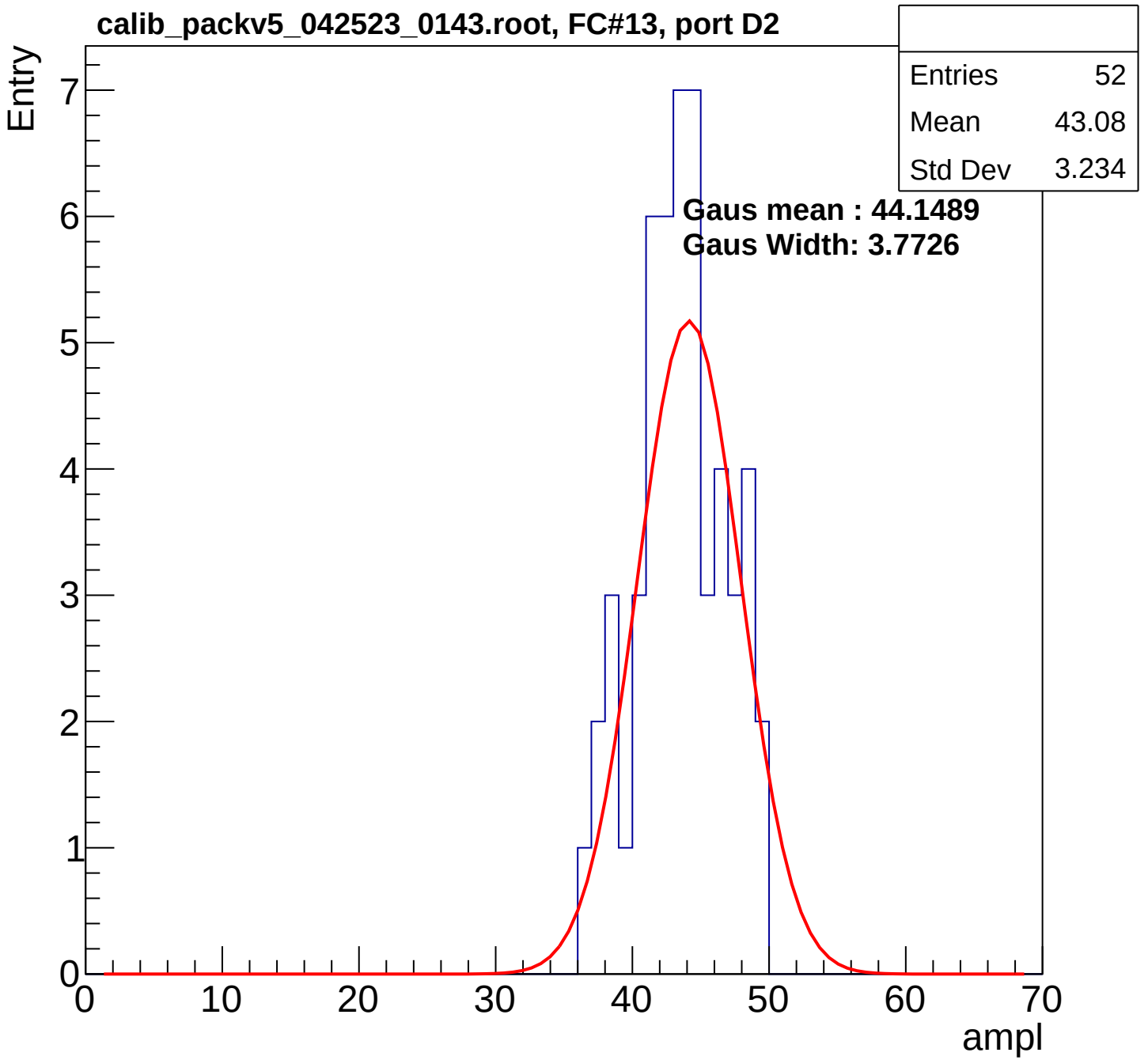
B1L003S, U5-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.08
Std Dev 3.234
Gaus mean : 44.1489
Gaus Width: 3.7726
Entry
ampl
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2
3
4
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6
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0
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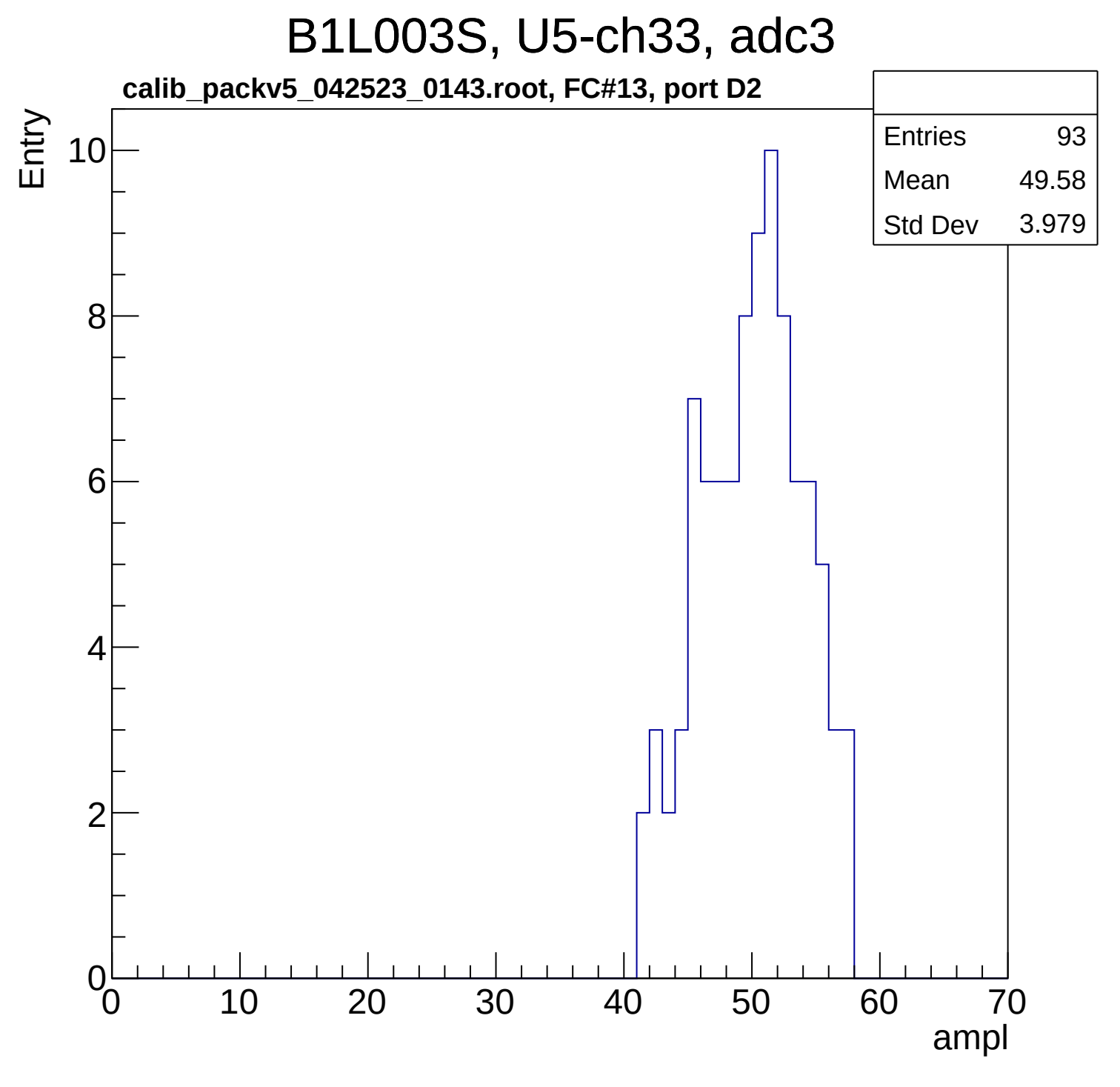
B1L003S, U5-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 93
Mean 49.58
Std Dev 3.979
Entry
ampl
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2
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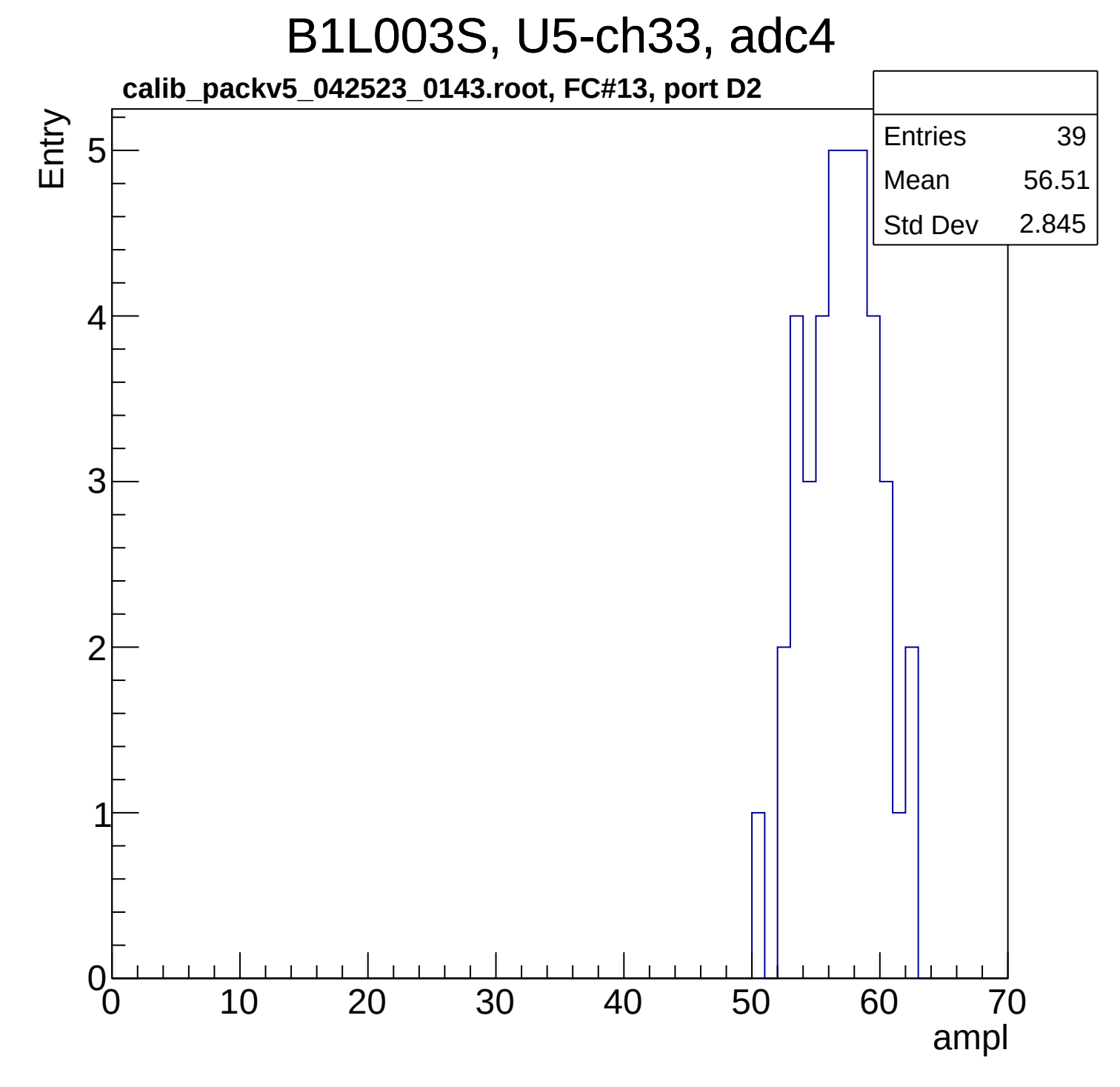
0
10
20
30
40
50
60
70


B1L003S, U5-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 56.51
Std Dev 2.845
Entry
ampl
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3
4
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0
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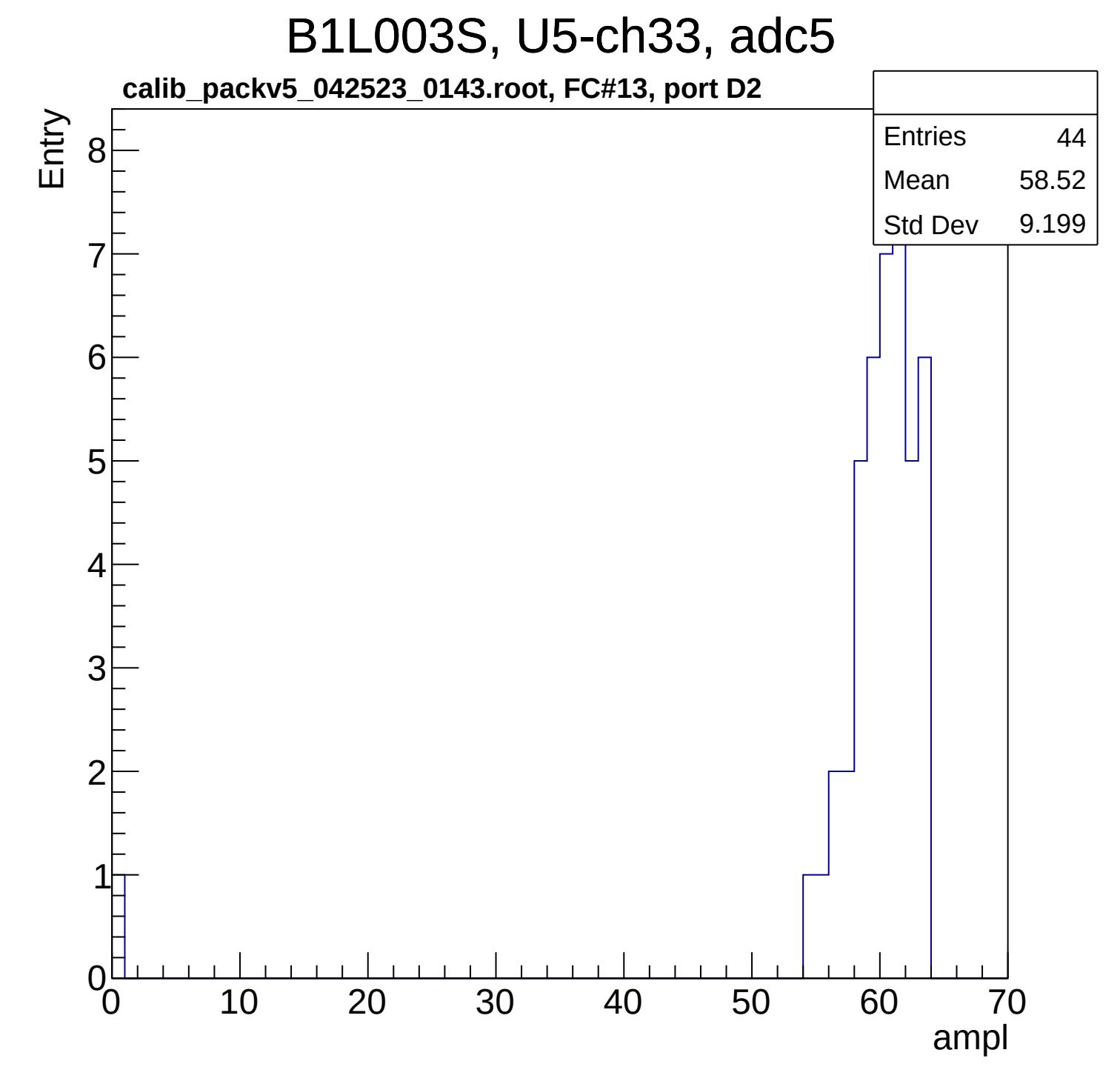
60
70


B1L003S, U5-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.52
Std Dev 9.199
Entry
ampl
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2
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4
5
6
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0
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40
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60
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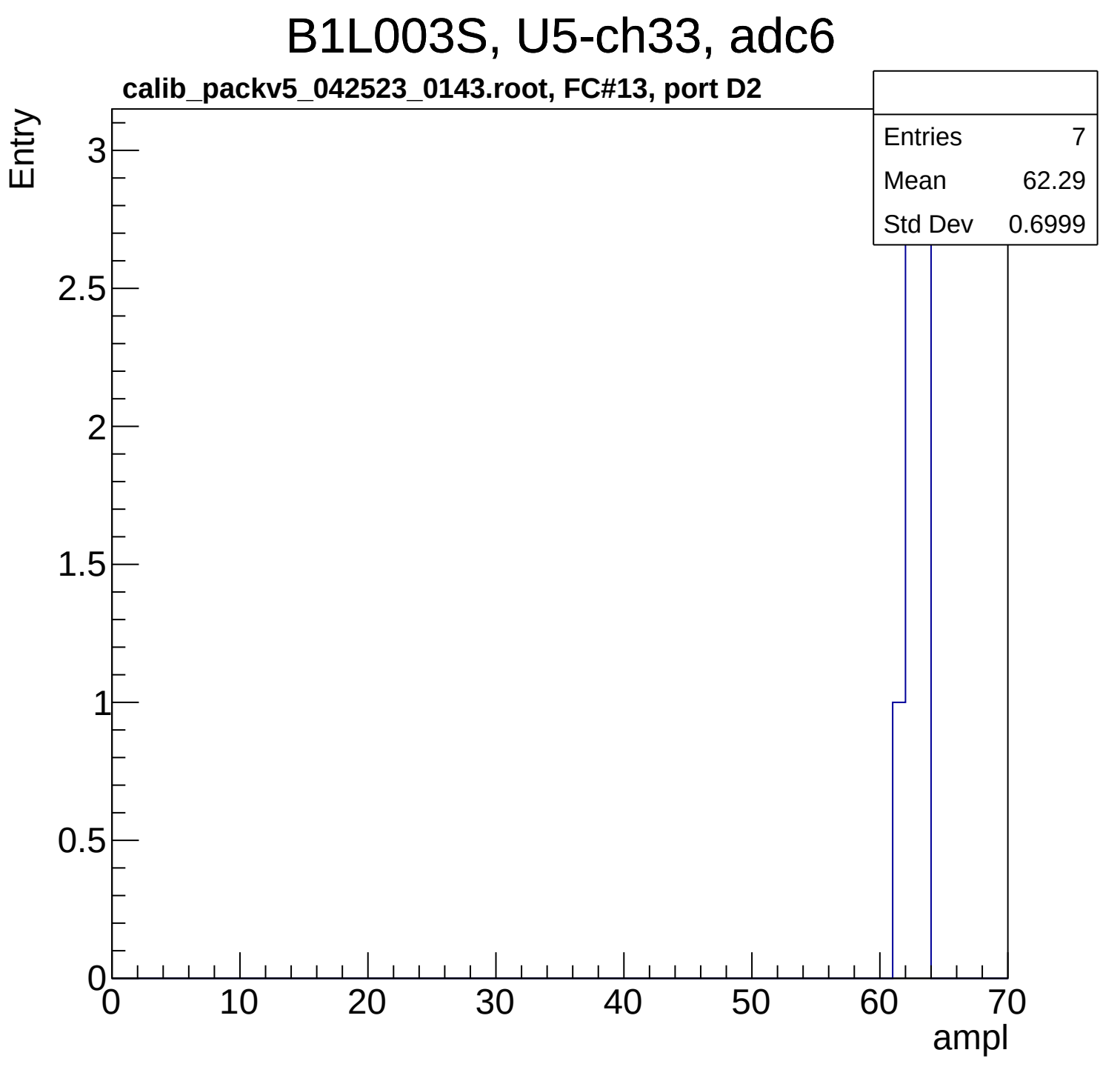
B1L003S, U5-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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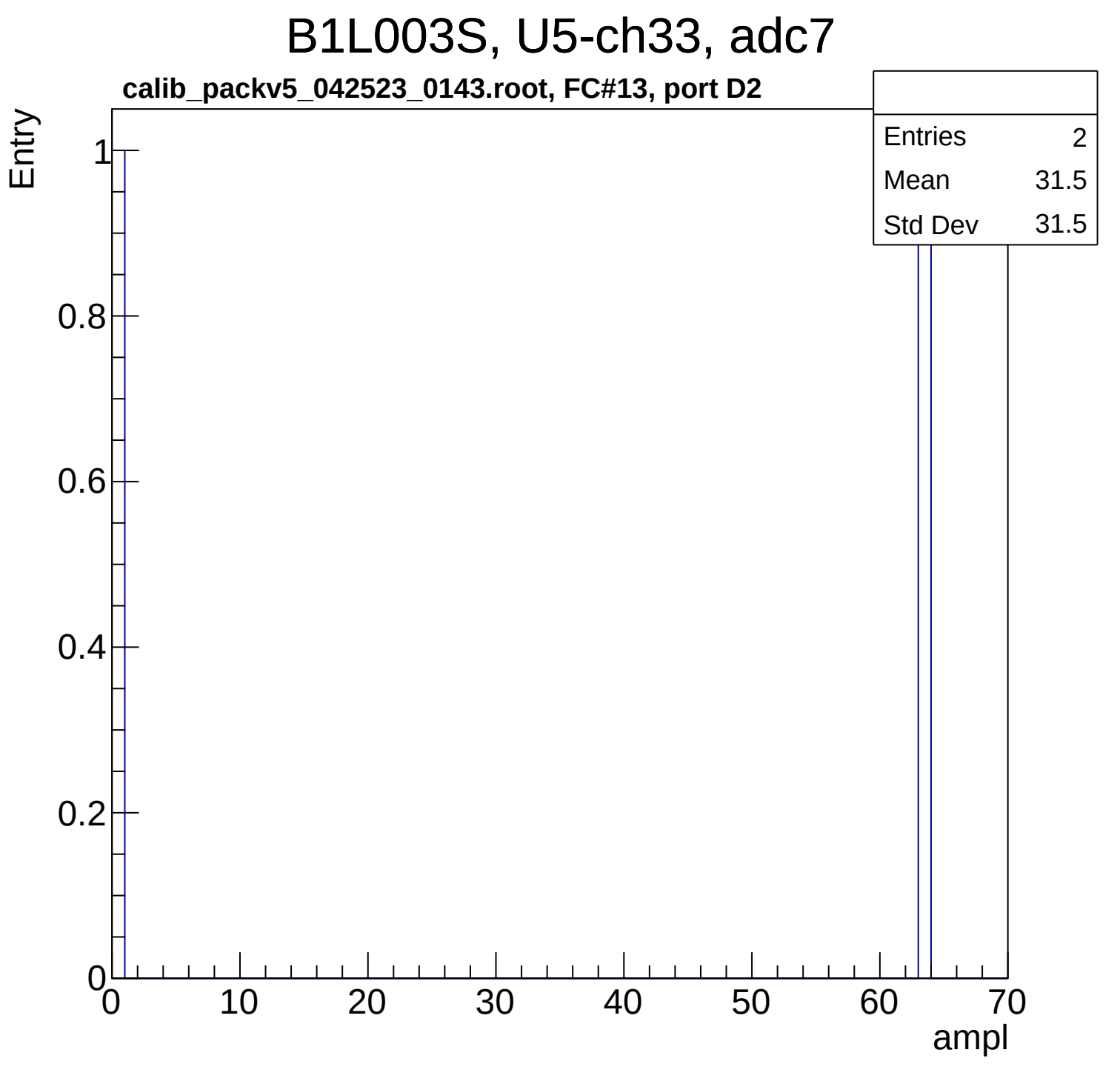
20
30
40
50
60
70


B1L003S, U5-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
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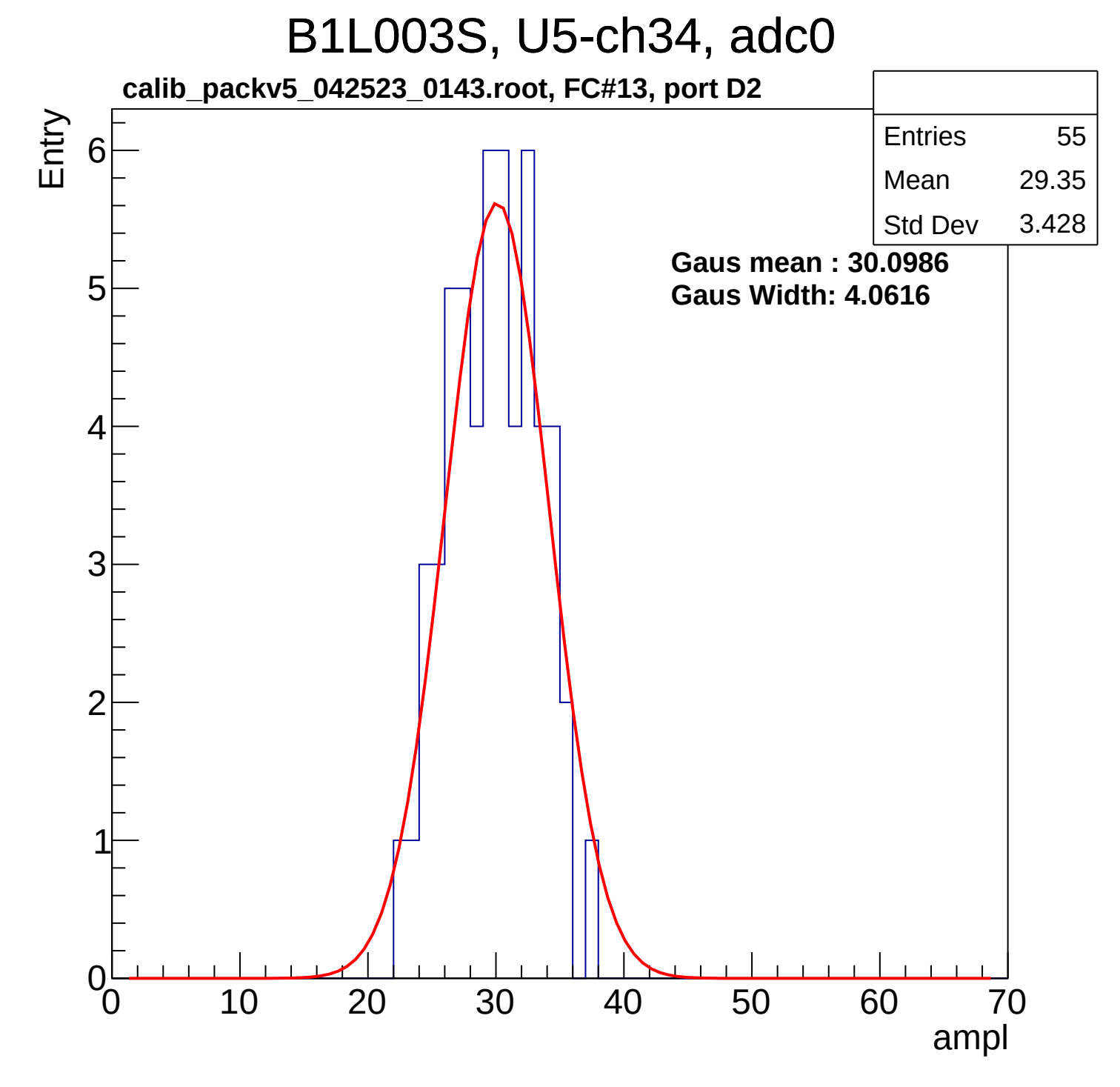
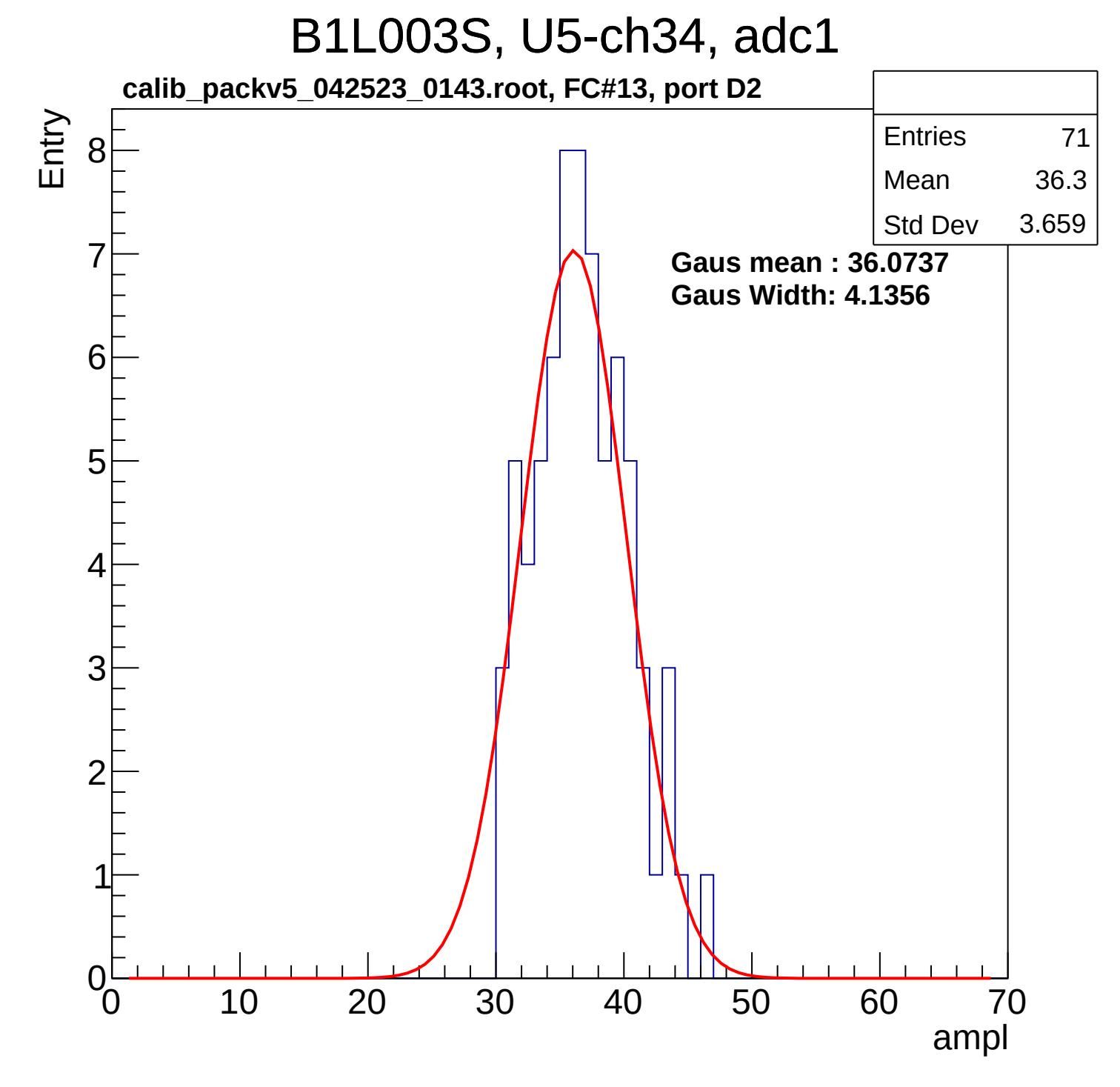
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.35
Std Dev 3.428
Gaus mean : 30.0986
Gaus Width: 4.0616
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U5-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.3
Std Dev 3.659
Gaus mean : 36.0737
Gaus Width: 4.1356
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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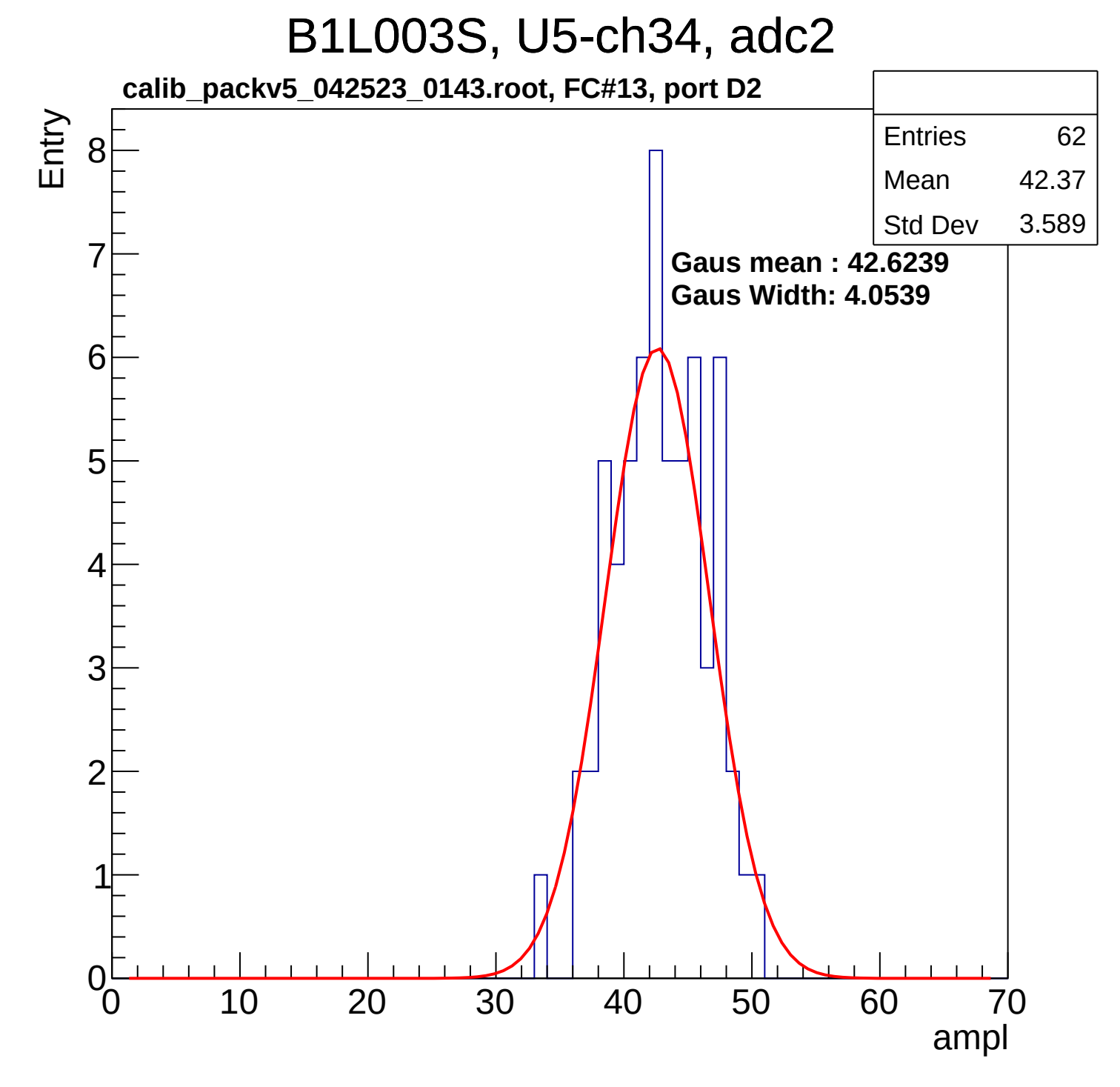
B1L003S, U5-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.37
Std Dev 3.589
Gaus mean : 42.6239
Gaus Width: 4.0539
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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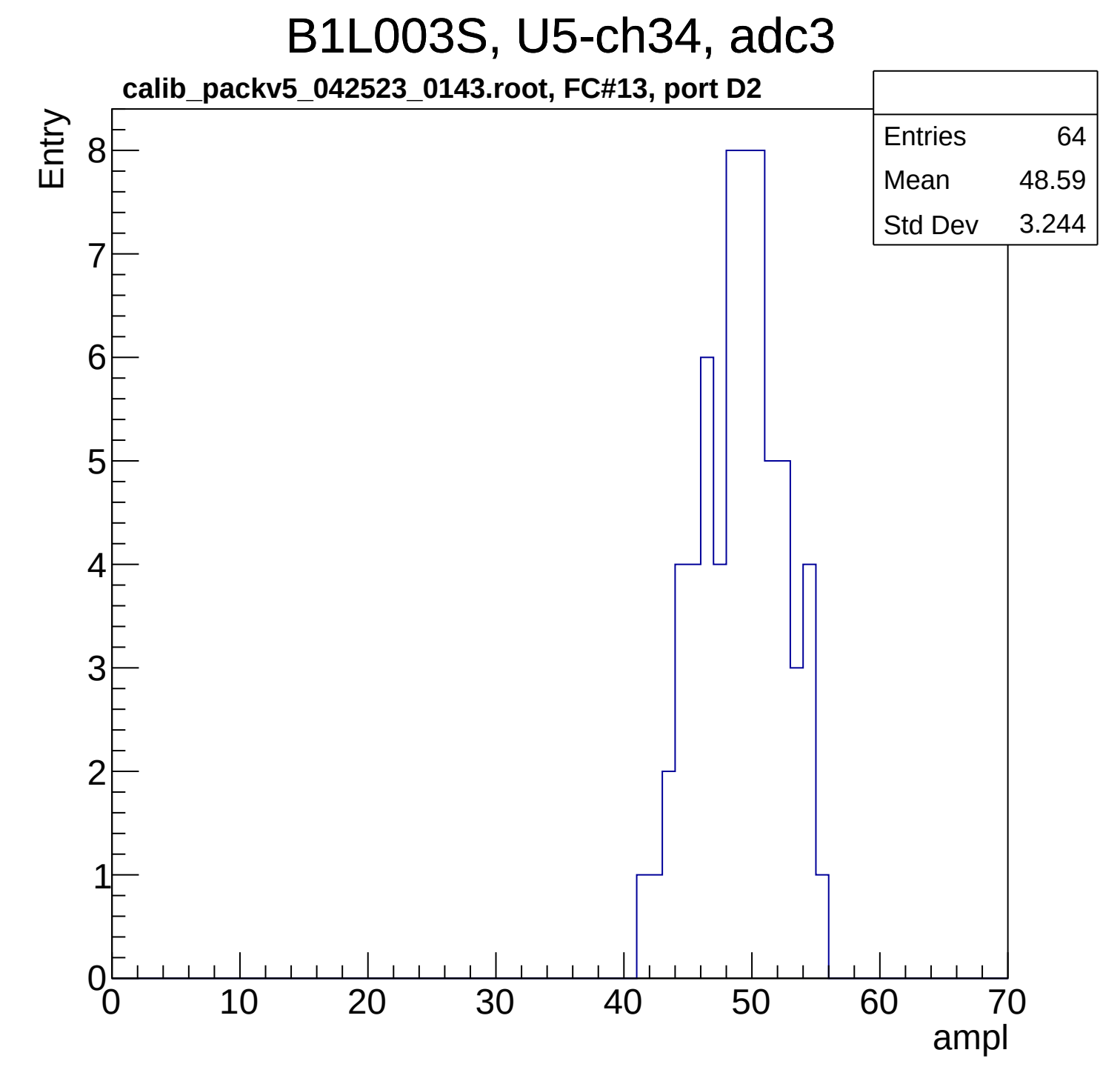
B1L003S, U5-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.59
Std Dev 3.244
Entry
ampl
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3
4
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6
7
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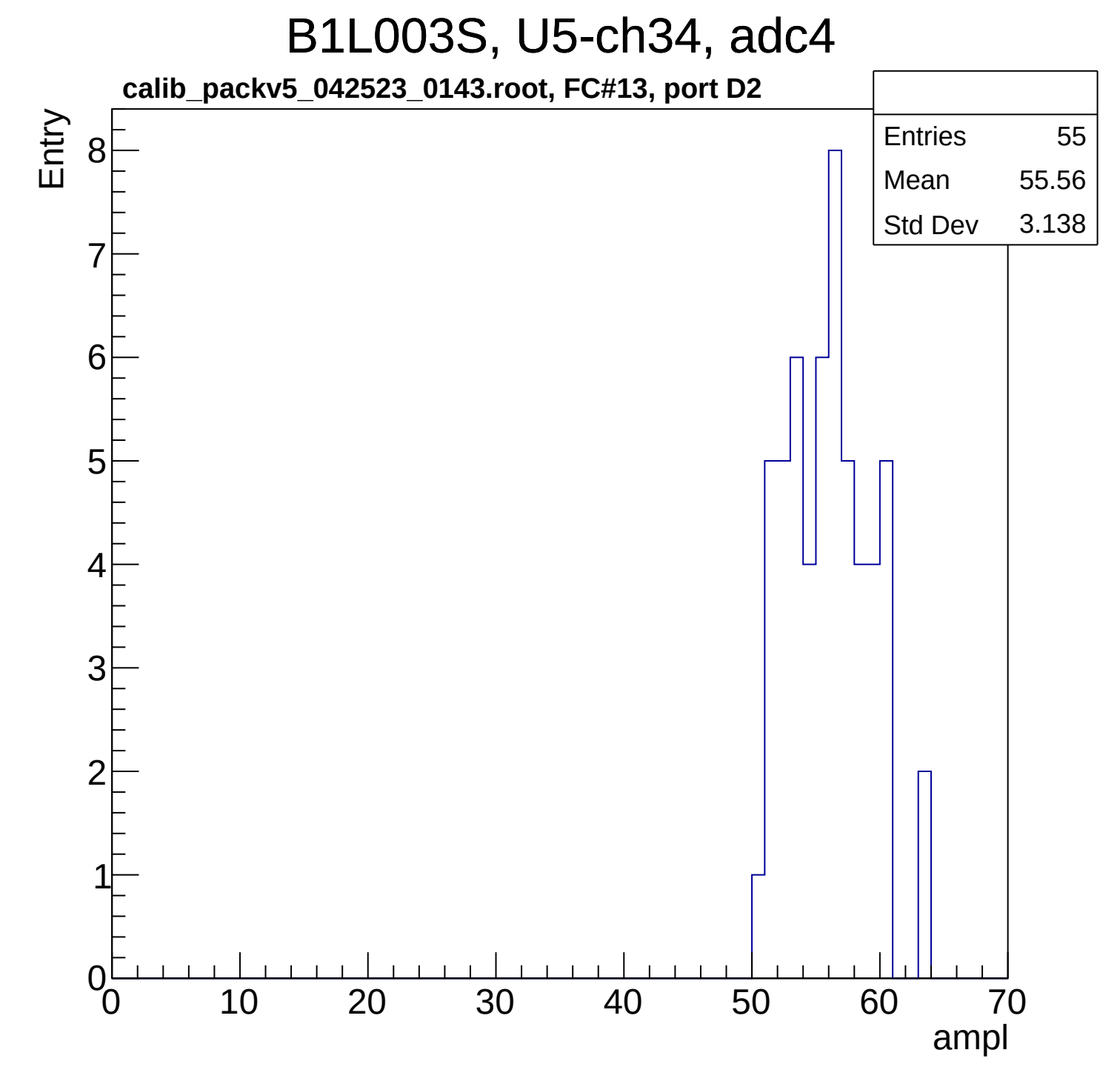
B1L003S, U5-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.56
Std Dev 3.138
Entry
ampl
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2
3
4
5
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7
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0
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20
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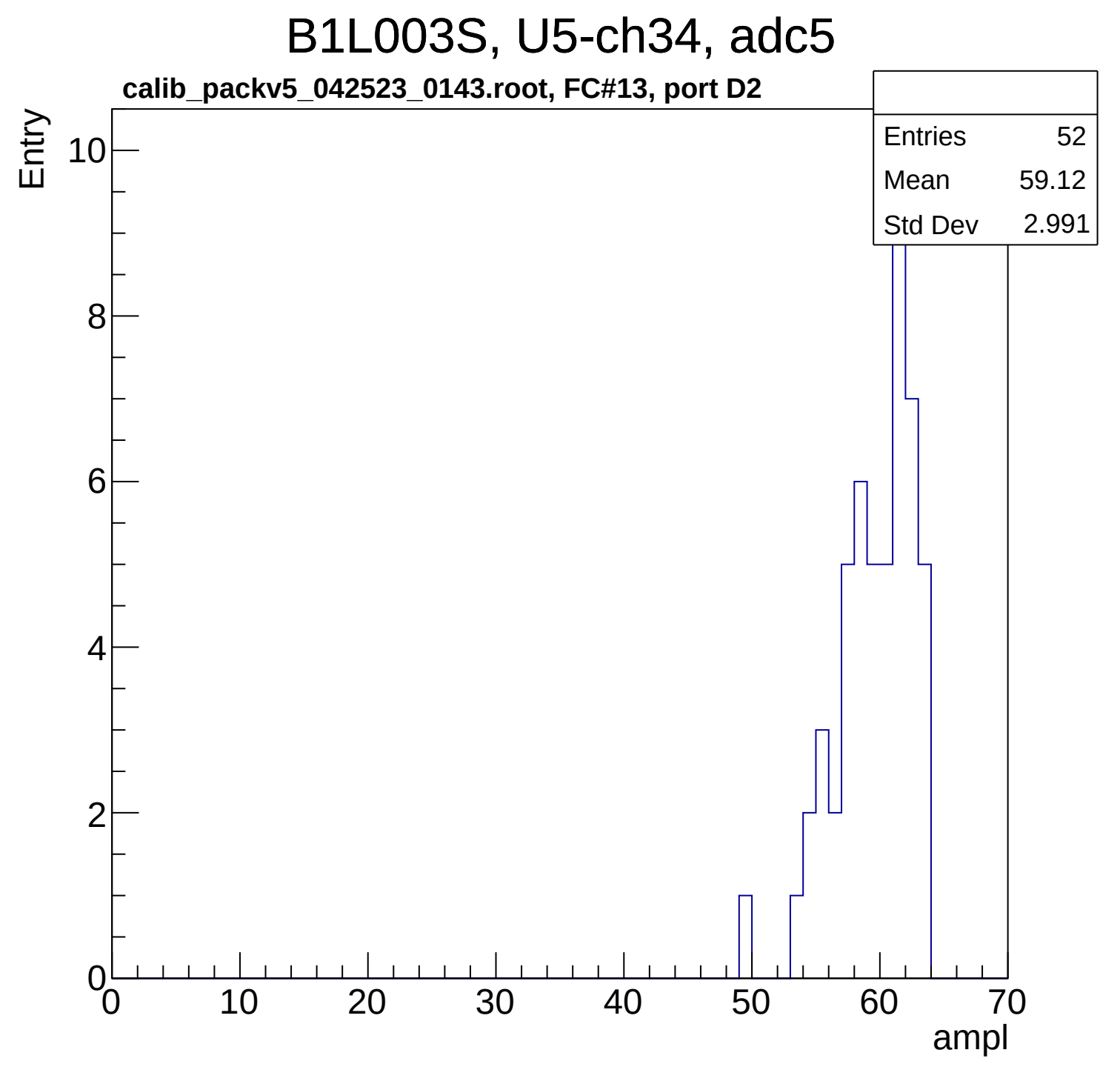
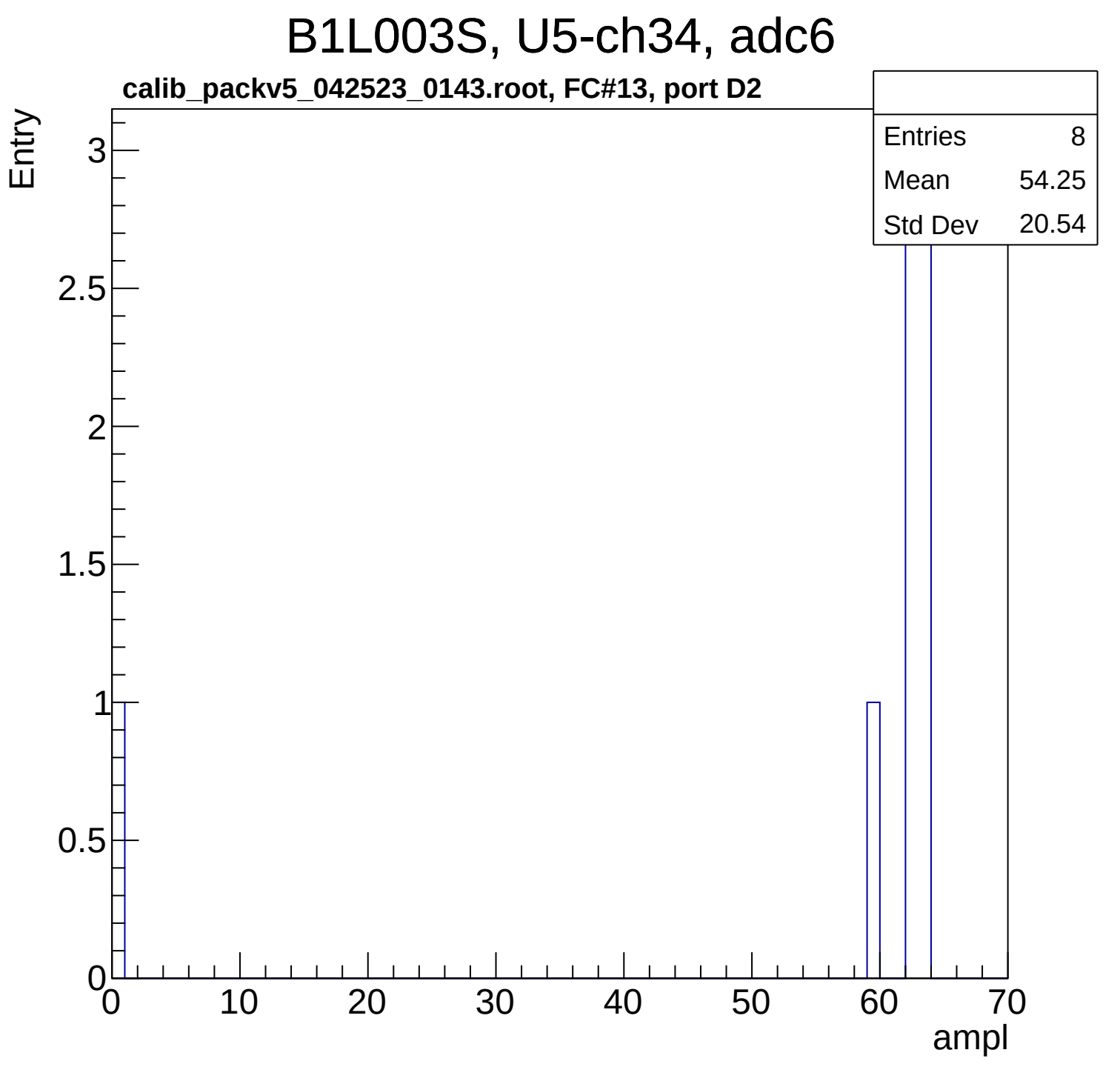

B1L003S, U5-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.12
Std Dev 2.991
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.25
Std Dev 20.54
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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40
50
60
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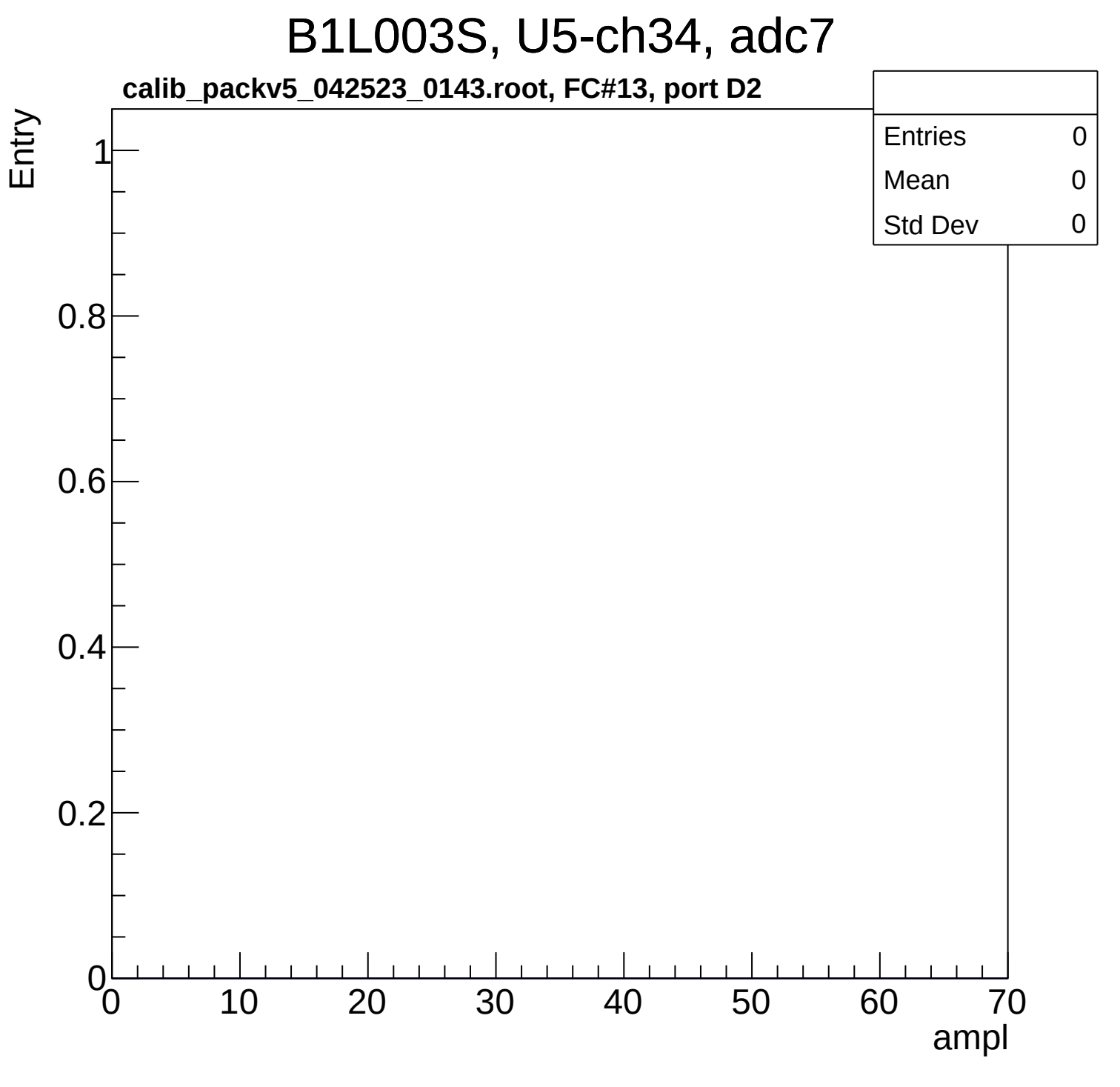
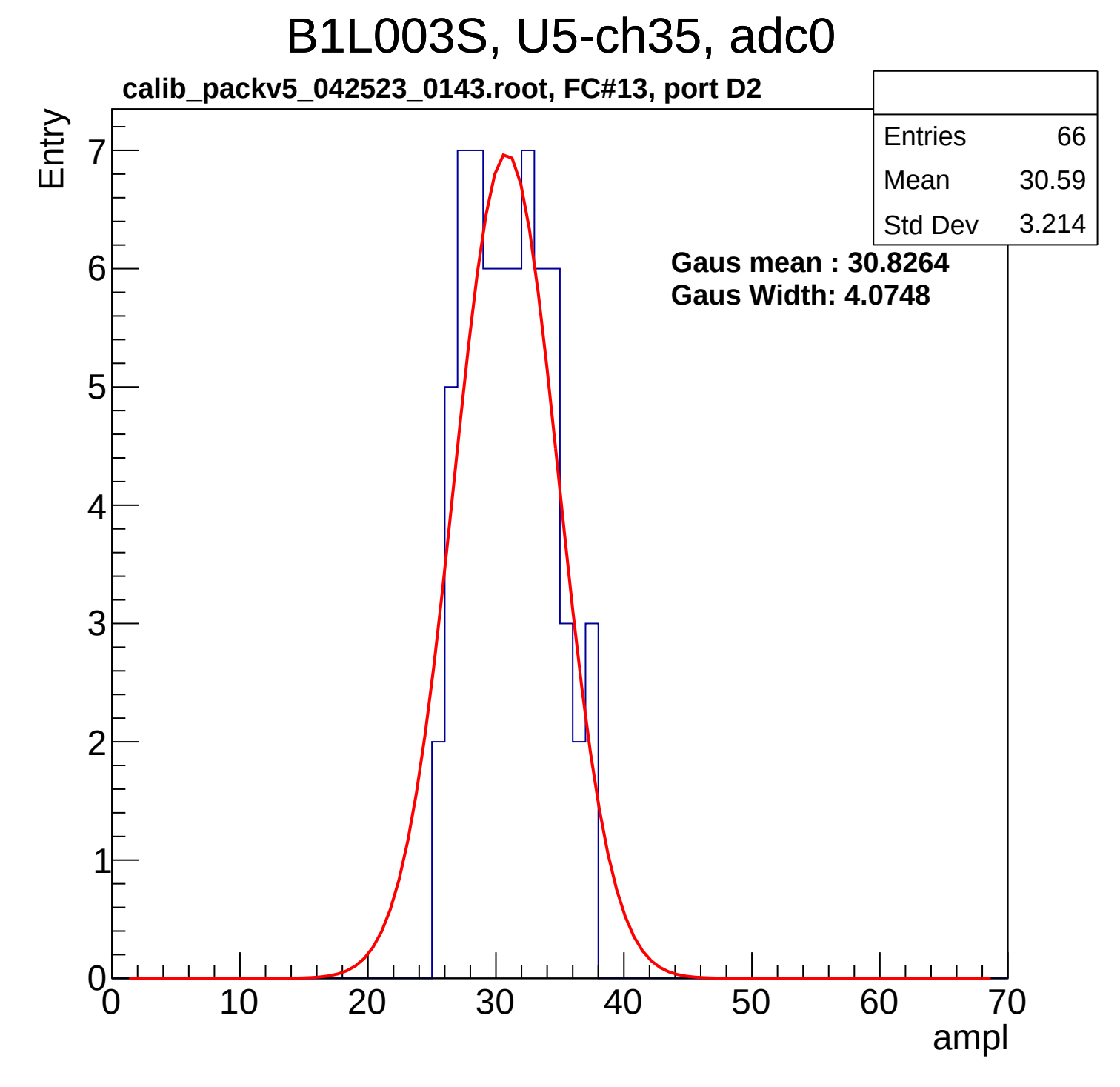

B1L003S, U5-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.59
Std Dev 3.214
Gaus mean : 30.8264
Gaus Width: 4.0748
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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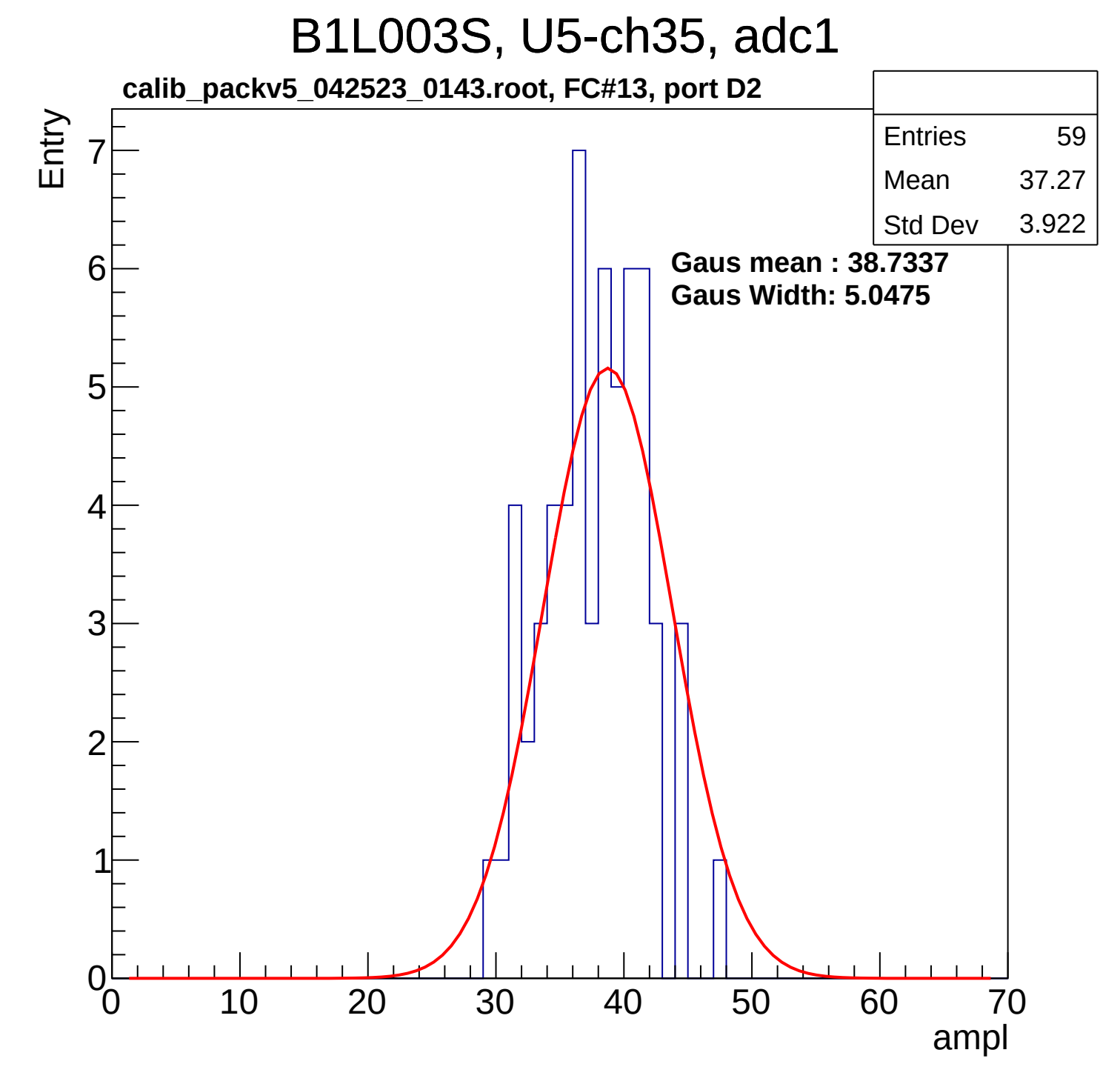
B1L003S, U5-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.27
Std Dev 3.922
Gaus mean : 38.7337
Gaus Width: 5.0475
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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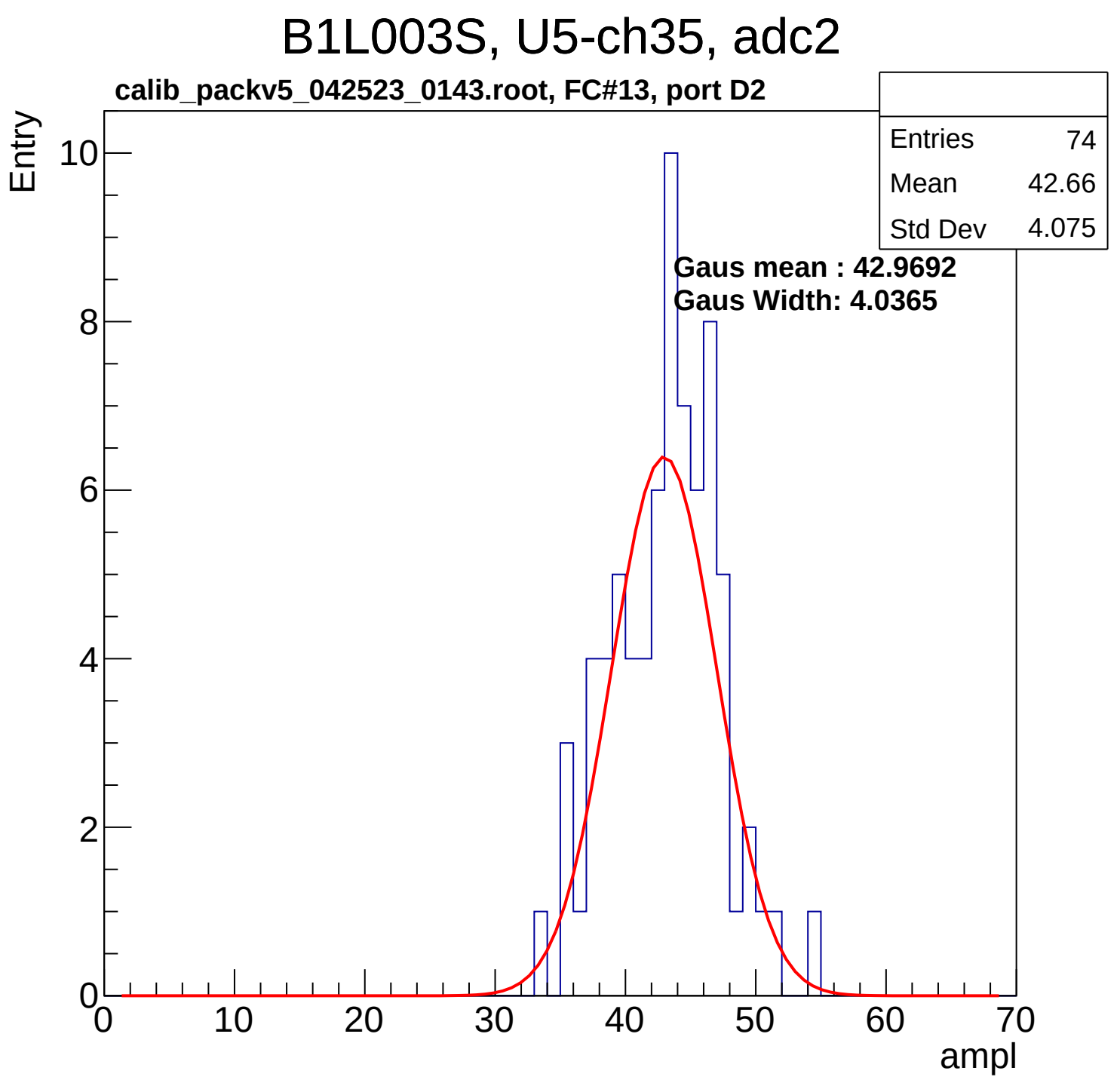
B1L003S, U5-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.66
Std Dev 4.075
Gaus mean : 42.9692
Gaus Width: 4.0365
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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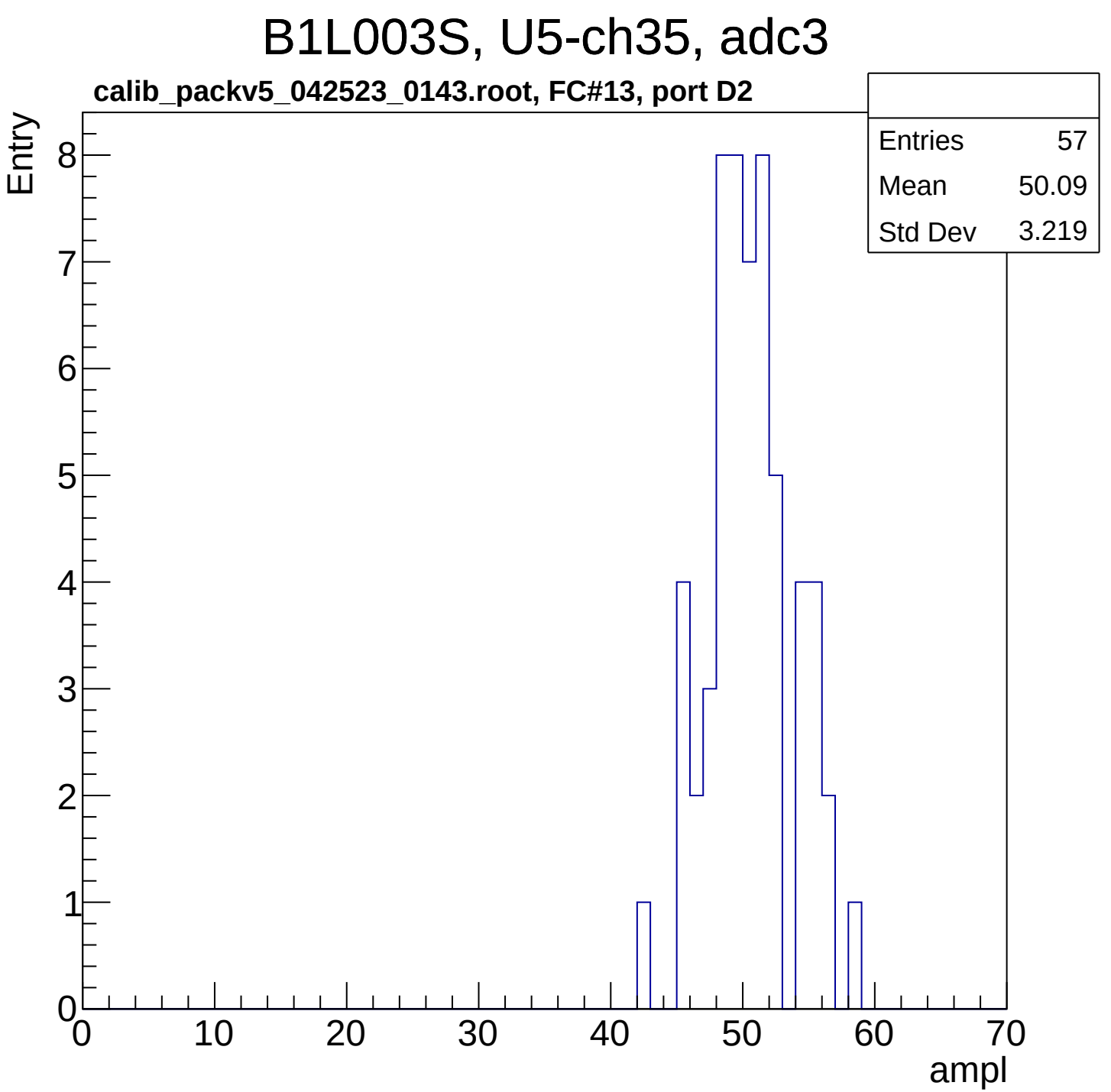
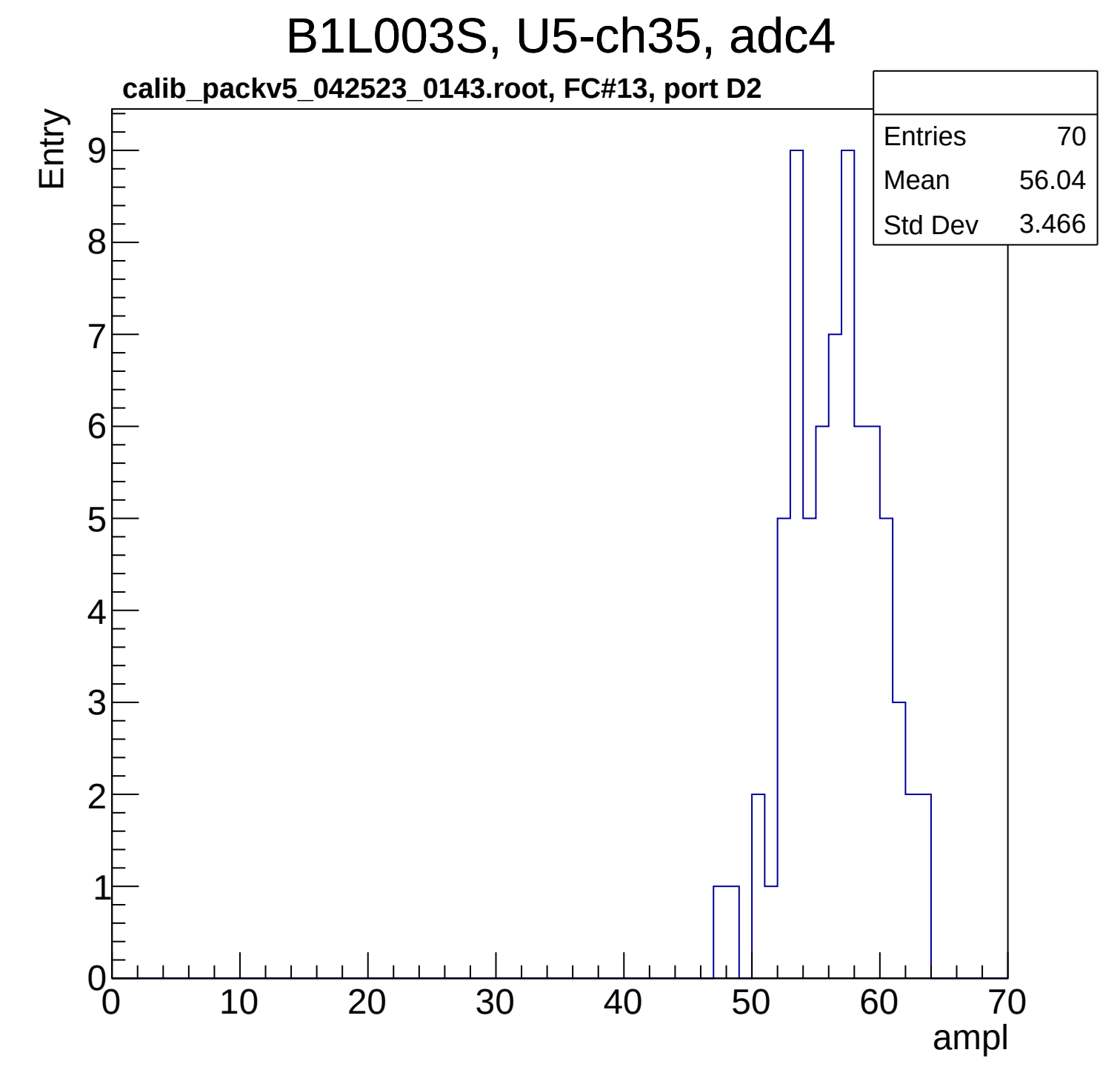

B1L003S, U5-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 50.09
Std Dev 3.219
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 56.04
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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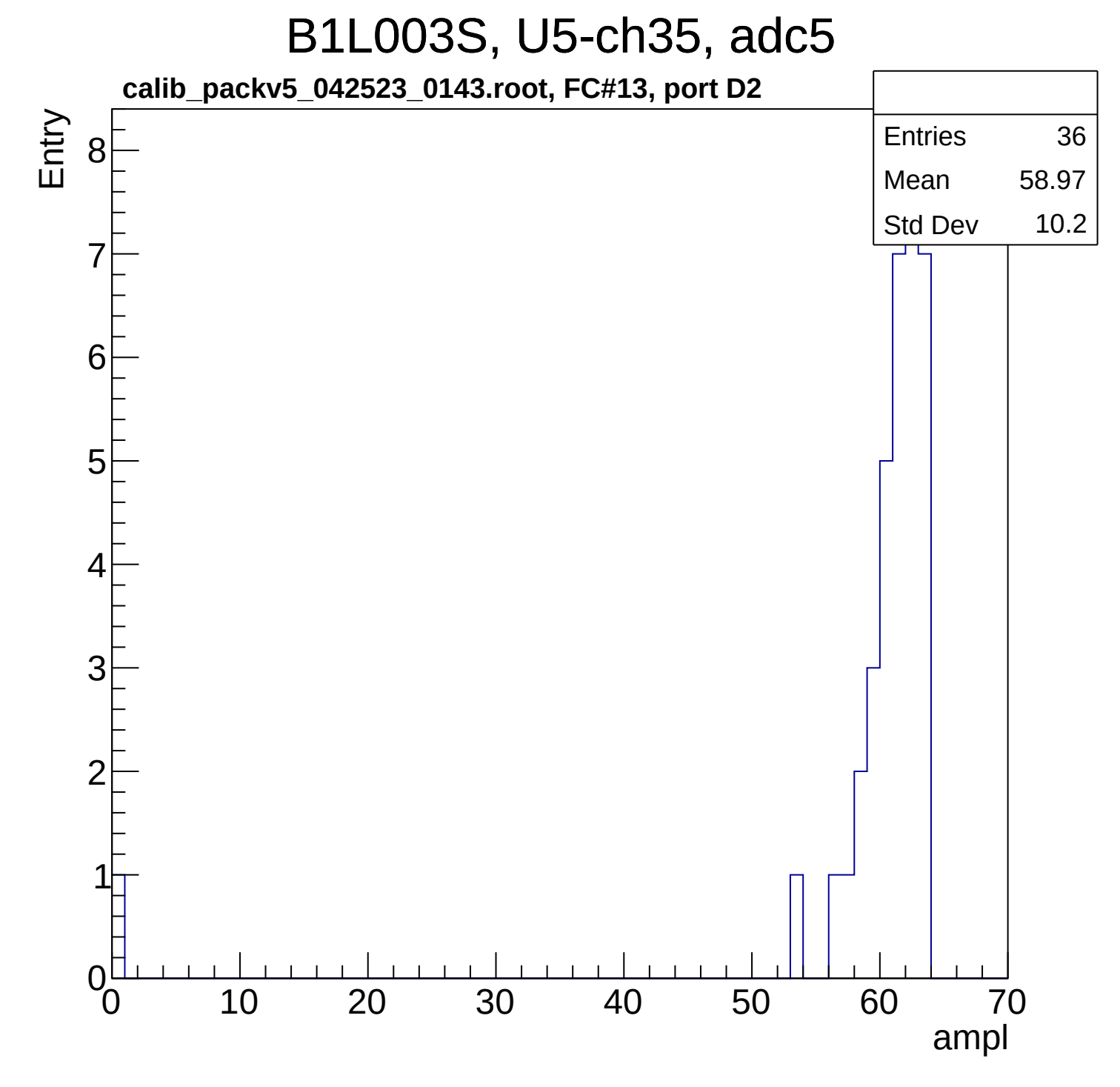
B1L003S, U5-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.97
Std Dev 10.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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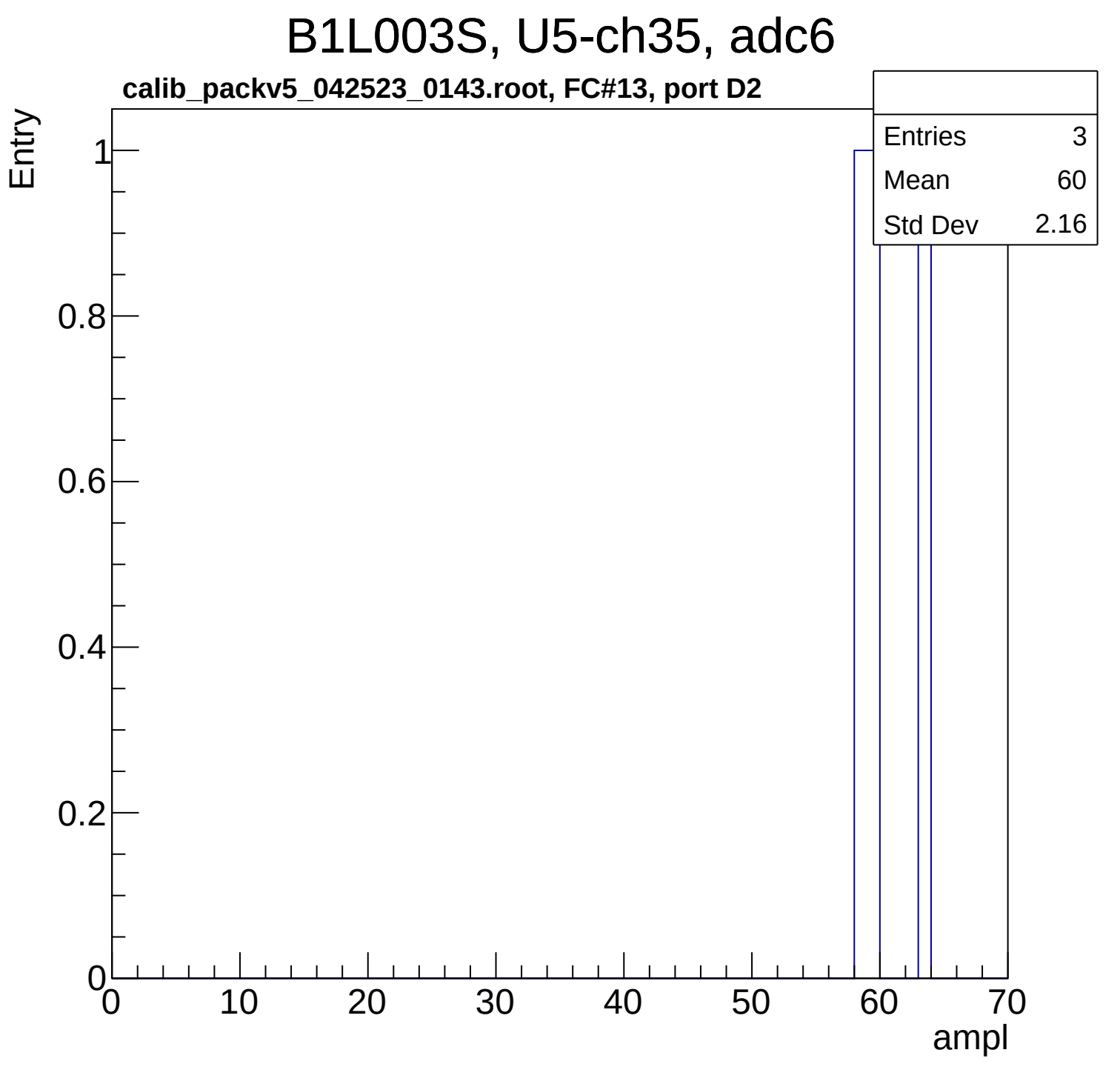
B1L003S, U5-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 60
Std Dev 2.16
Entry
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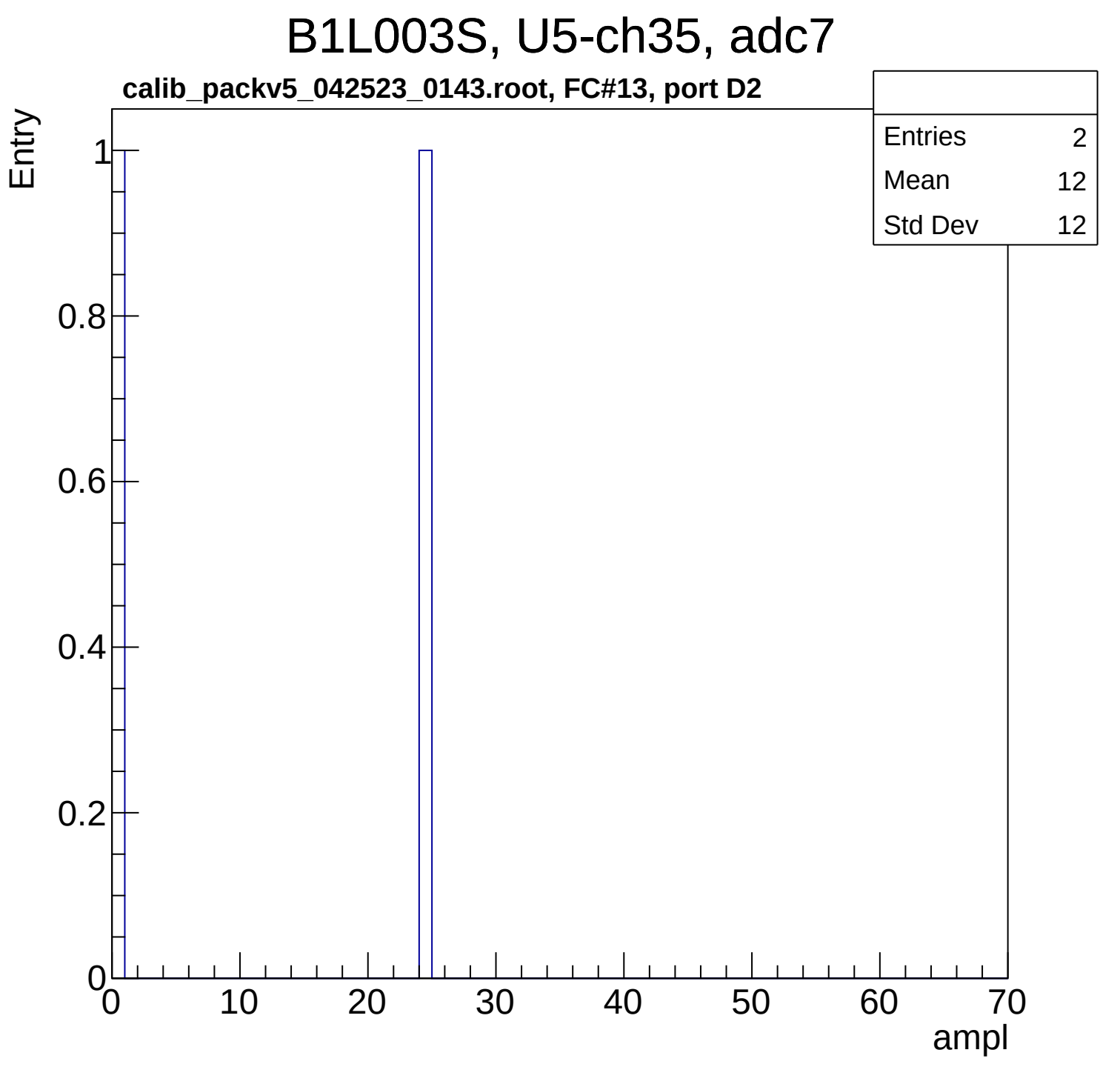
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
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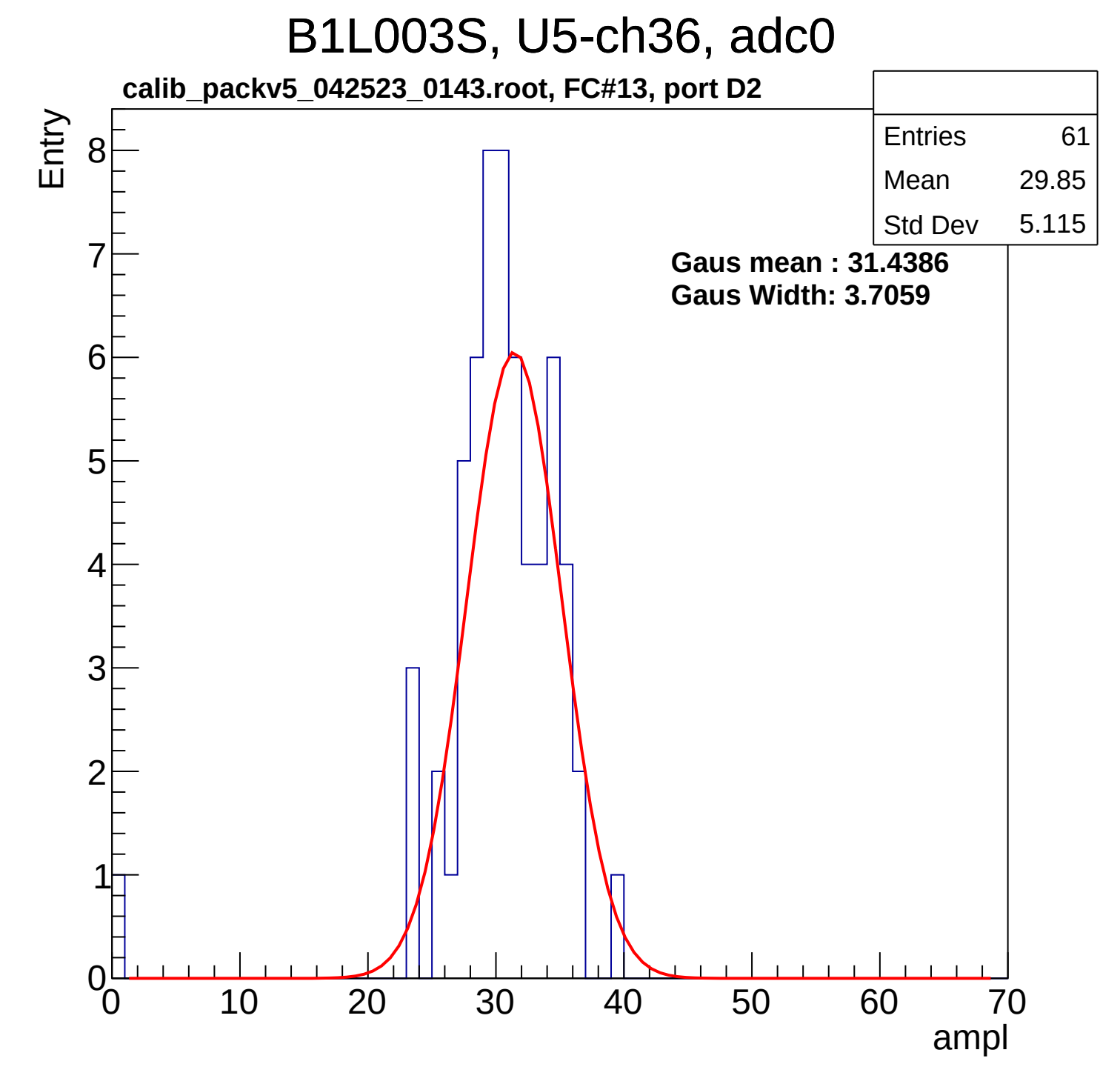
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.85
Std Dev 5.115
Gaus mean : 31.4386
Gaus Width: 3.7059
Entry
ampl
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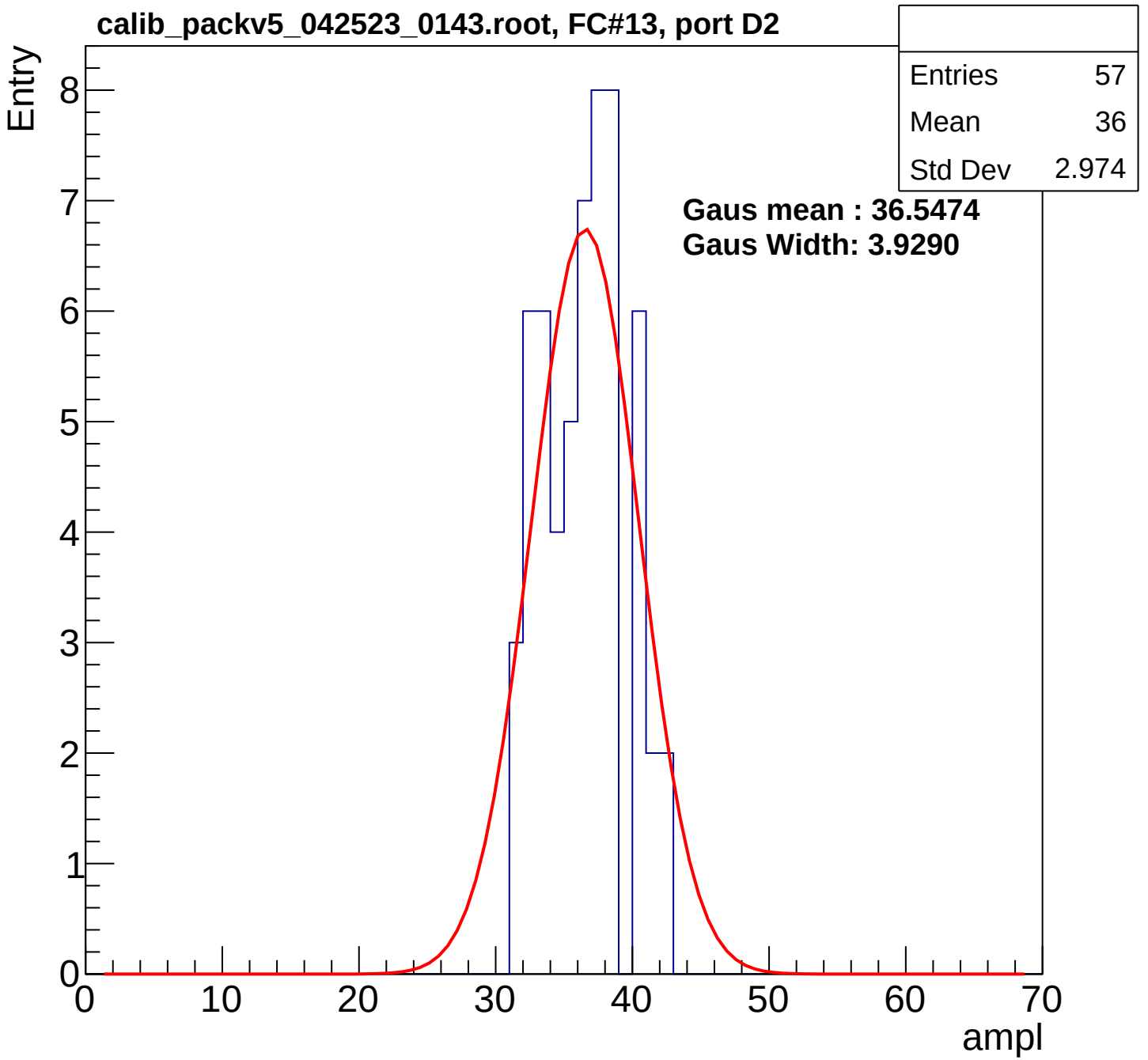
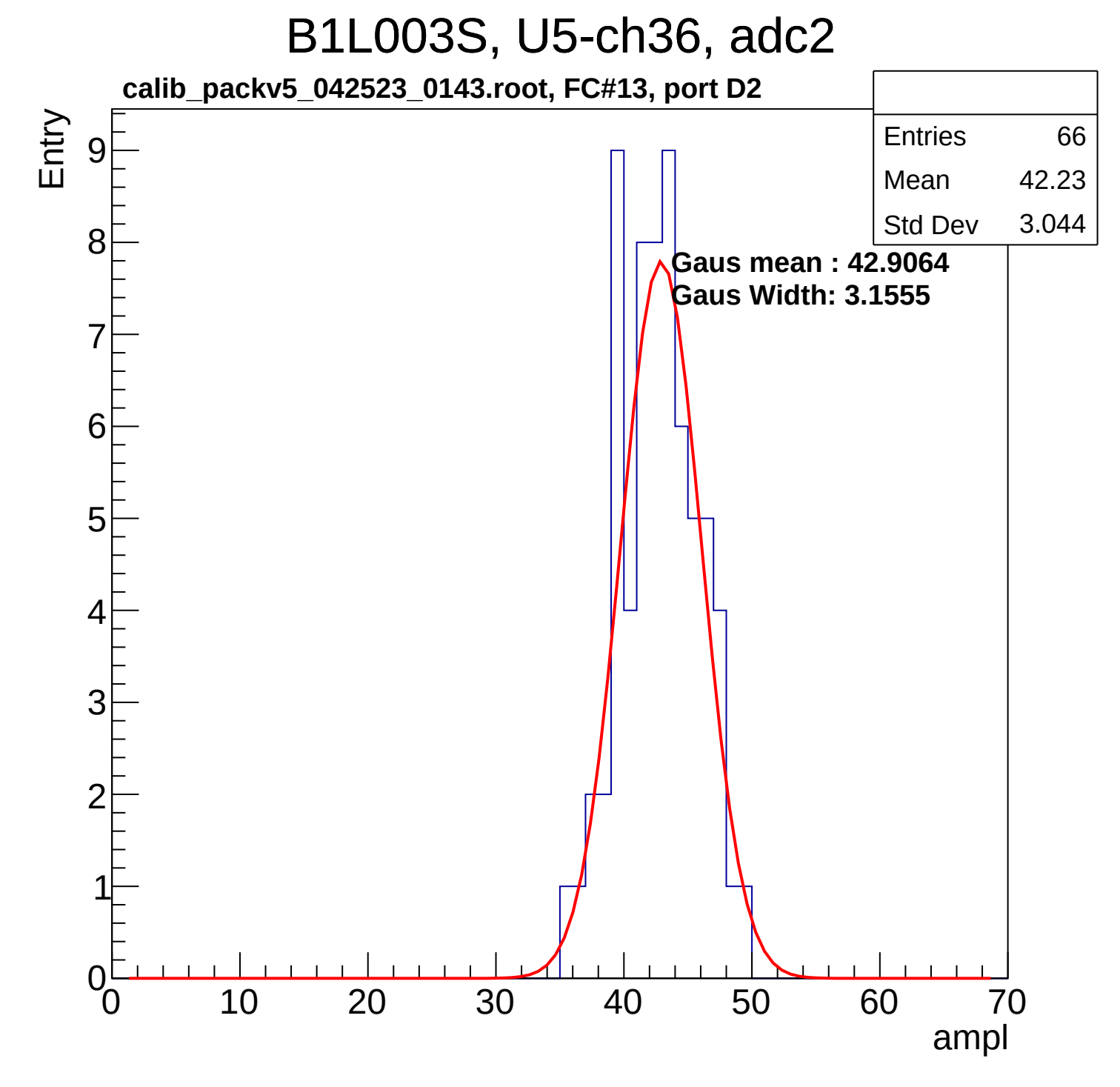
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36
Std Dev 2.974
Gaus mean : 36.5474
Gaus Width: 3.9290
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.23
Std Dev 3.044
Gaus mean : 42.9064
Gaus Width: 3.1555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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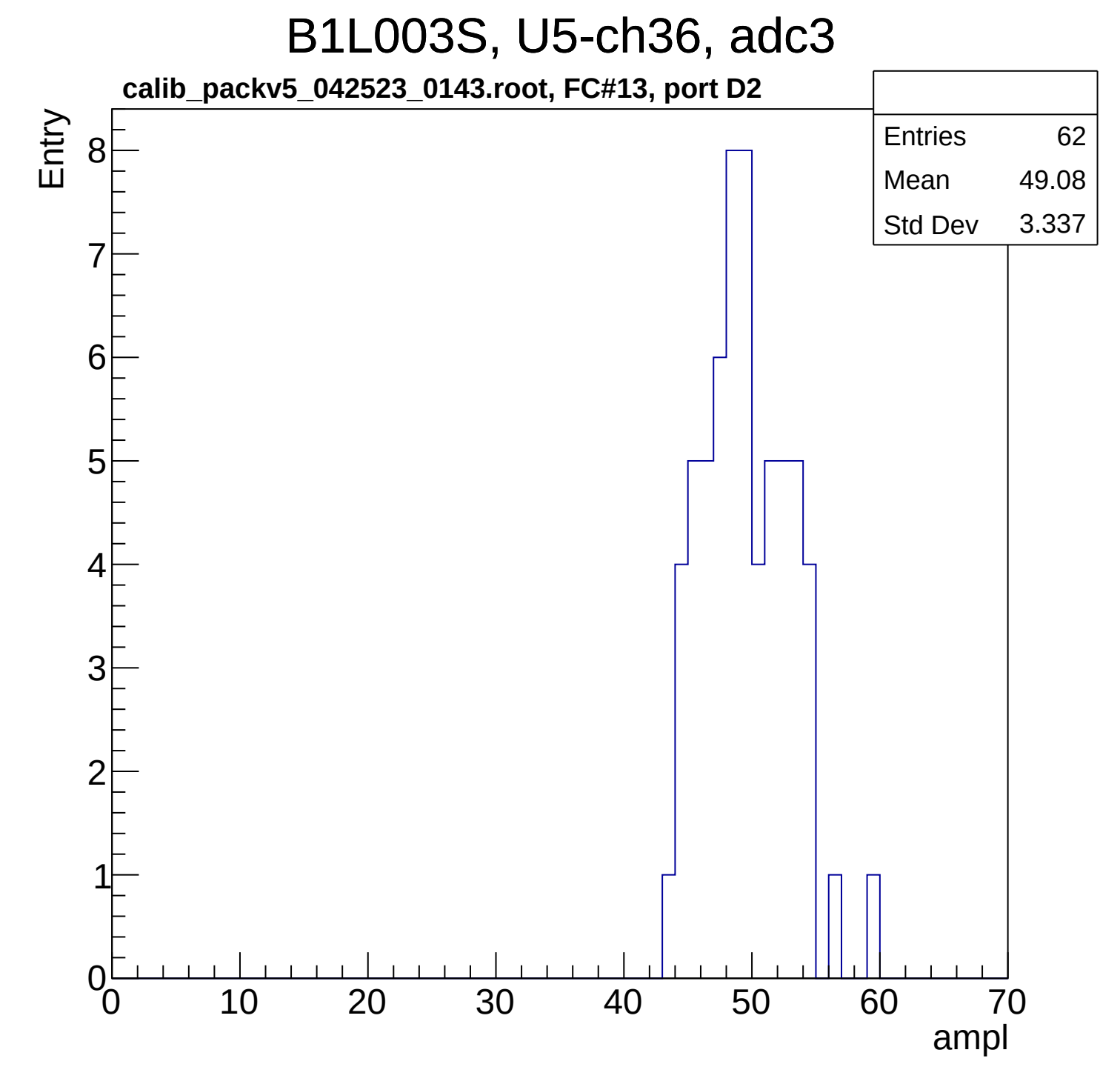
B1L003S, U5-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 49.08
Std Dev 3.337
Entry
ampl
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1
2
3
4
5
6
7
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0
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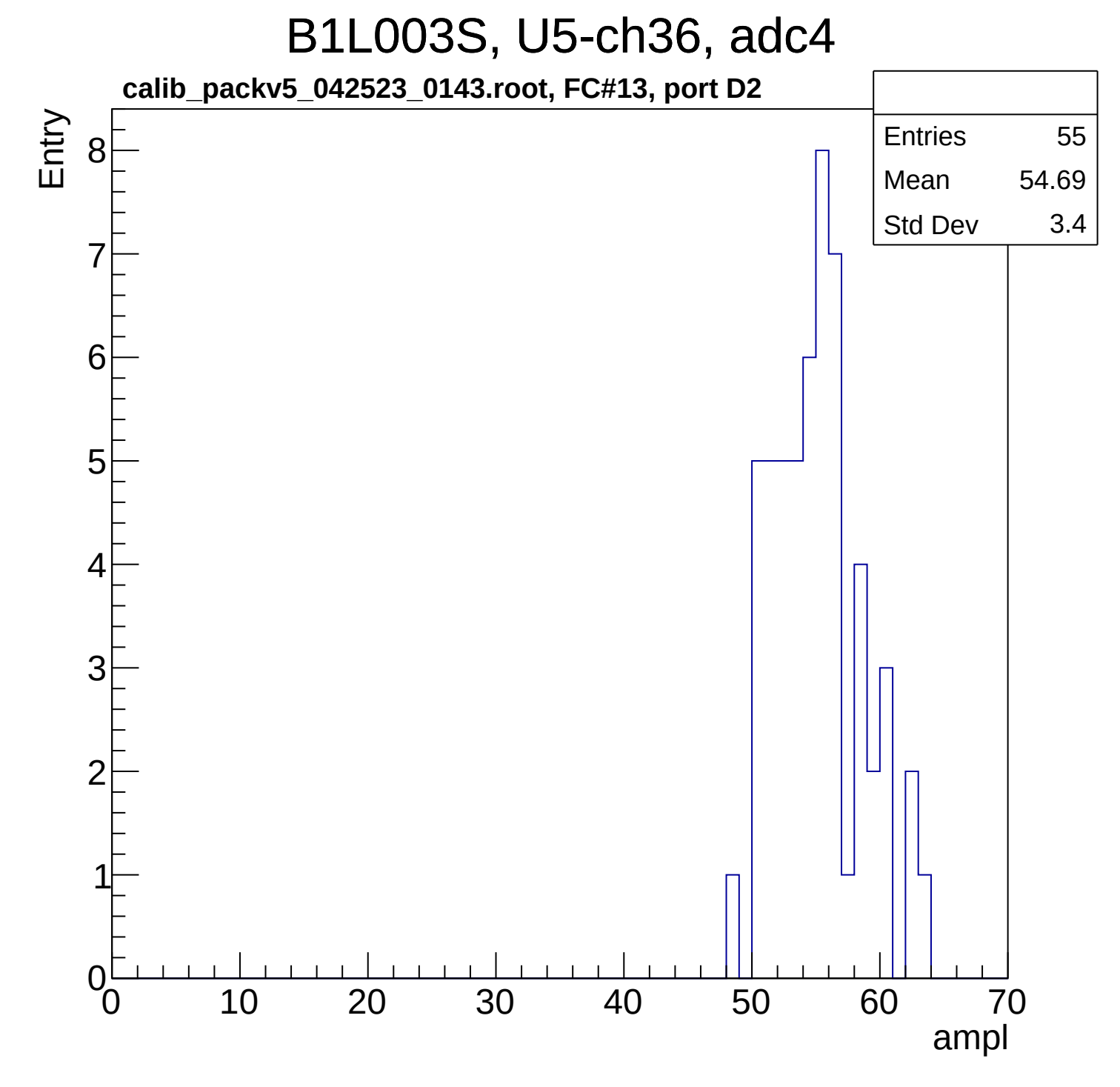
B1L003S, U5-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.69
Std Dev 3.4
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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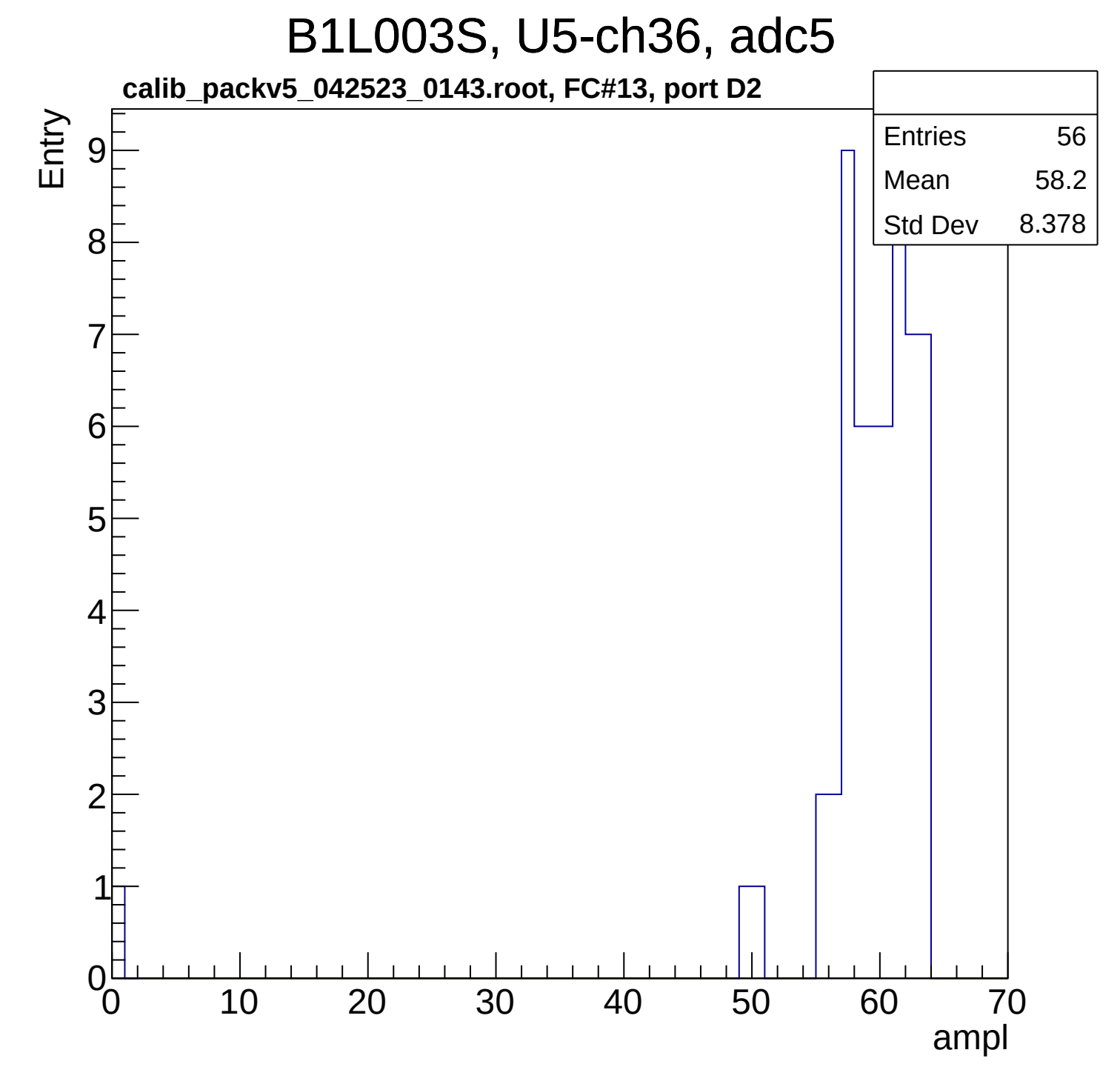
B1L003S, U5-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.2
Std Dev 8.378
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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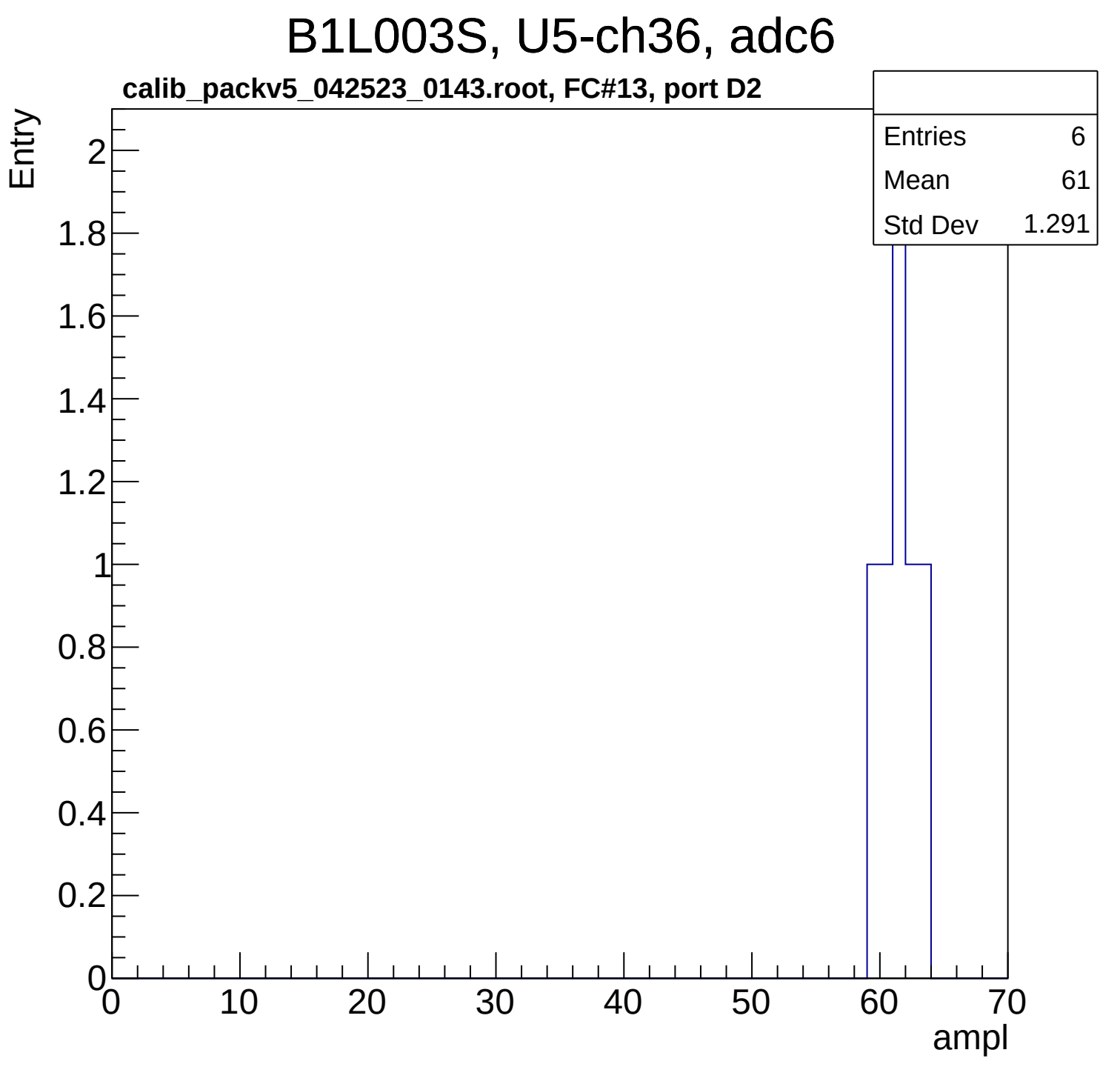
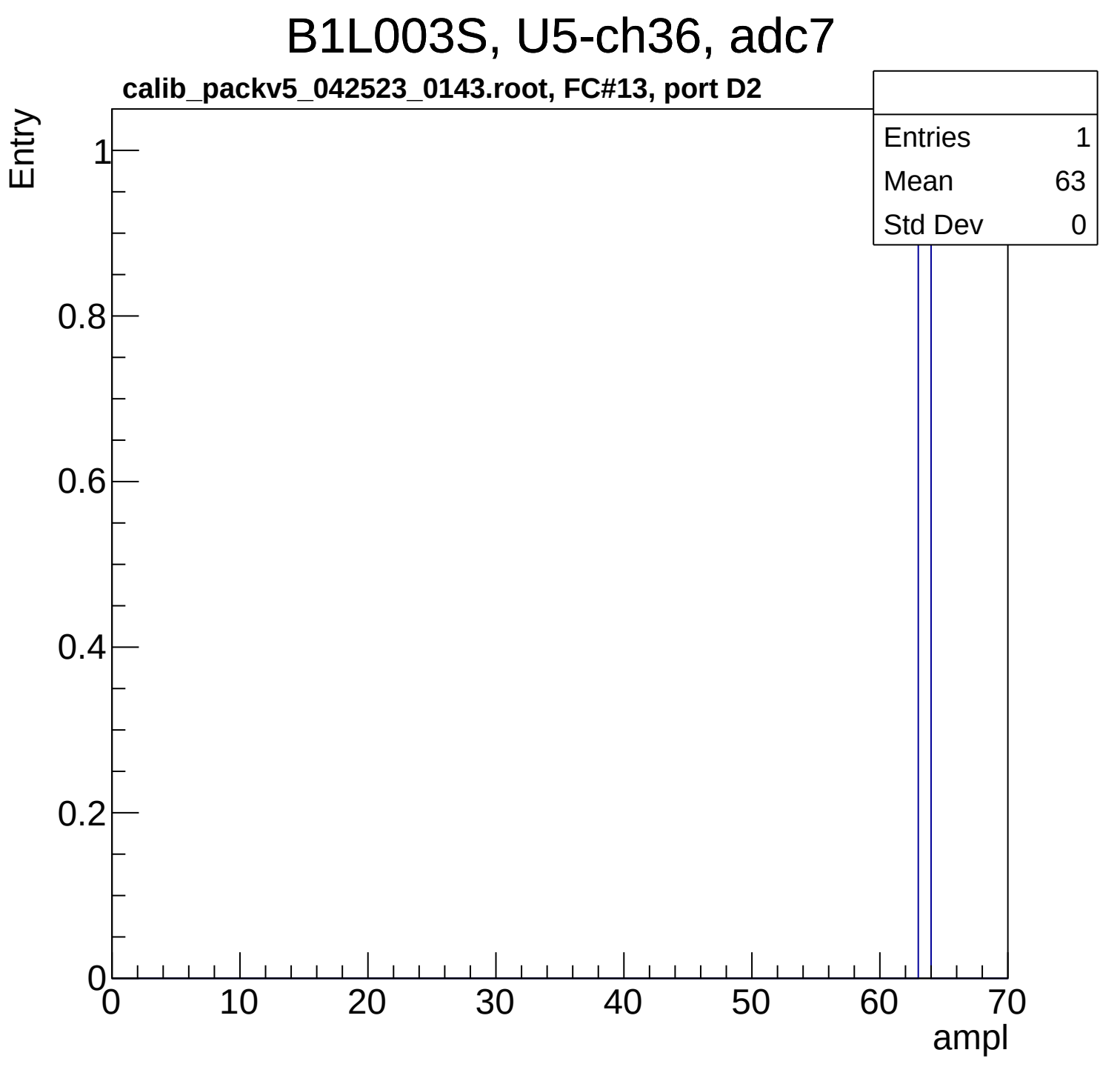
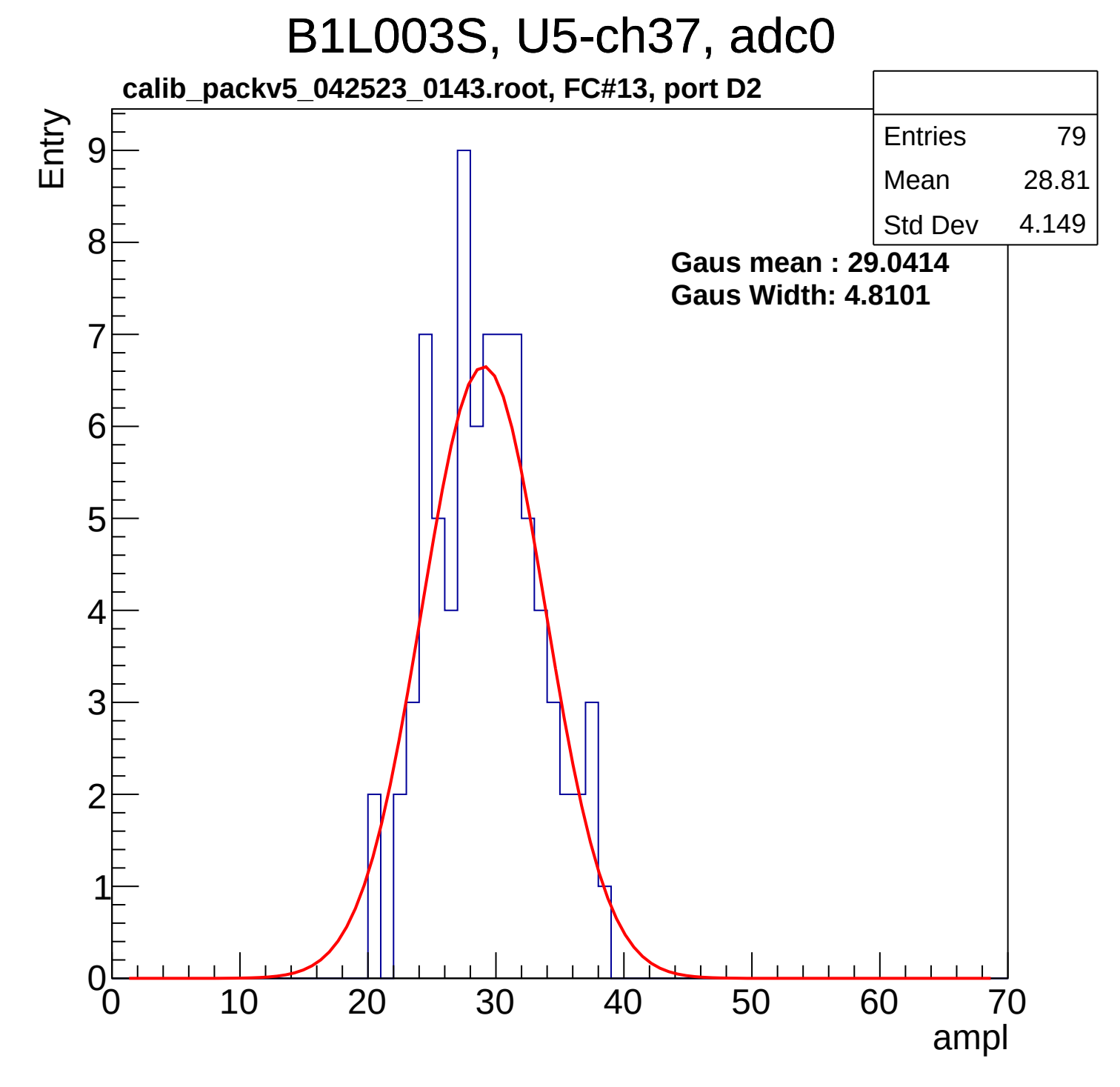

B1L003S, U5-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61
Std Dev 1.291
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.81
Std Dev 4.149
Gaus mean : 29.0414
Gaus Width: 4.8101
Entry
ampl
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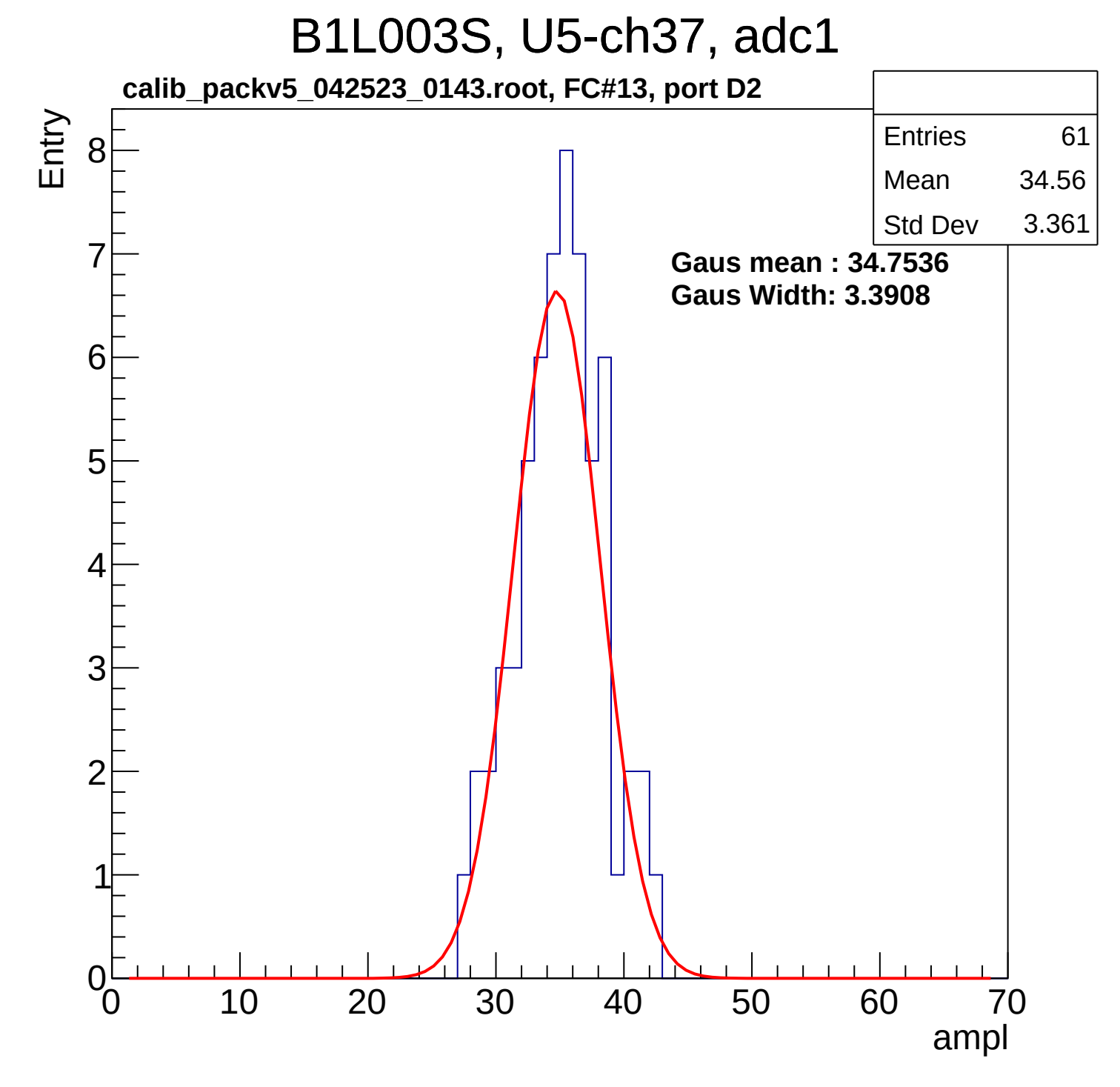
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 34.56
Std Dev 3.361
Gaus mean : 34.7536
Gaus Width: 3.3908
Entry
ampl
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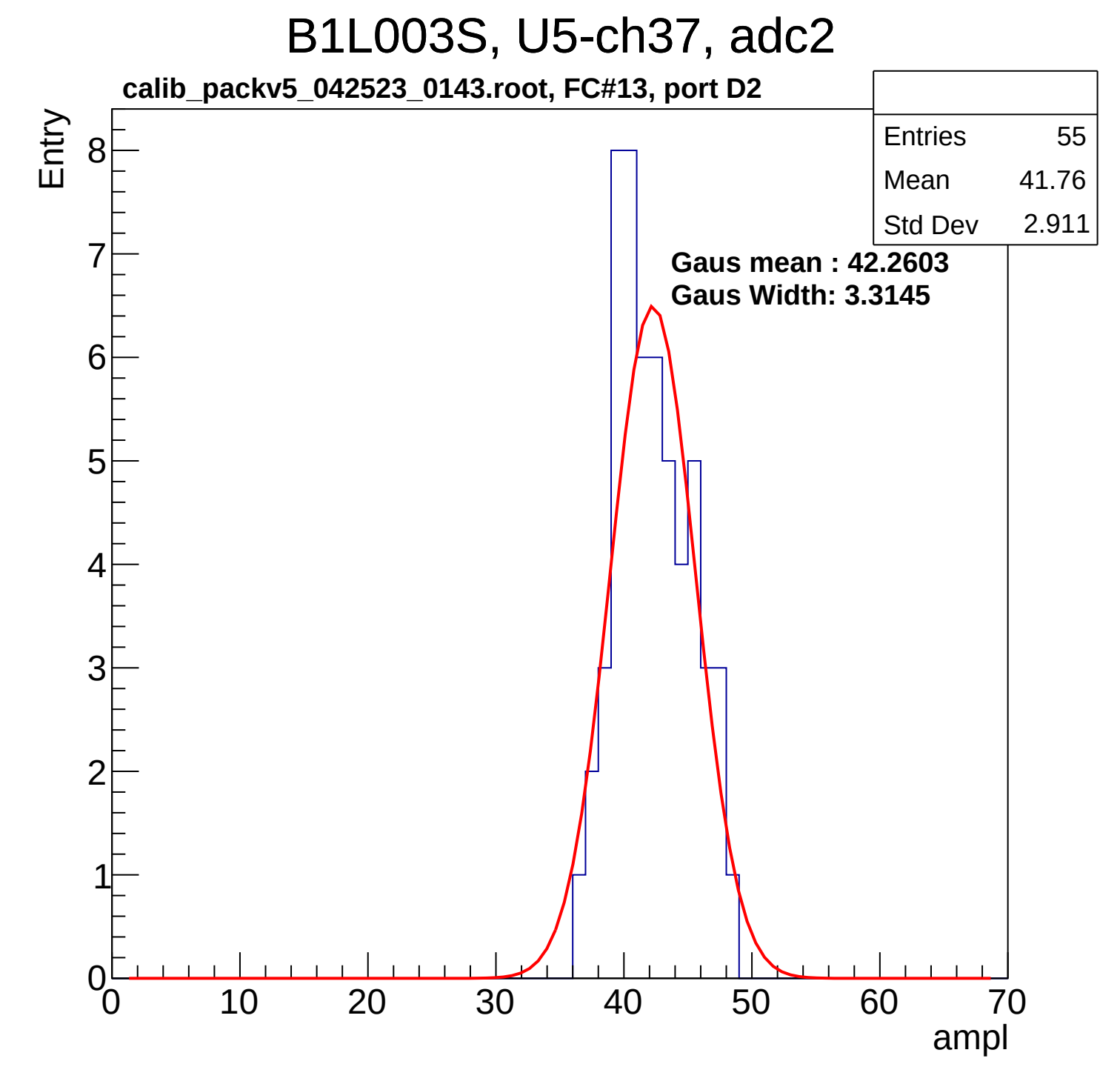
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.76
Std Dev 2.911
Gaus mean : 42.2603
Gaus Width: 3.3145
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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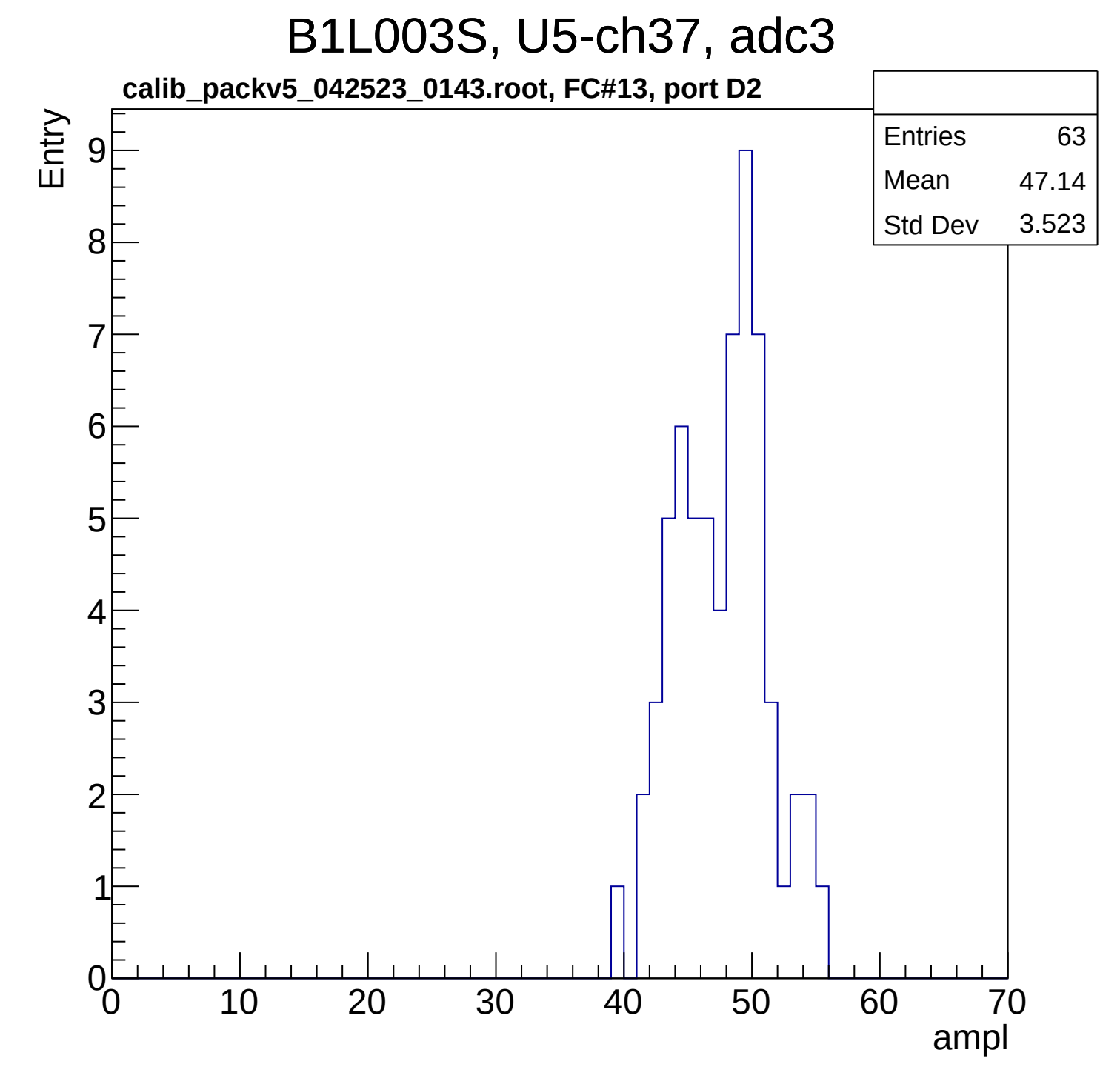
B1L003S, U5-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 47.14
Std Dev 3.523
Entry
ampl
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5
6
7
8
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0
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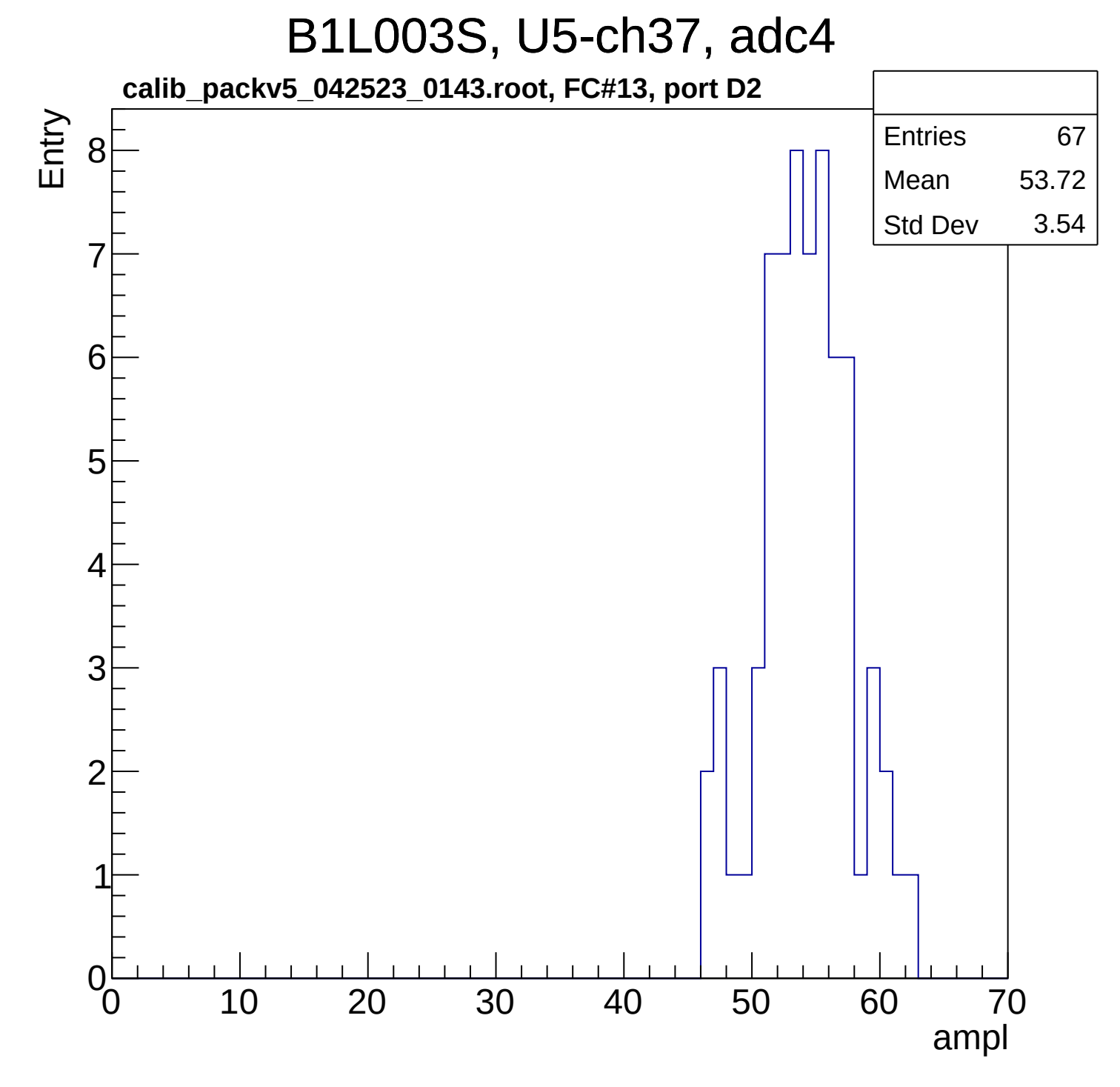
B1L003S, U5-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 53.72
Std Dev 3.54
Entry
ampl
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5
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7
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0
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20
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40
50
60
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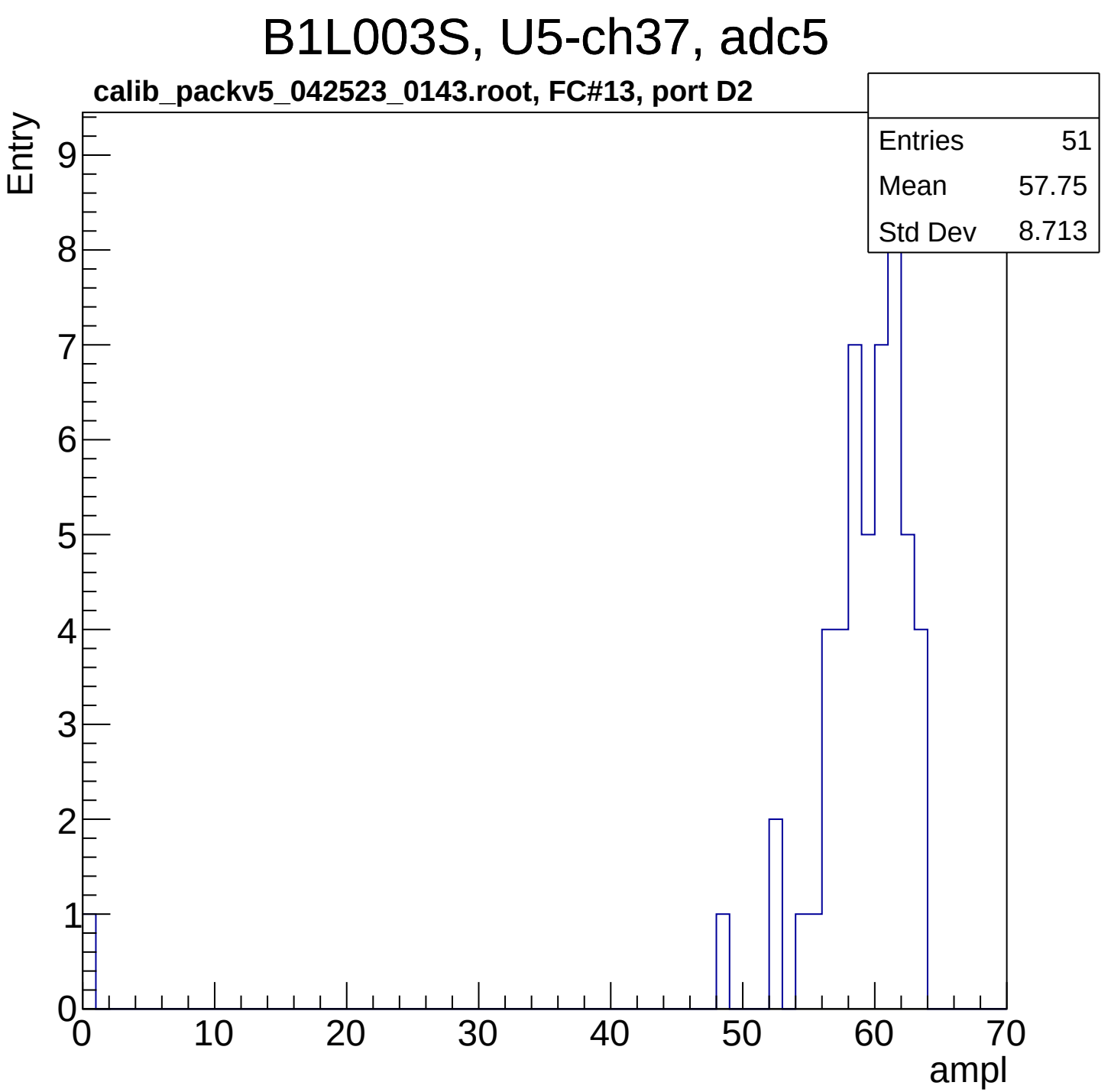
B1L003S, U5-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 57.75
Std Dev 8.713
Entry
ampl
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2
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4
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6
7
8
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0
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60
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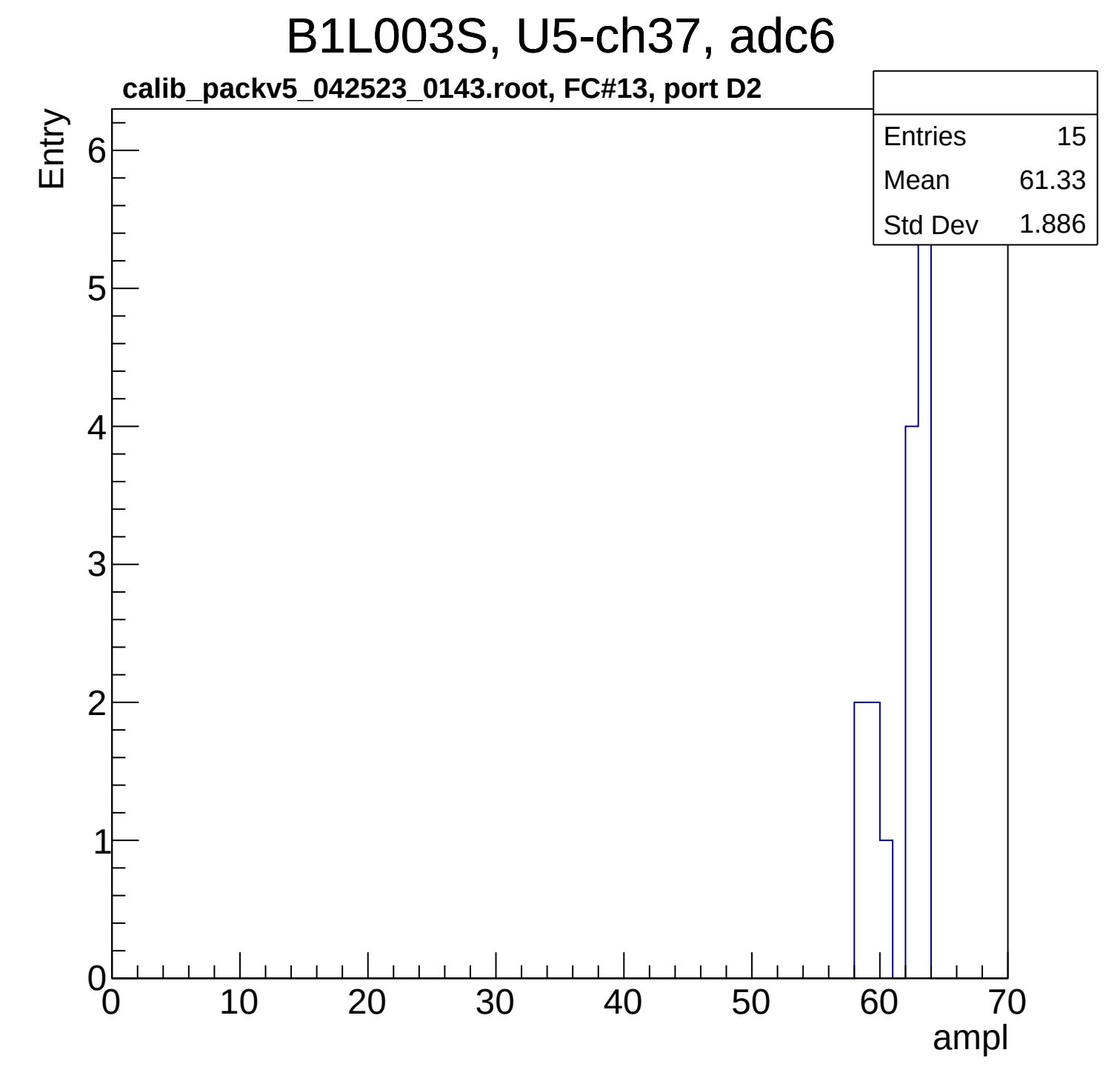
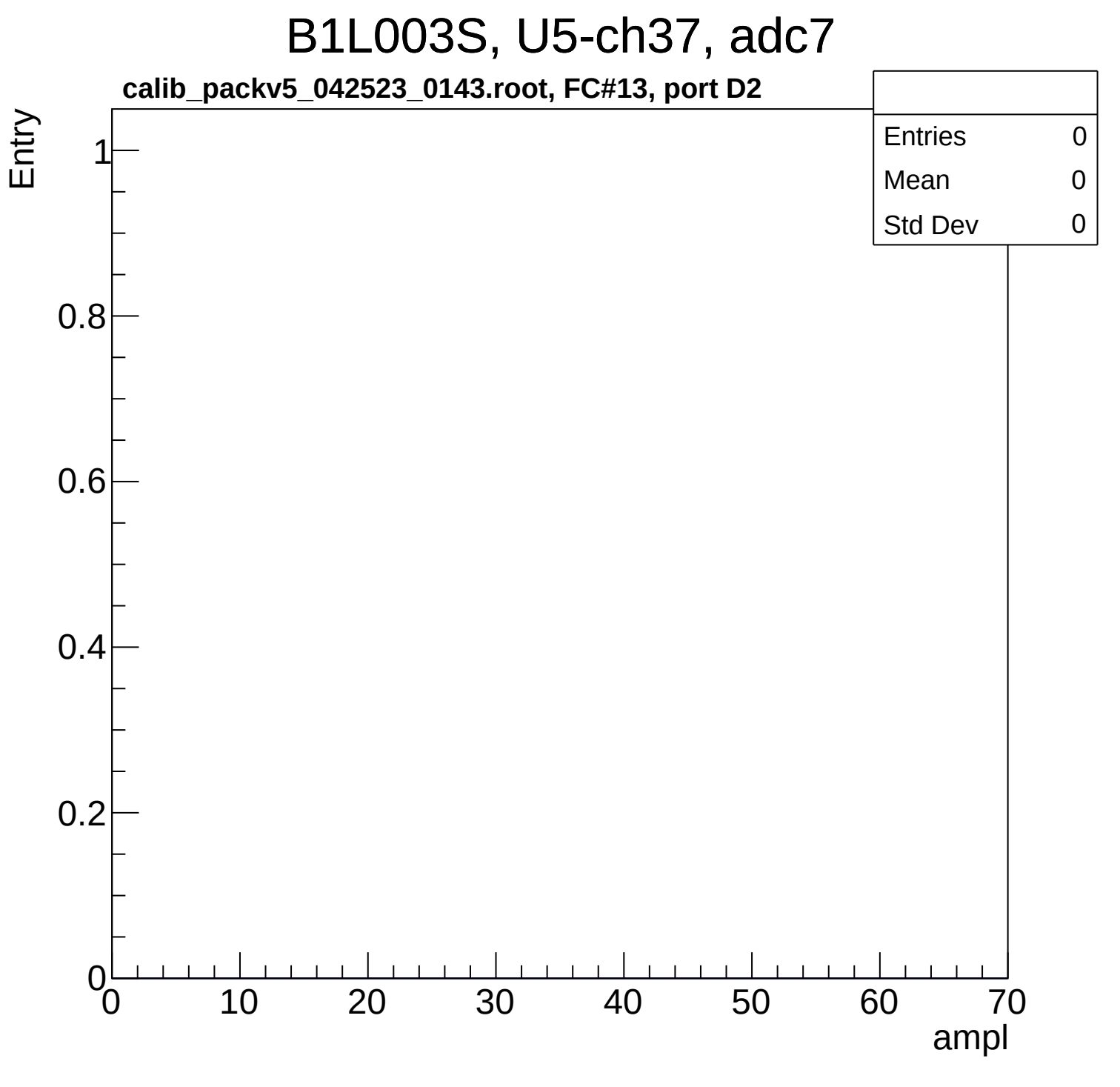

B1L003S, U5-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.33
Std Dev 1.886
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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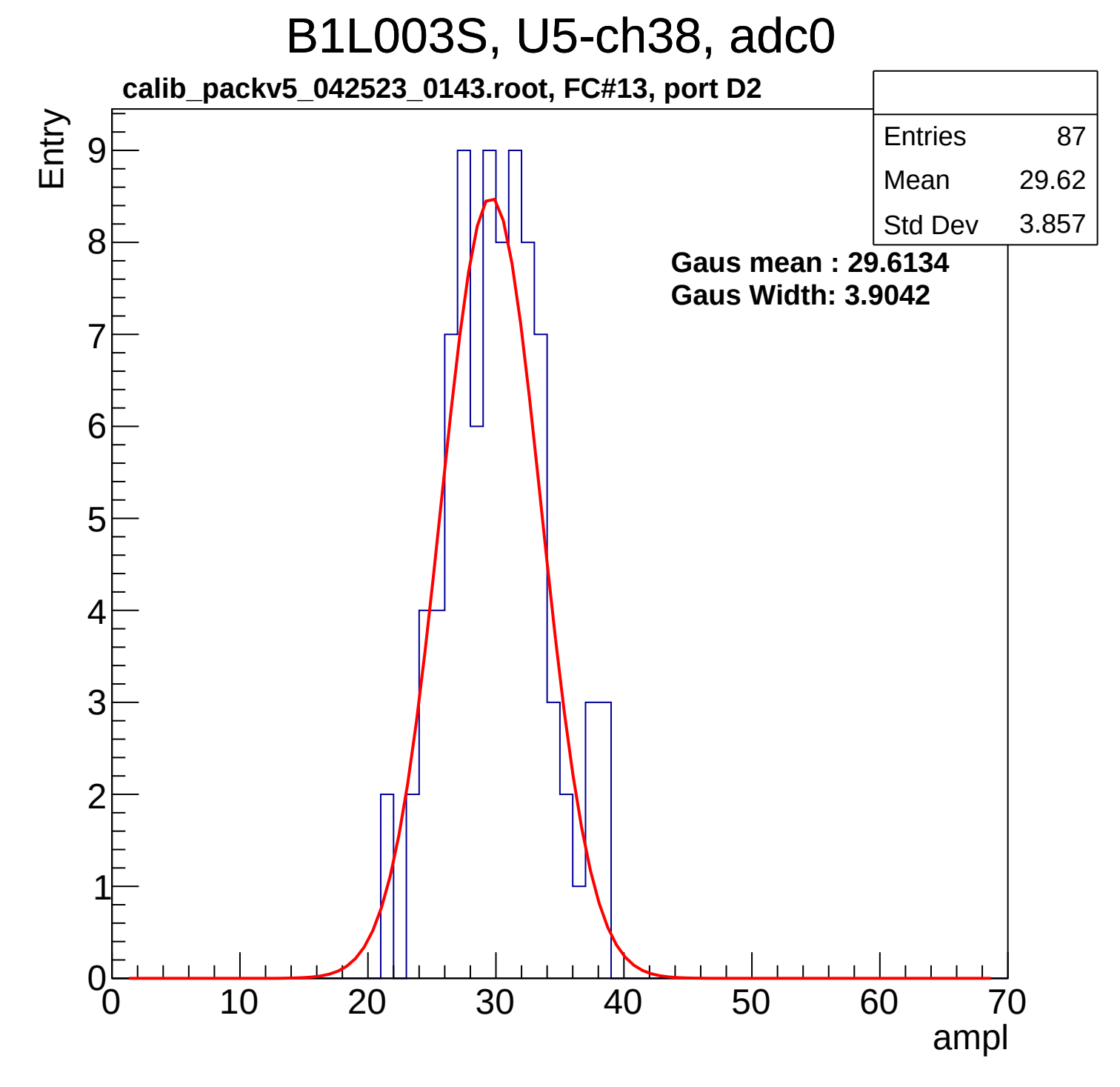
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 29.62
Std Dev 3.857
Gaus mean : 29.6134
Gaus Width: 3.9042
Entry
ampl
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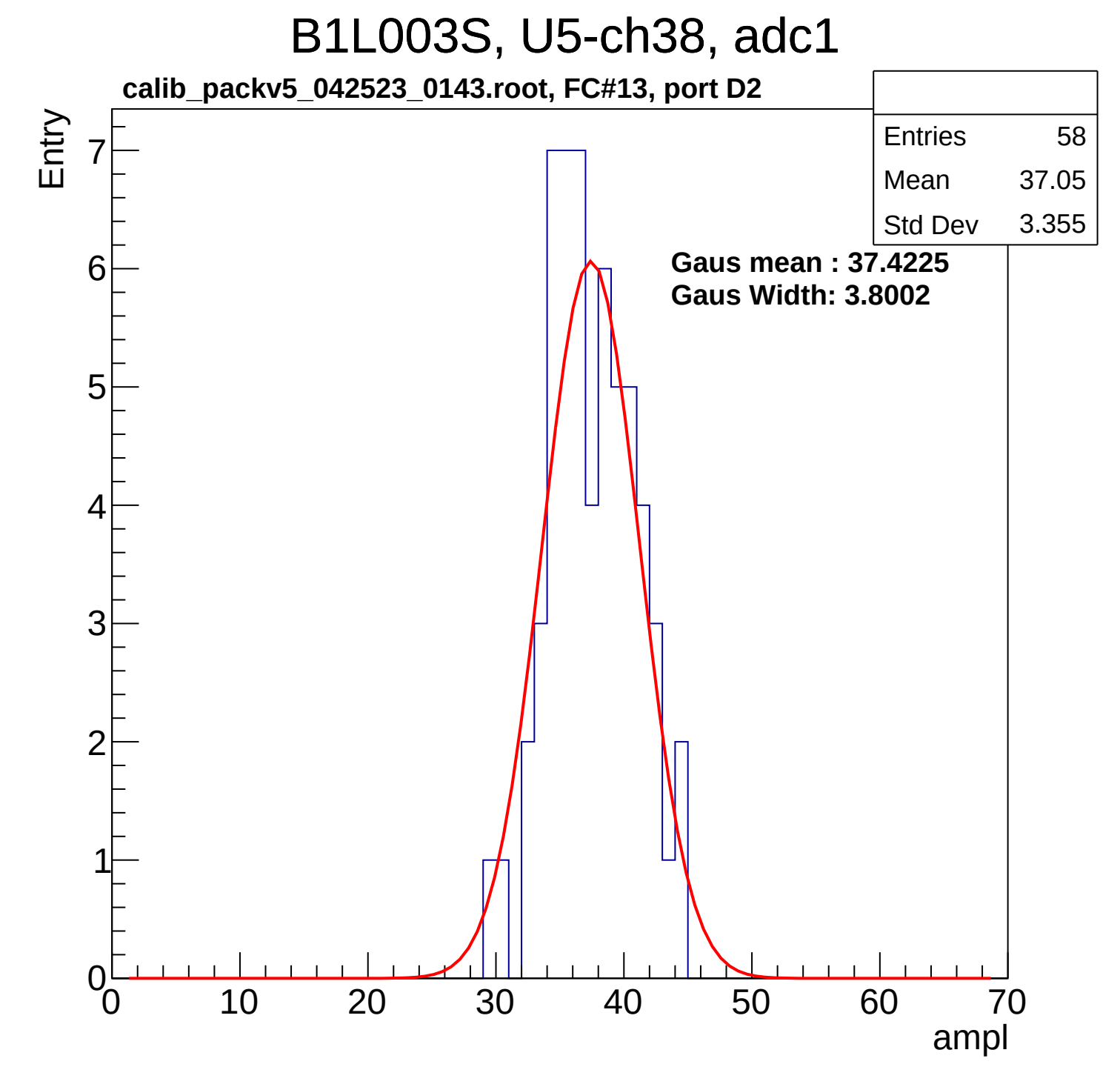
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 37.05
Std Dev 3.355
Gaus mean : 37.4225
Gaus Width: 3.8002
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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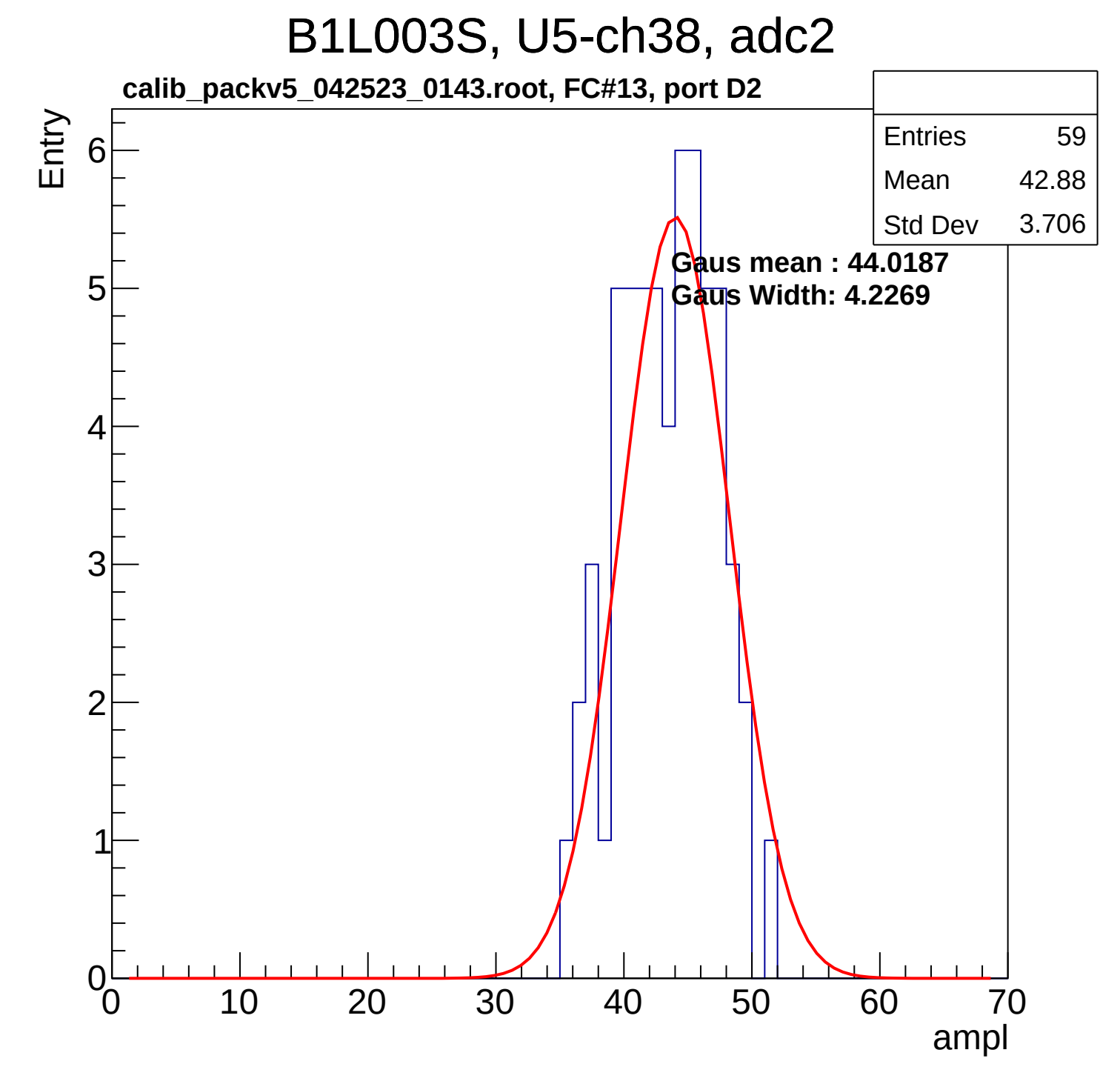
B1L003S, U5-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.88
Std Dev 3.706
Gaus mean : 44.0187
Gaus Width: 4.2269
Entry
ampl
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2
3
4
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0
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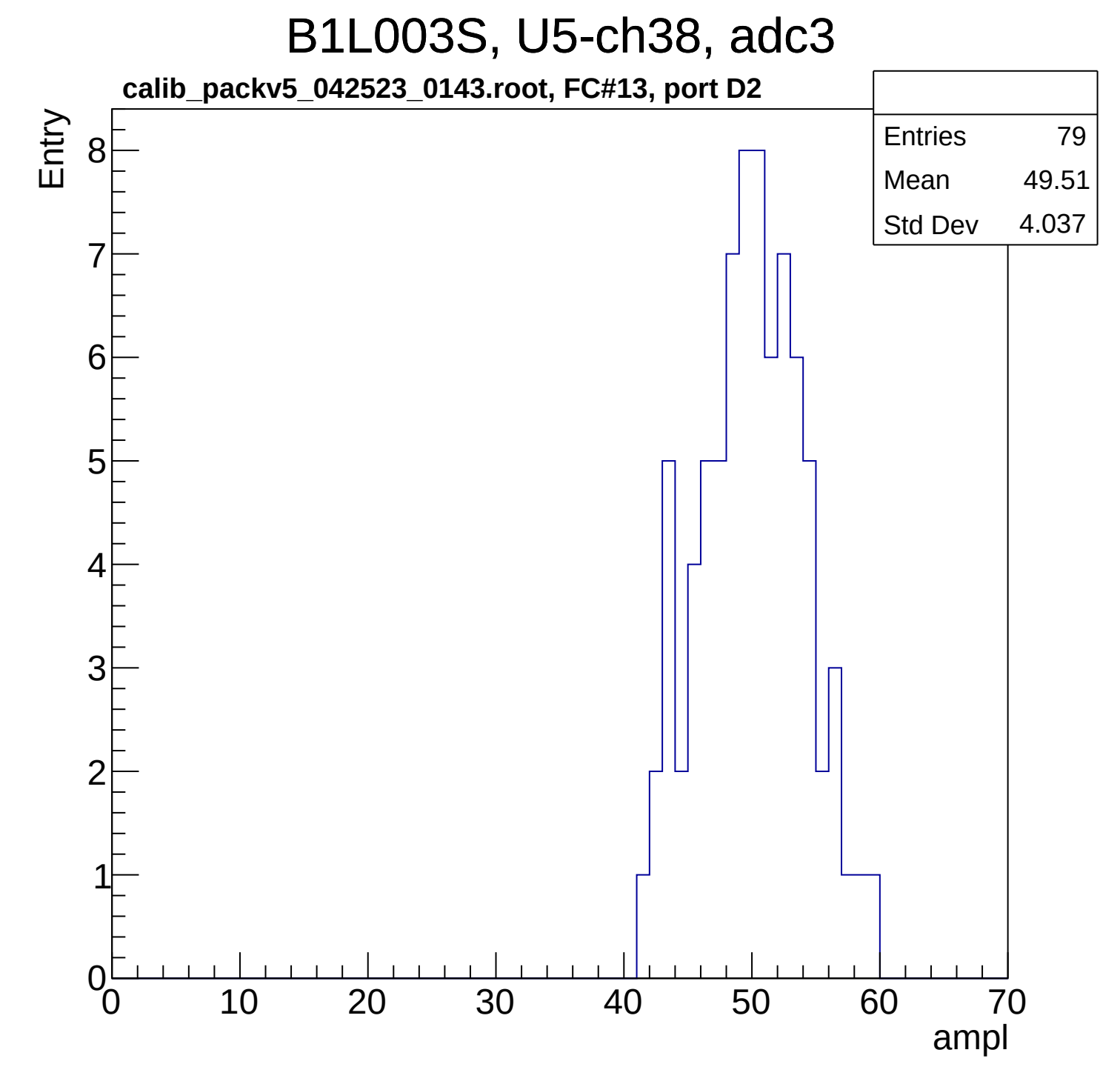
B1L003S, U5-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.51
Std Dev 4.037
Entry
ampl
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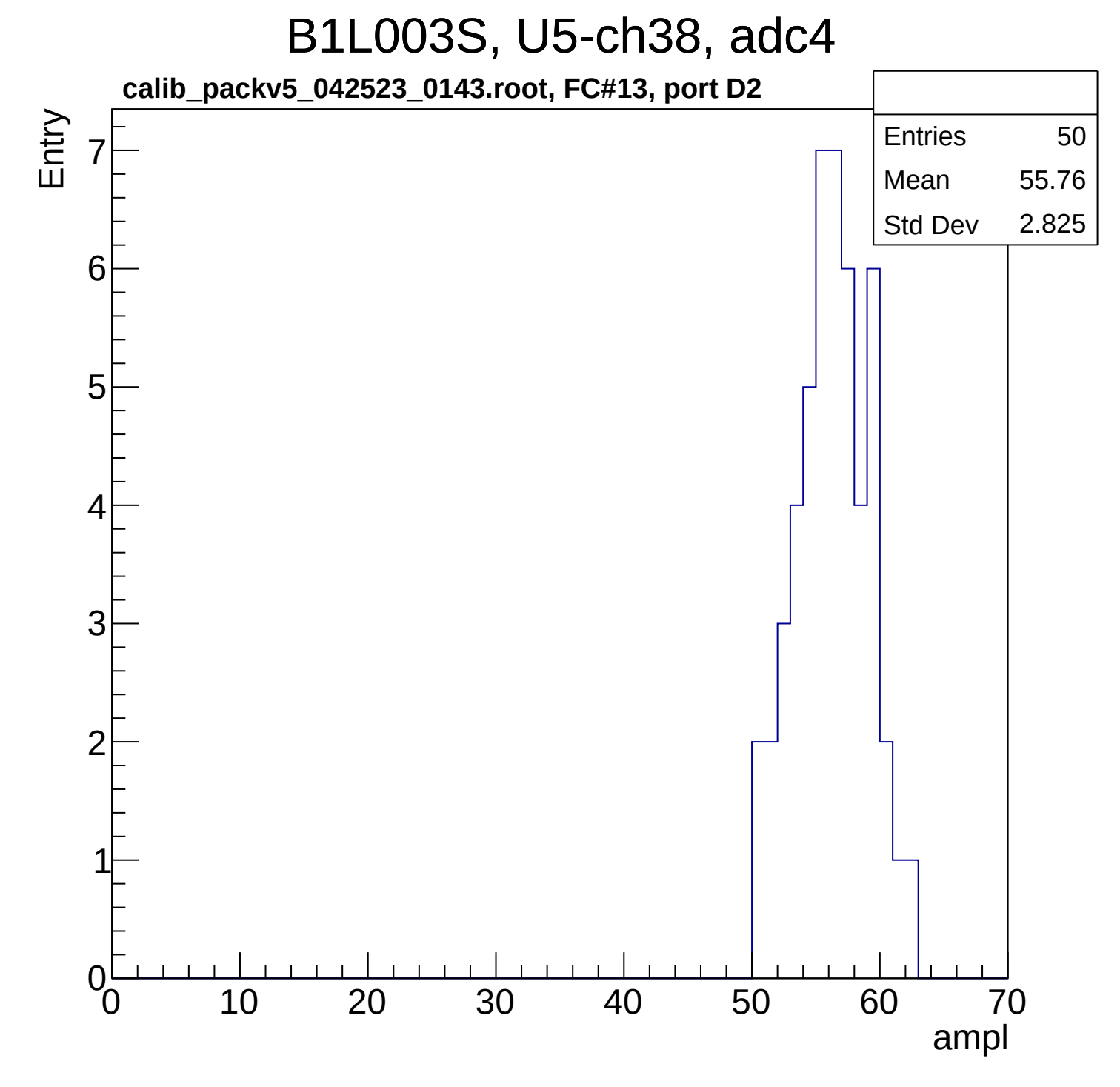
B1L003S, U5-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.76
Std Dev 2.825
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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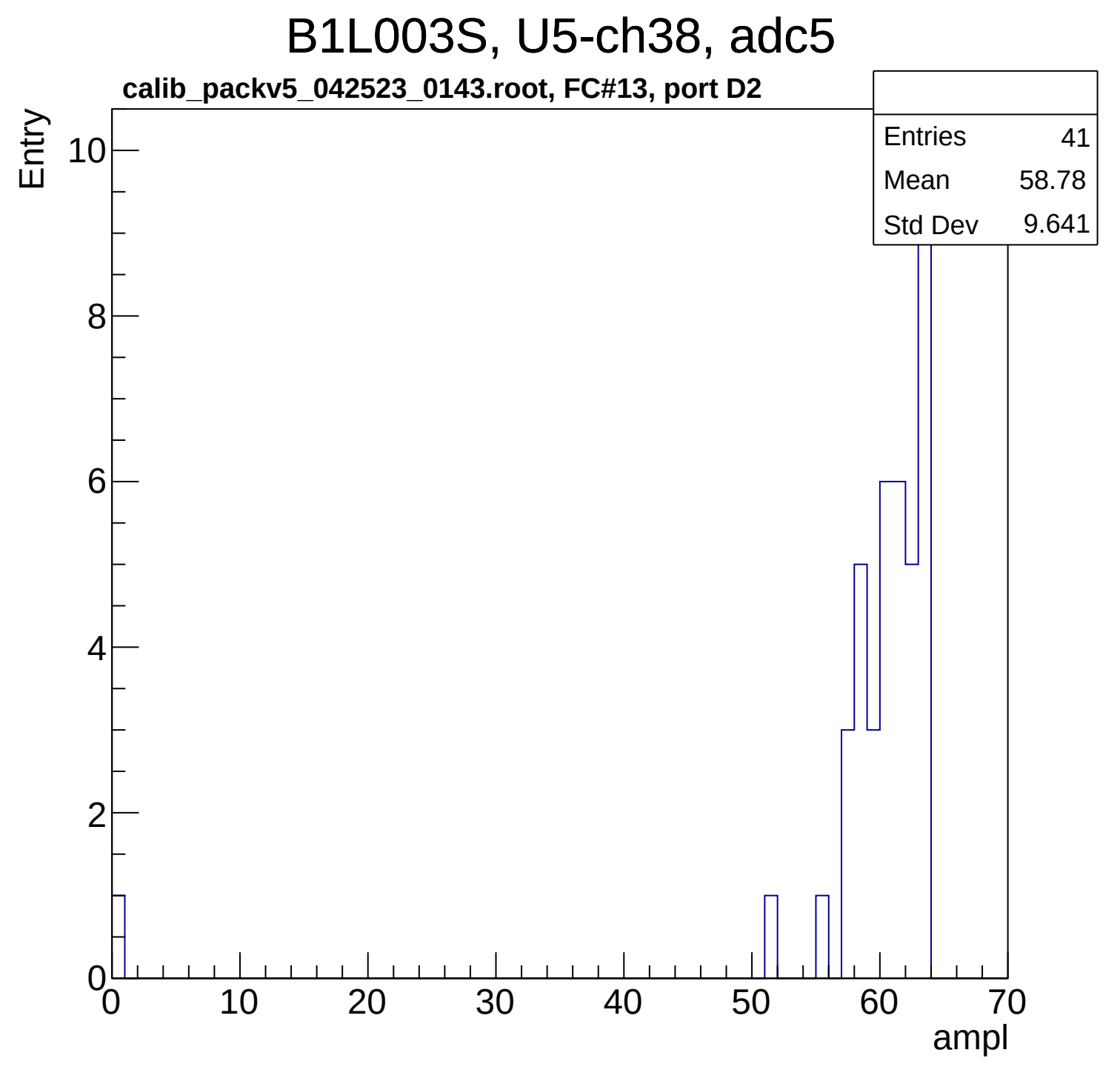
B1L003S, U5-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.78
Std Dev 9.641
Entry
0
2
4
6
8
10
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50
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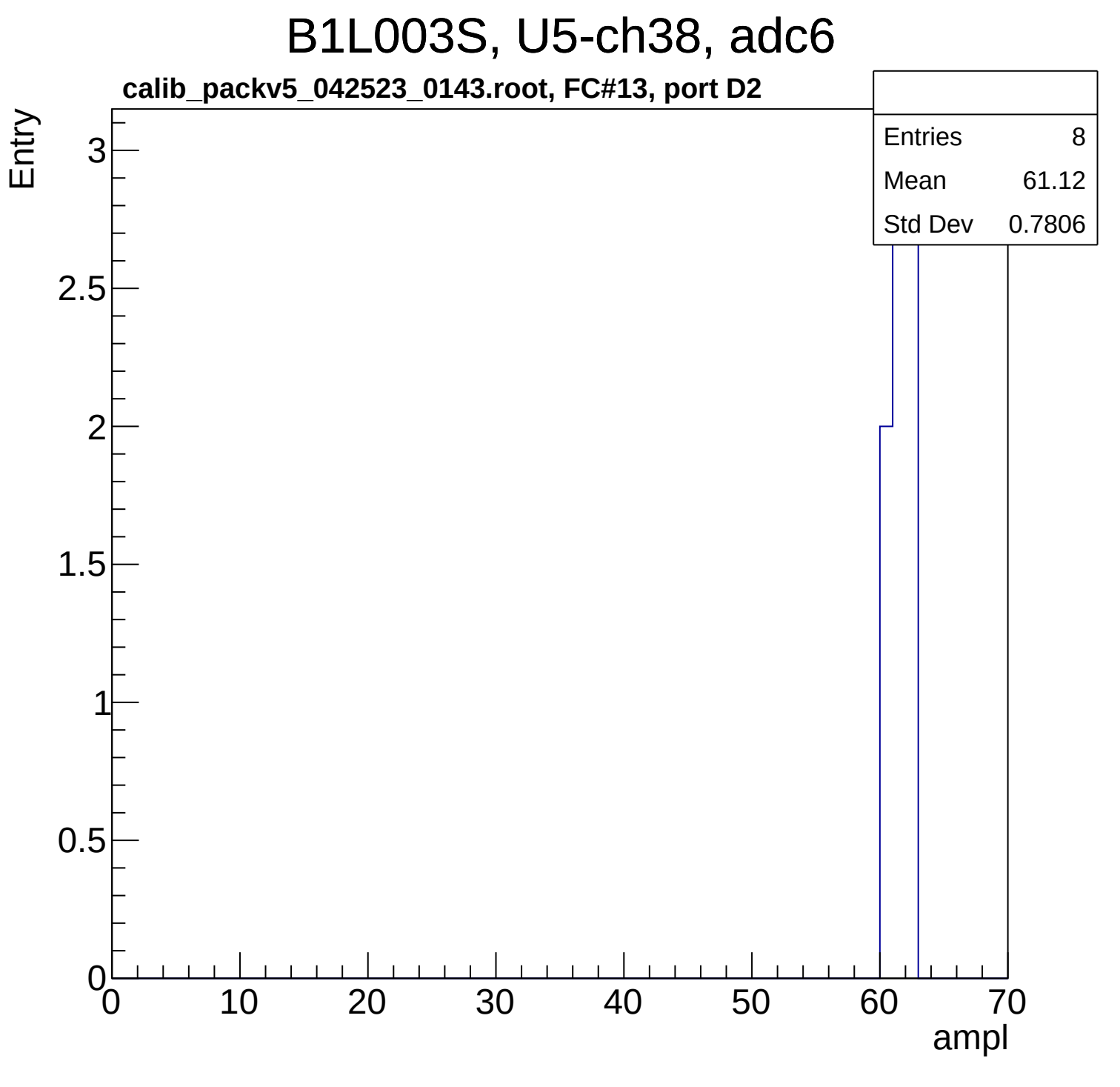
70
ampl


B1L003S, U5-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.12
Std Dev 0.7806
Entry
0
0.5
1
1.5
2
2.5
3
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10
20
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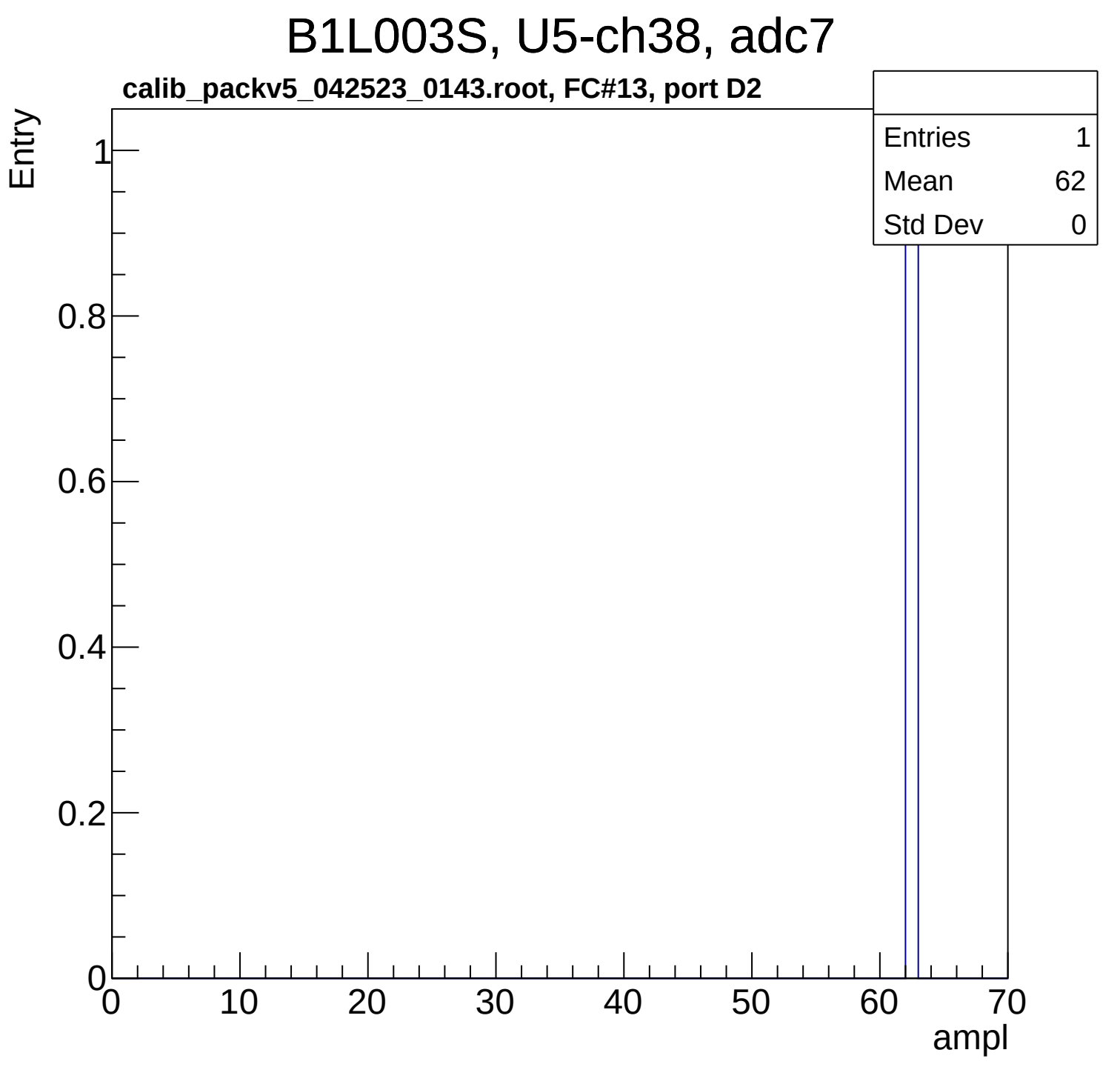
50
60
70
ampl


B1L003S, U5-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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10
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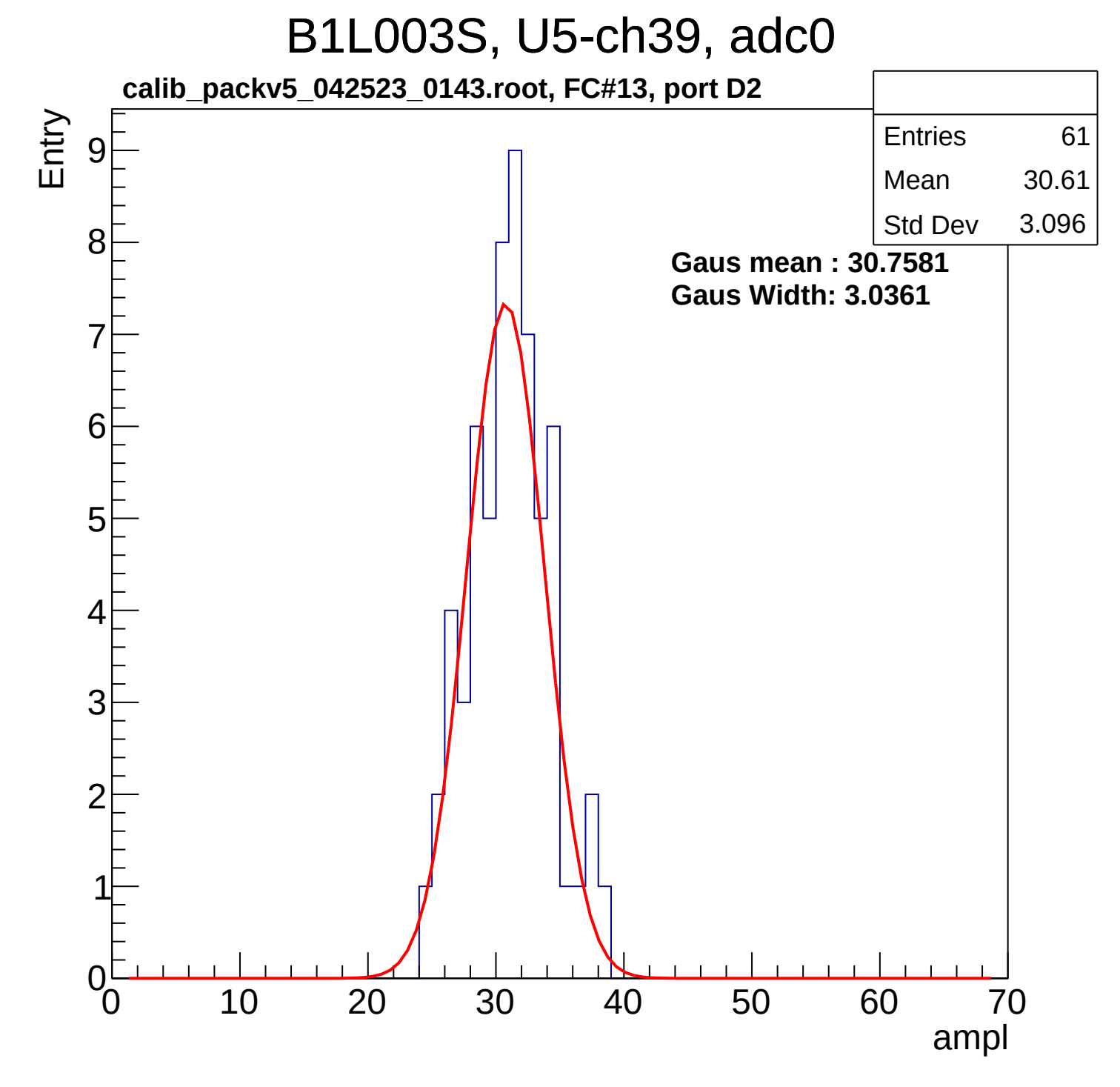
50
60
70
ampl


B1L003S, U5-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 30.61
Std Dev 3.096
Gaus mean : 30.7581
Gaus Width: 3.0361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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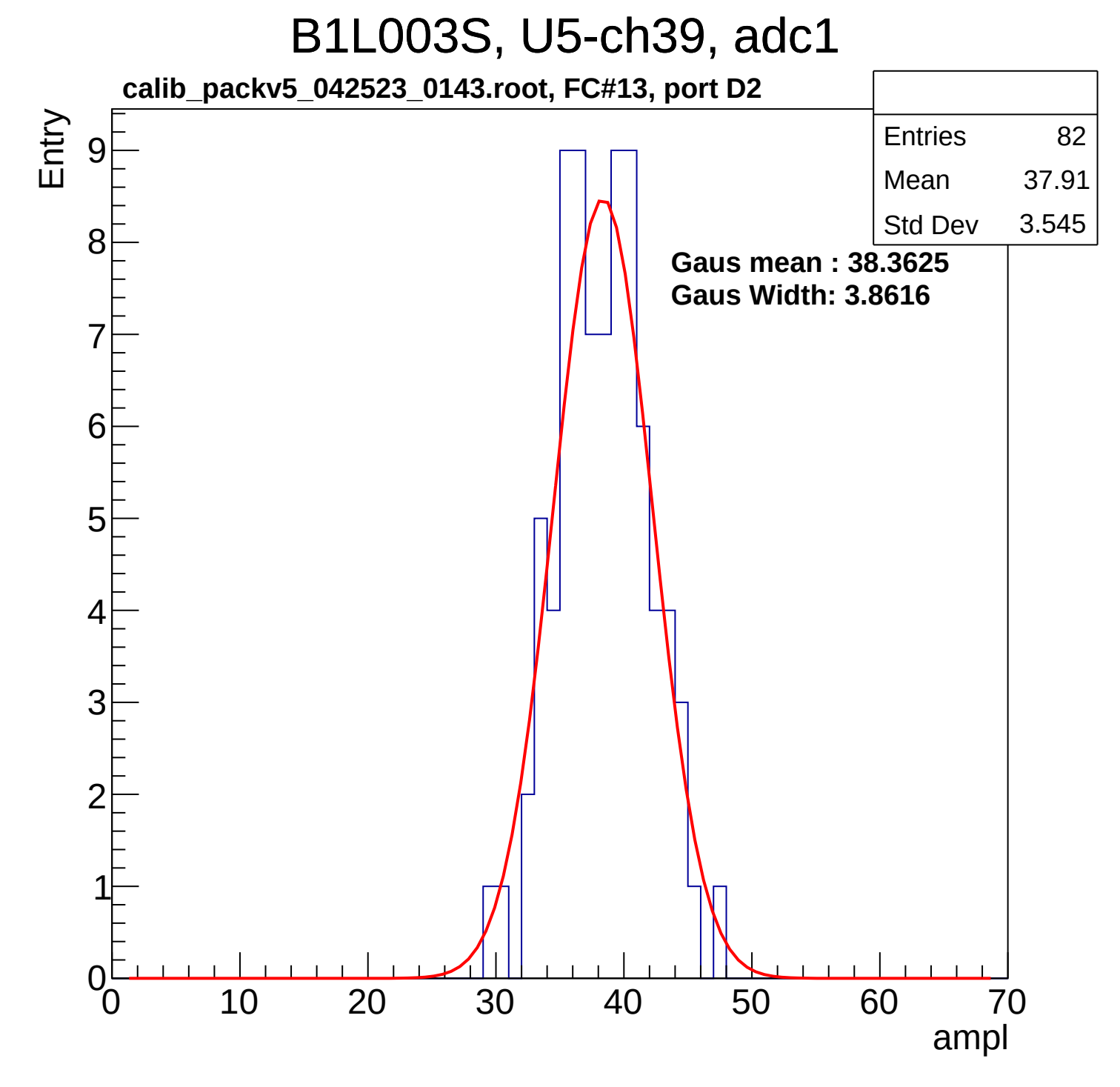
B1L003S, U5-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 37.91
Std Dev 3.545
Gaus mean : 38.3625
Gaus Width: 3.8616
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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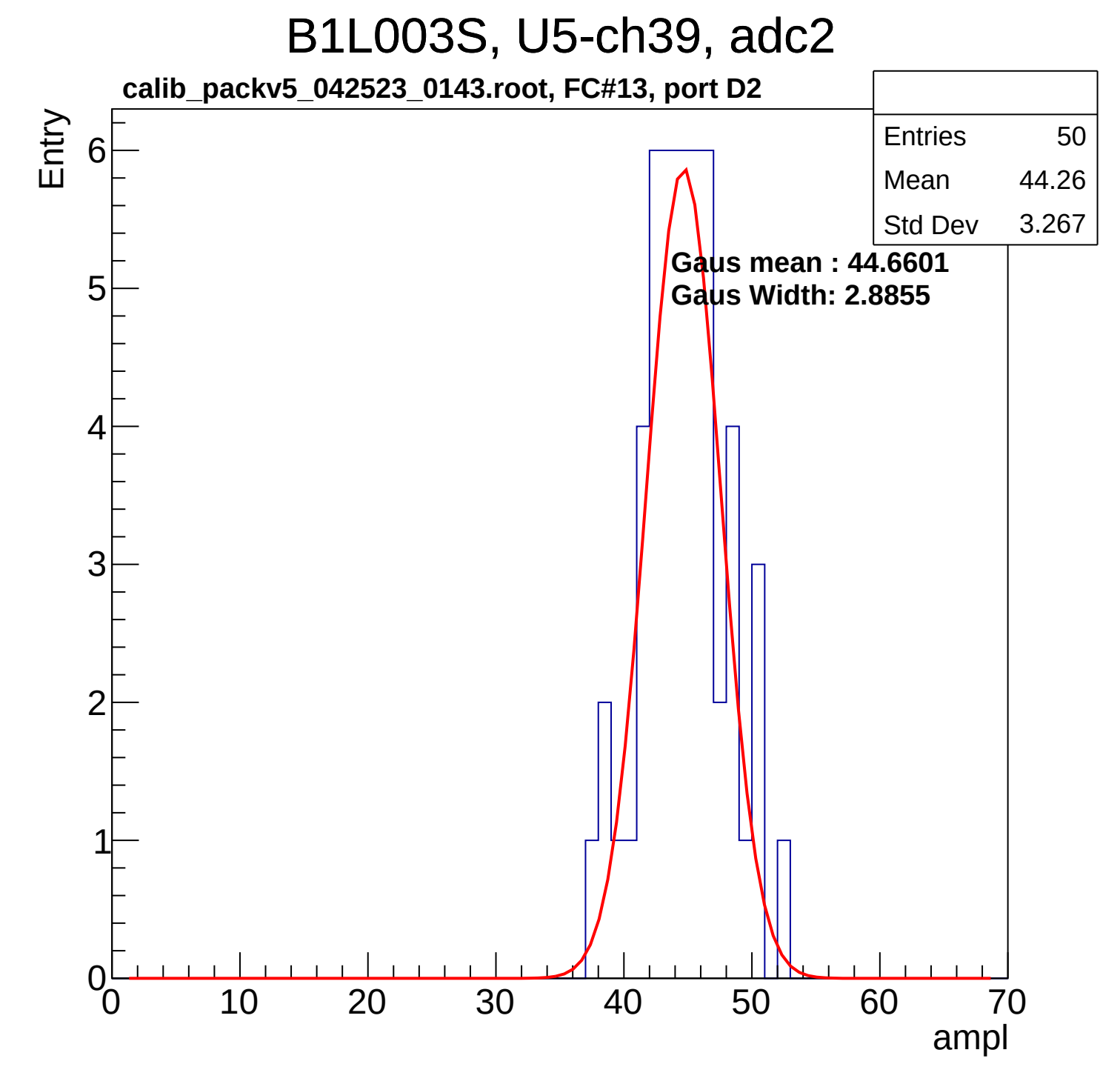
B1L003S, U5-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 44.26
Std Dev 3.267
Gaus mean : 44.6601
Gaus Width: 2.8855
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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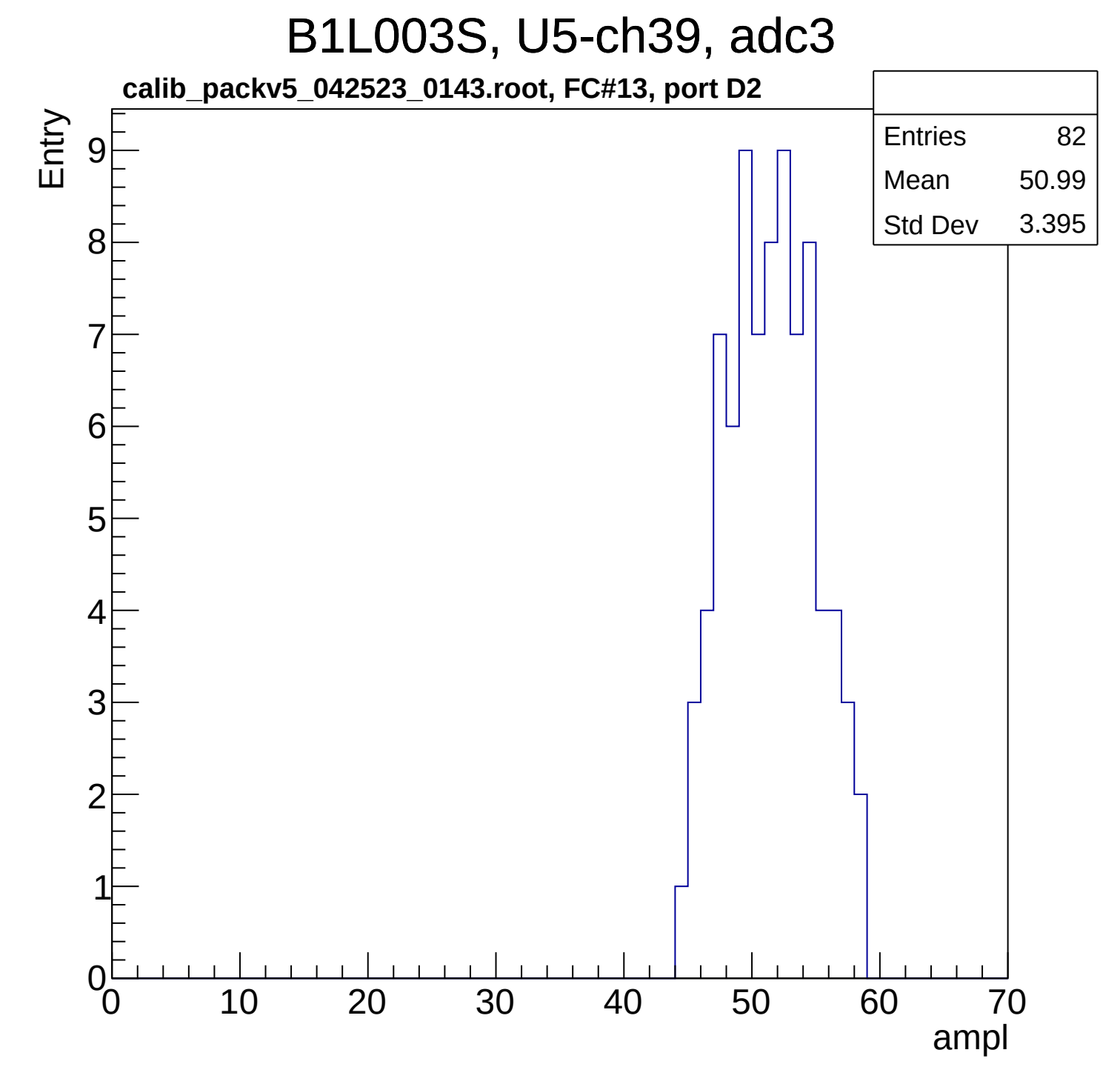
B1L003S, U5-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 50.99
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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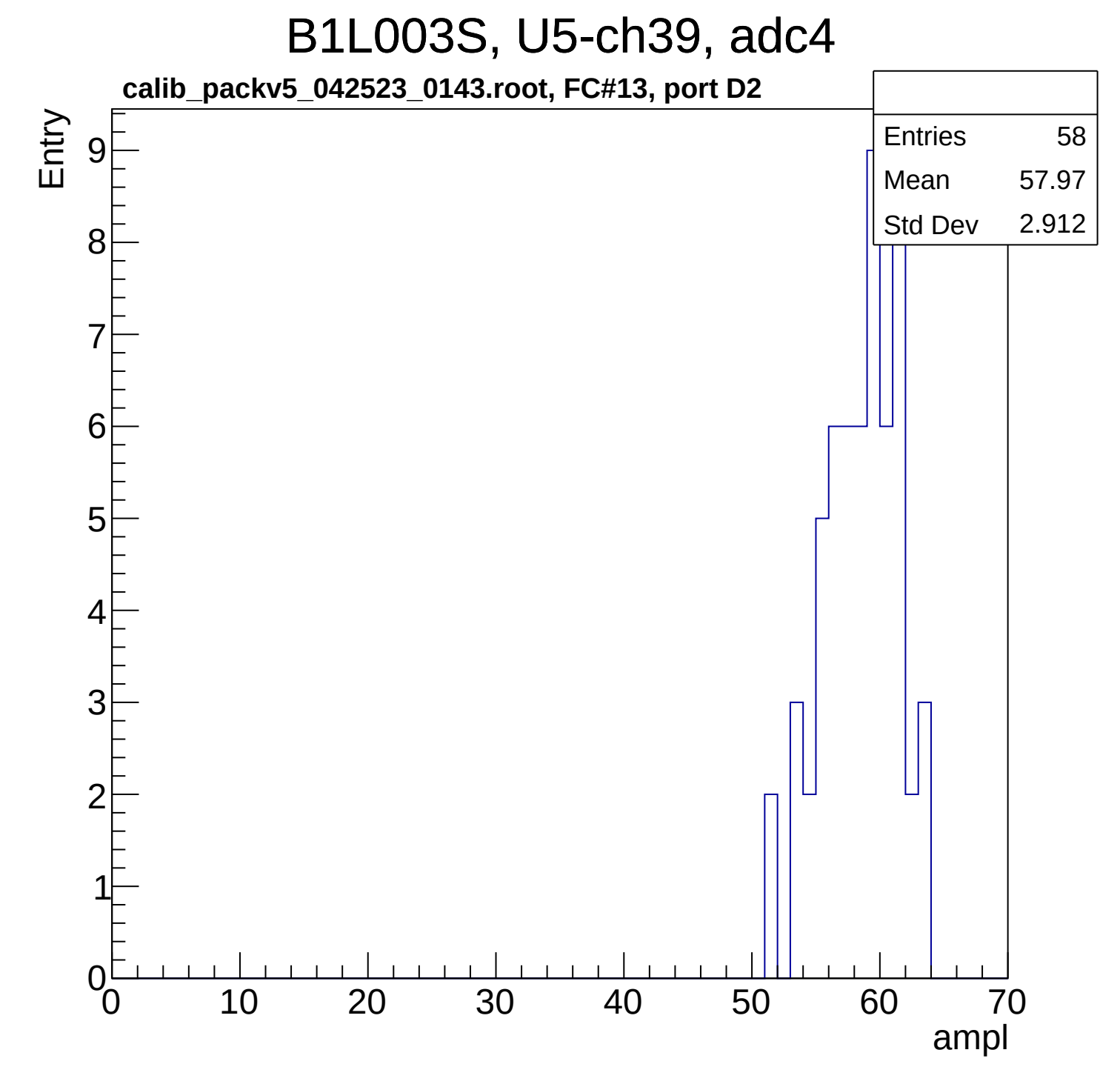
B1L003S, U5-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.97
Std Dev 2.912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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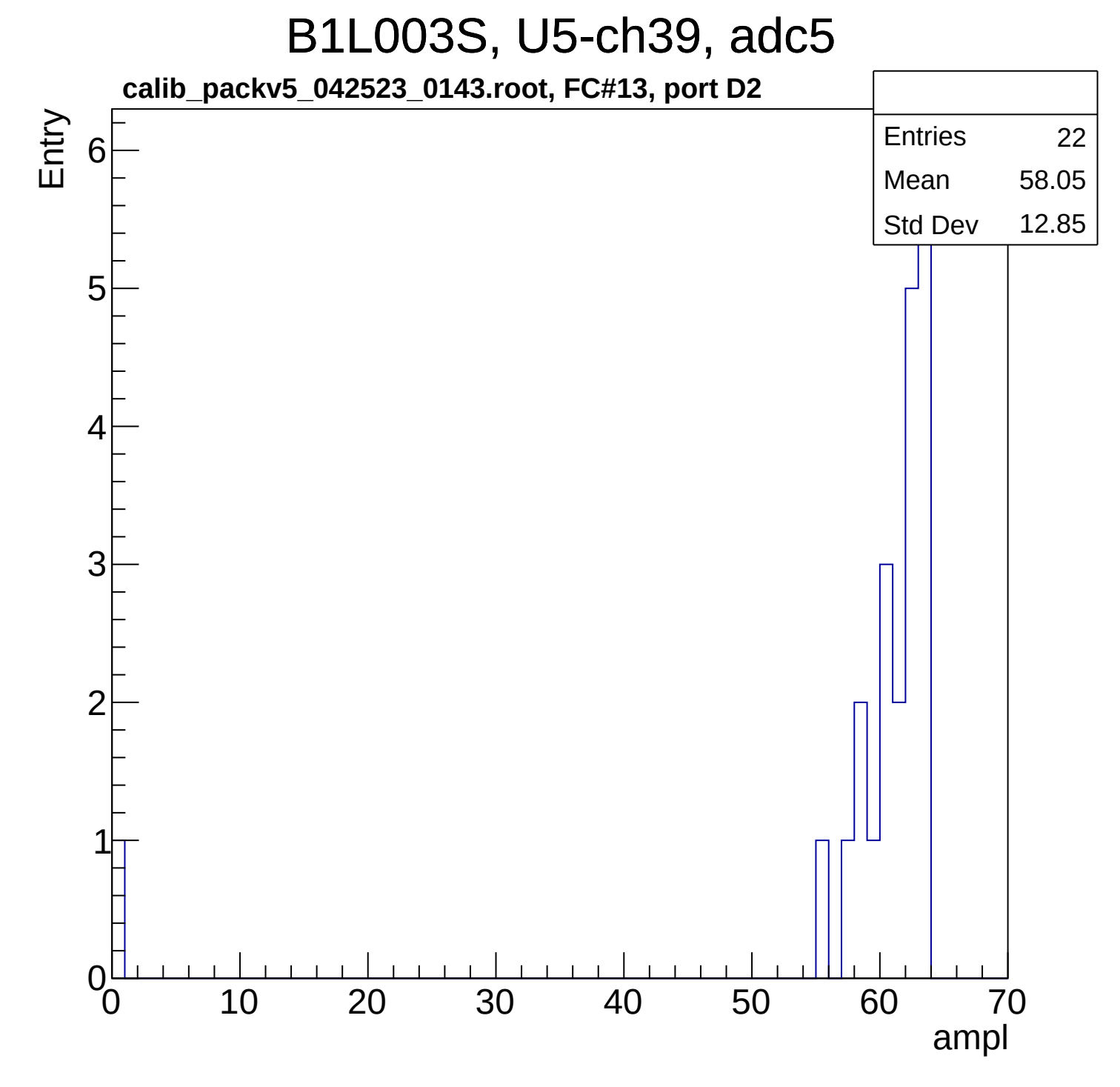
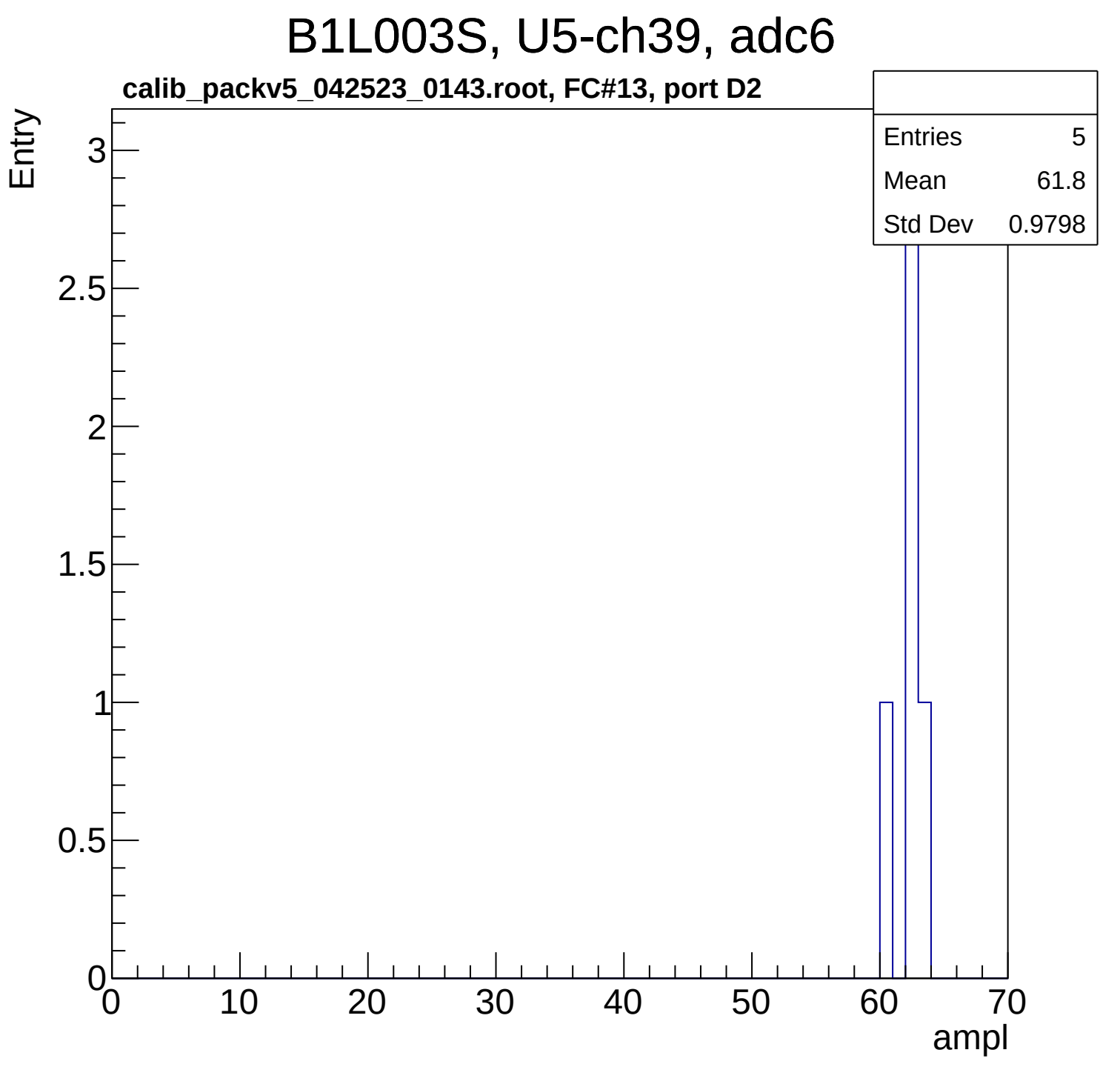
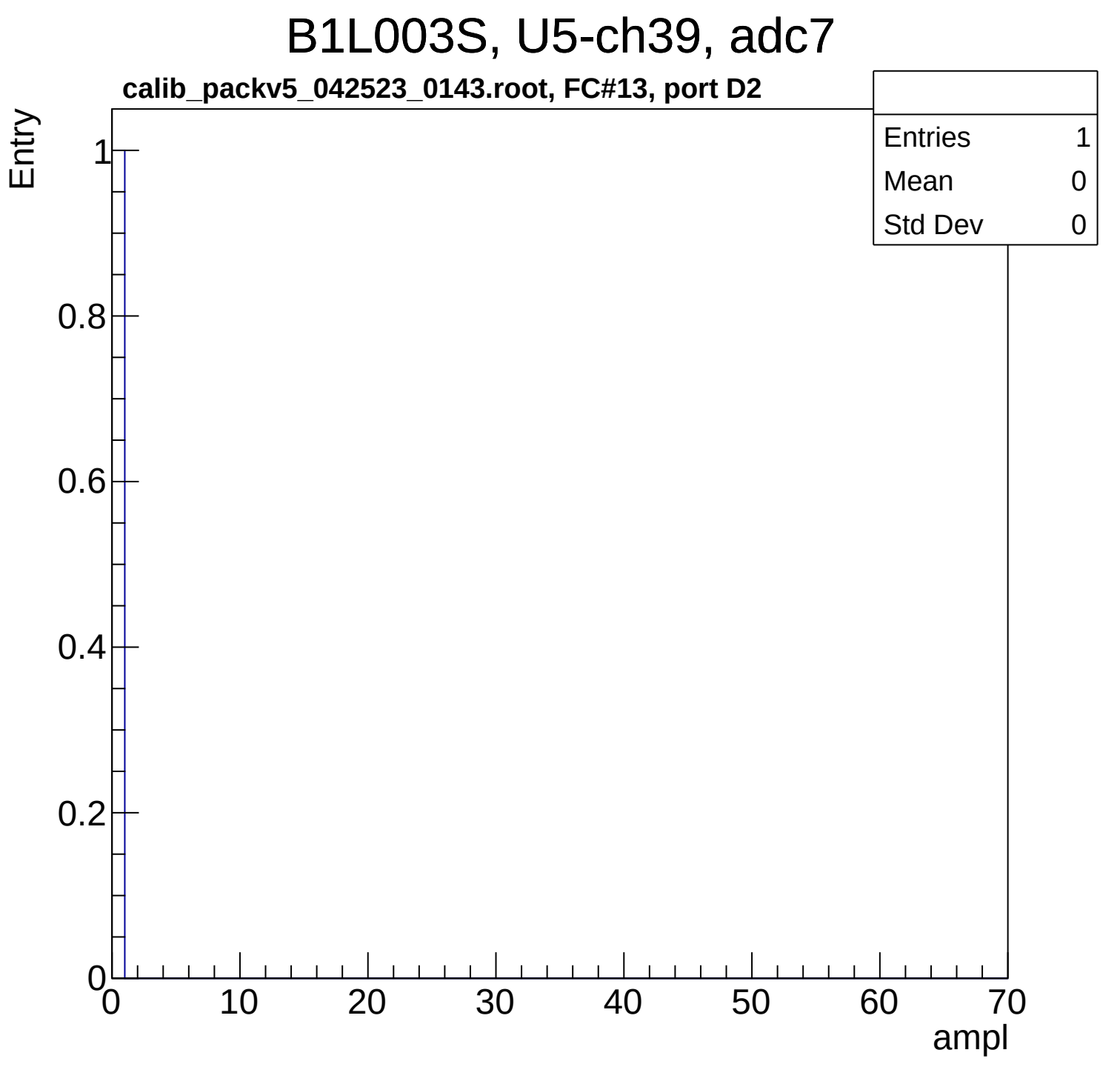

B1L003S, U5-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.05
Std Dev 12.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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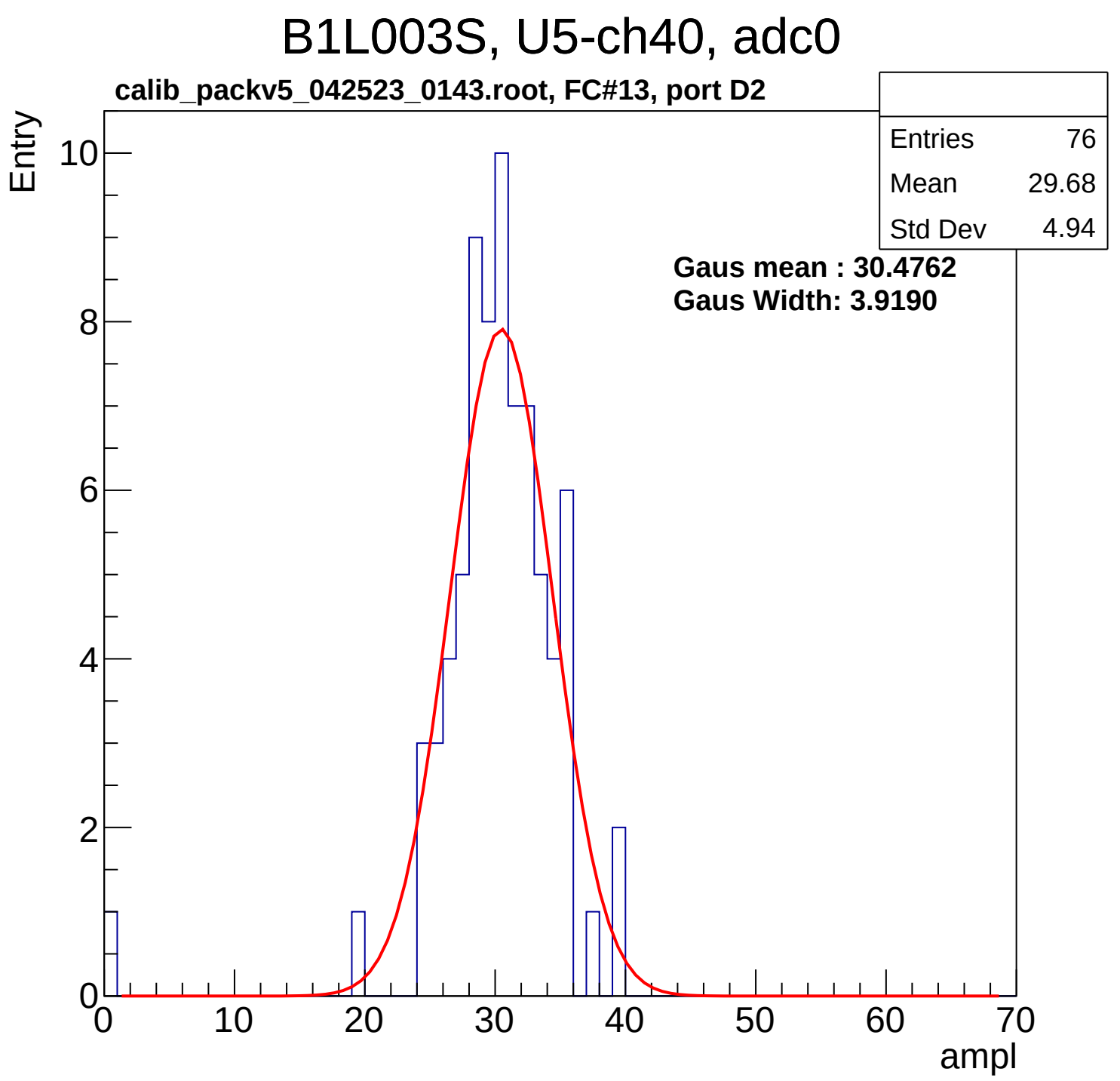
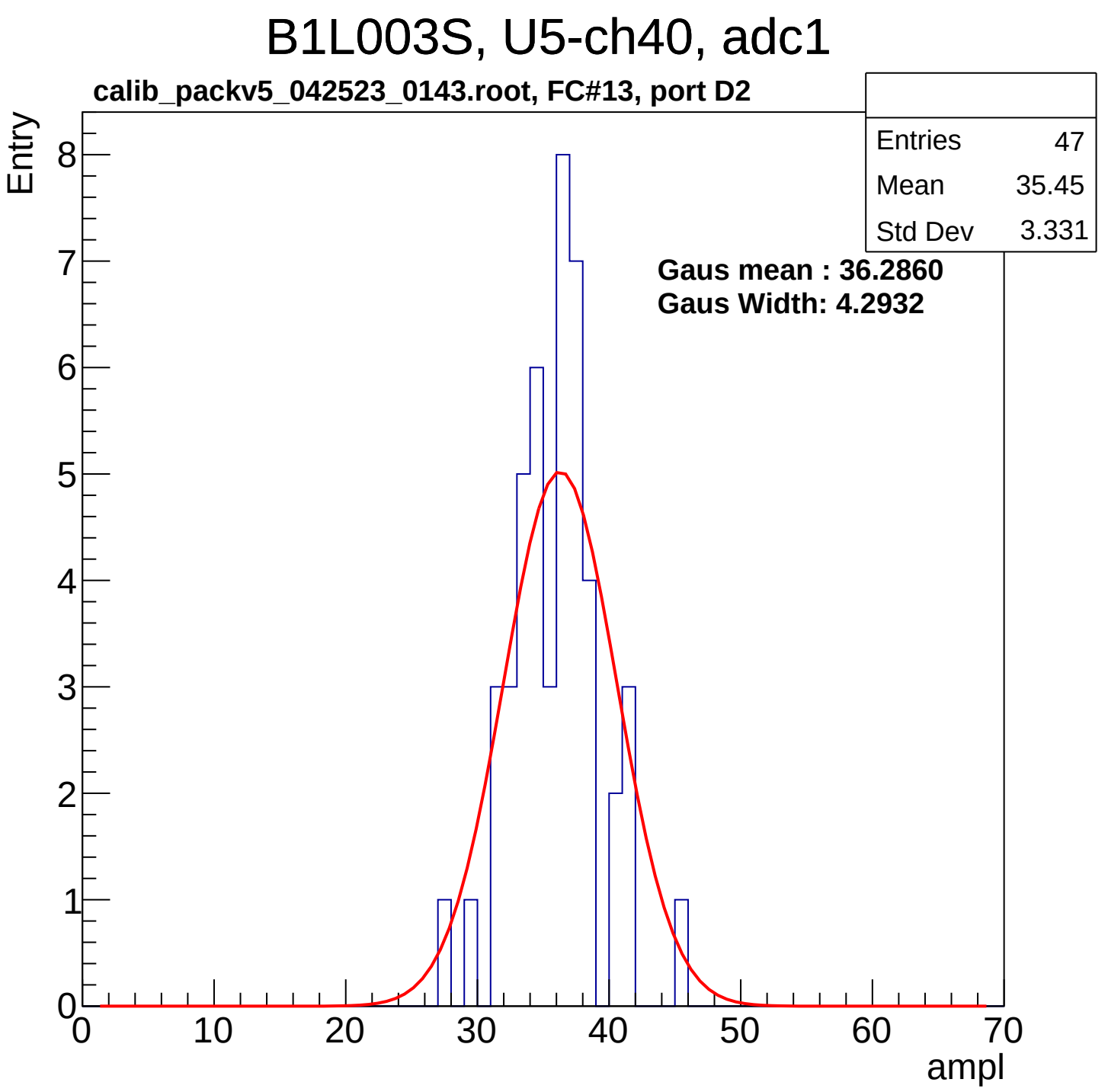
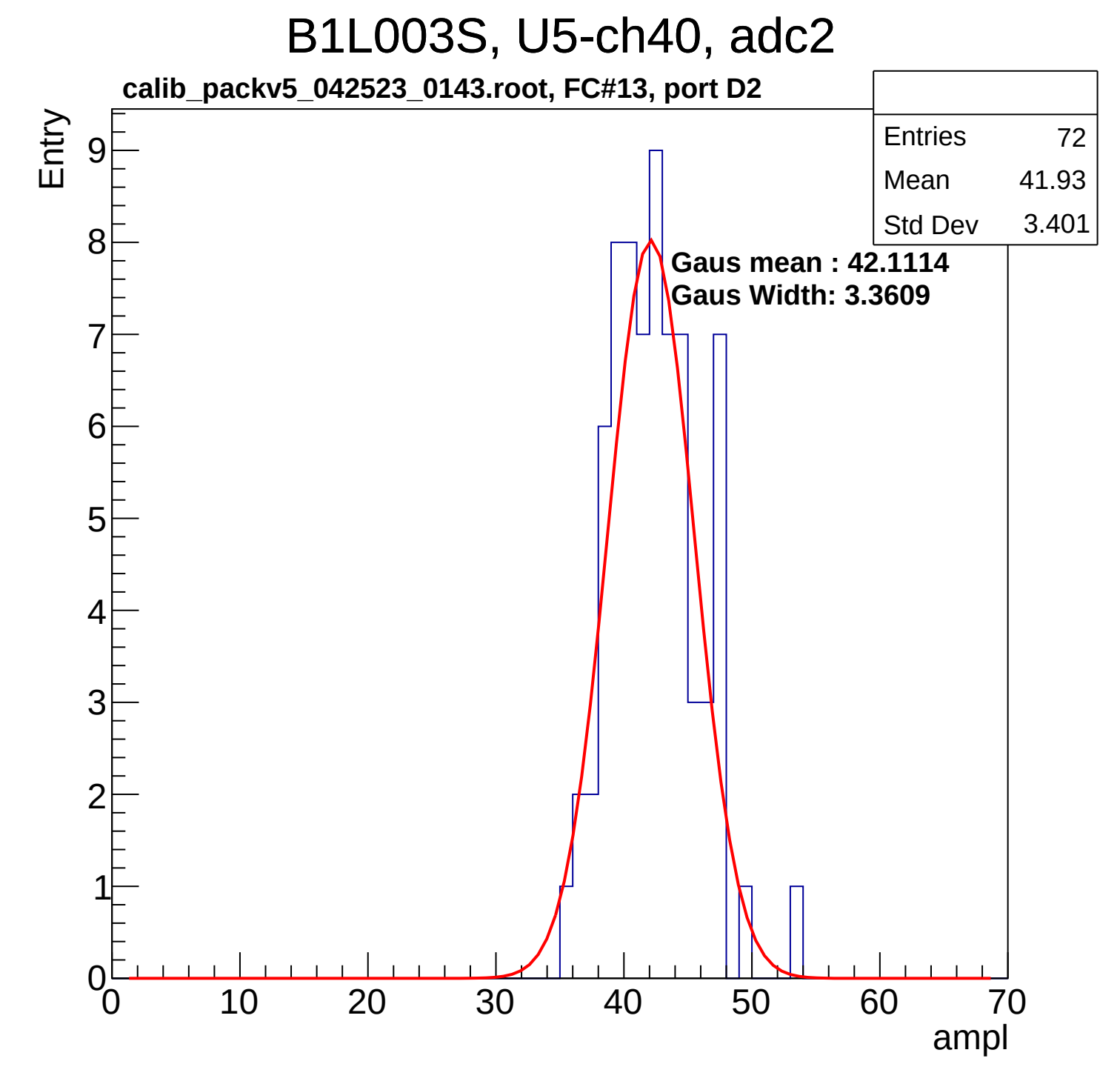
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 29.68
Std Dev 4.94
Gaus mean : 30.4762
Gaus Width: 3.9190
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 35.45
Std Dev 3.331
Gaus mean : 36.2860
Gaus Width: 4.2932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 41.93
Std Dev 3.401
Gaus mean : 42.1114
Gaus Width: 3.3609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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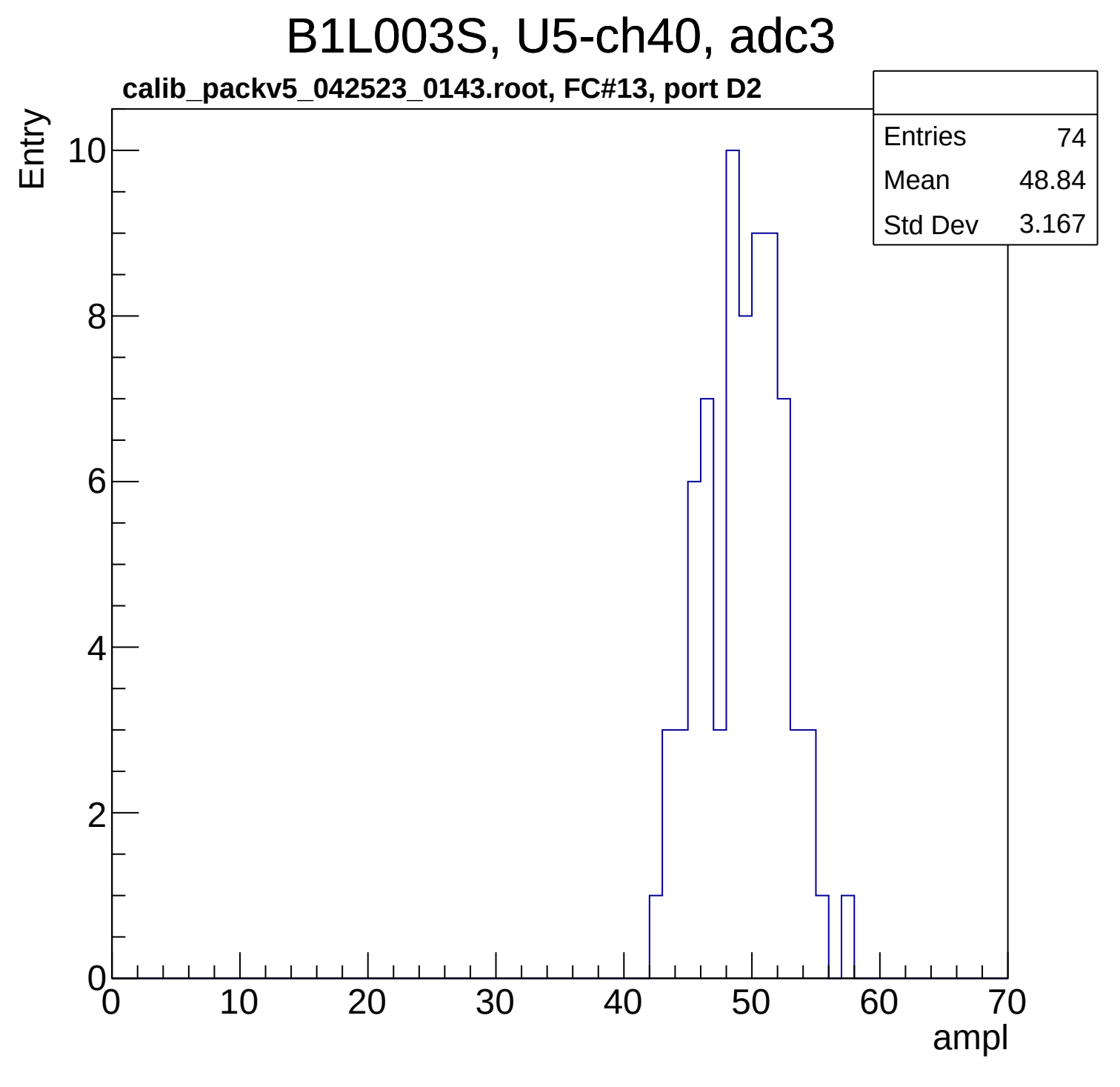
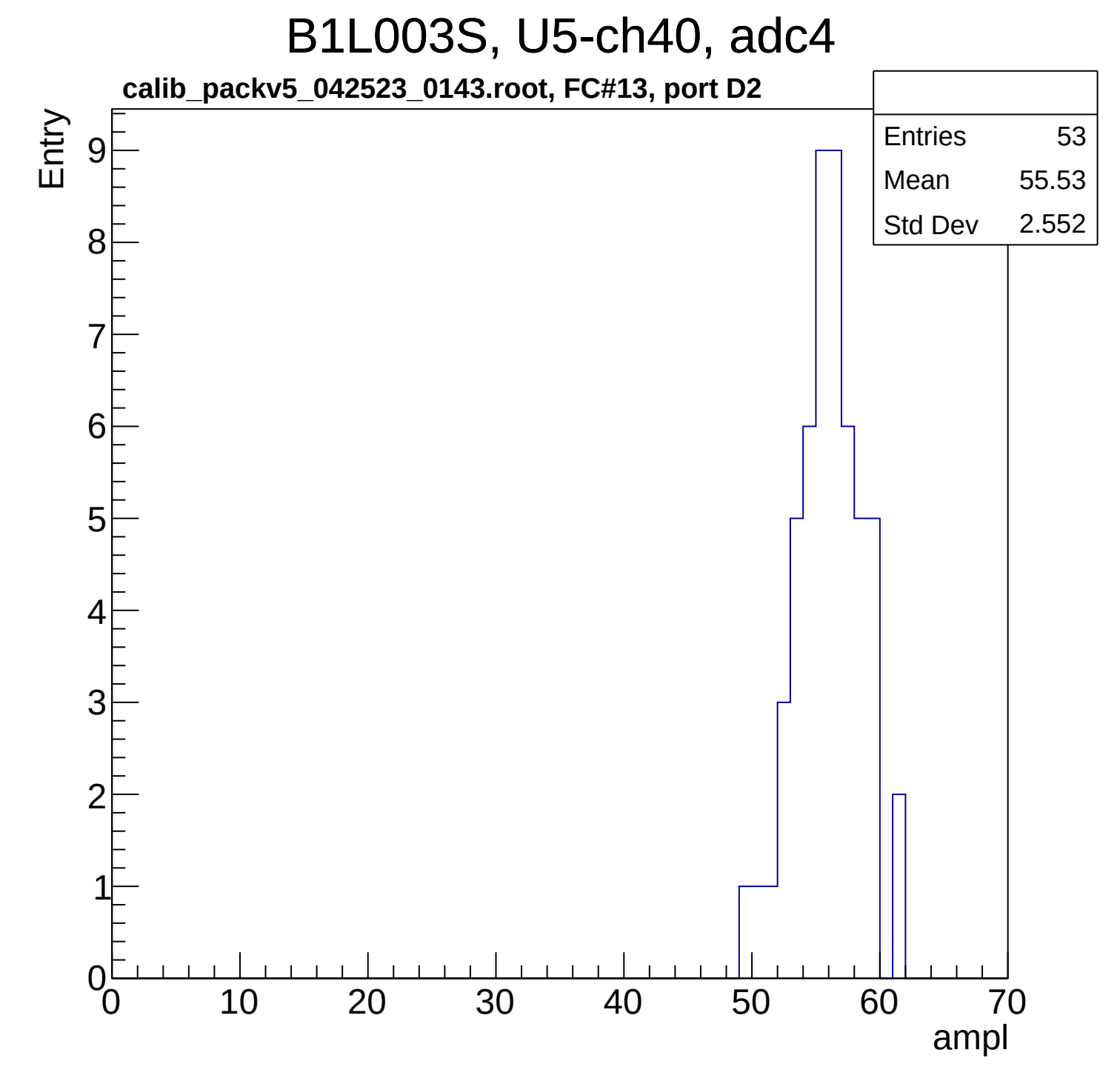
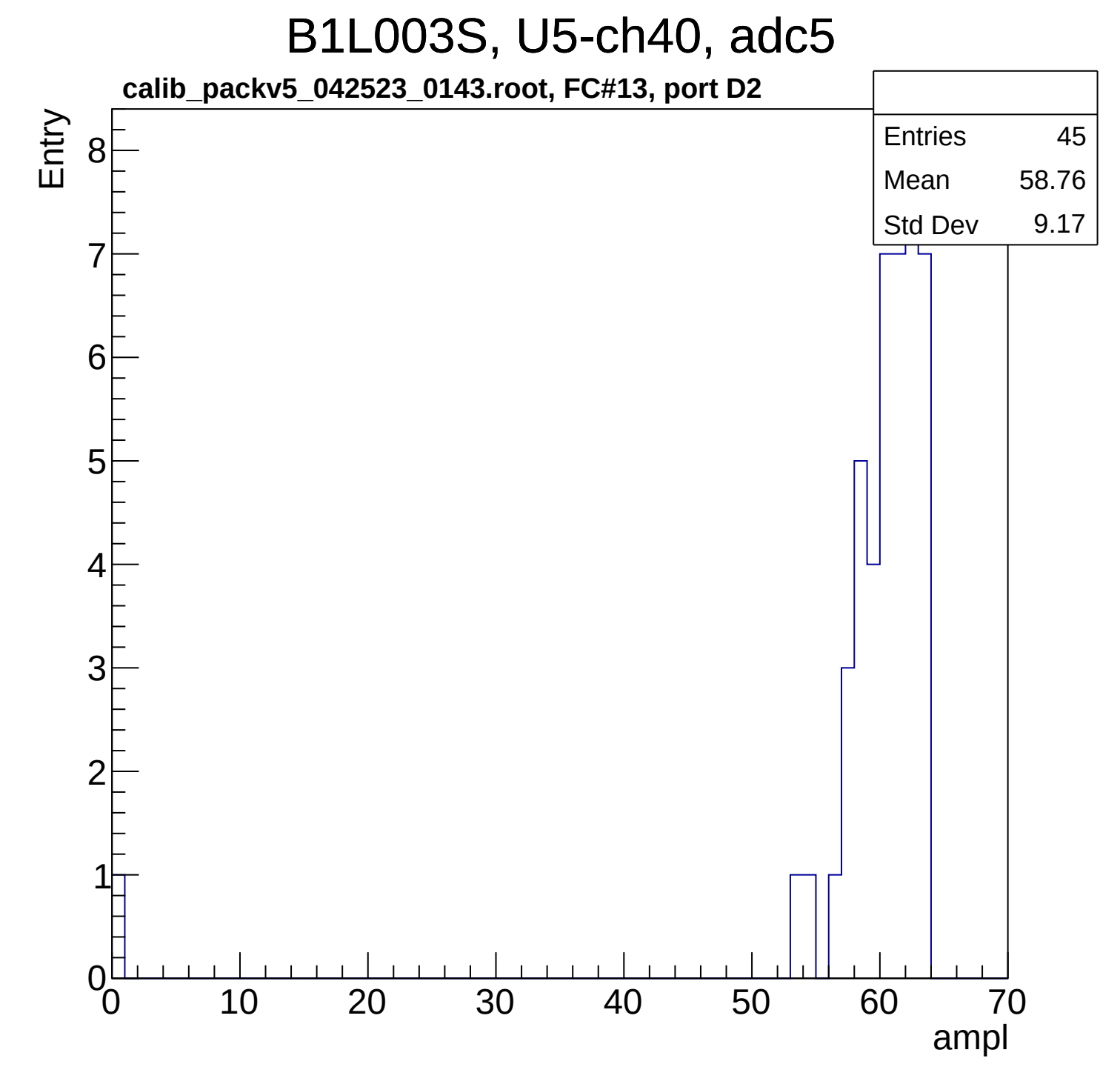

B1L003S, U5-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.84
Std Dev 3.167
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.53
Std Dev 2.552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.76
Std Dev 9.17
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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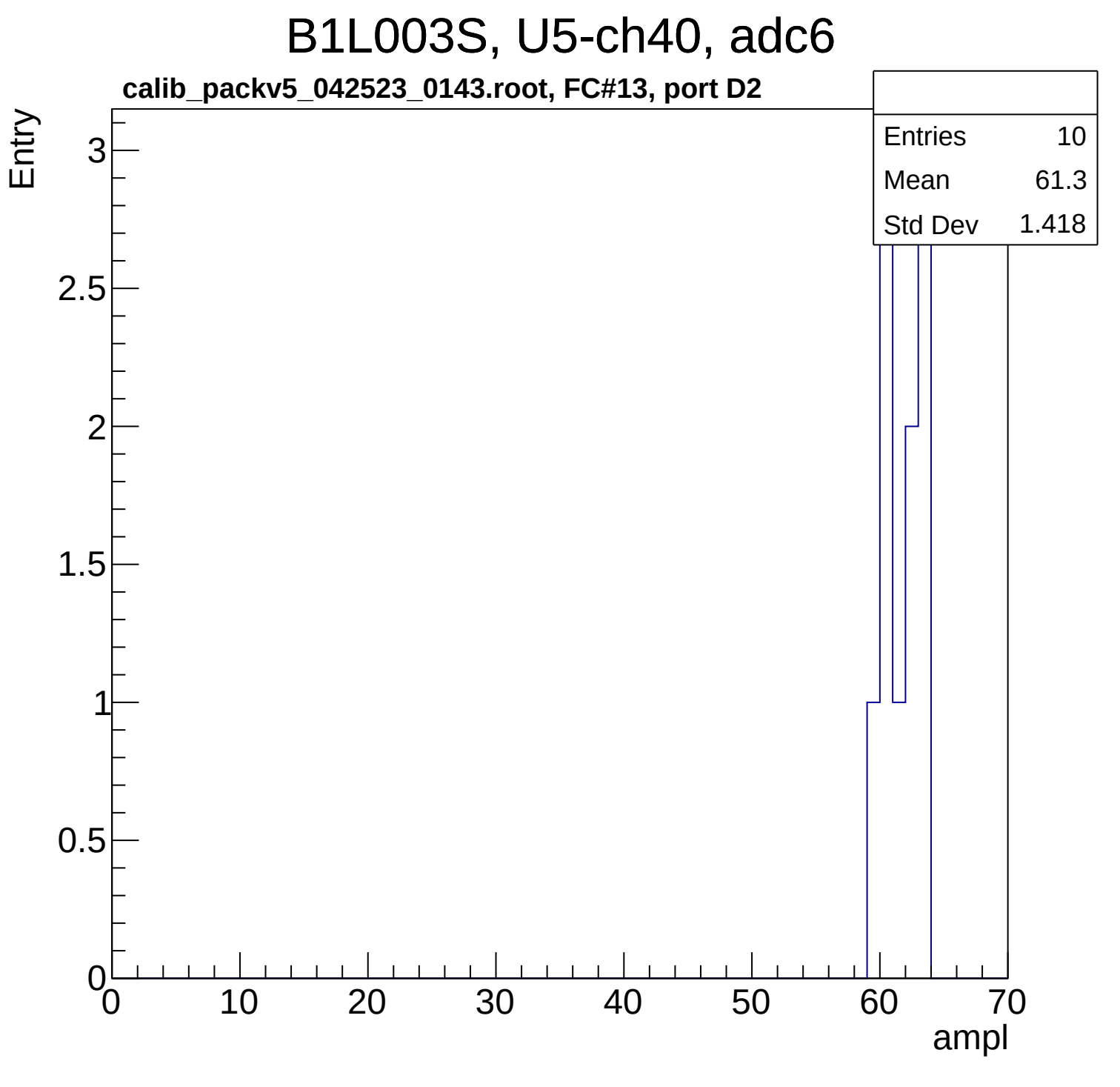
B1L003S, U5-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.3
Std Dev 1.418
Entry
ampl
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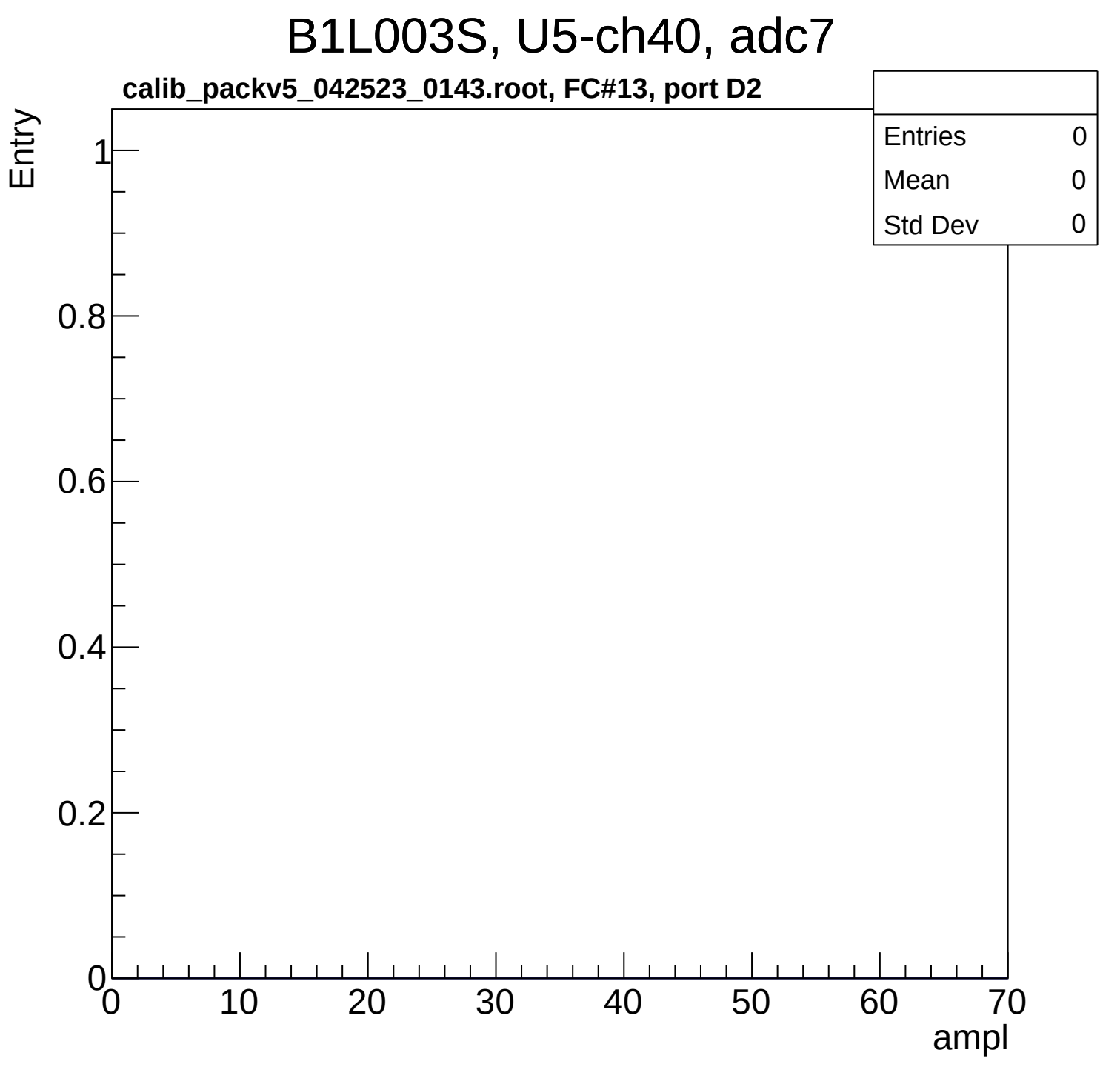
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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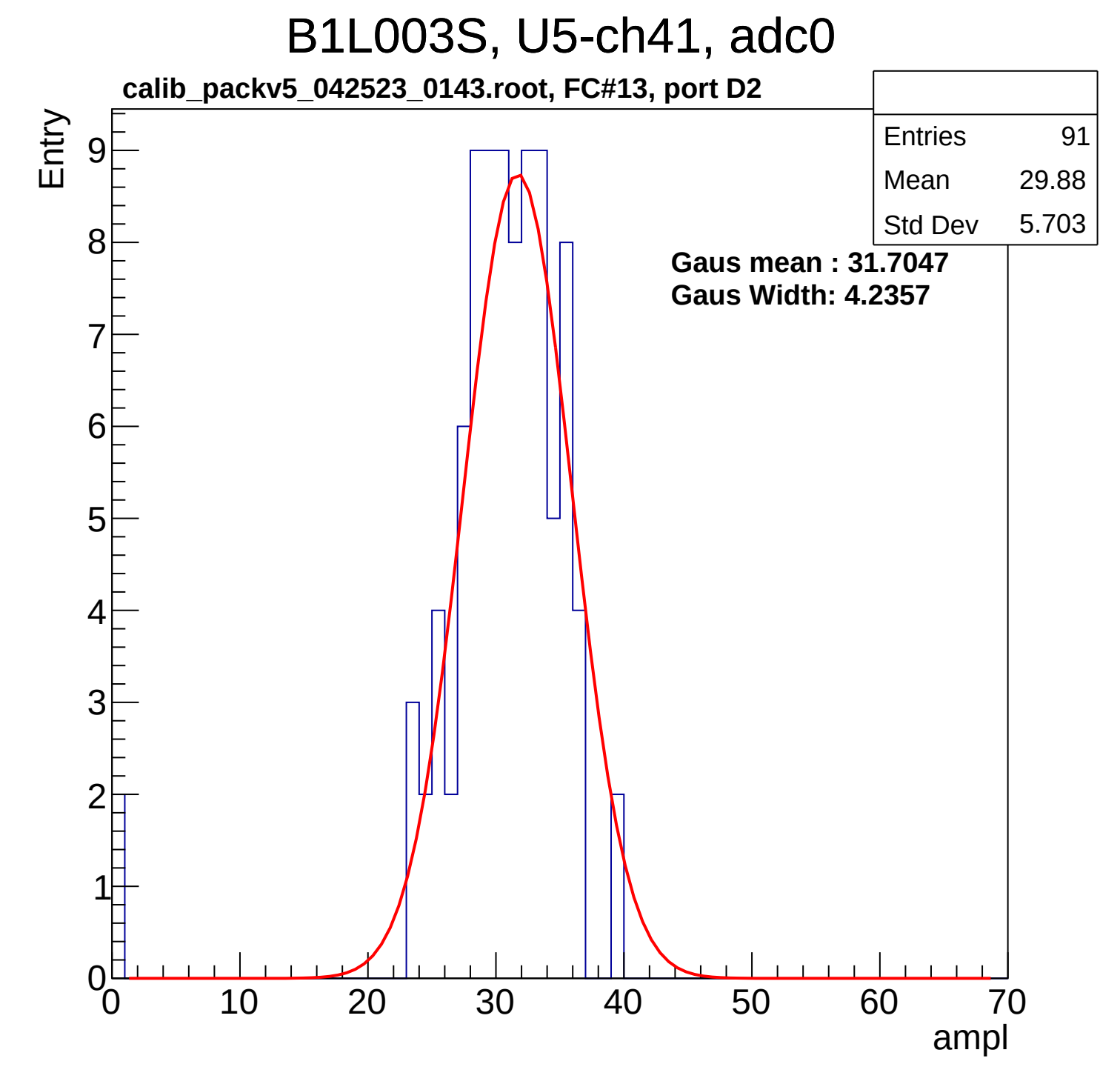
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 29.88
Std Dev 5.703
Gaus mean : 31.7047
Gaus Width: 4.2357
Entry
ampl
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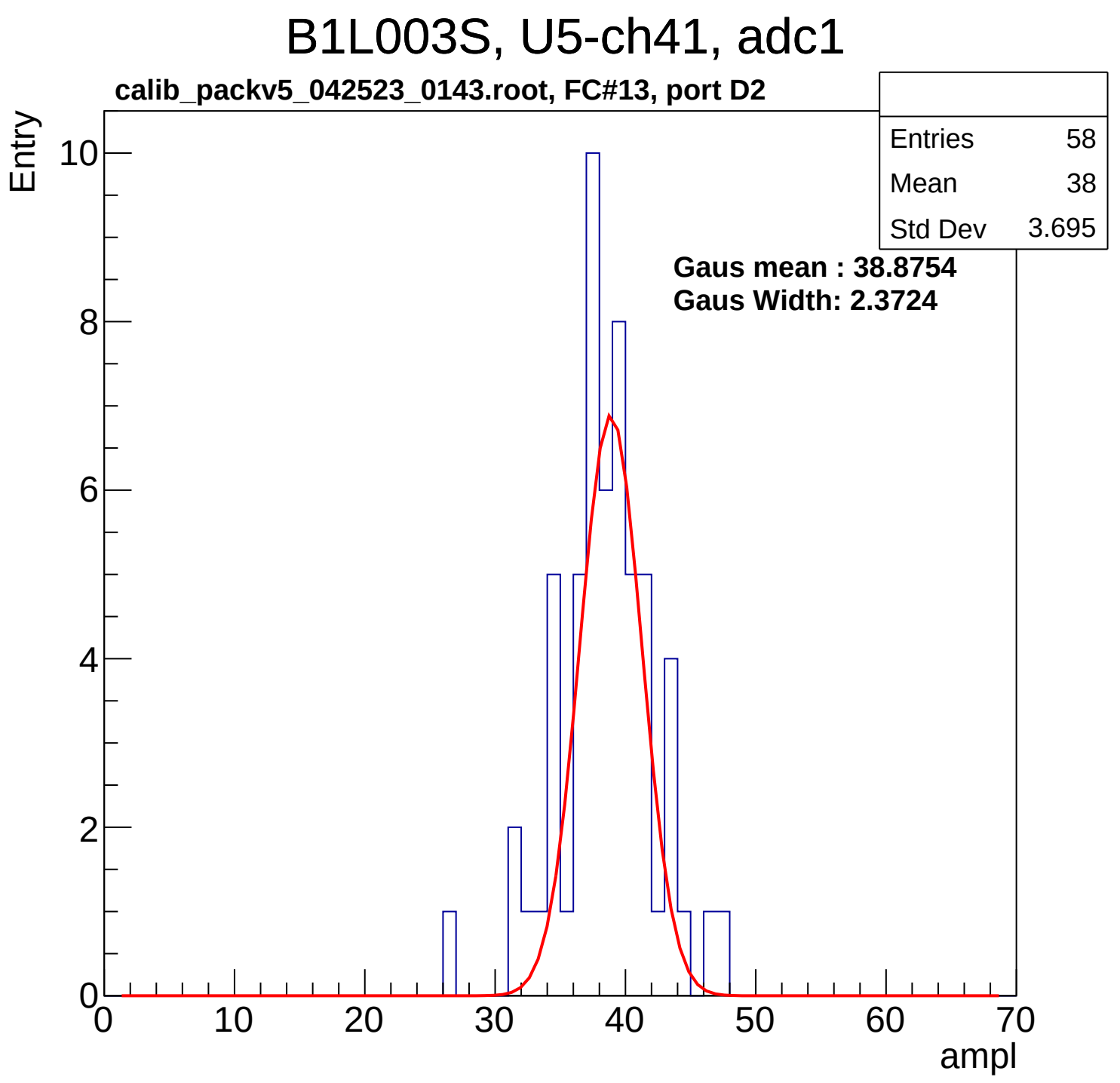
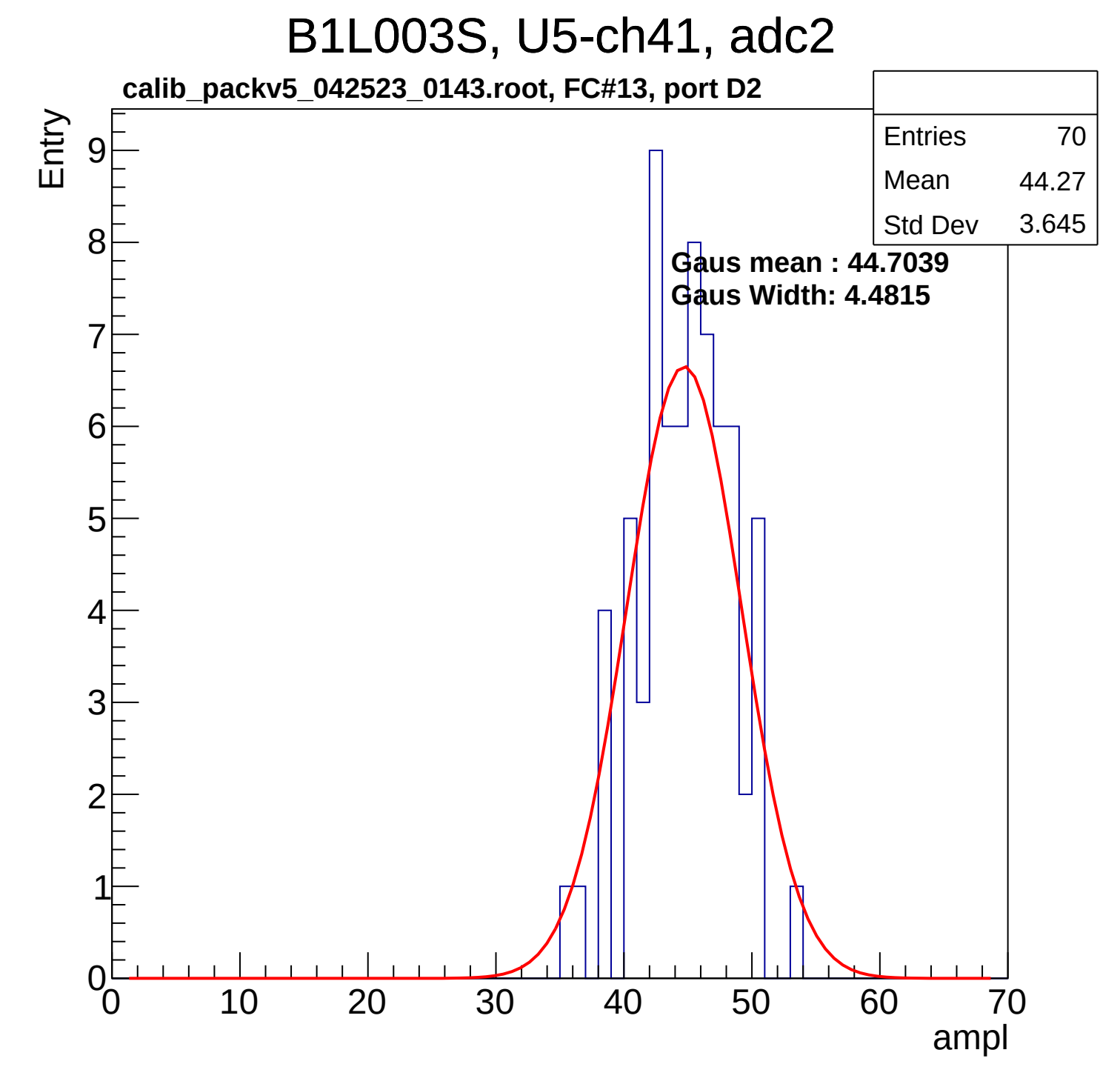
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 38
Std Dev 3.695
Gaus mean : 38.8754
Gaus Width: 2.3724
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 44.27
Std Dev 3.645
Gaus mean : 44.7039
Gaus Width: 4.4815
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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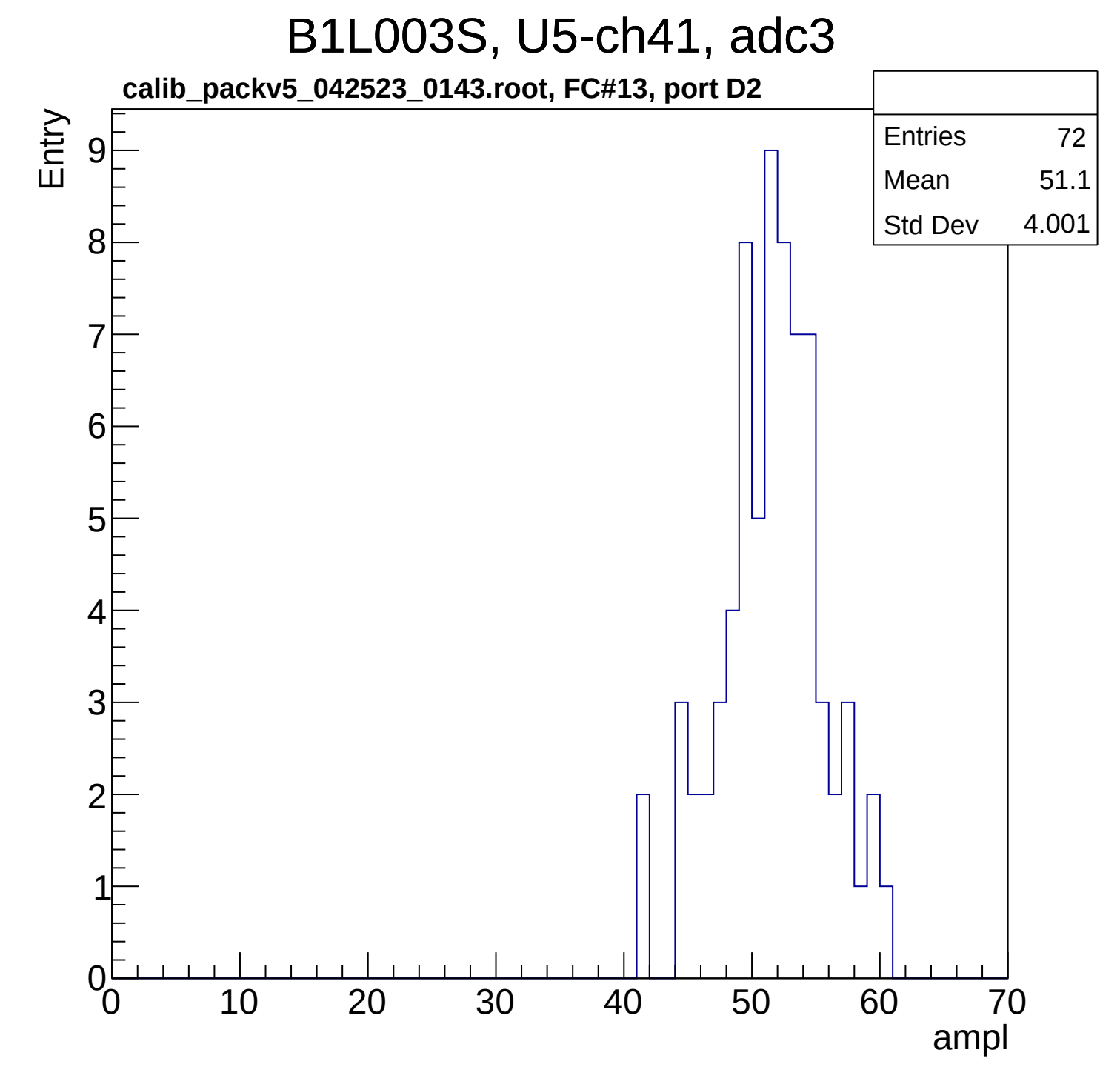
40
50
60
70


B1L003S, U5-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 51.1
Std Dev 4.001
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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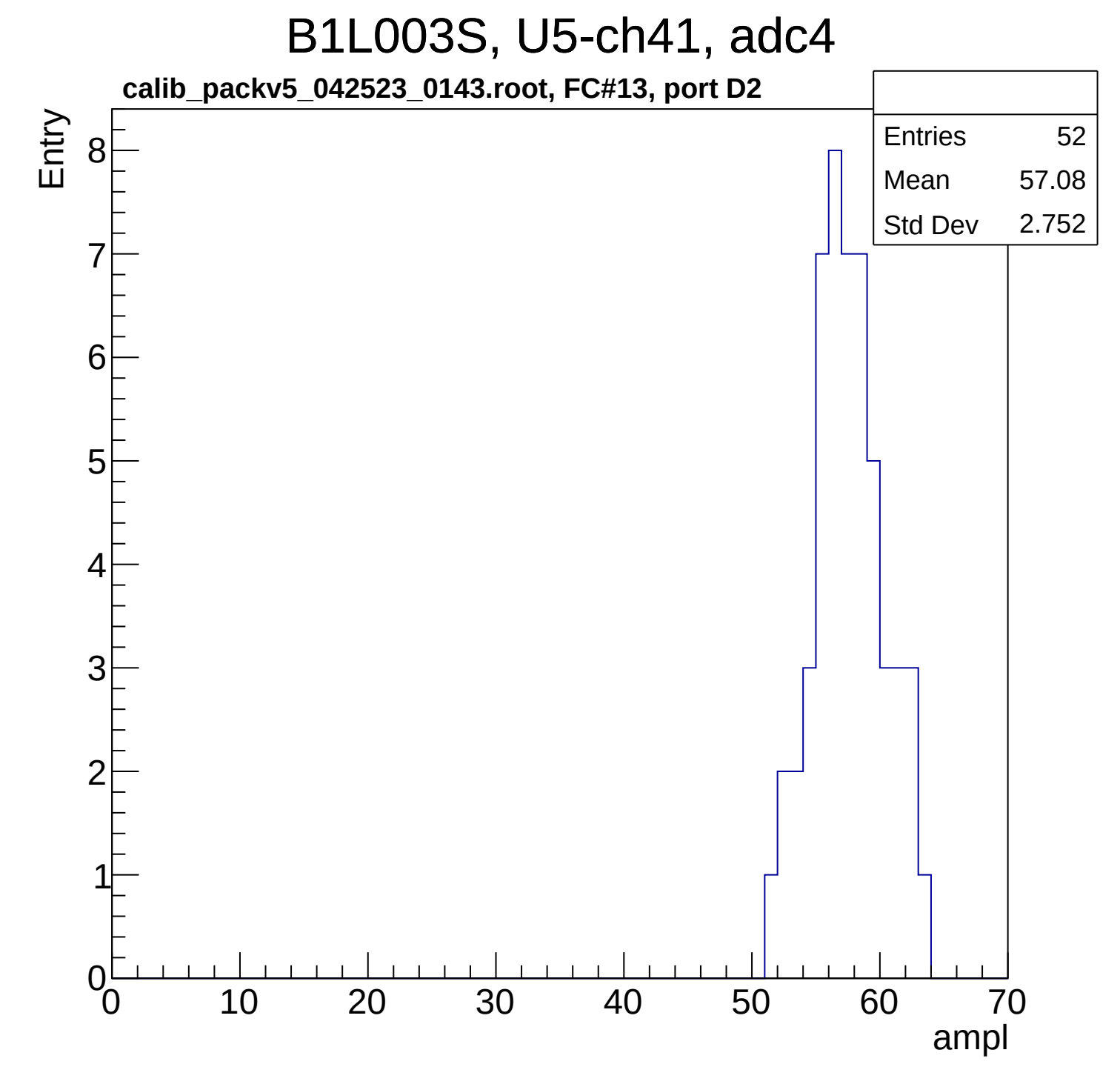
B1L003S, U5-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.08
Std Dev 2.752
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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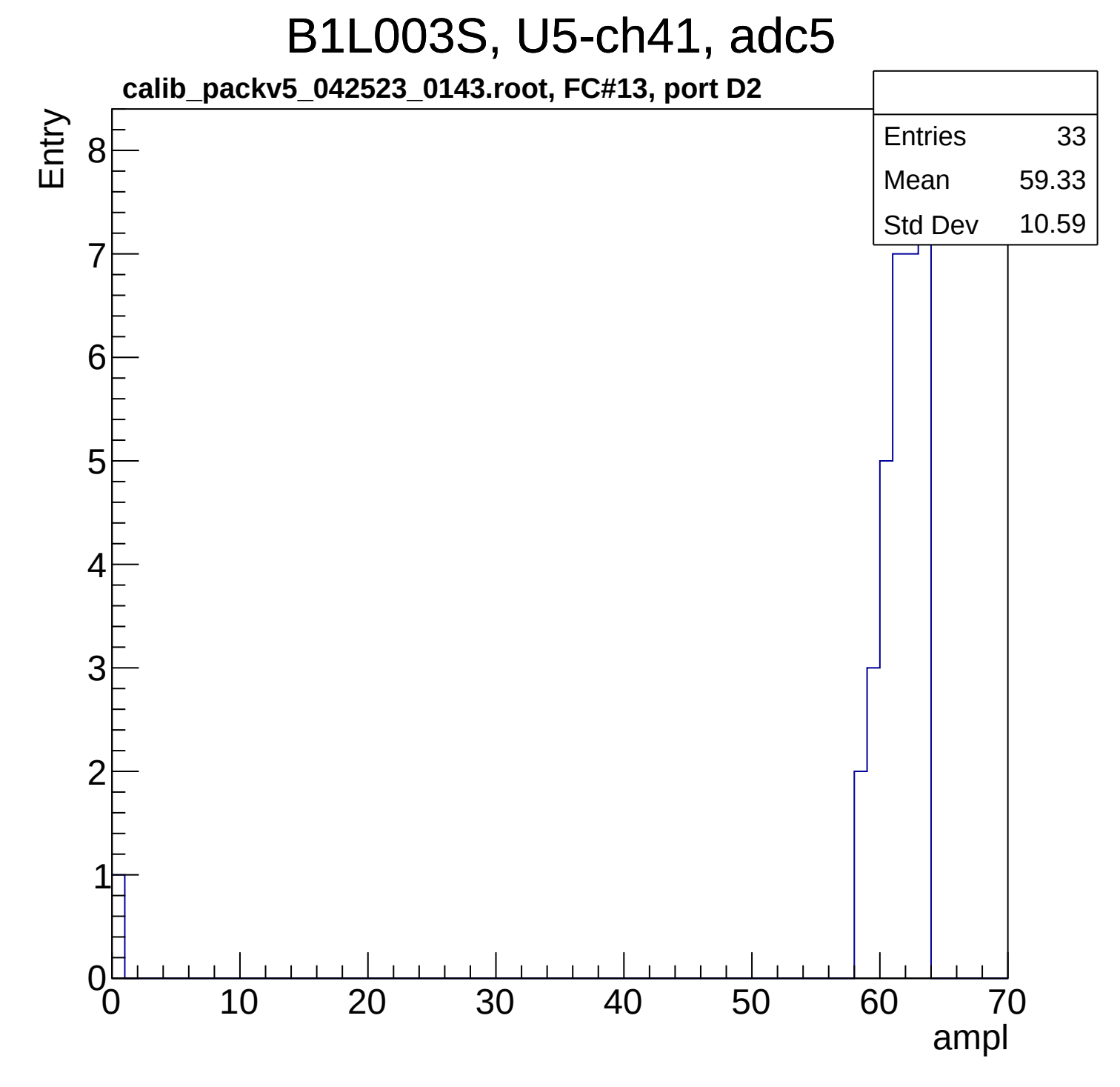
B1L003S, U5-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 59.33
Std Dev 10.59
Entry
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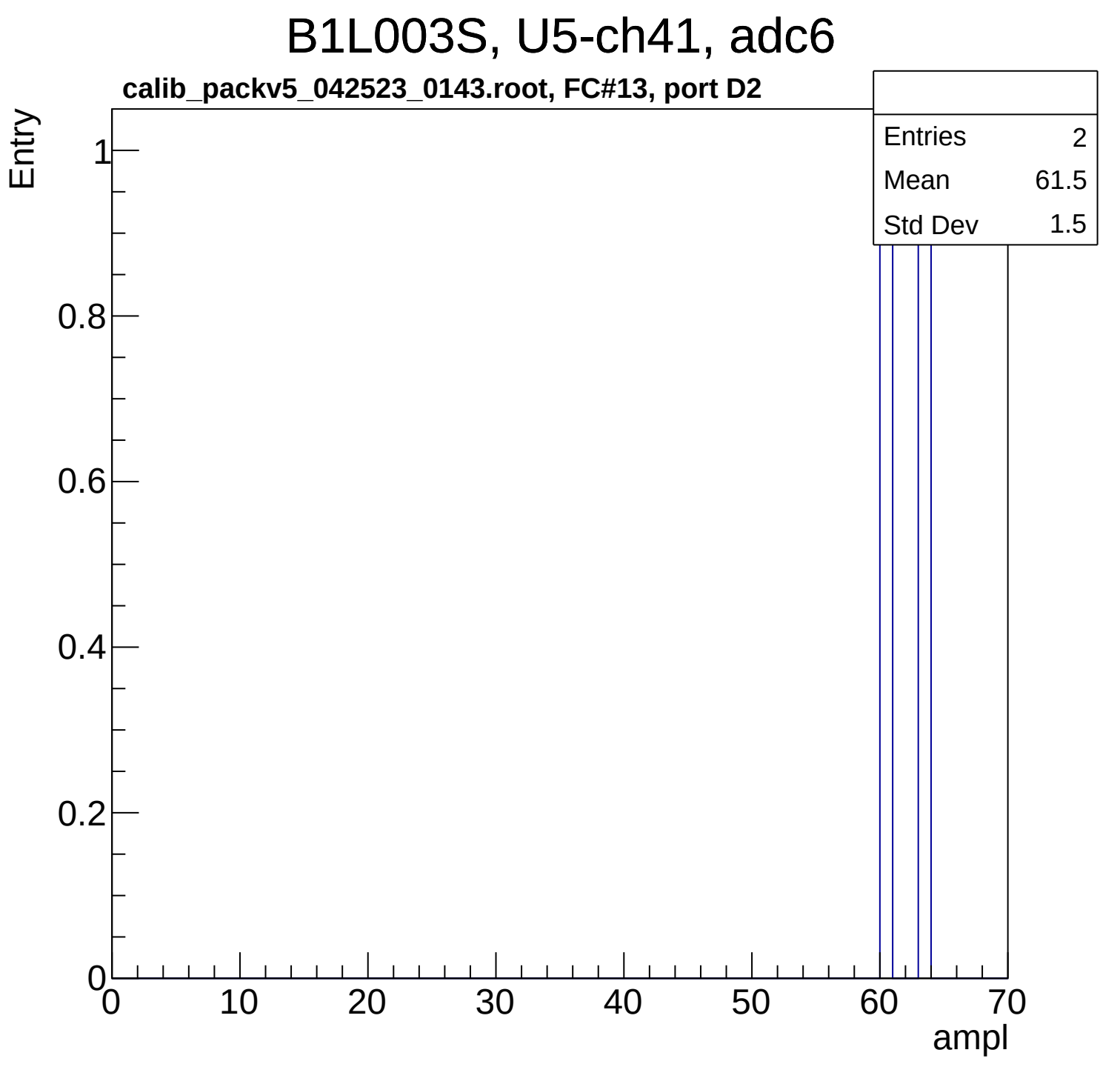
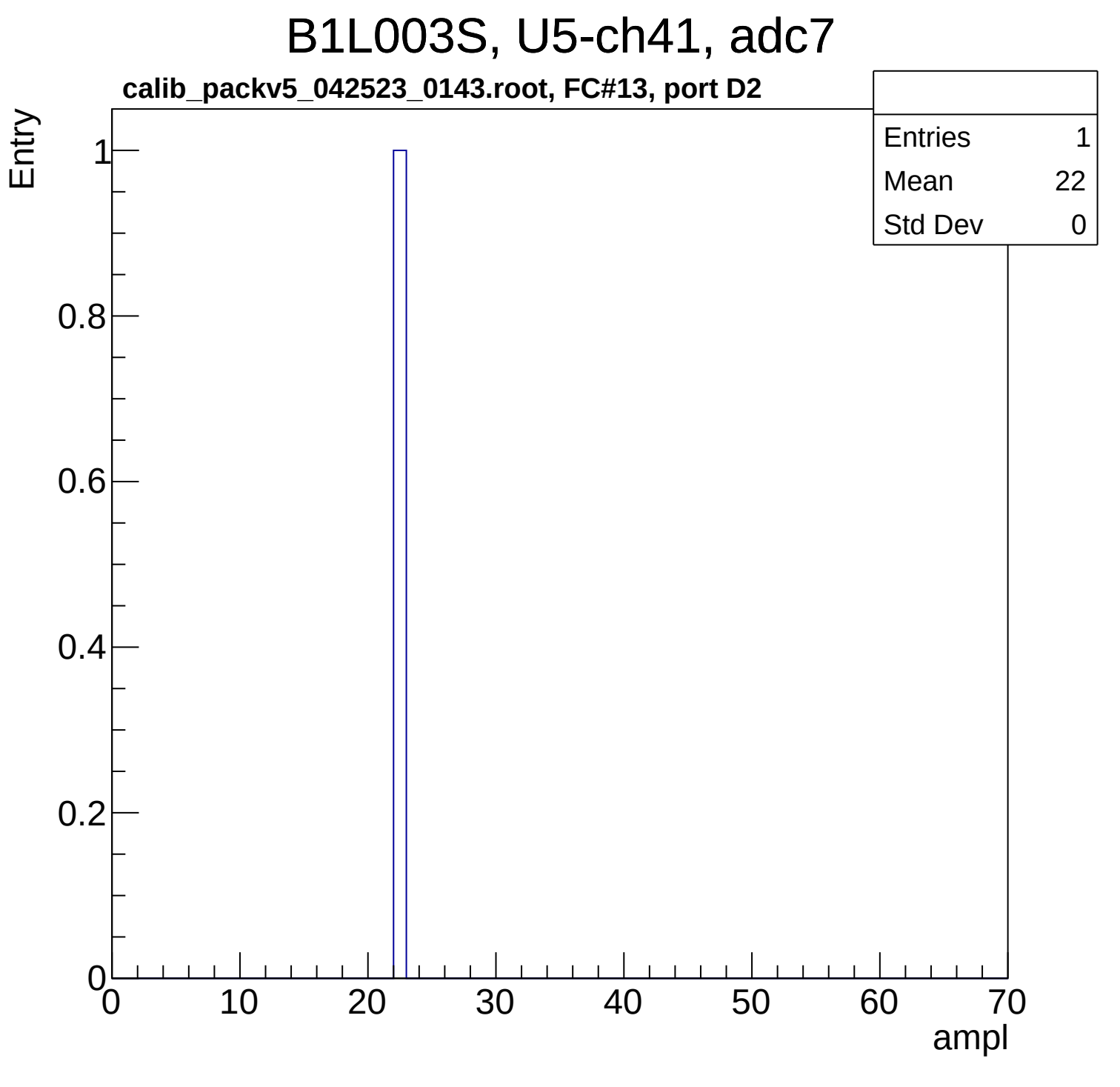
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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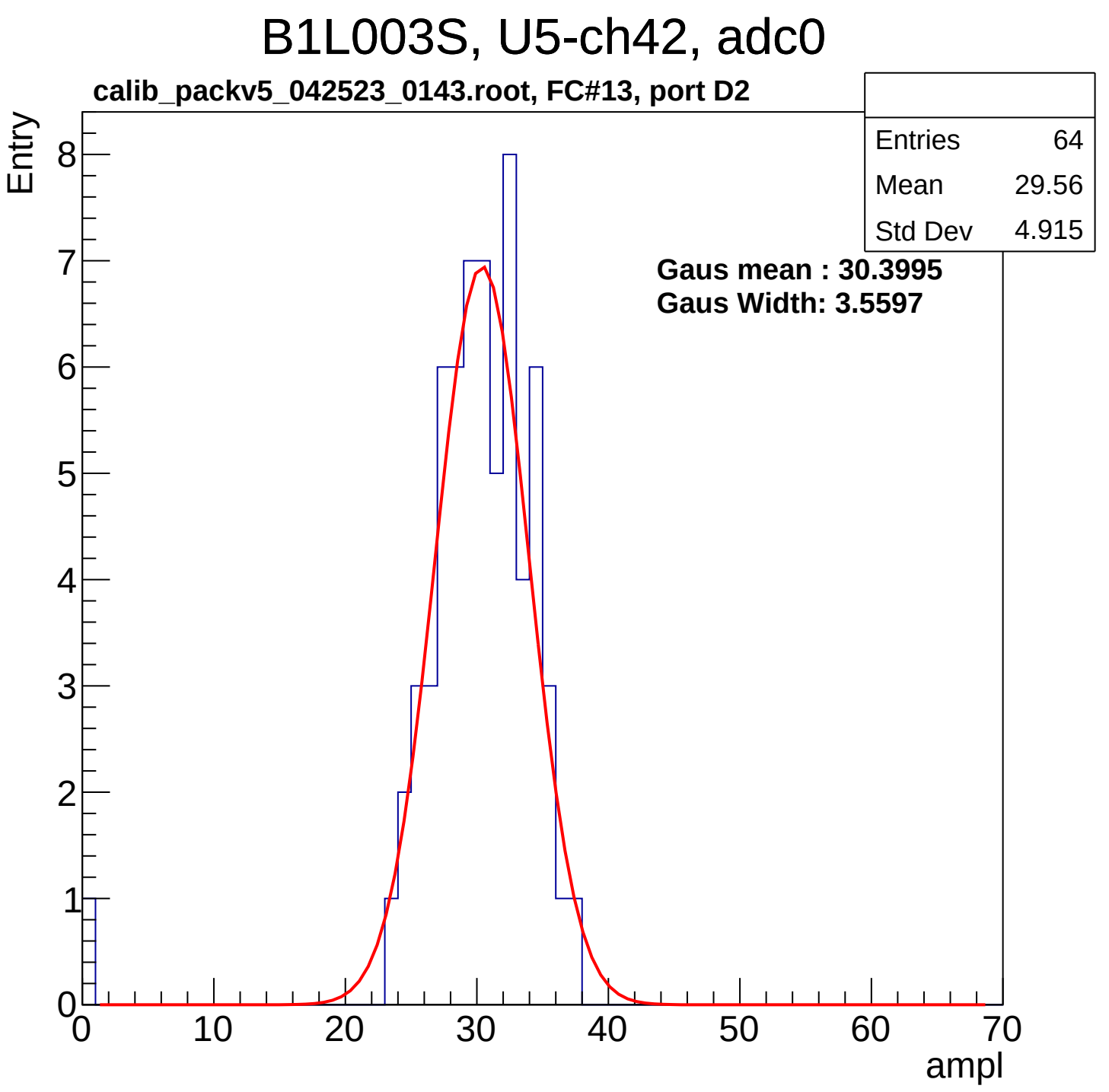
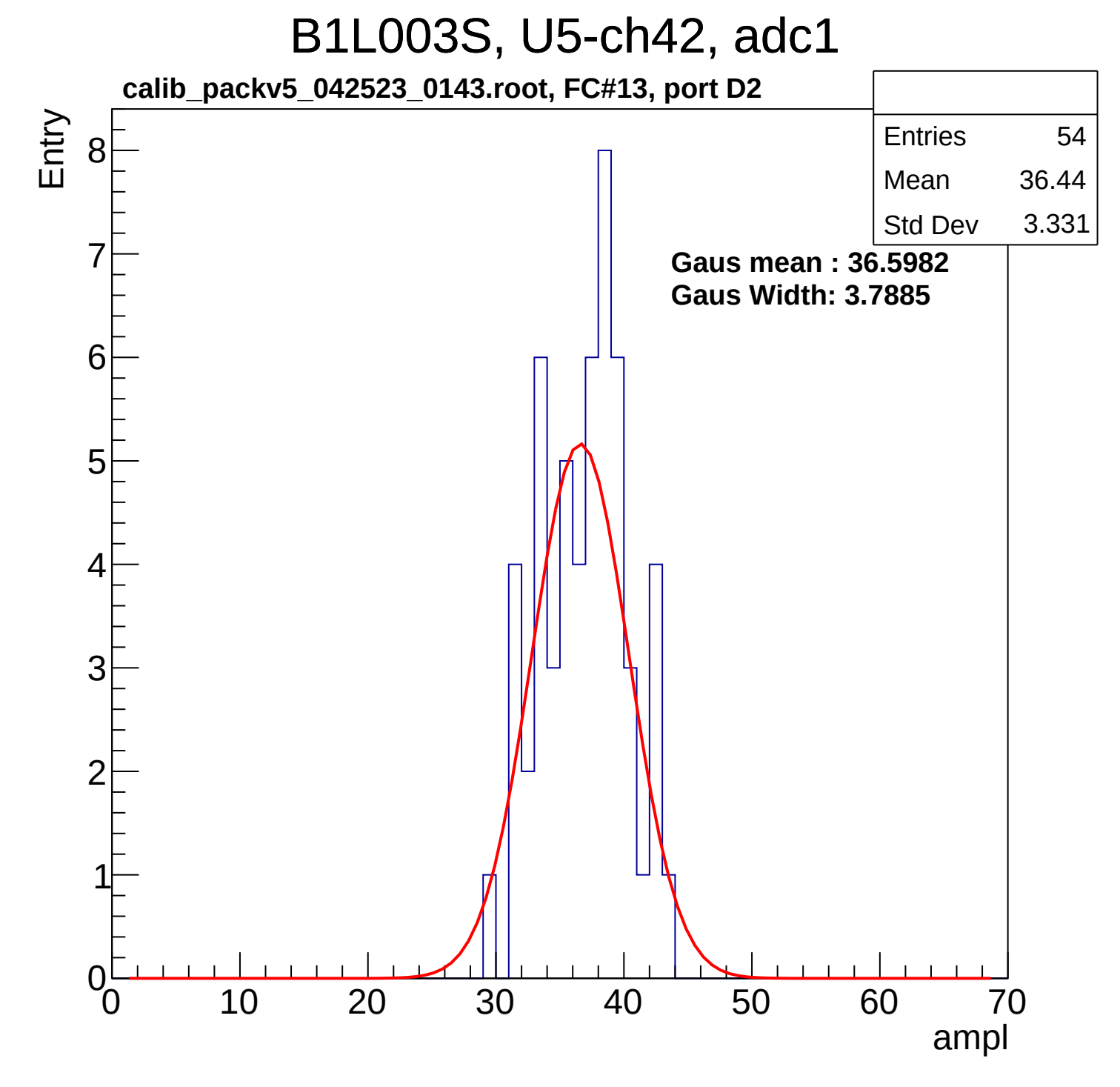
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.56
Std Dev 4.915
Gaus mean : 30.3995
Gaus Width: 3.5597
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 36.44
Std Dev 3.331
Gaus mean : 36.5982
Gaus Width: 3.7885
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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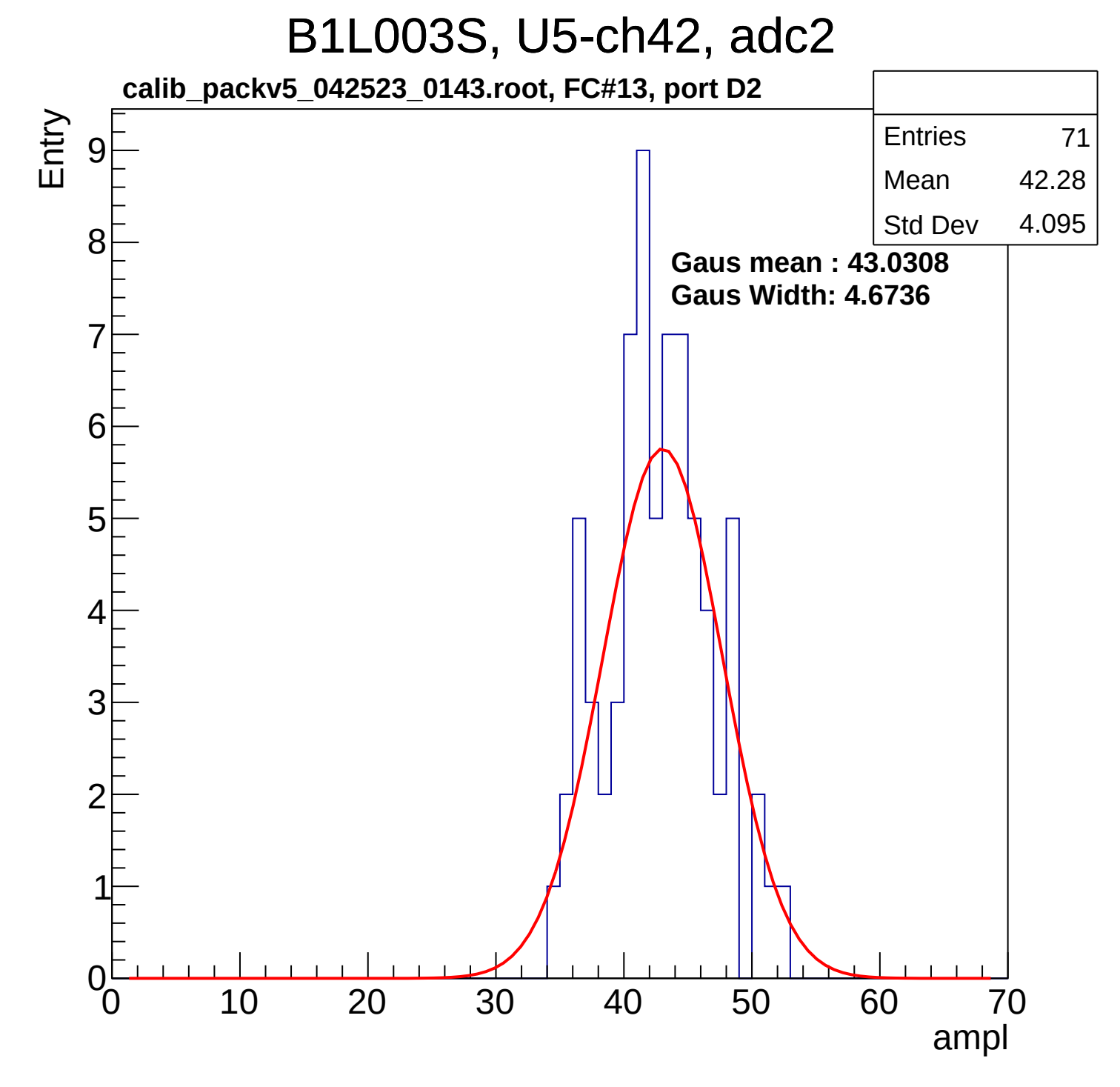
B1L003S, U5-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.28
Std Dev 4.095
Gaus mean : 43.0308
Gaus Width: 4.6736
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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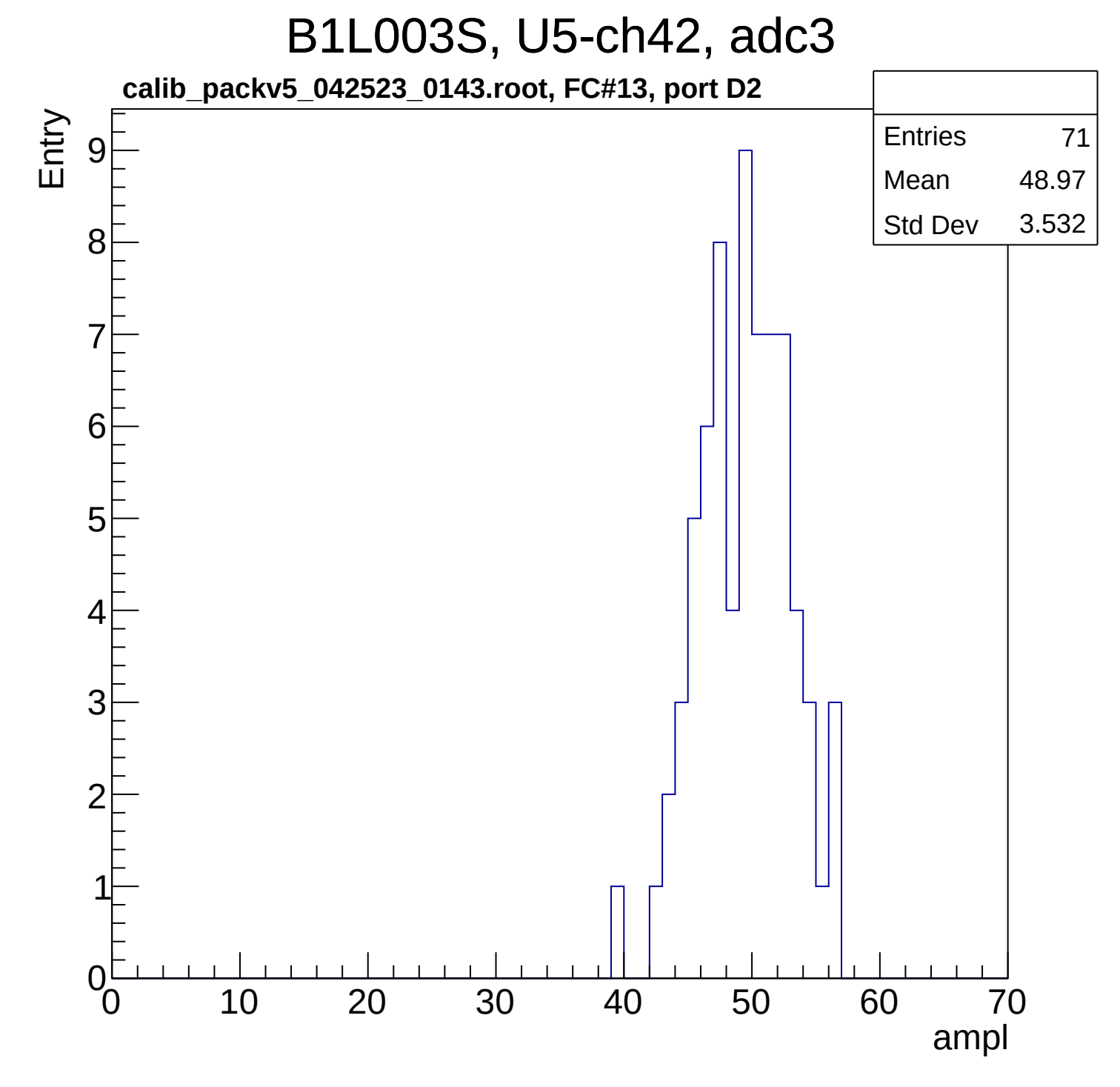
B1L003S, U5-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 48.97
Std Dev 3.532
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
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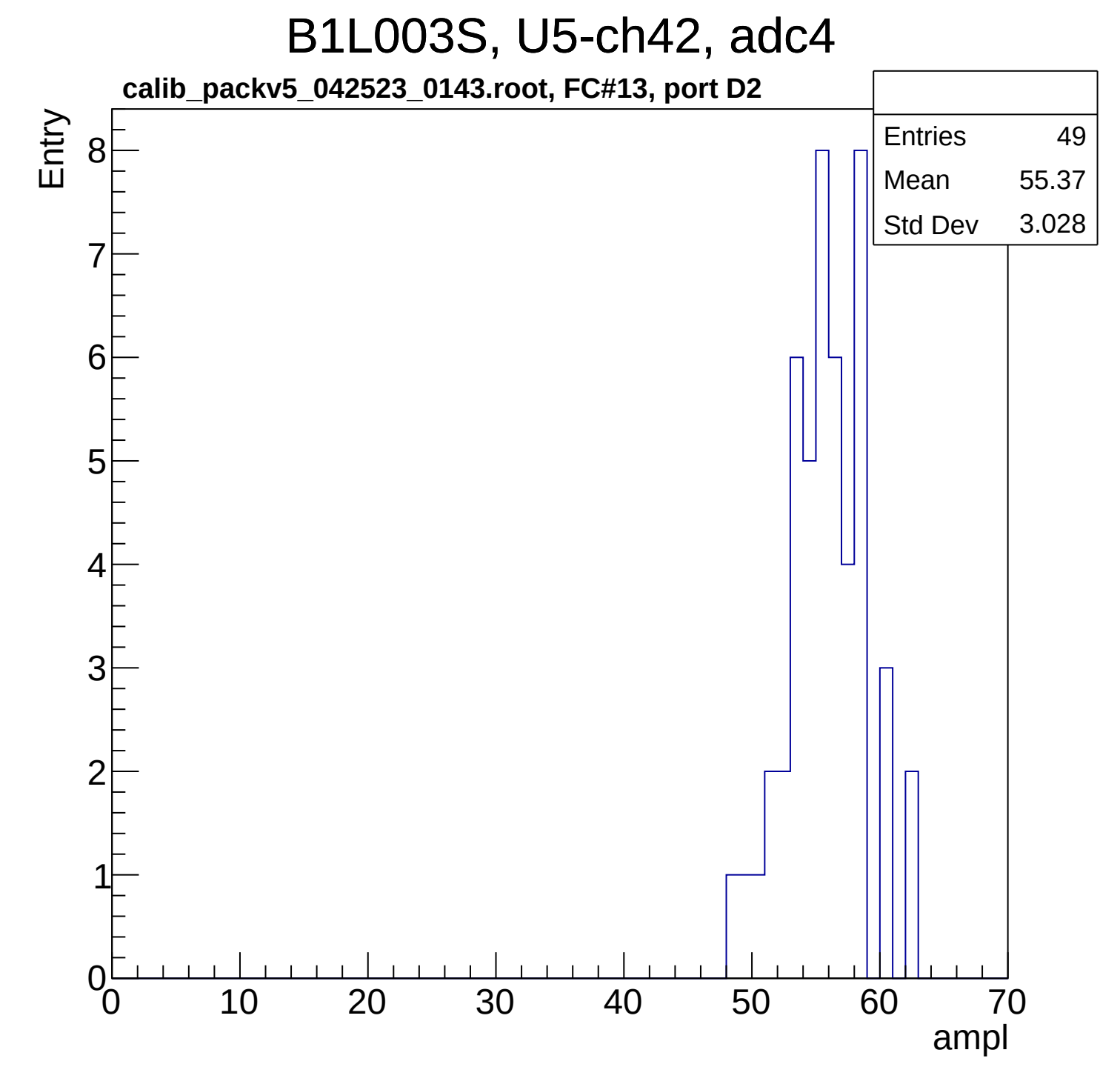
B1L003S, U5-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.37
Std Dev 3.028
Entry
ampl
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2
3
4
5
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7
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0
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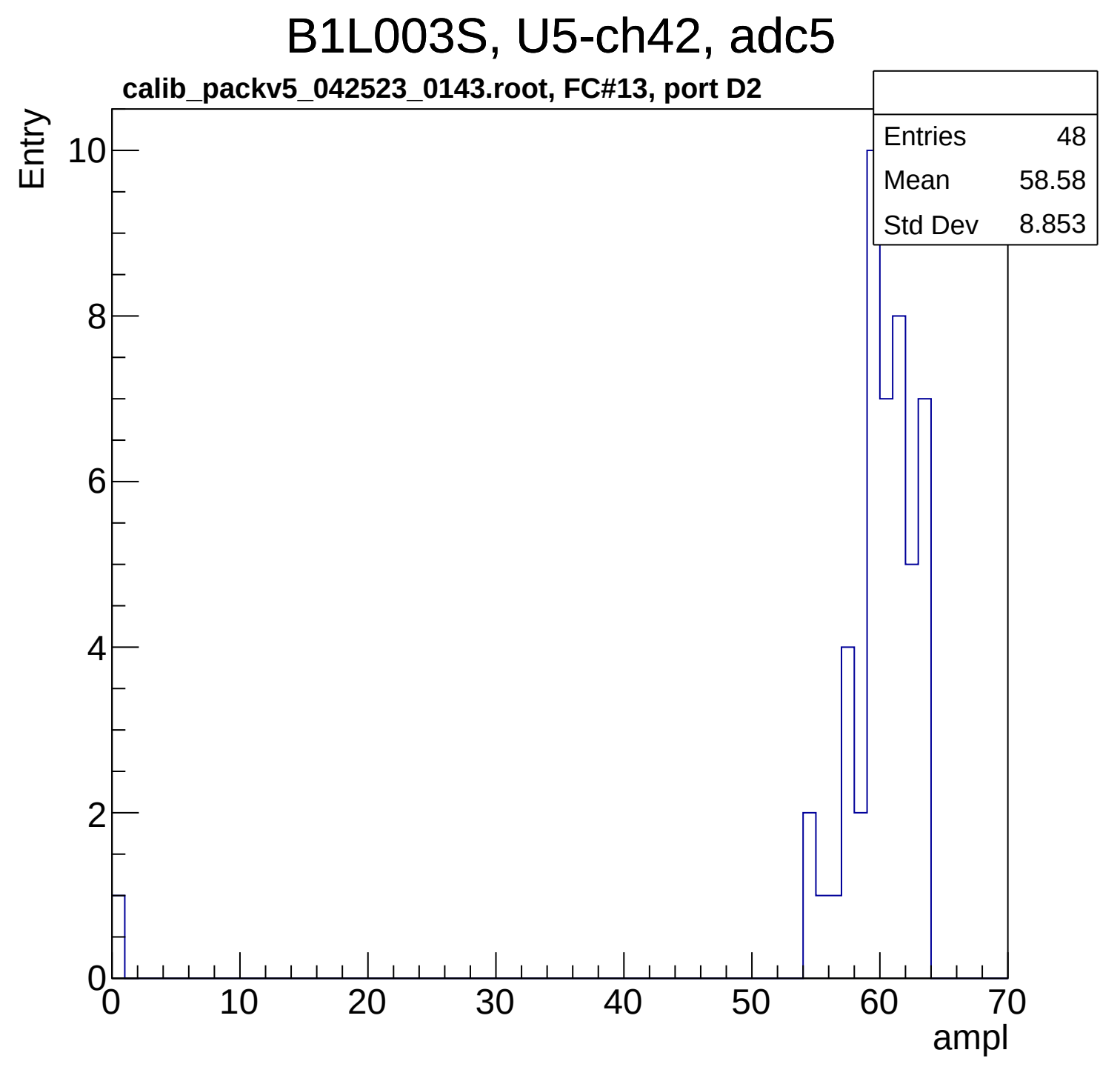
B1L003S, U5-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.58
Std Dev 8.853
Entry
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6
8
10
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50
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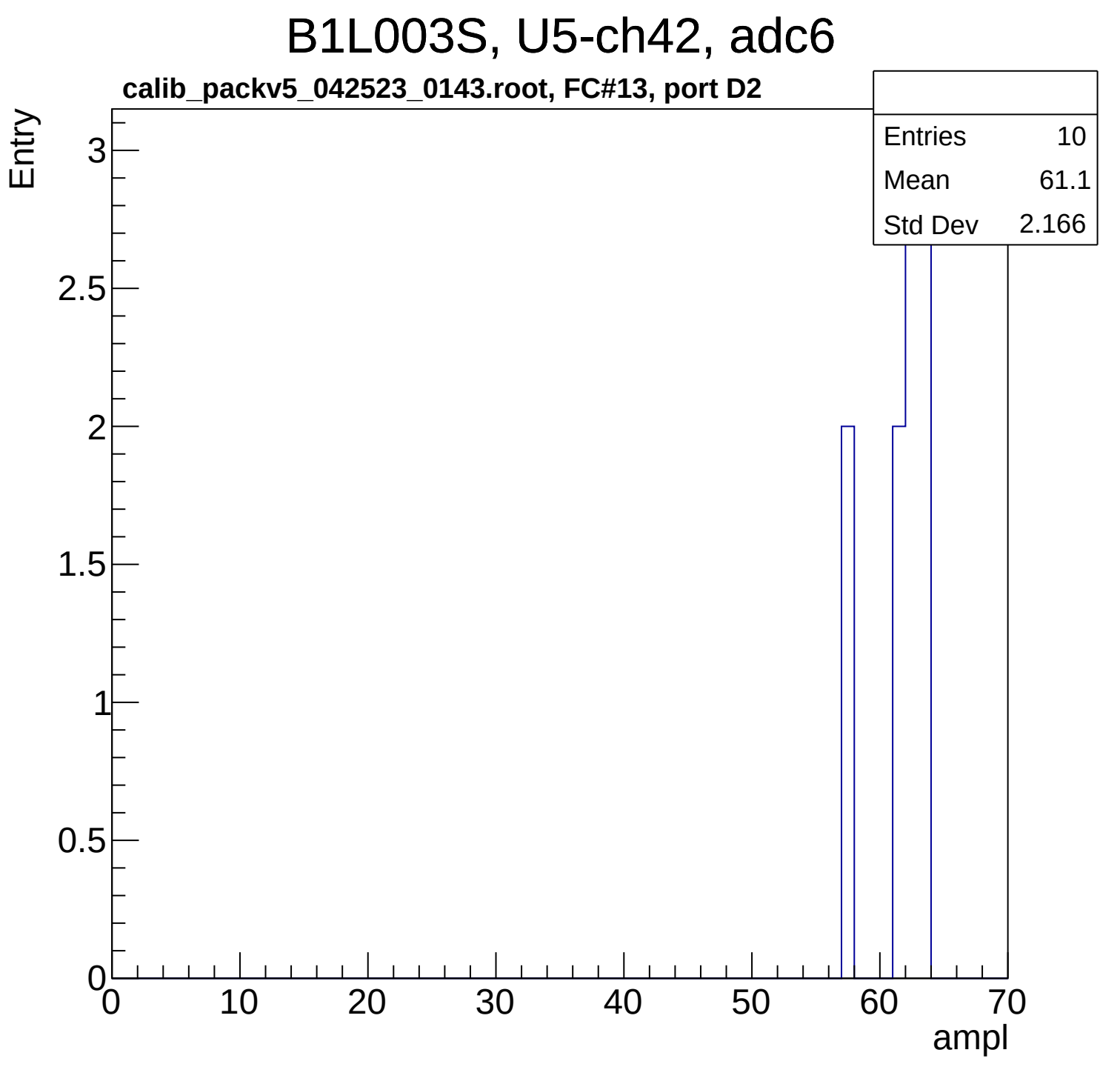
70
ampl


B1L003S, U5-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.1
Std Dev 2.166
Entry
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1
1.5
2
2.5
3
0
10
20
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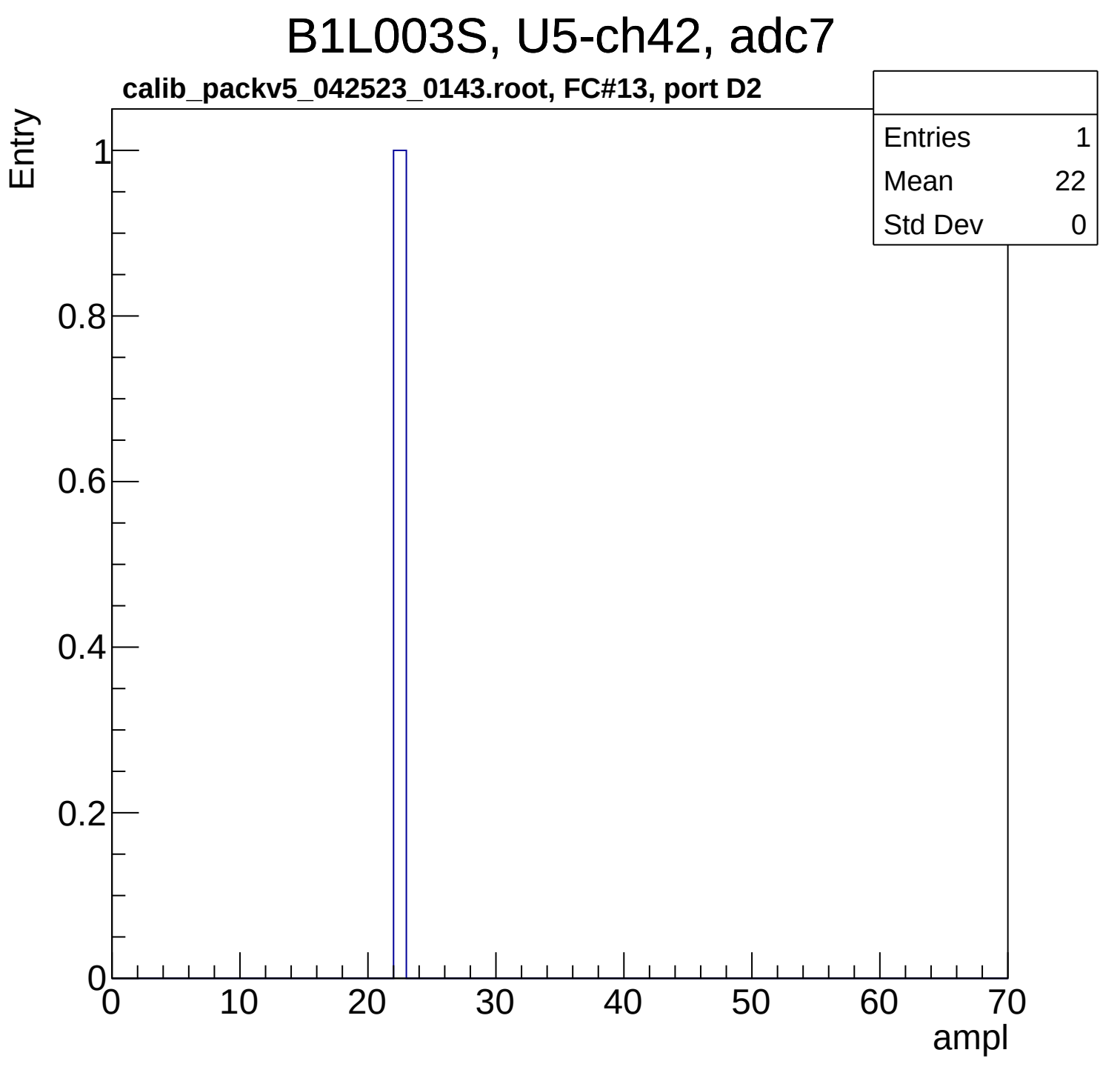
50
60
70
ampl


B1L003S, U5-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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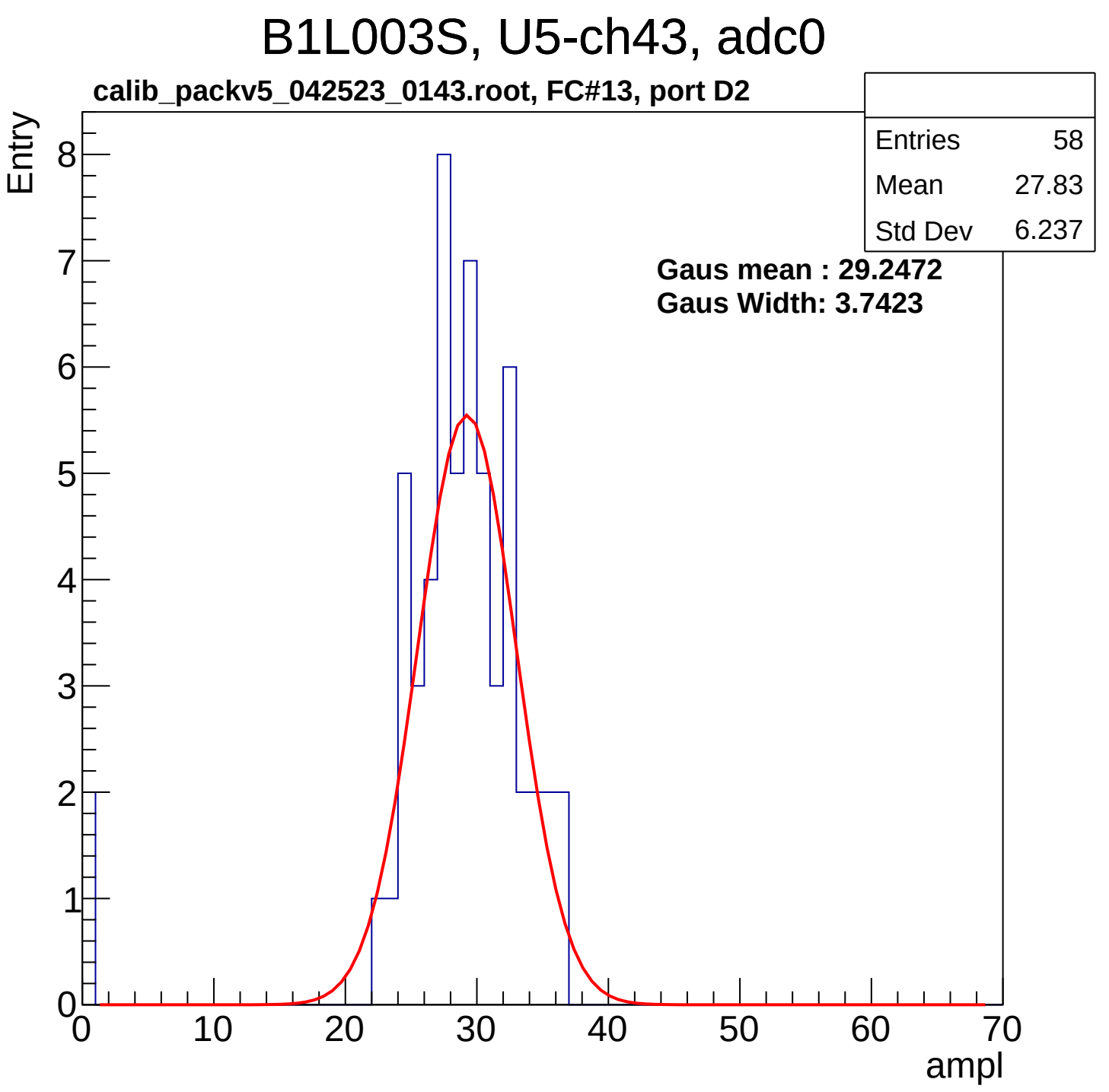
50
60
70
ampl


B1L003S, U5-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 27.83
Std Dev 6.237
Gaus mean : 29.2472
Gaus Width: 3.7423
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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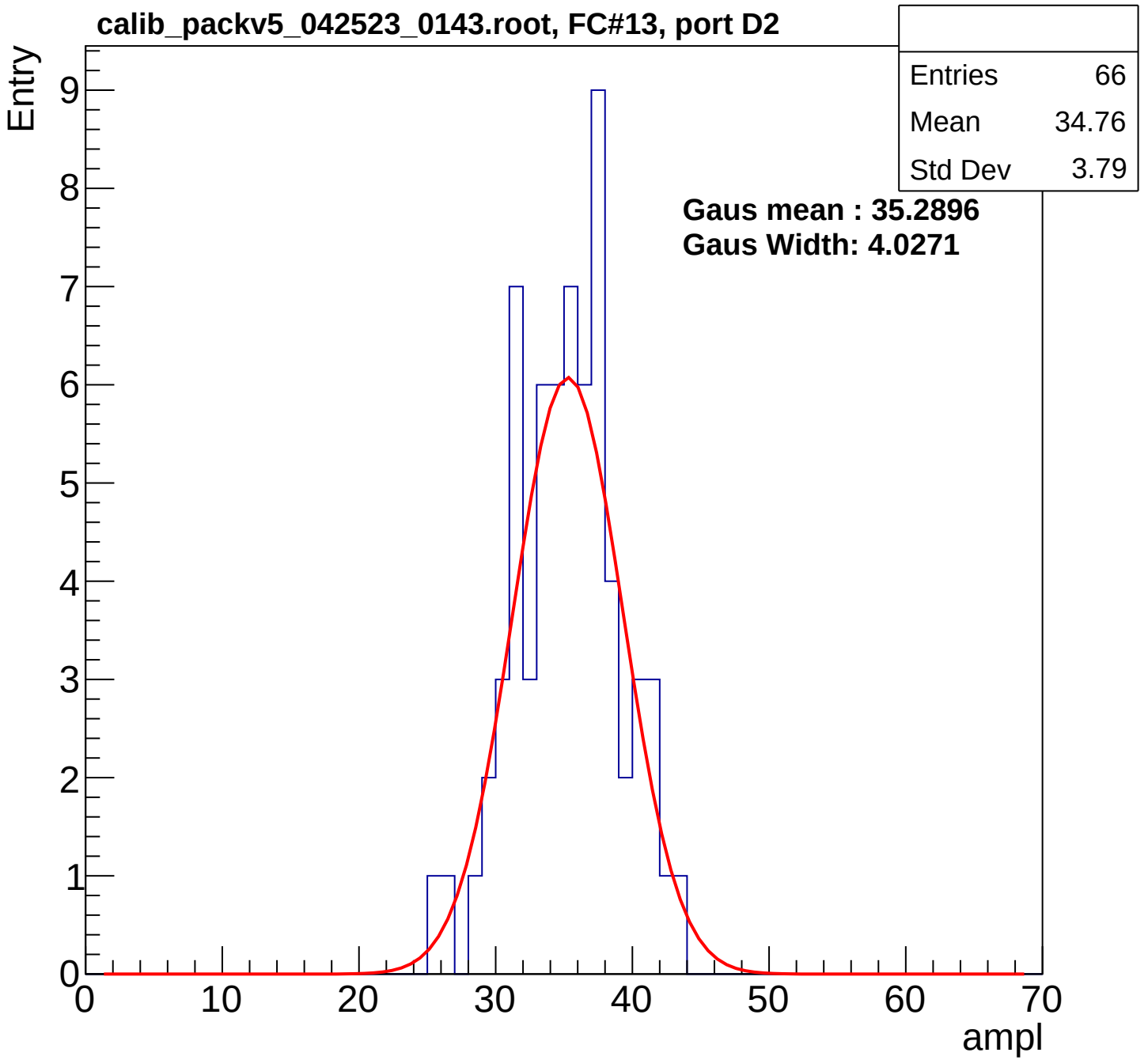
B1L003S, U5-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 34.76
Std Dev 3.79
Gaus mean : 35.2896
Gaus Width: 4.0271
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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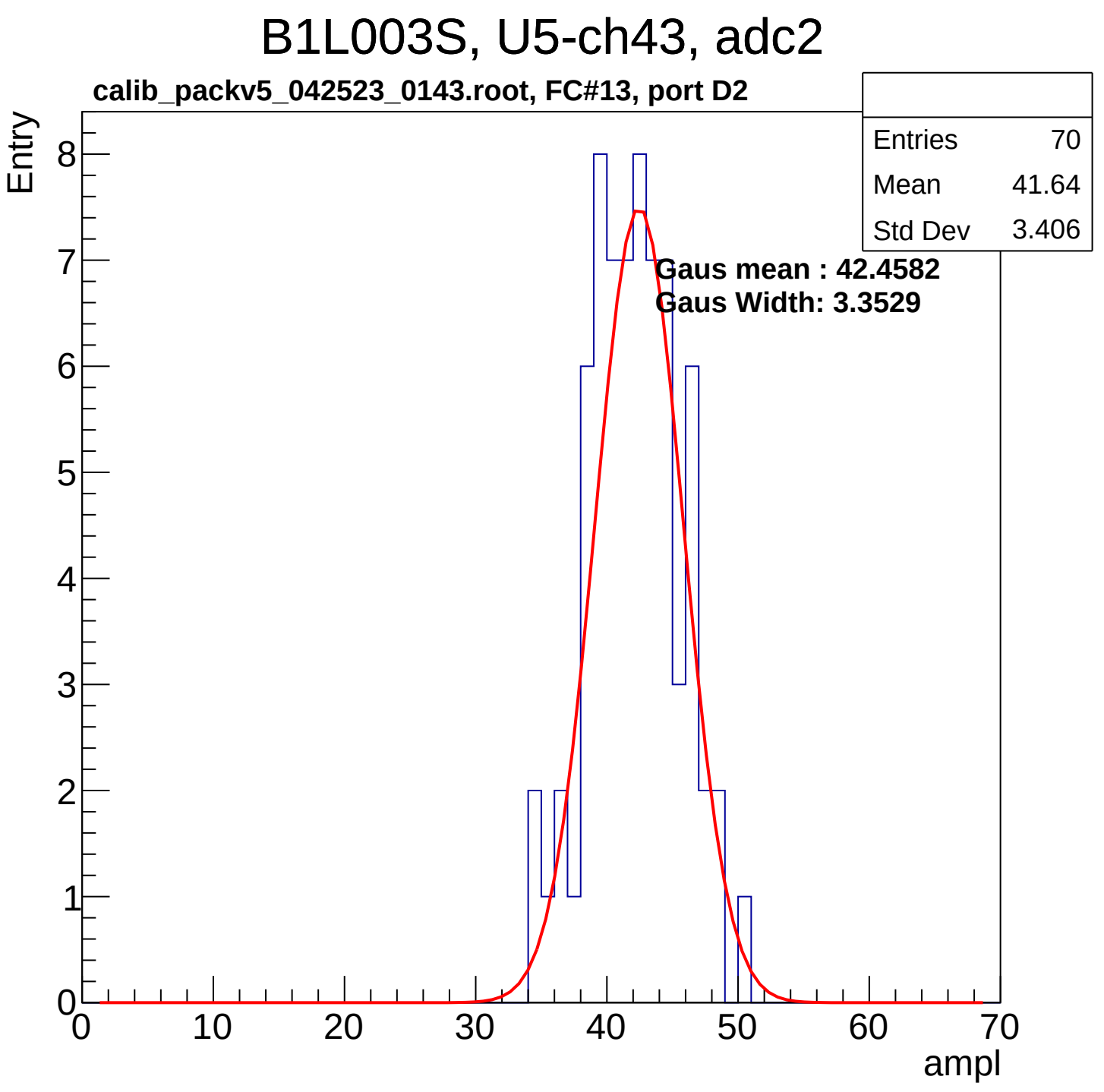
B1L003S, U5-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.64
Std Dev 3.406
Gaus mean : 42.4582
Gaus Width: 3.3529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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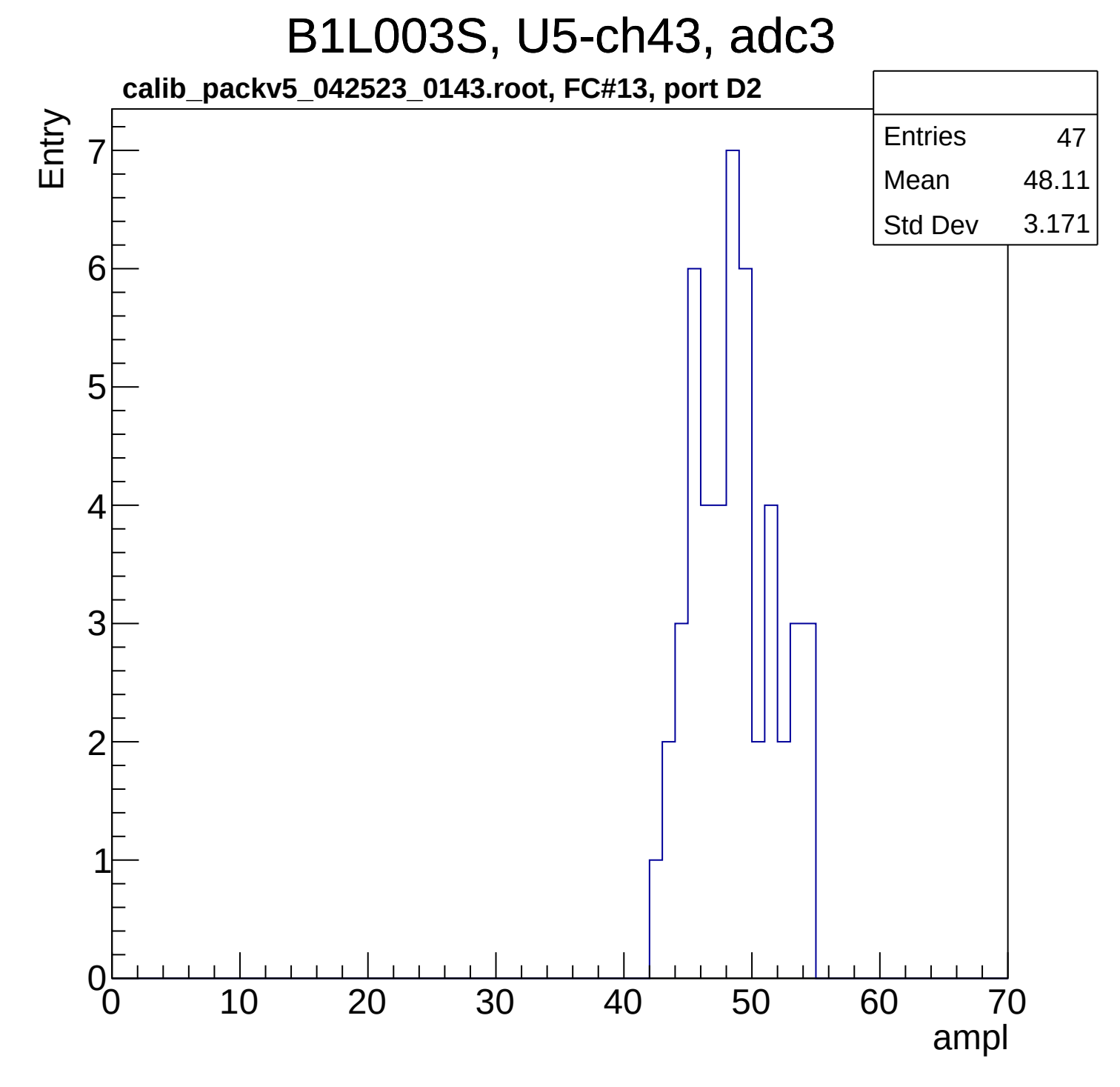
B1L003S, U5-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 48.11
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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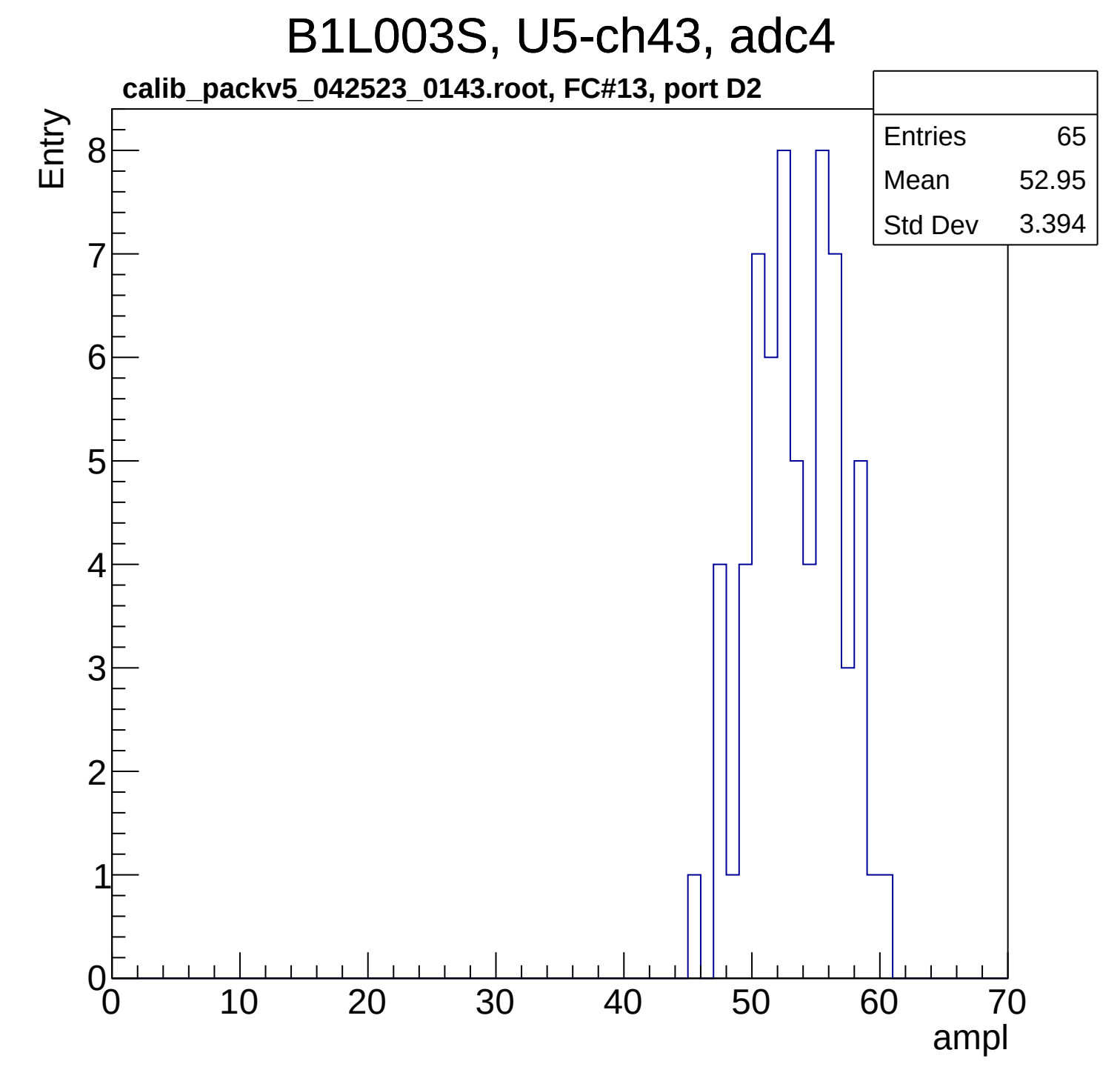
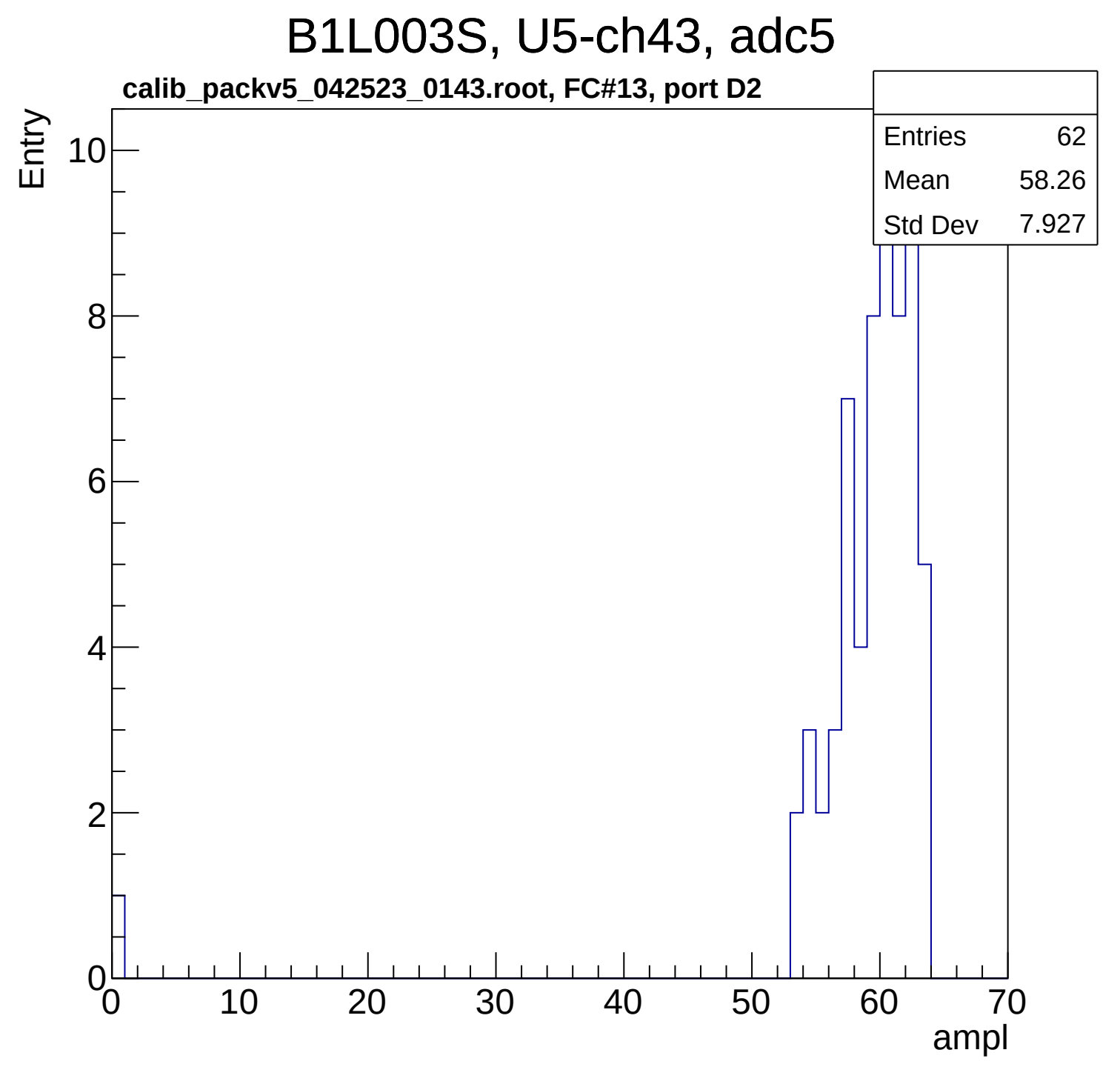
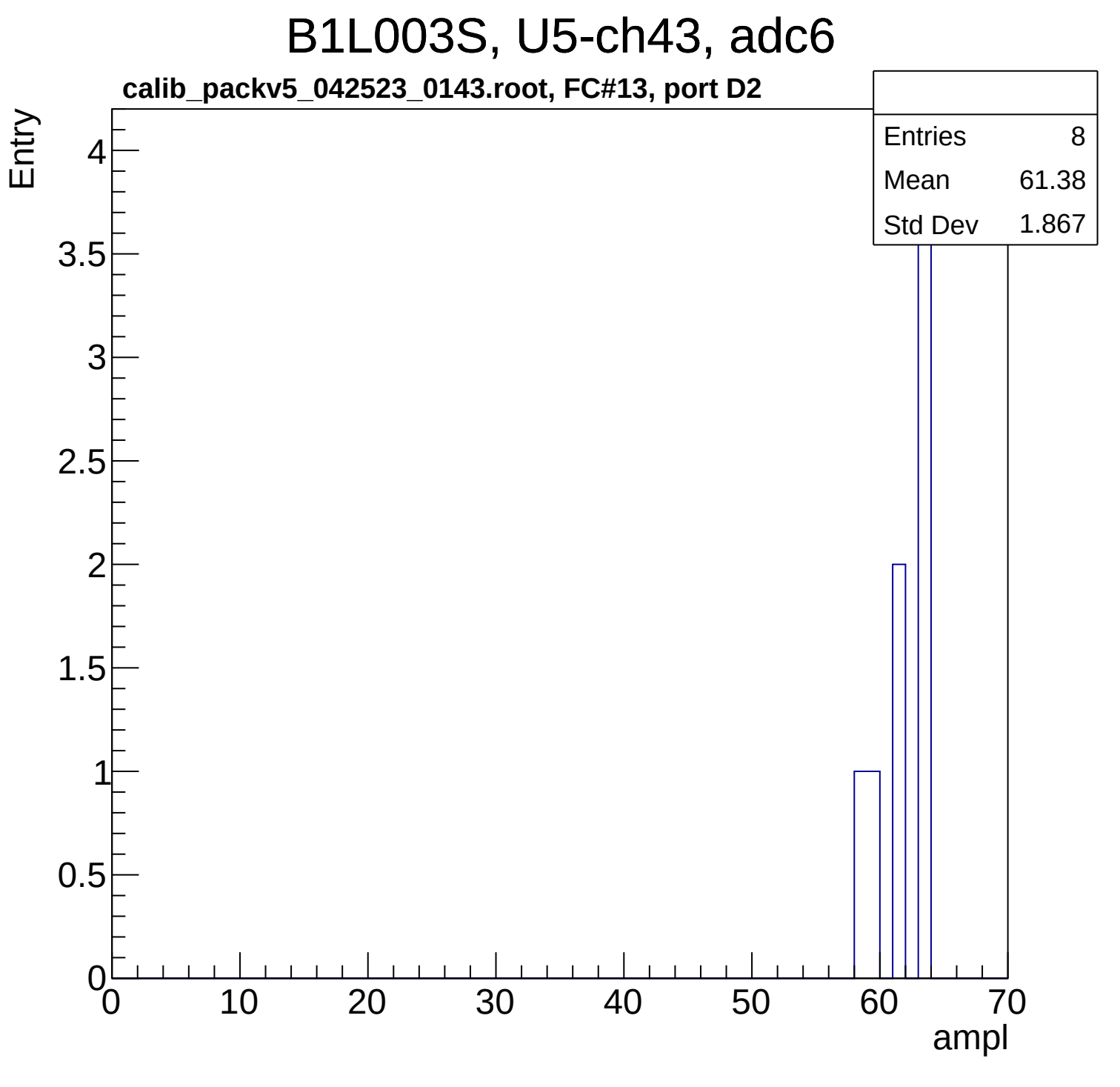

B1L003S, U5-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 52.95
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.26
Std Dev 7.927
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.38
Std Dev 1.867
Entry
ampl
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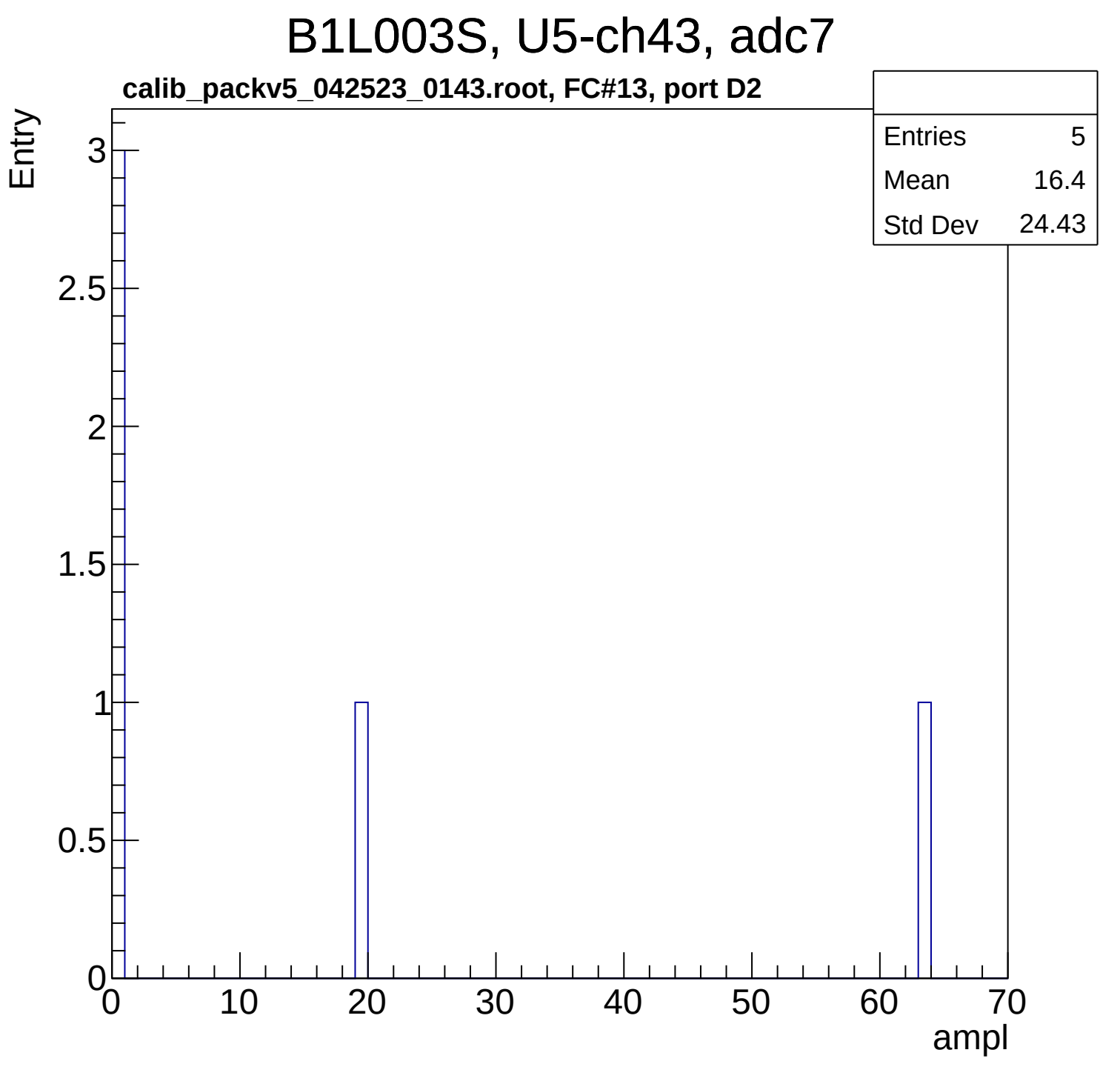
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 16.4
Std Dev 24.43
Entry
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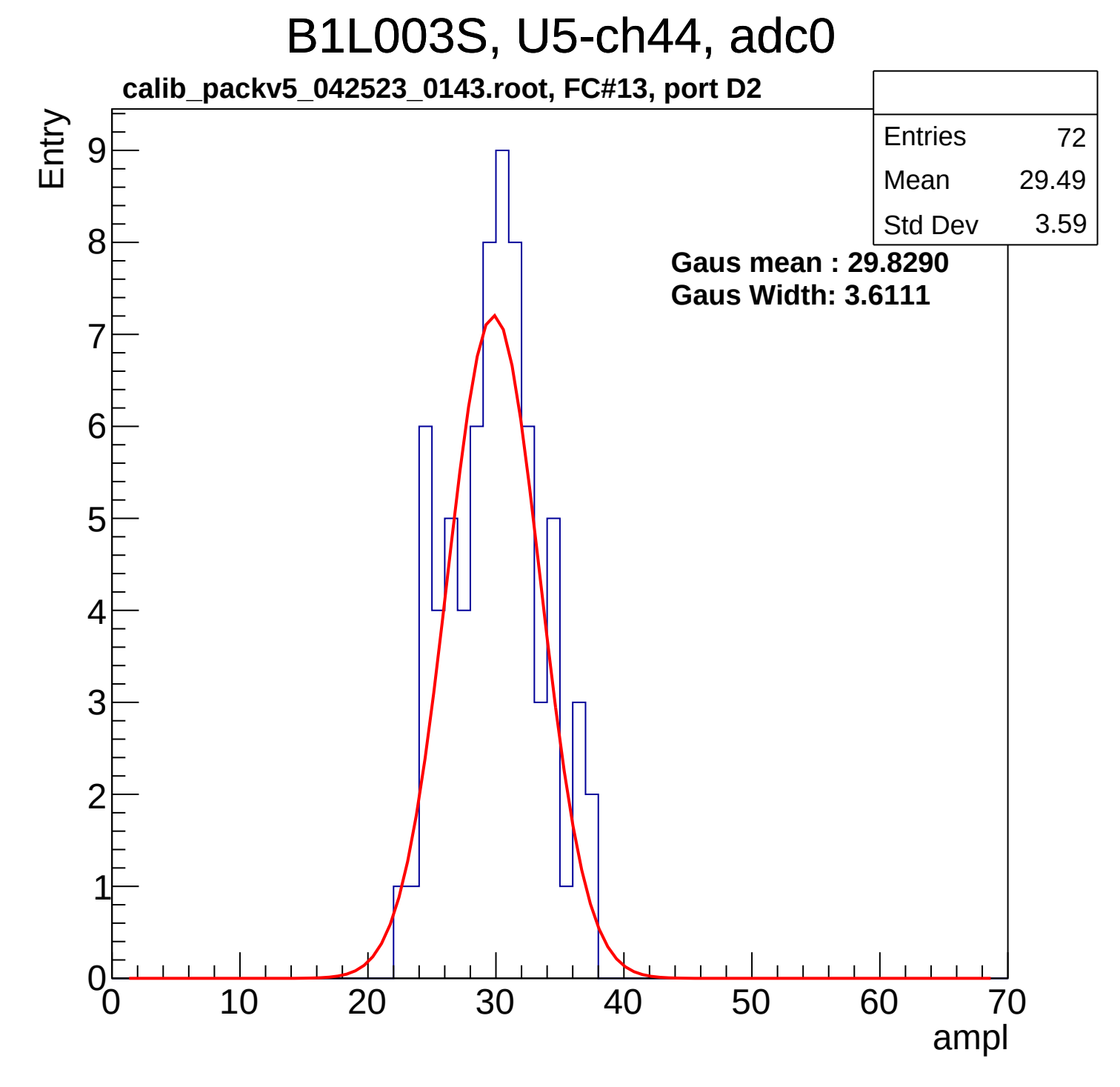
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.49
Std Dev 3.59
Gaus mean : 29.8290
Gaus Width: 3.6111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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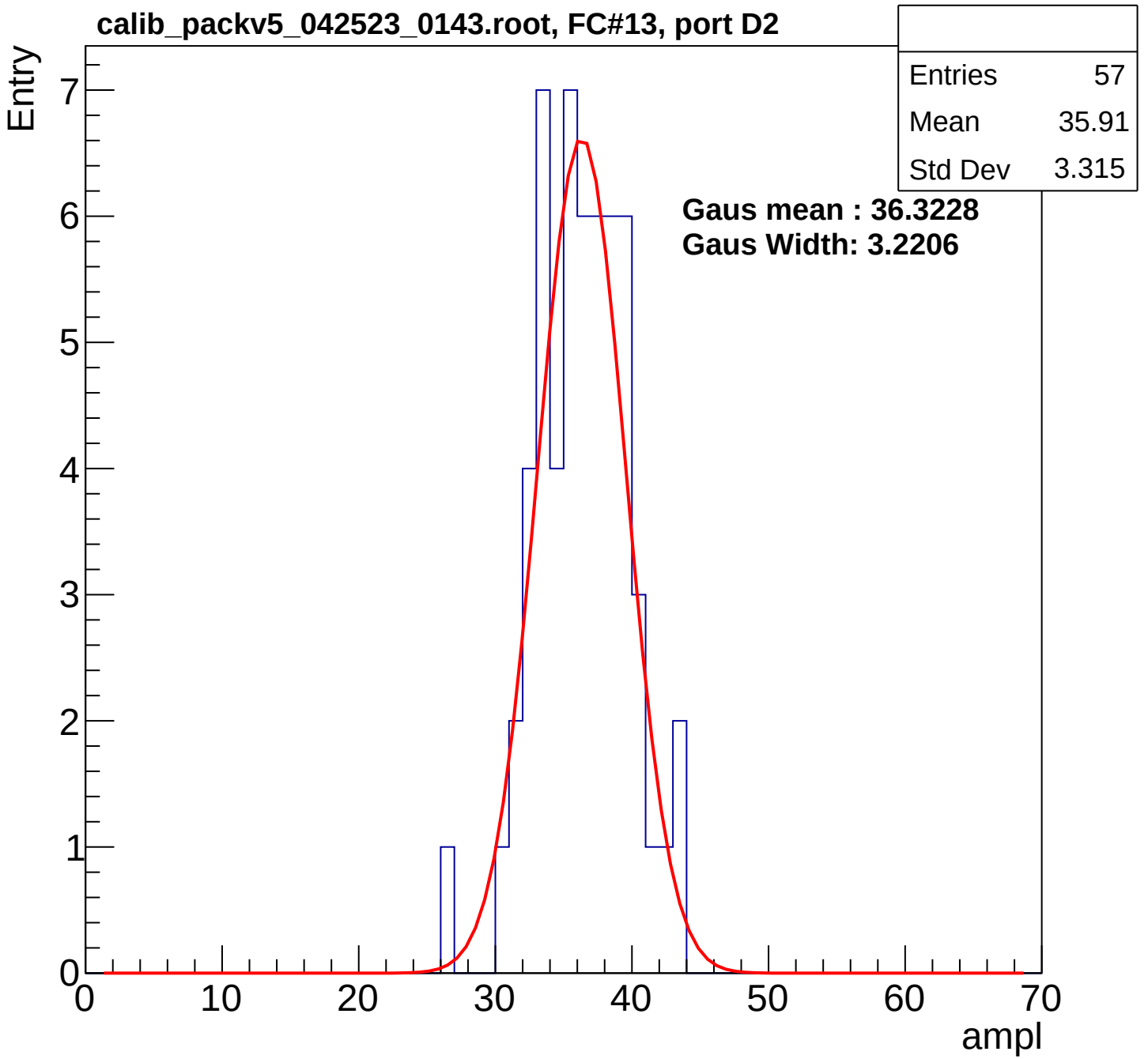
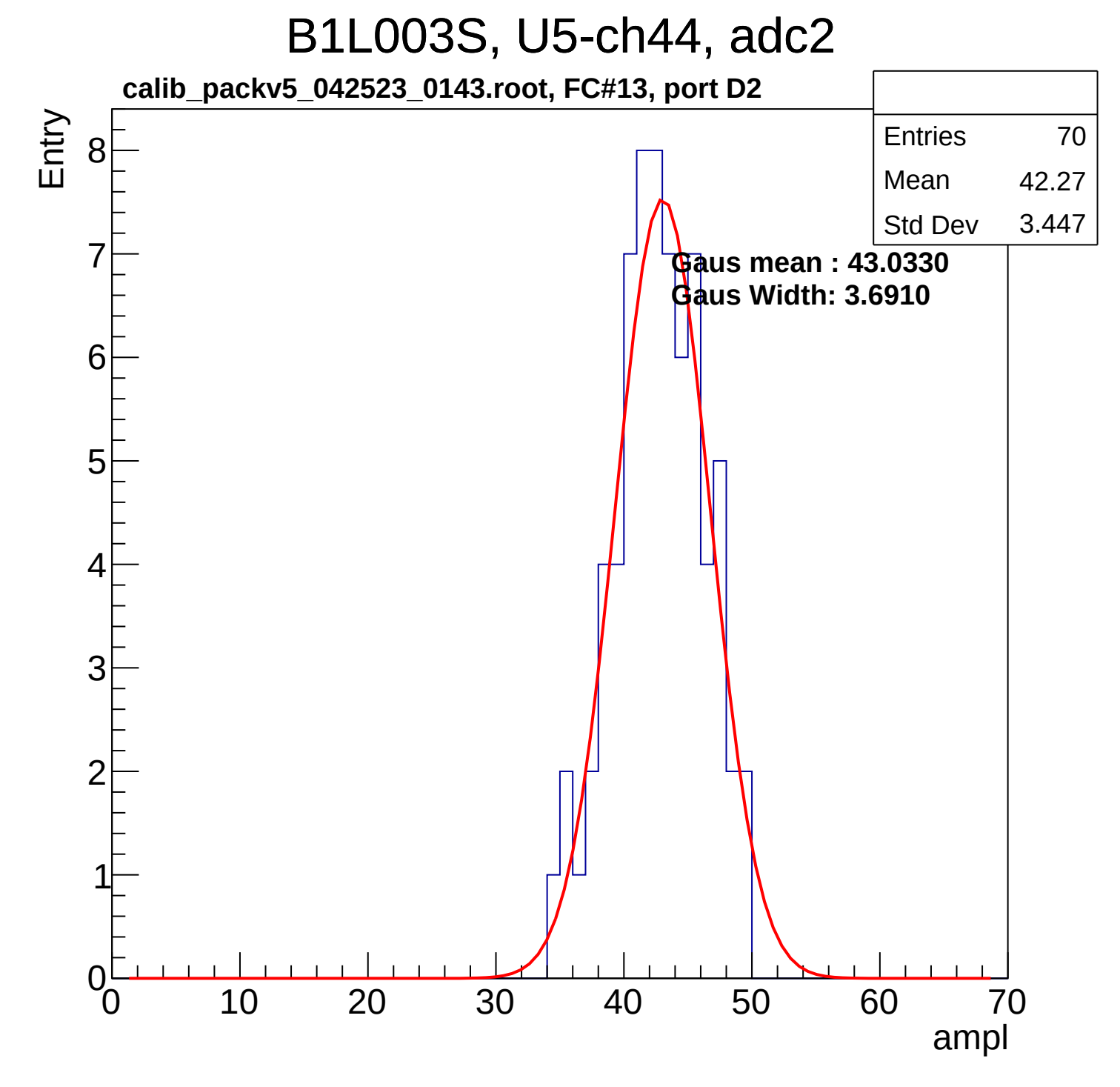

B1L003S, U5-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.91
Std Dev 3.315
Gaus mean : 36.3228
Gaus Width: 3.2206
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.27
Std Dev 3.447
Gaus mean : 43.0330
Gaus Width: 3.6910
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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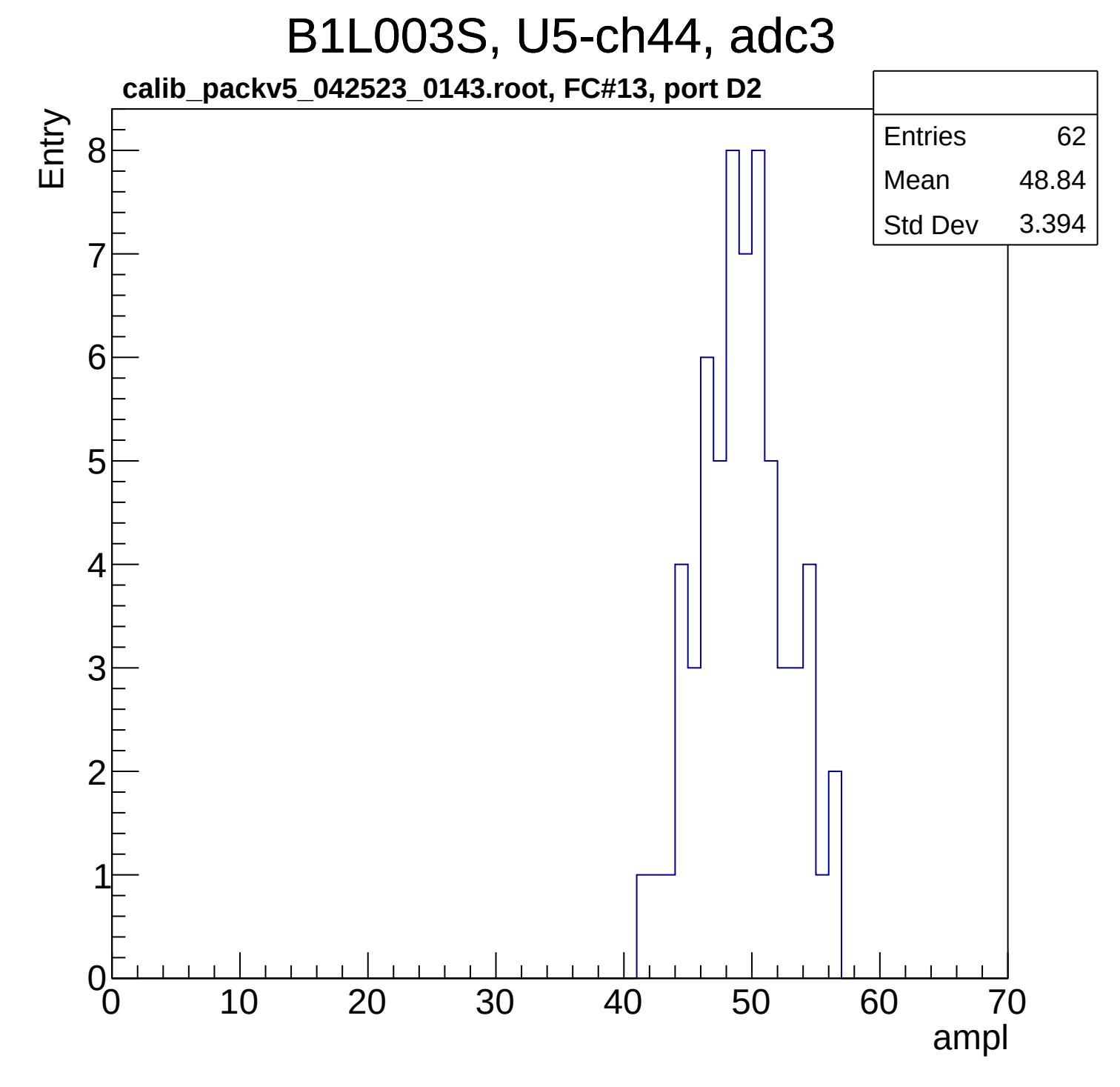
B1L003S, U5-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.84
Std Dev 3.394
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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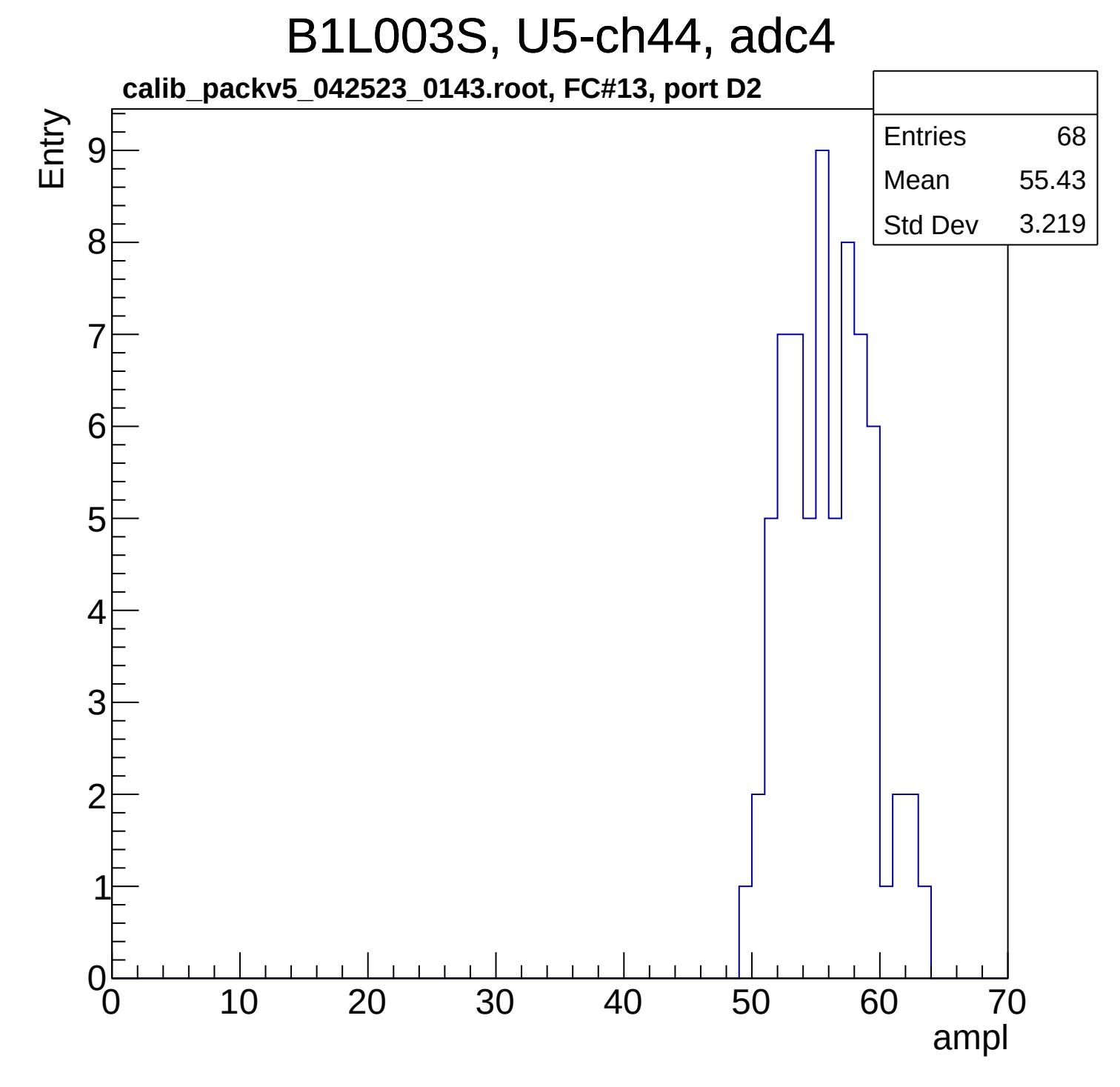
B1L003S, U5-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 55.43
Std Dev 3.219
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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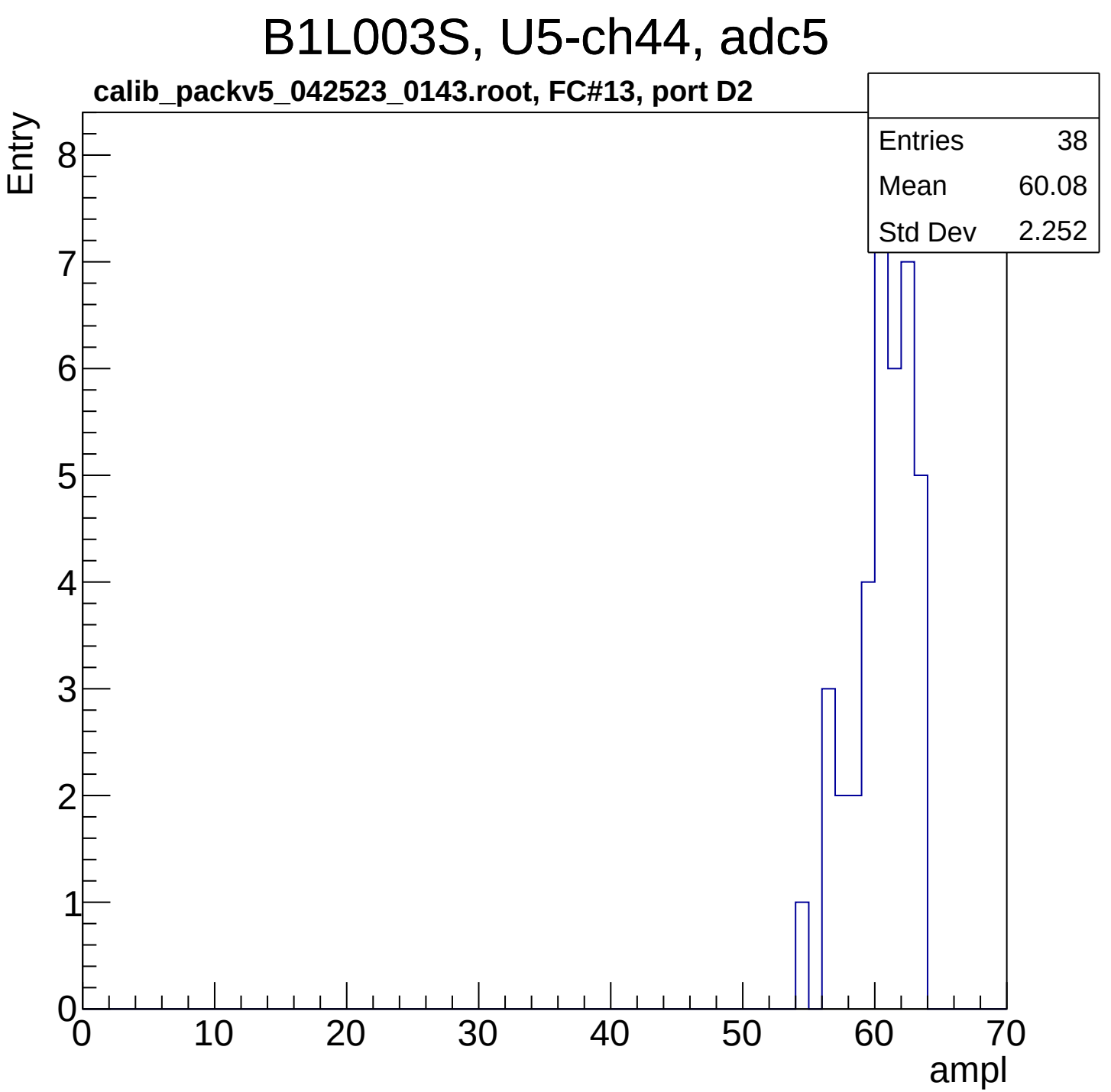
B1L003S, U5-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.08
Std Dev 2.252
Entry
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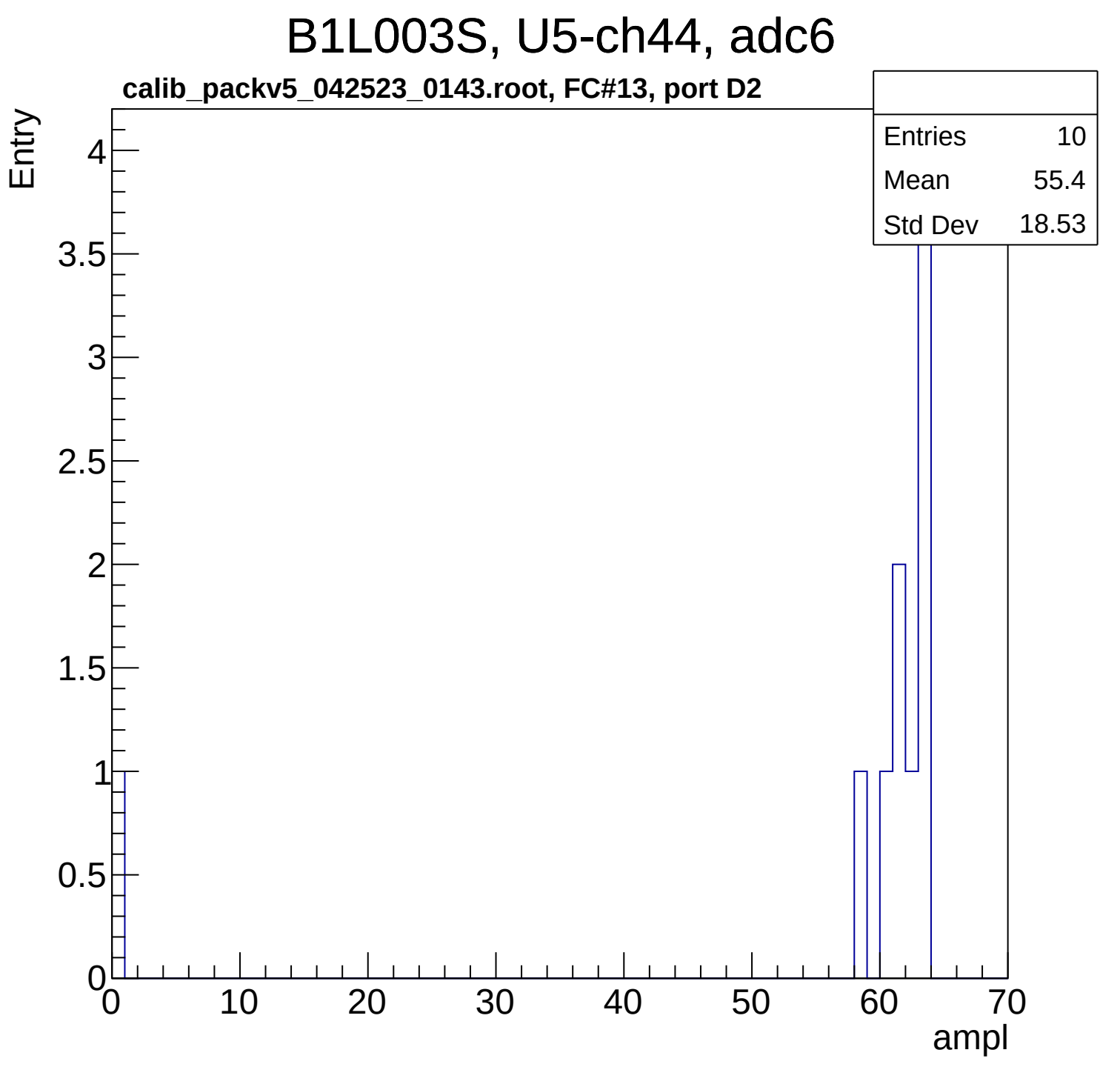
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.4
Std Dev 18.53
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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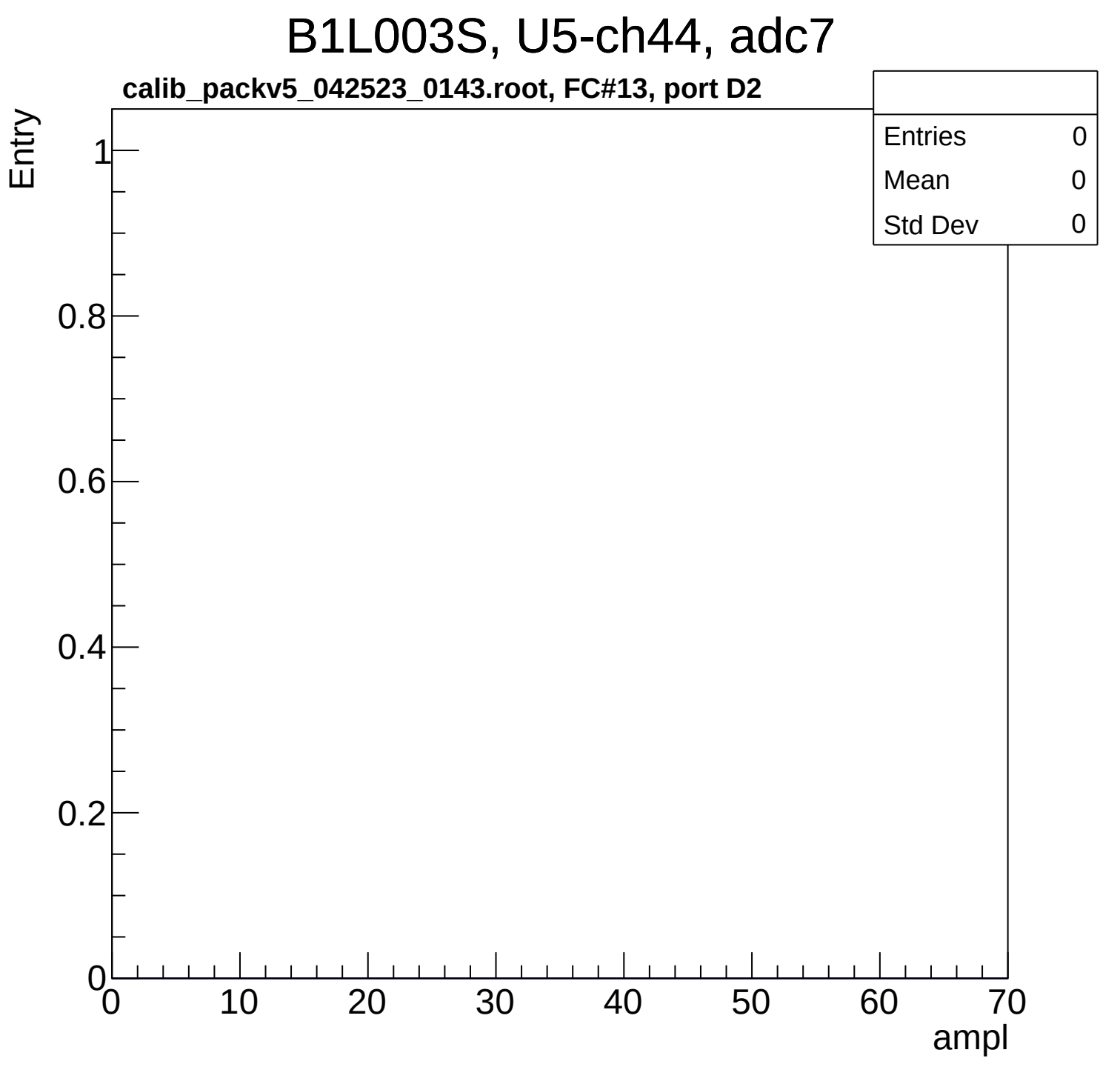
B1L003S, U5-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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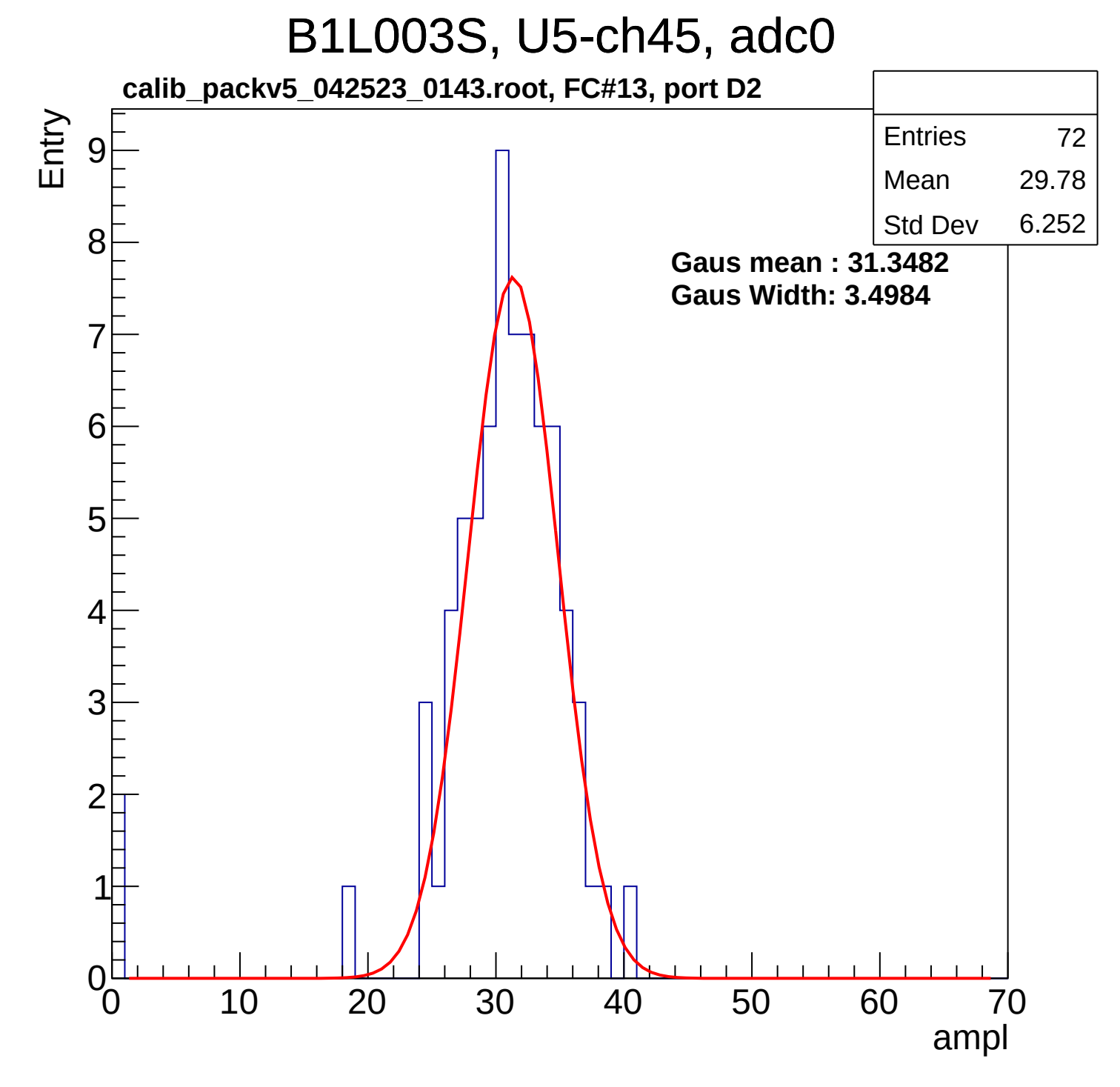
60
70
ampl


B1L003S, U5-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.78
Std Dev 6.252
Gaus mean : 31.3482
Gaus Width: 3.4984
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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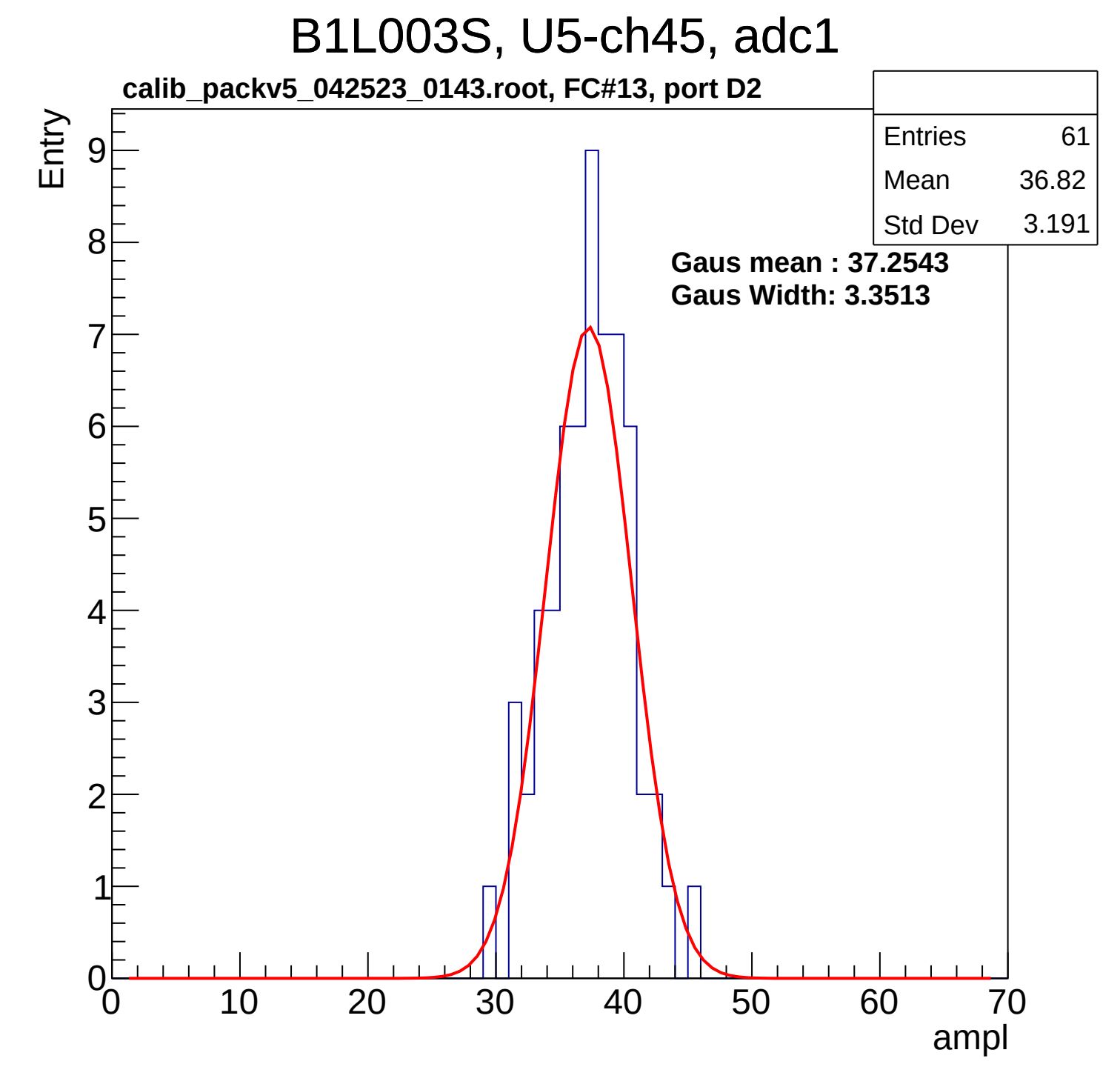
B1L003S, U5-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.82
Std Dev 3.191
Gaus mean : 37.2543
Gaus Width: 3.3513
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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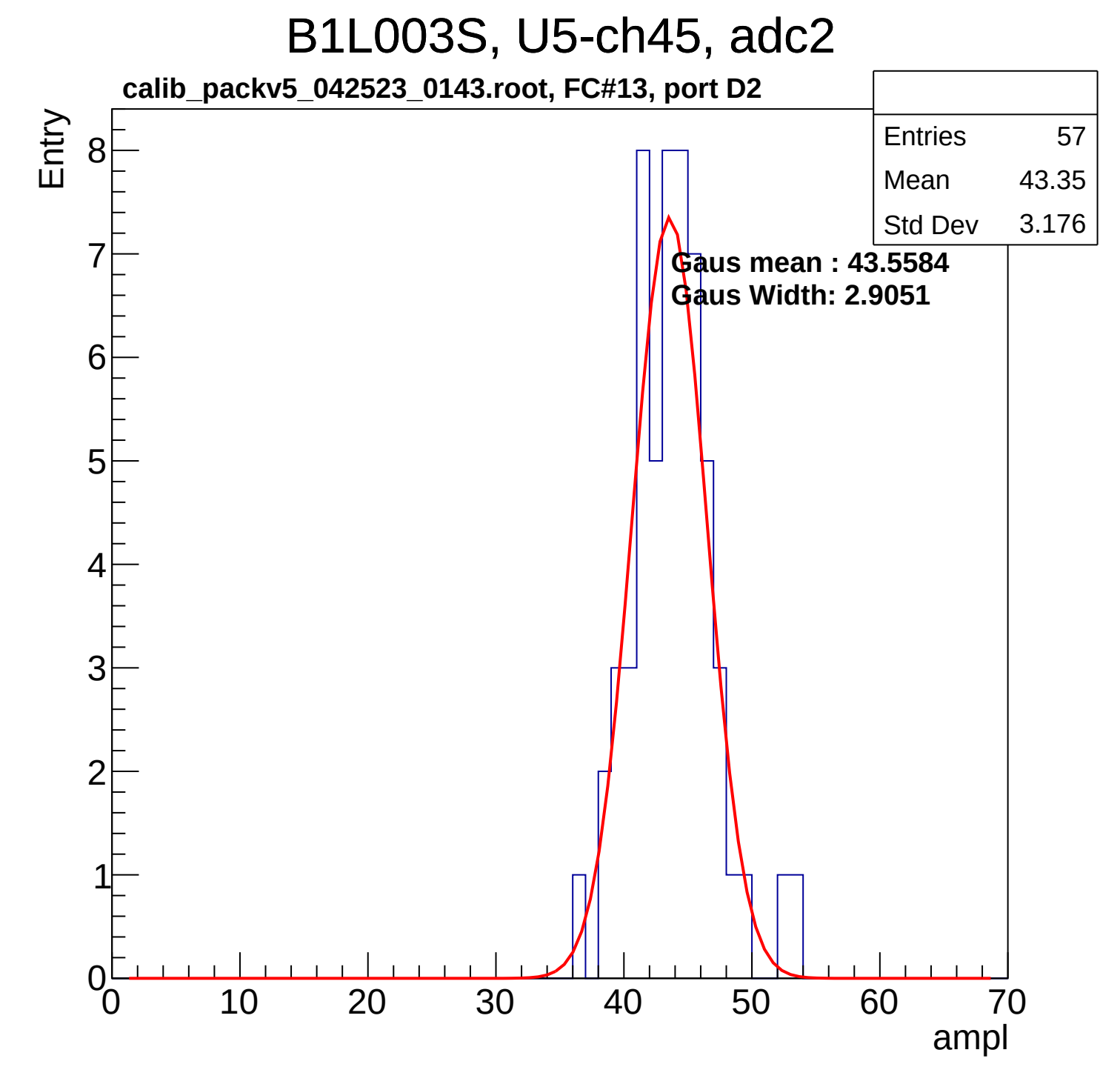
B1L003S, U5-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.35
Std Dev 3.176
Gaus mean : 43.5584
Gaus Width: 2.9051
Entry
ampl
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3
4
5
6
7
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0
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60
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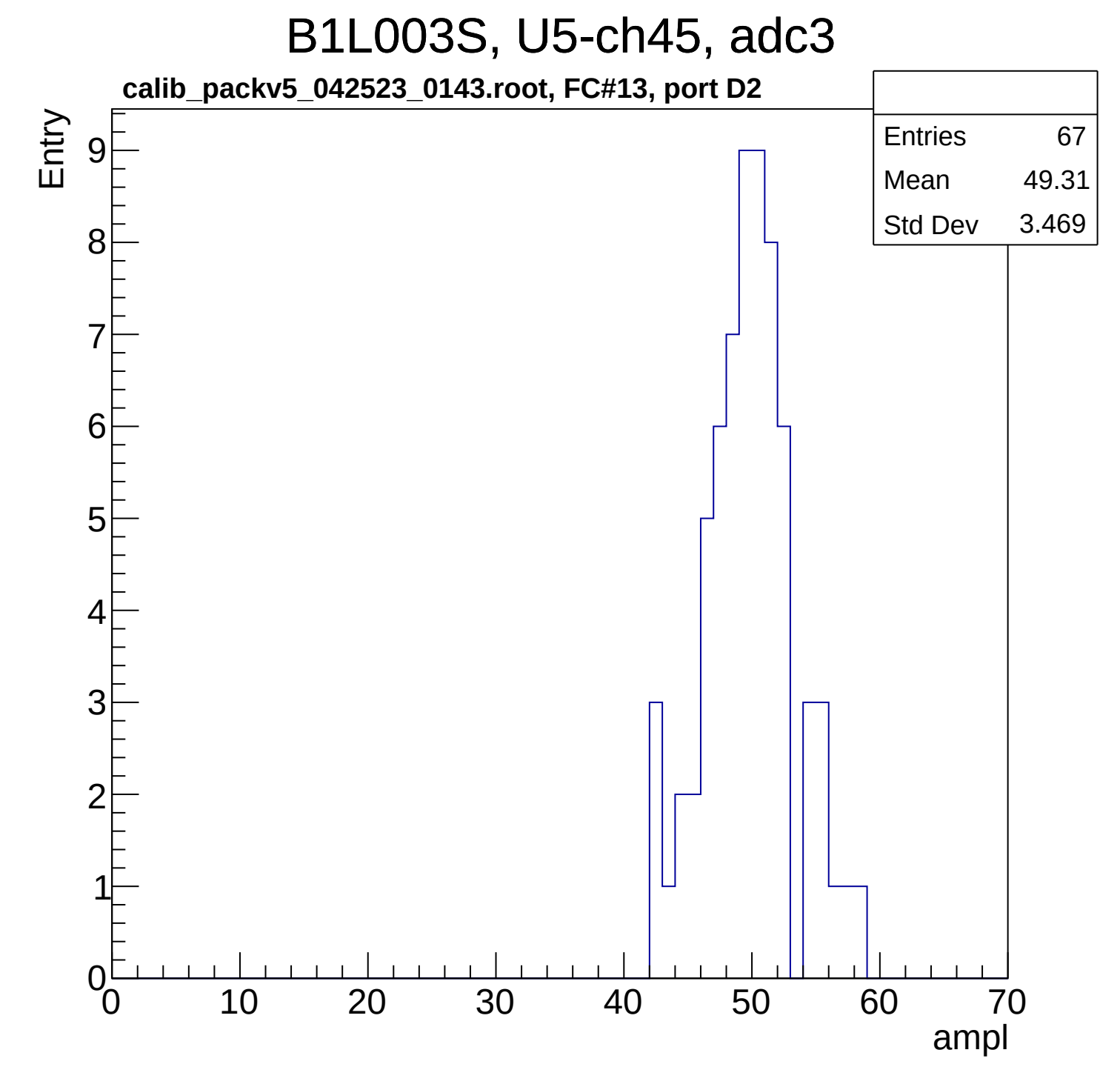
B1L003S, U5-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.31
Std Dev 3.469
Entry
ampl
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3
4
5
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7
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60
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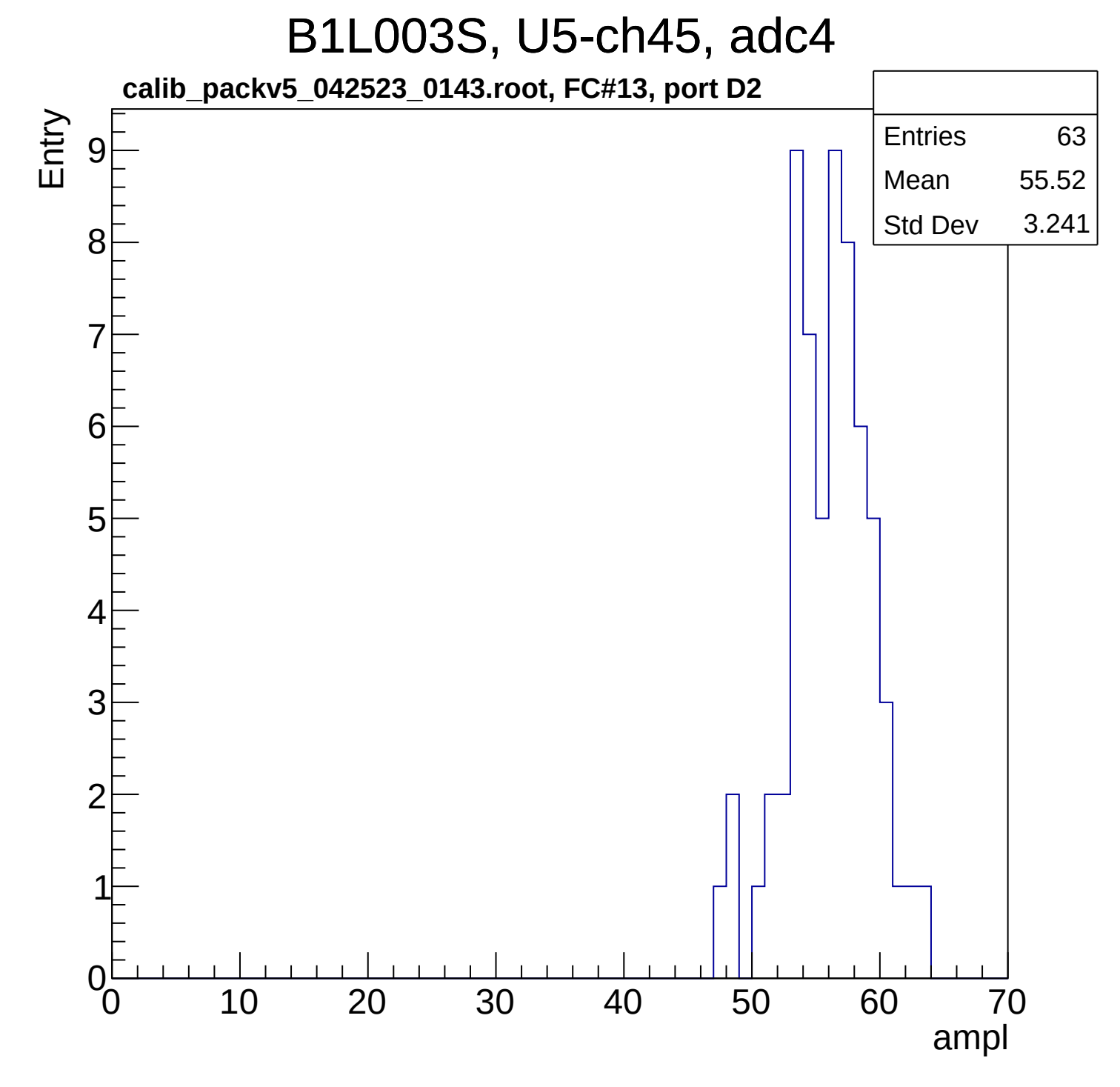
B1L003S, U5-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.52
Std Dev 3.241
Entry
ampl
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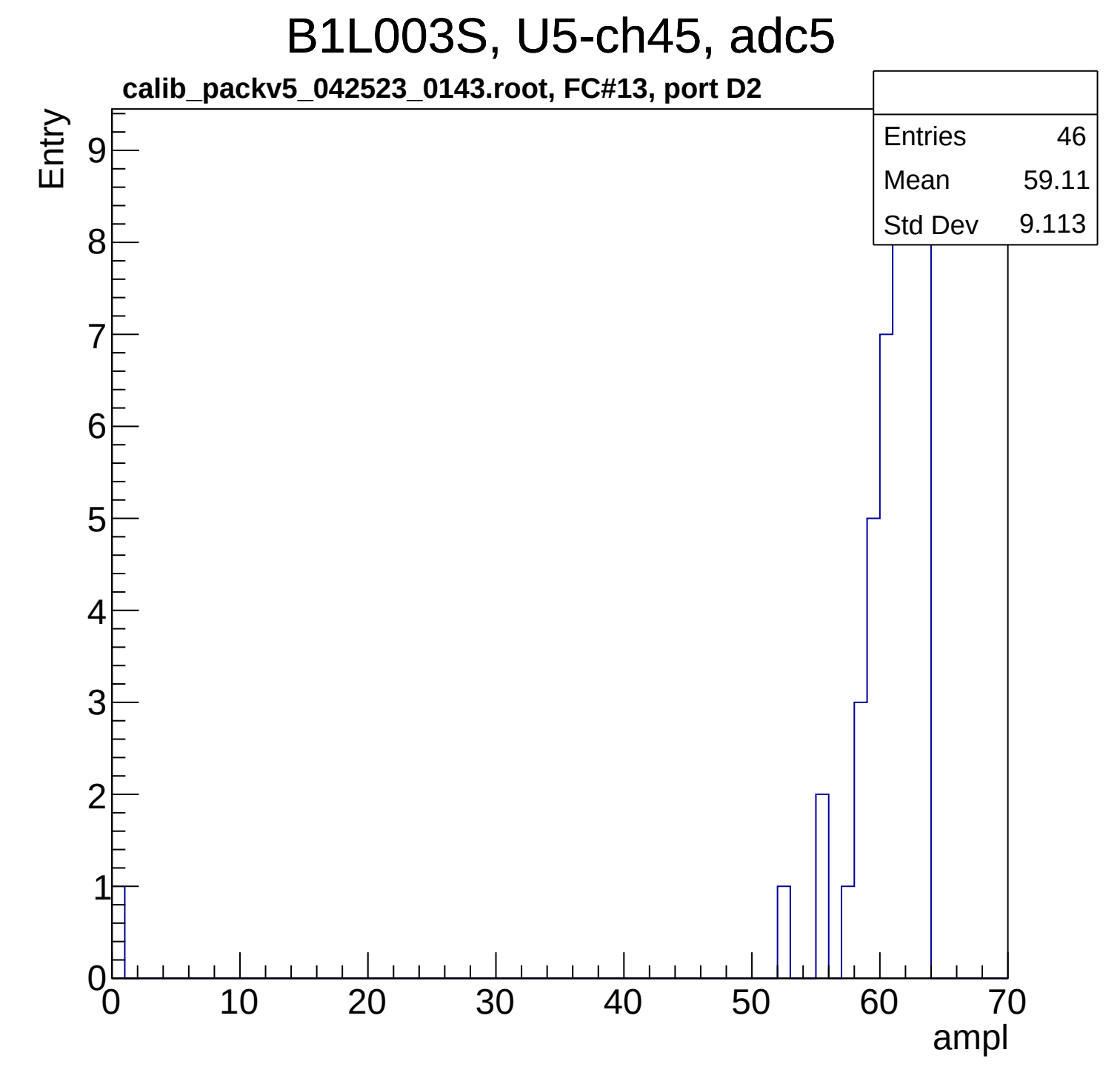
B1L003S, U5-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.11
Std Dev 9.113
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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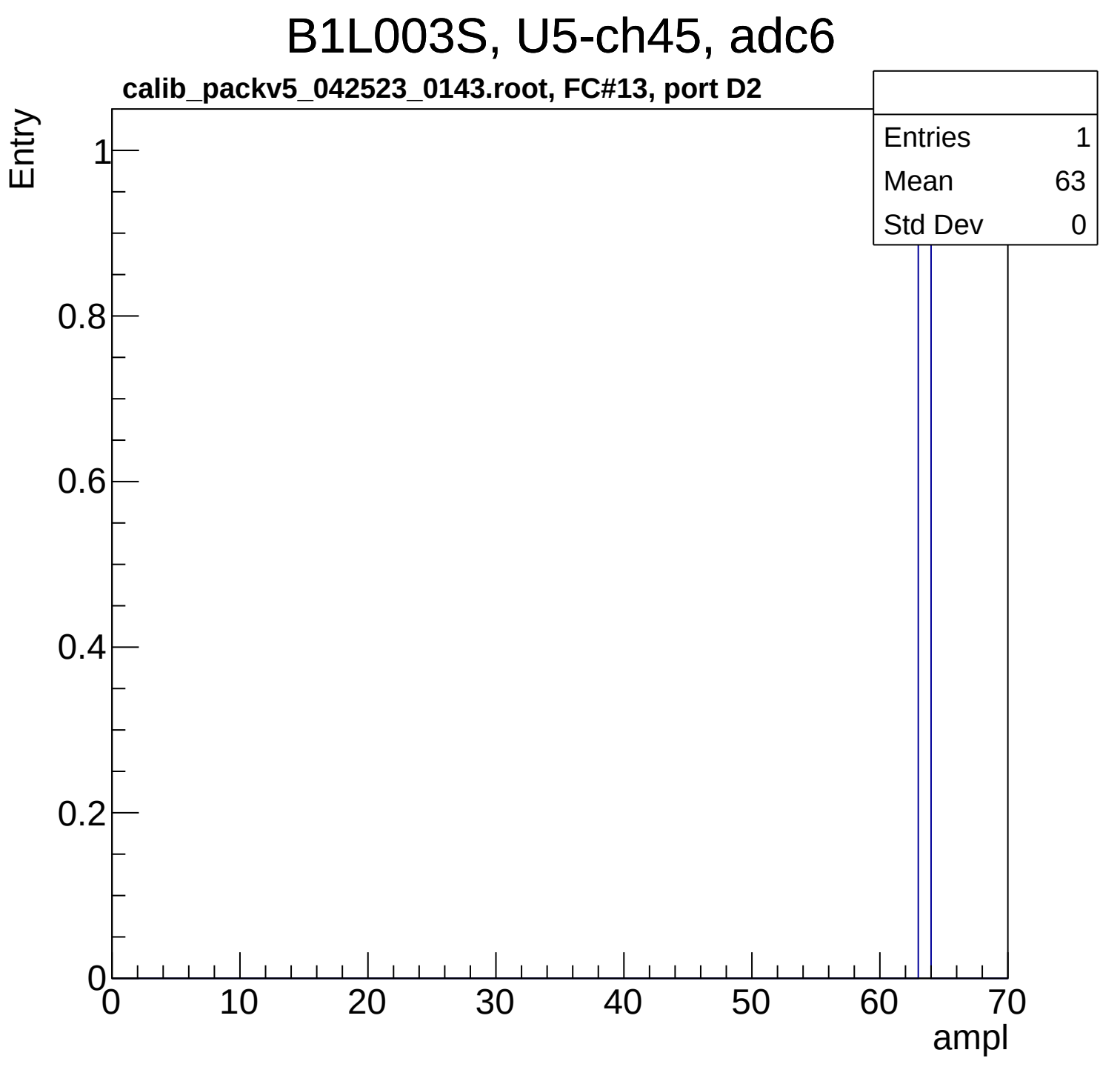
B1L003S, U5-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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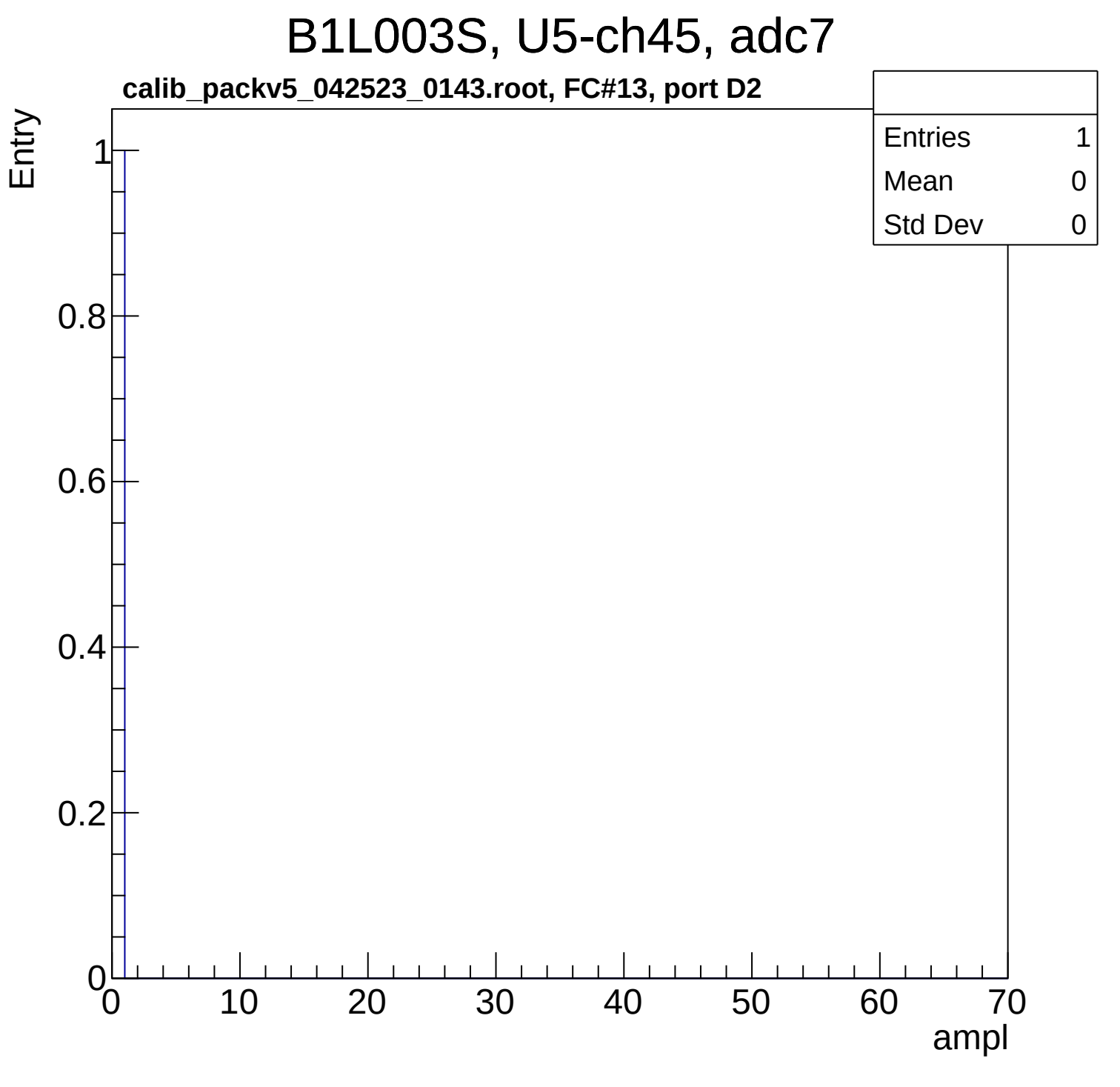
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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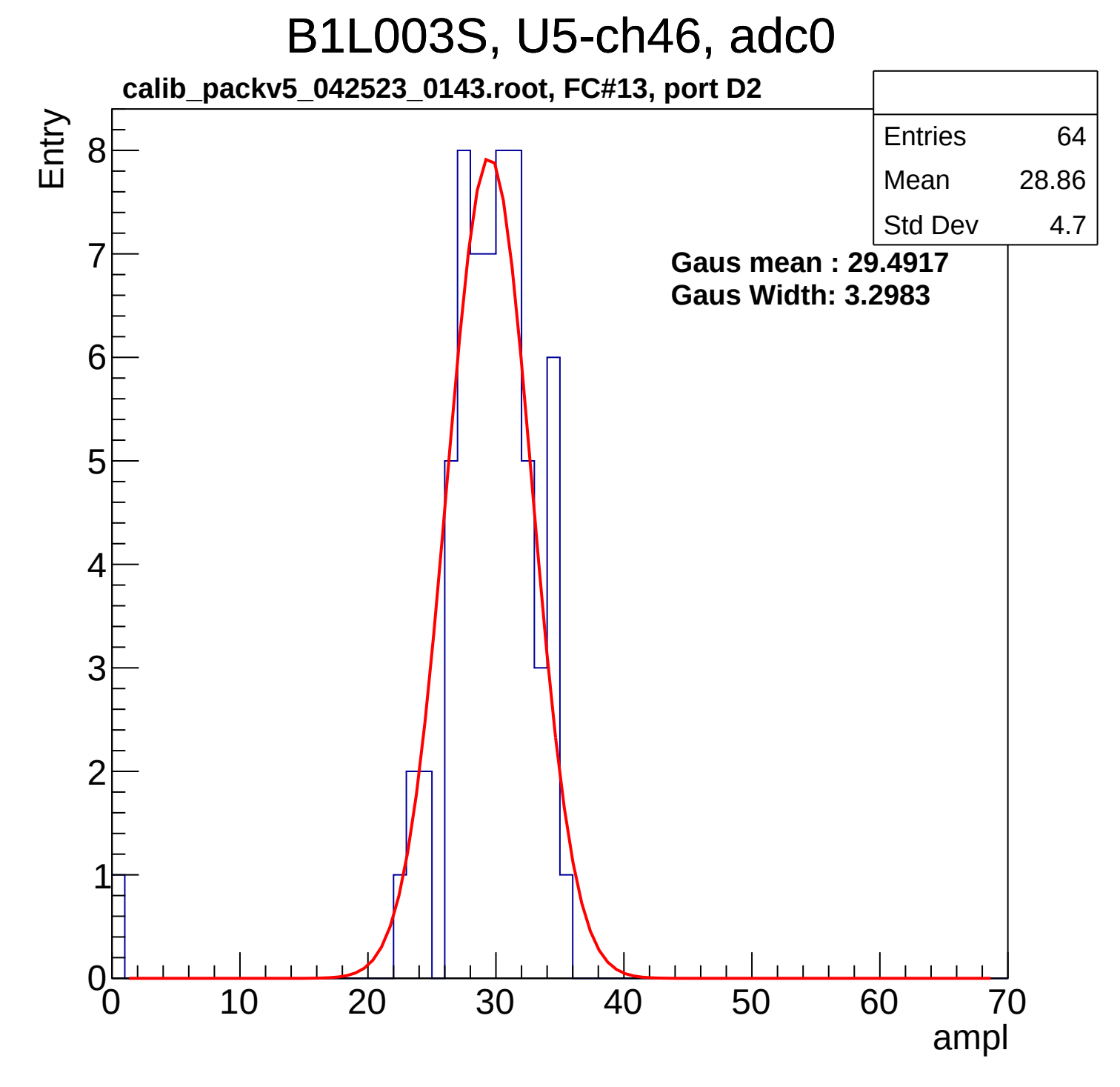
50
60
70
ampl


B1L003S, U5-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.86
Std Dev 4.7
Gaus mean : 29.4917
Gaus Width: 3.2983
Entry
ampl
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2
3
4
5
6
7
8
0
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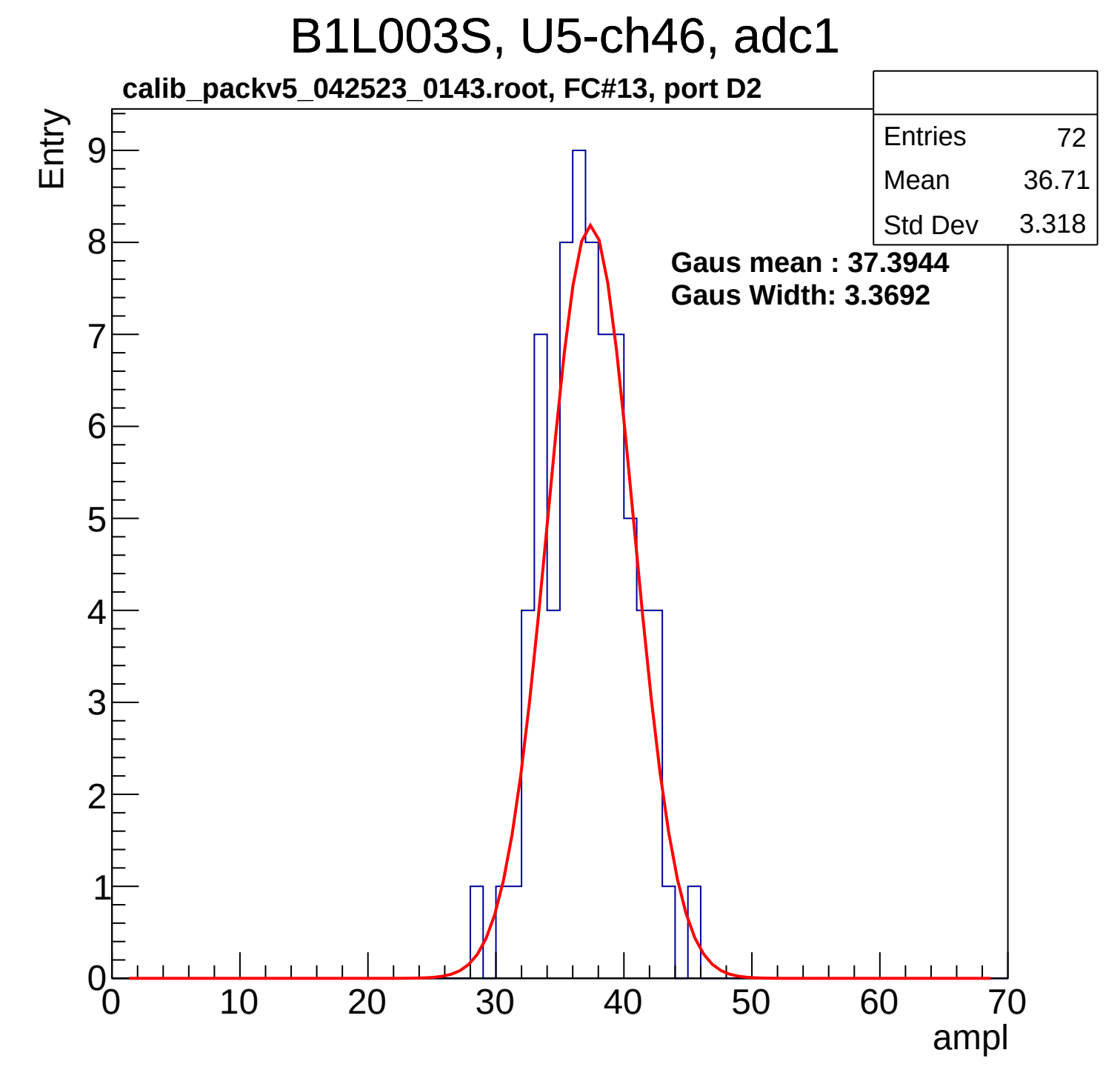
B1L003S, U5-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.71
Std Dev 3.318
Gaus mean : 37.3944
Gaus Width: 3.3692
Entry
ampl
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3
4
5
6
7
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0
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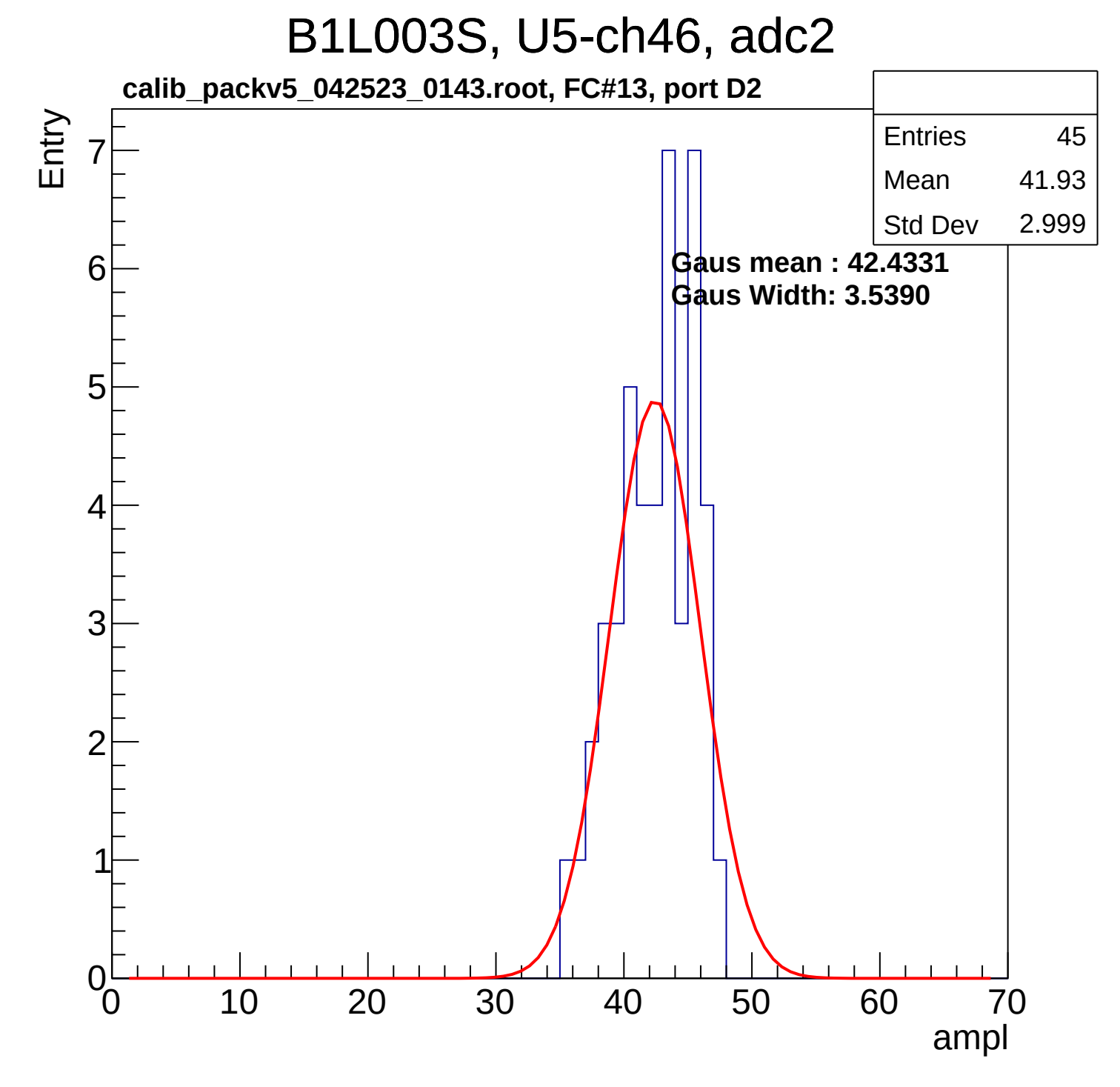
B1L003S, U5-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 41.93
Std Dev 2.999
Gaus mean : 42.4331
Gaus Width: 3.5390
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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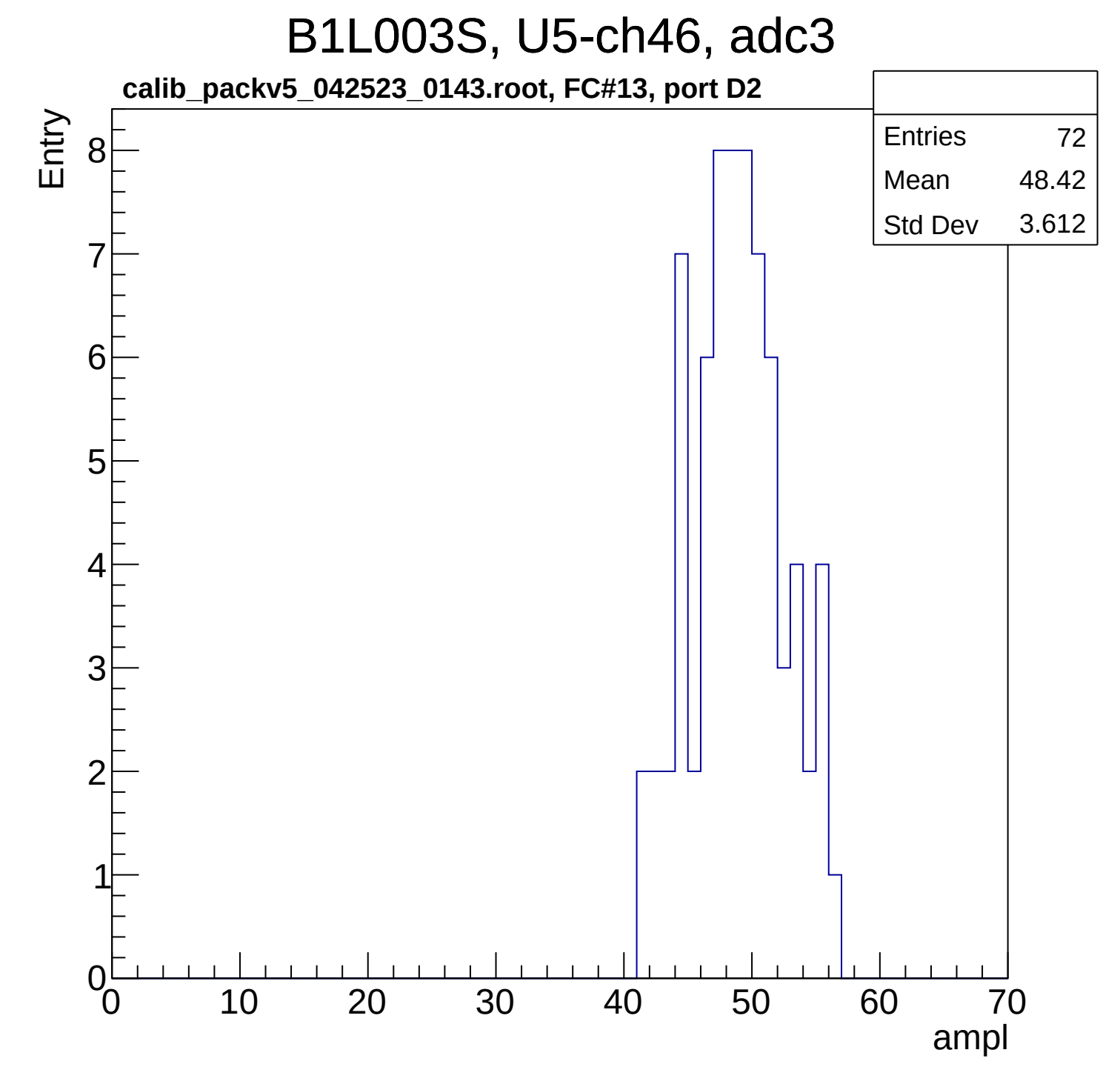
B1L003S, U5-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.42
Std Dev 3.612
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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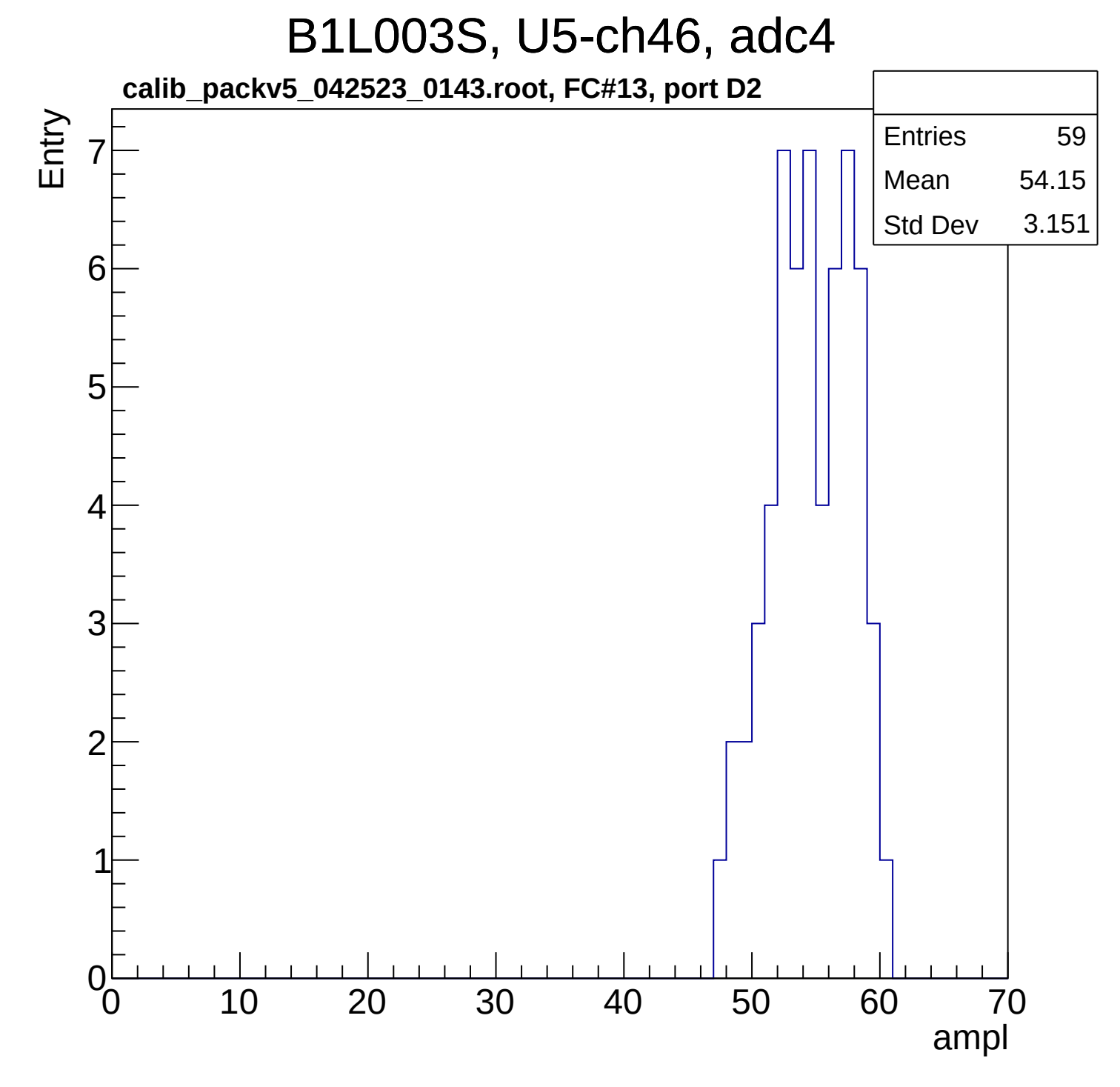
B1L003S, U5-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.15
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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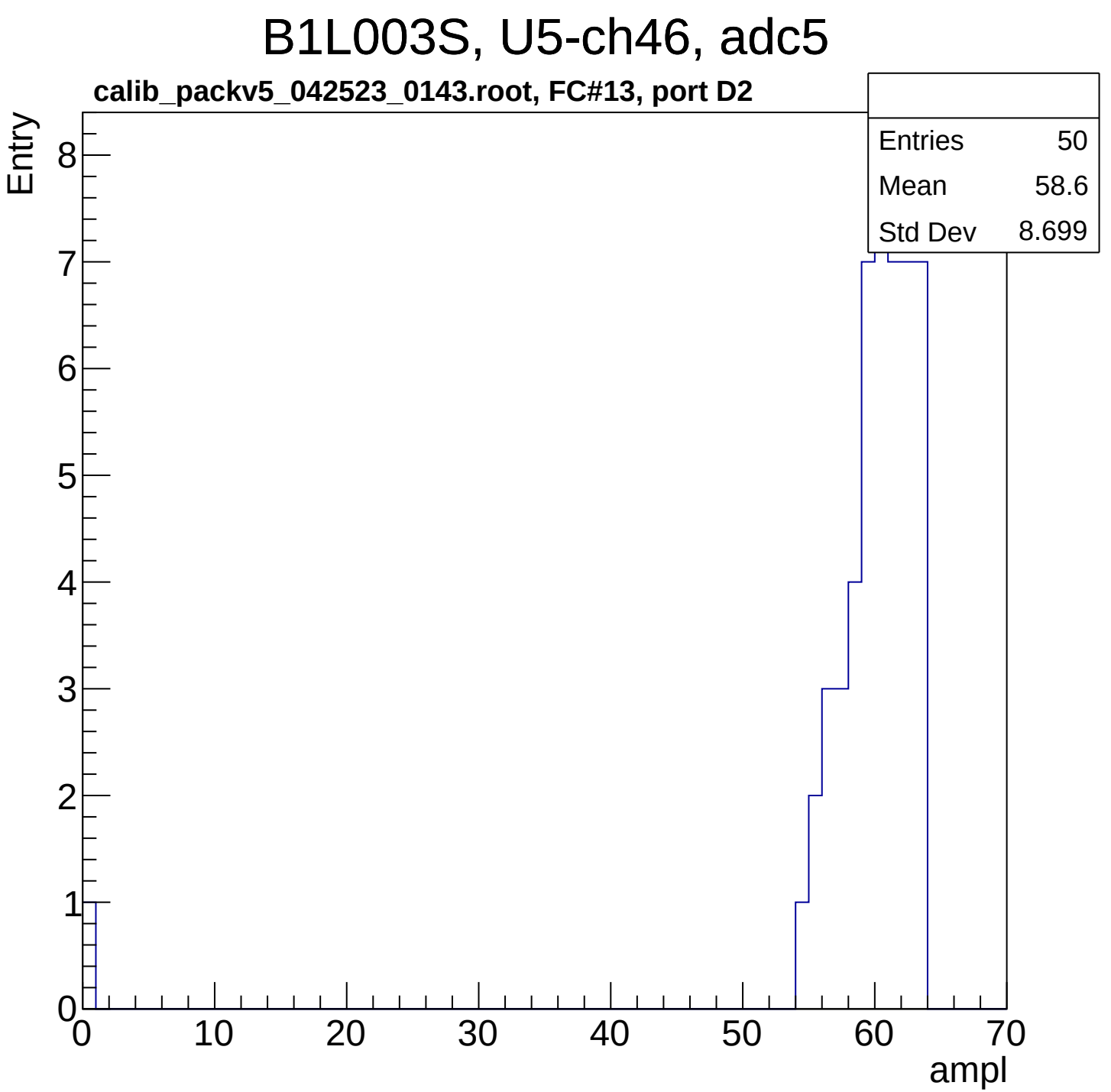
40
50
60
70


B1L003S, U5-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.6
Std Dev 8.699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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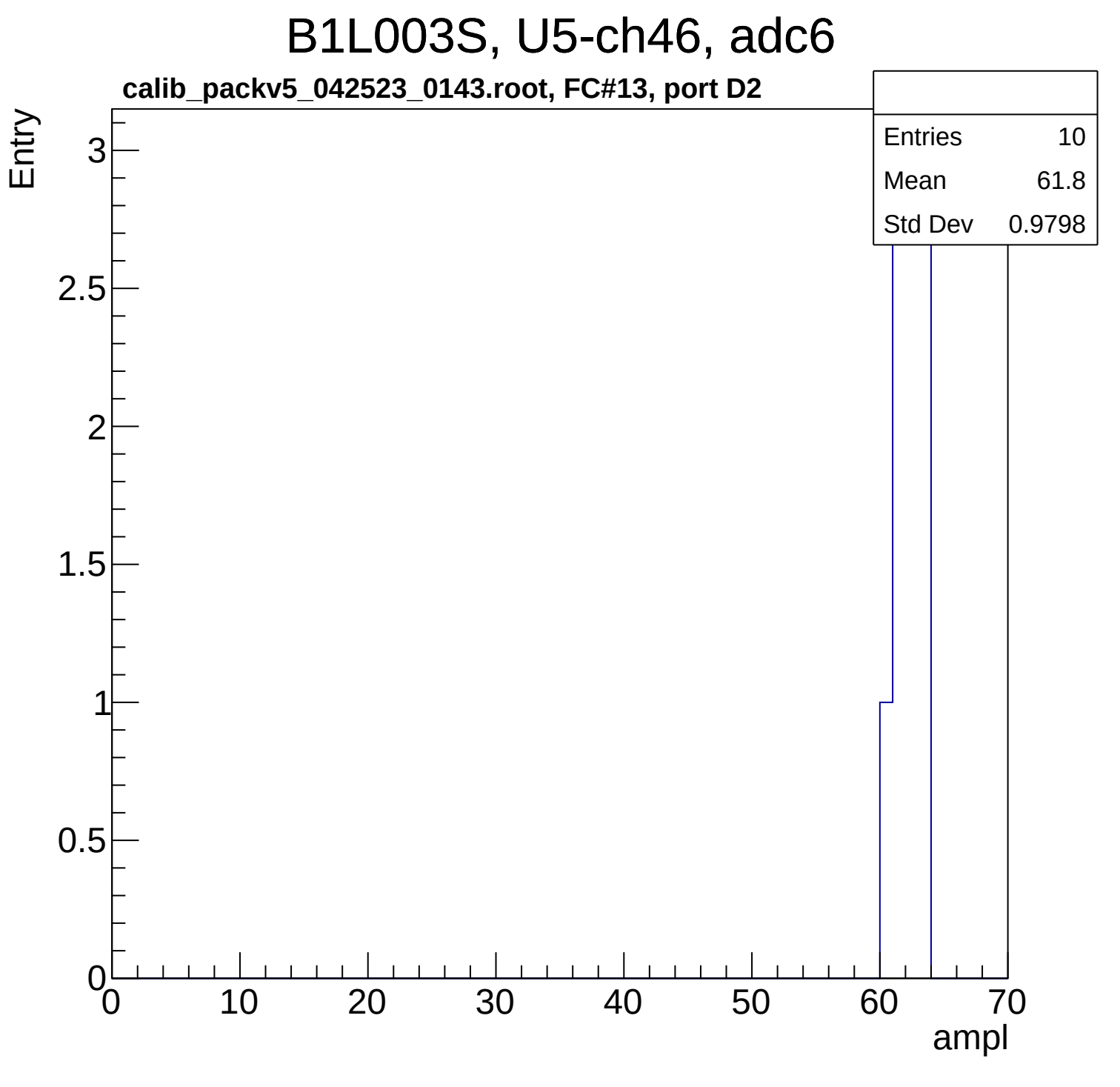
B1L003S, U5-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 0.9798
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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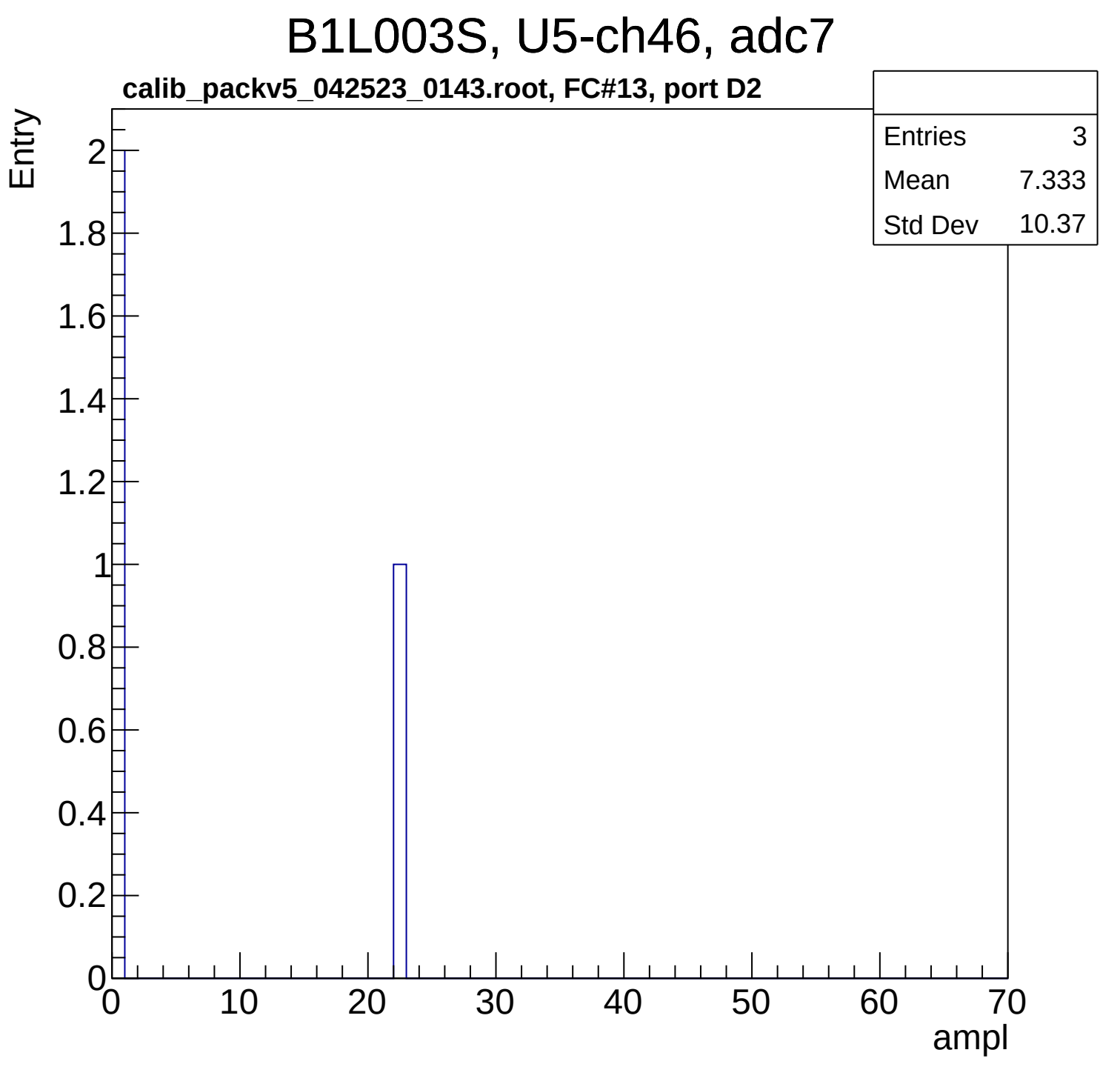
B1L003S, U5-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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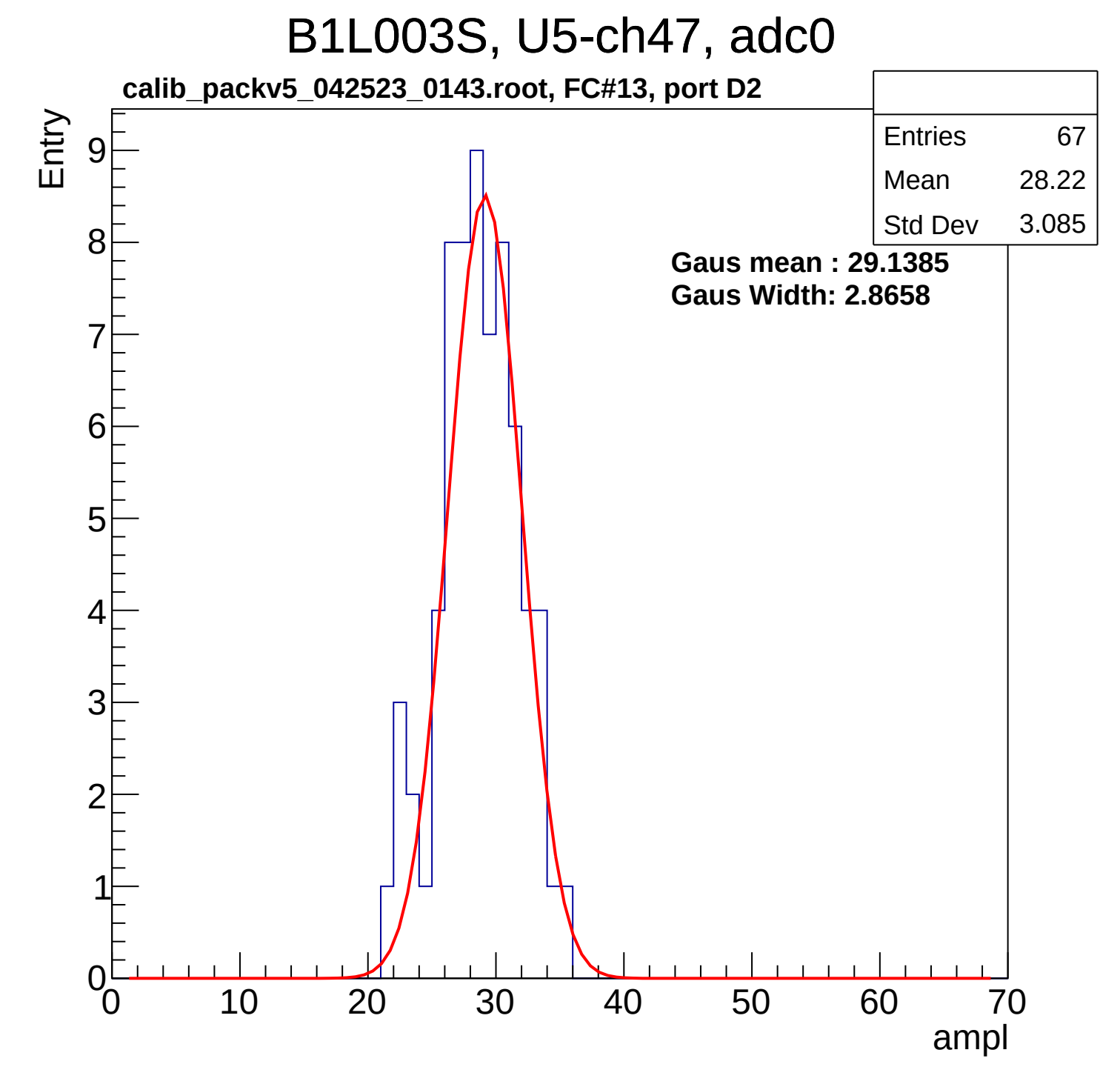
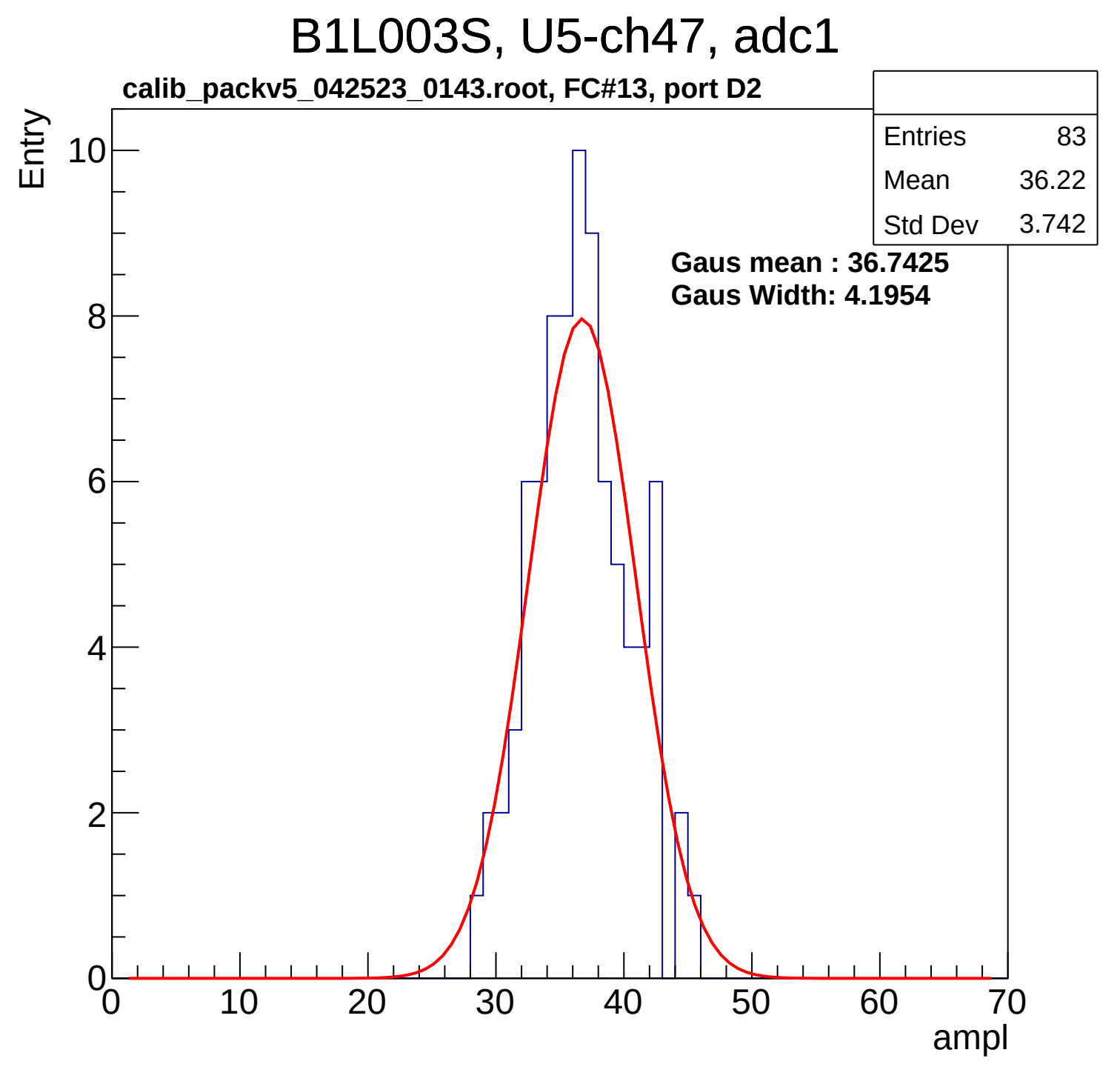
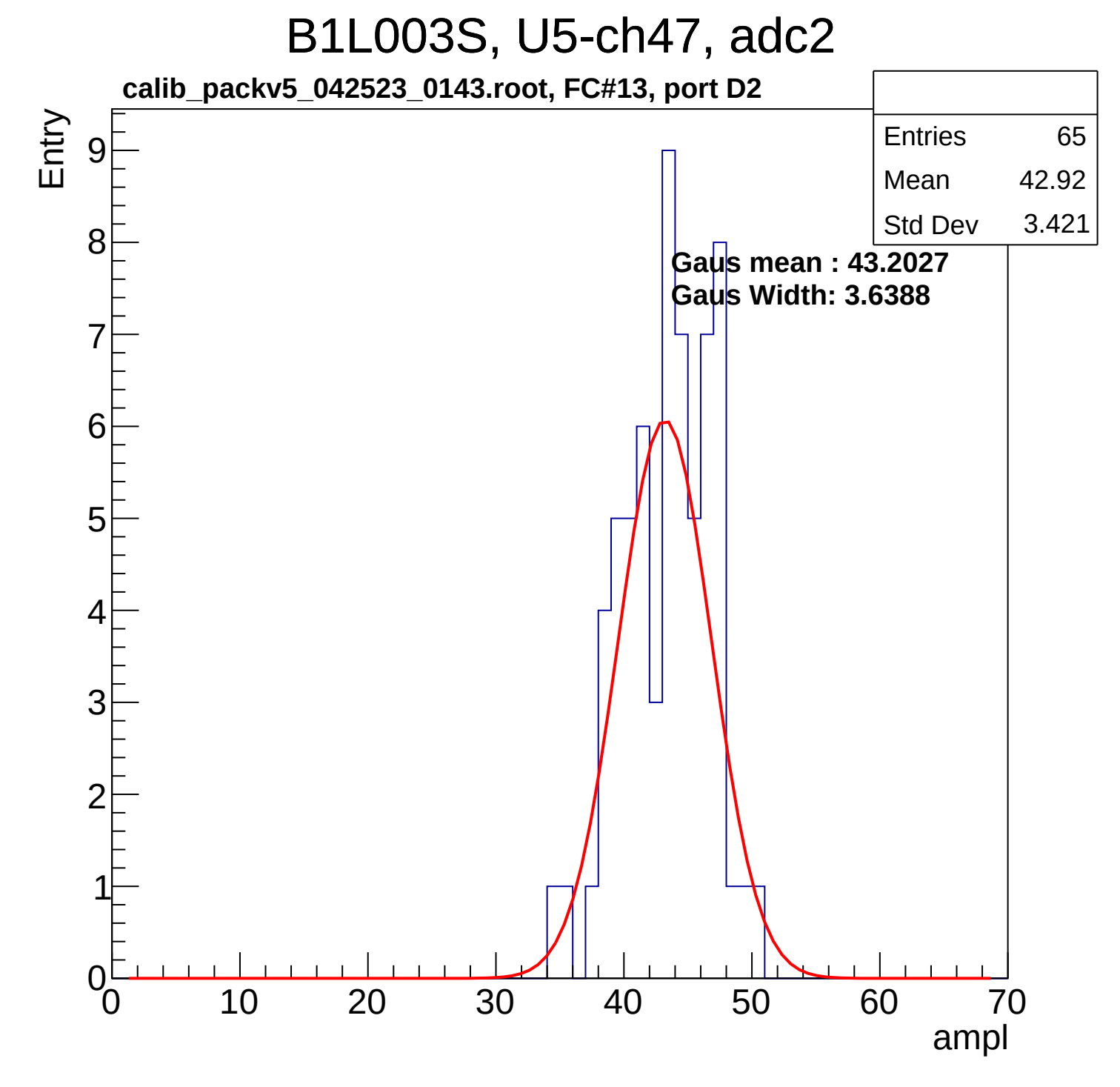

B1L003S, U5-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.22
Std Dev 3.085
Gaus mean : 29.1385
Gaus Width: 2.8658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 36.22
Std Dev 3.742
Gaus mean : 36.7425
Gaus Width: 4.1954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.92
Std Dev 3.421
Gaus mean : 43.2027
Gaus Width: 3.6388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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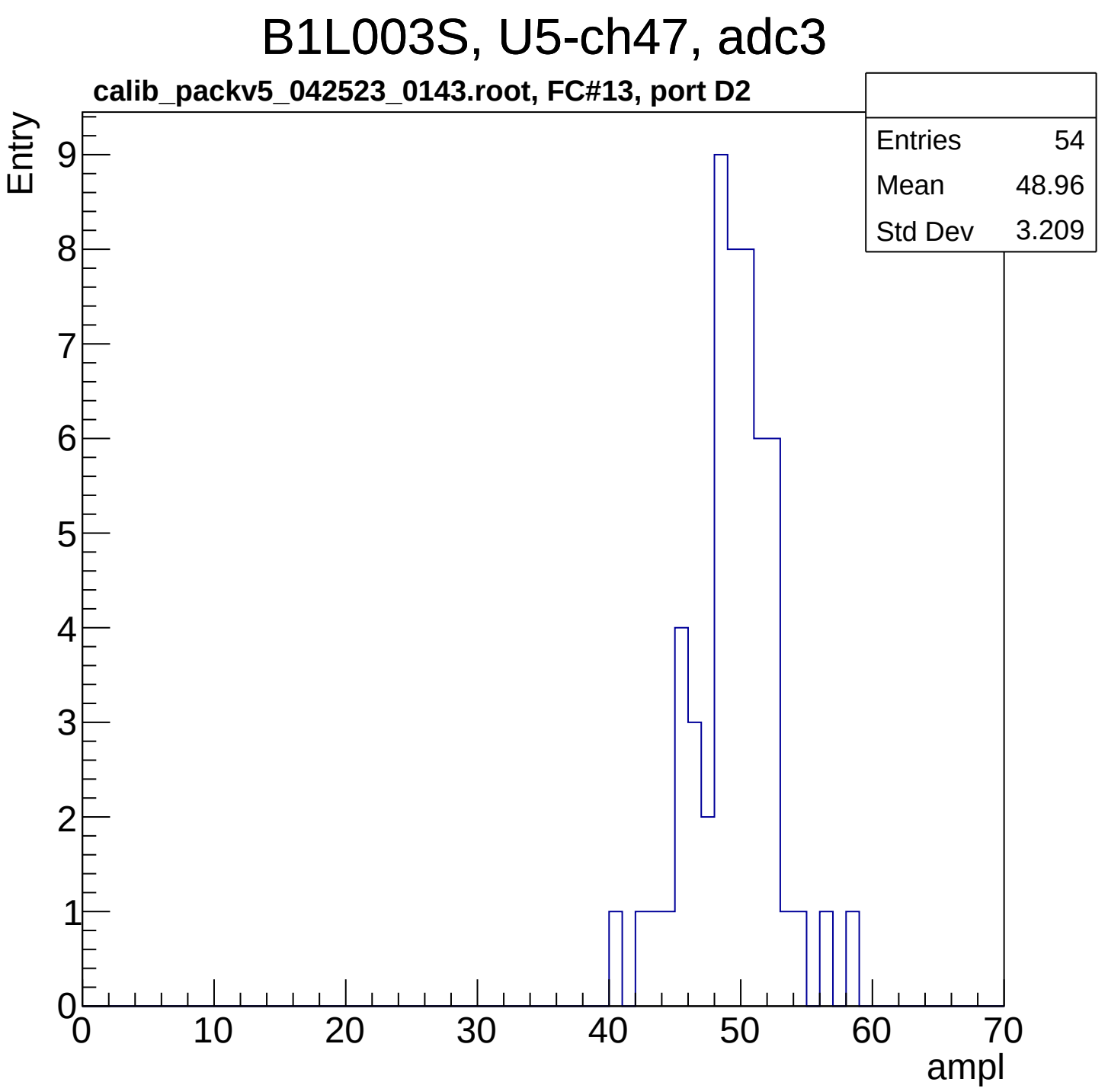
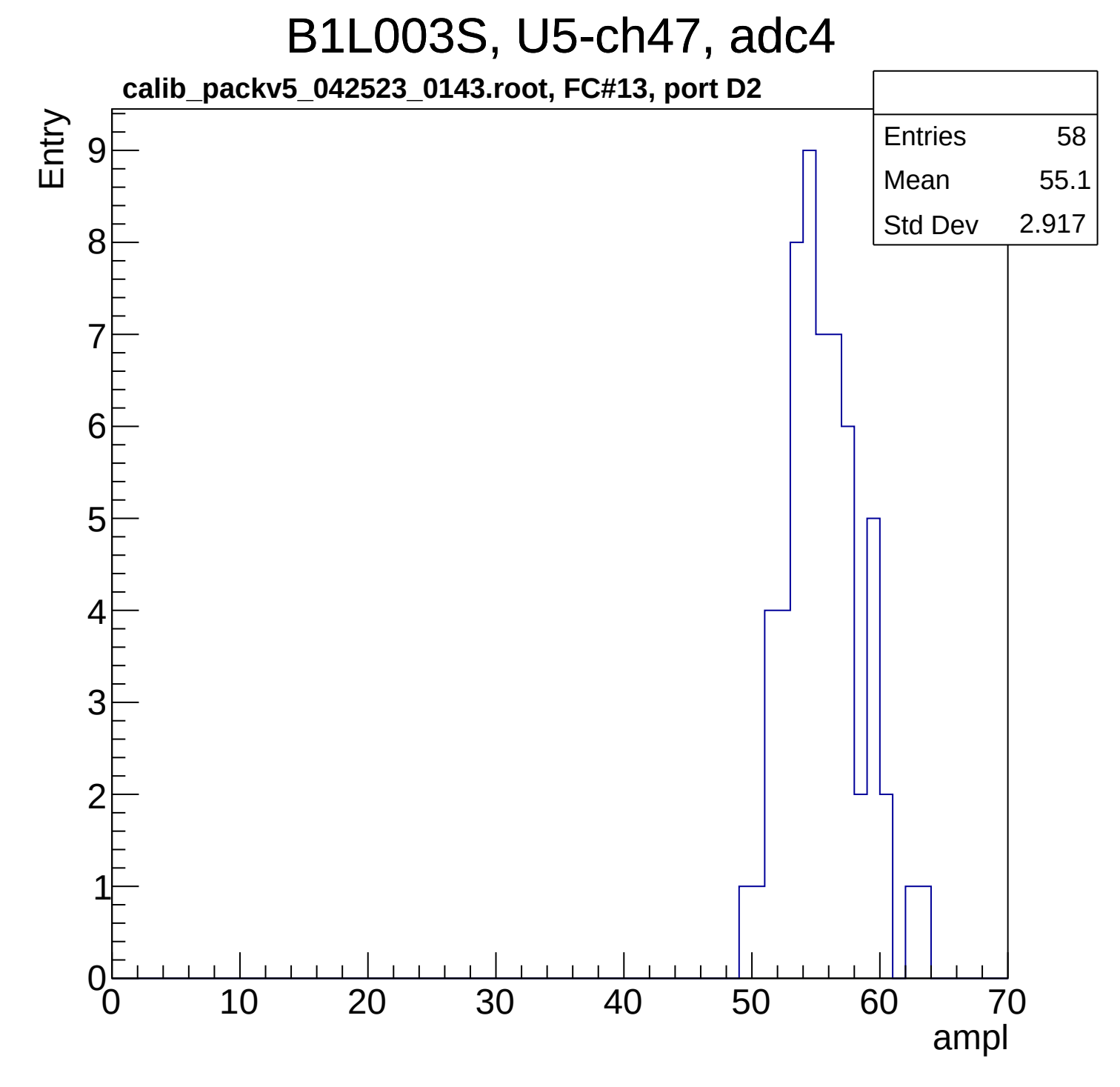
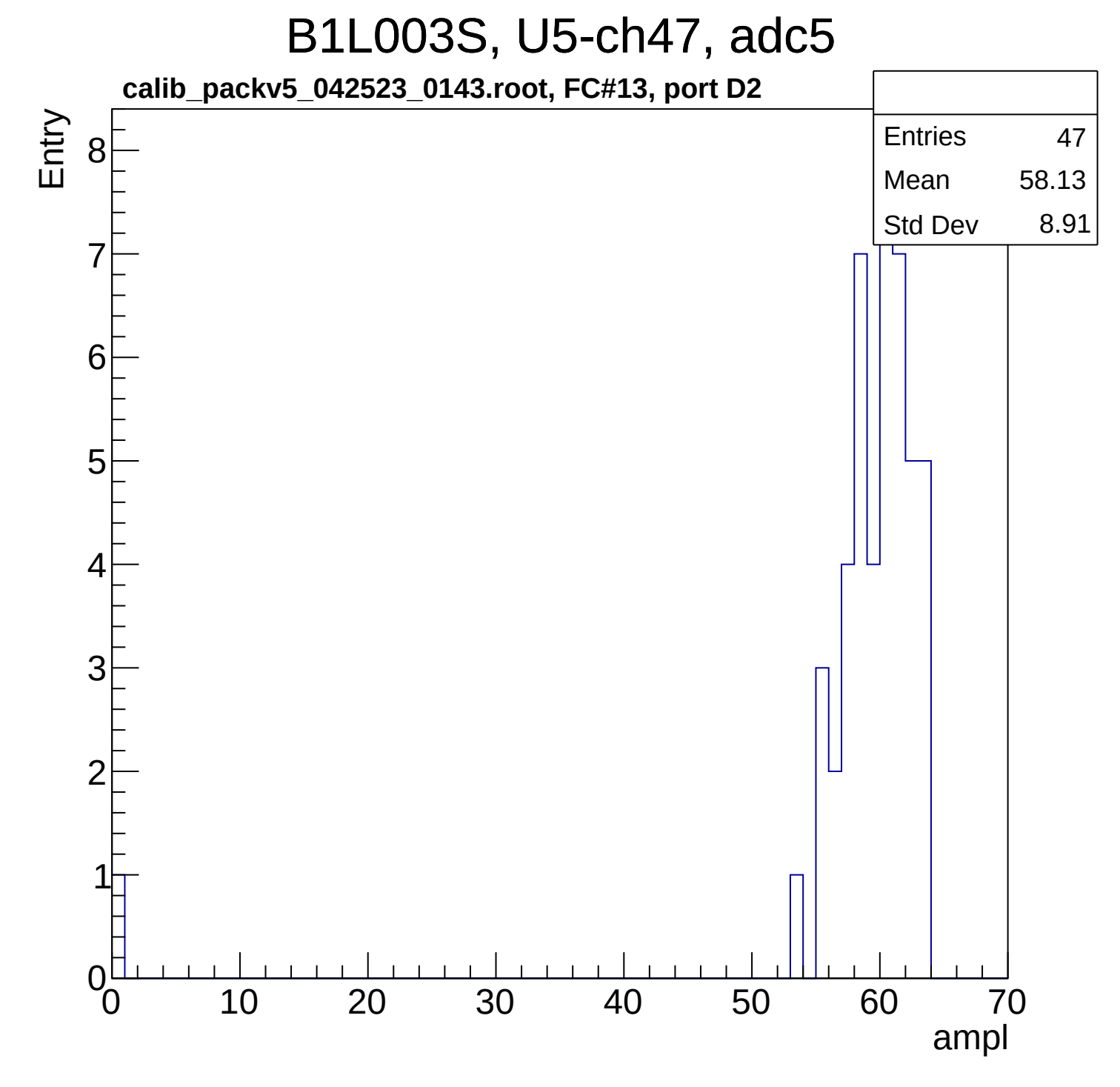
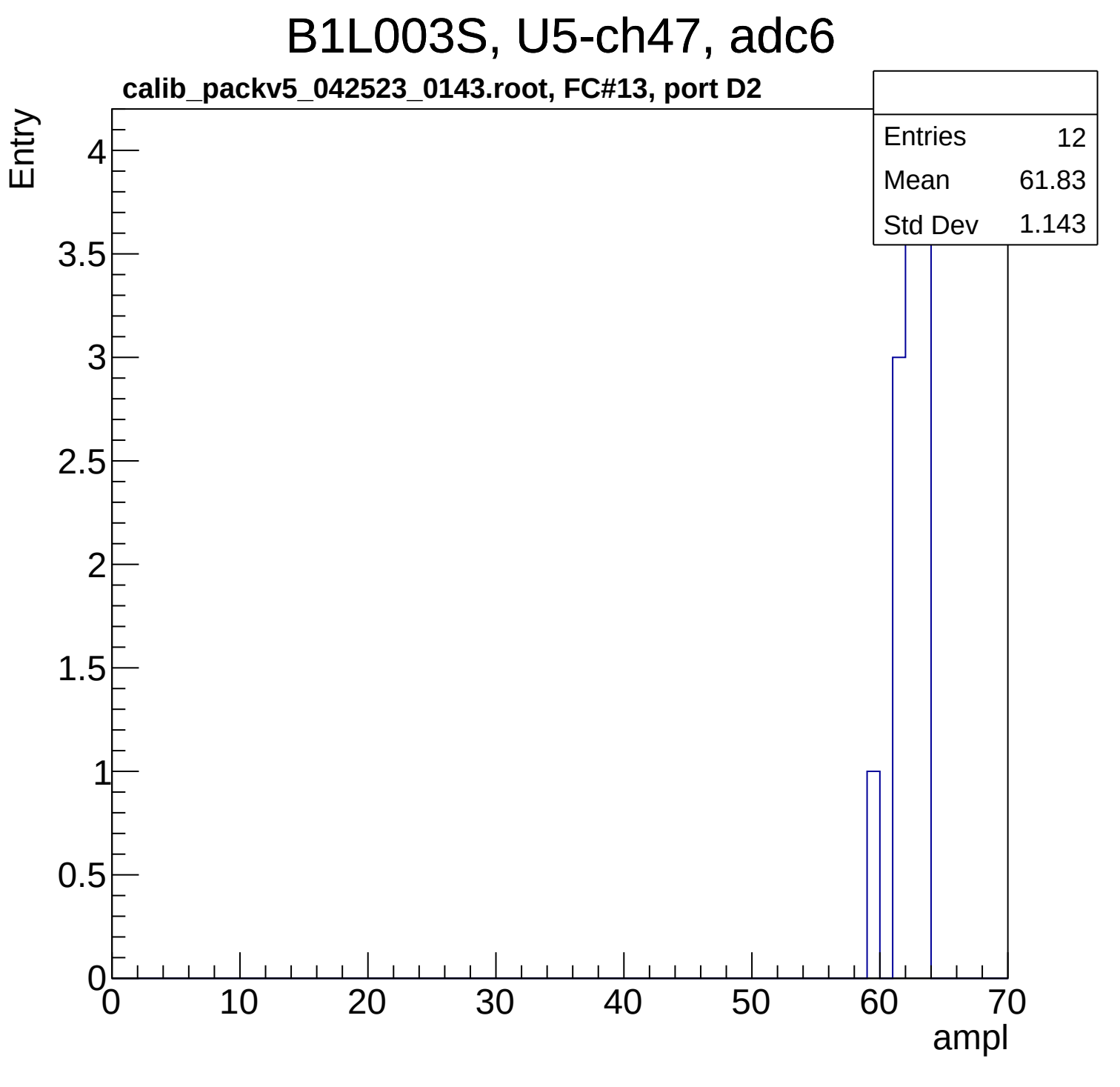

B1L003S, U5-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 48.96
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.1
Std Dev 2.917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.13
Std Dev 8.91
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.83
Std Dev 1.143
Entry
ampl
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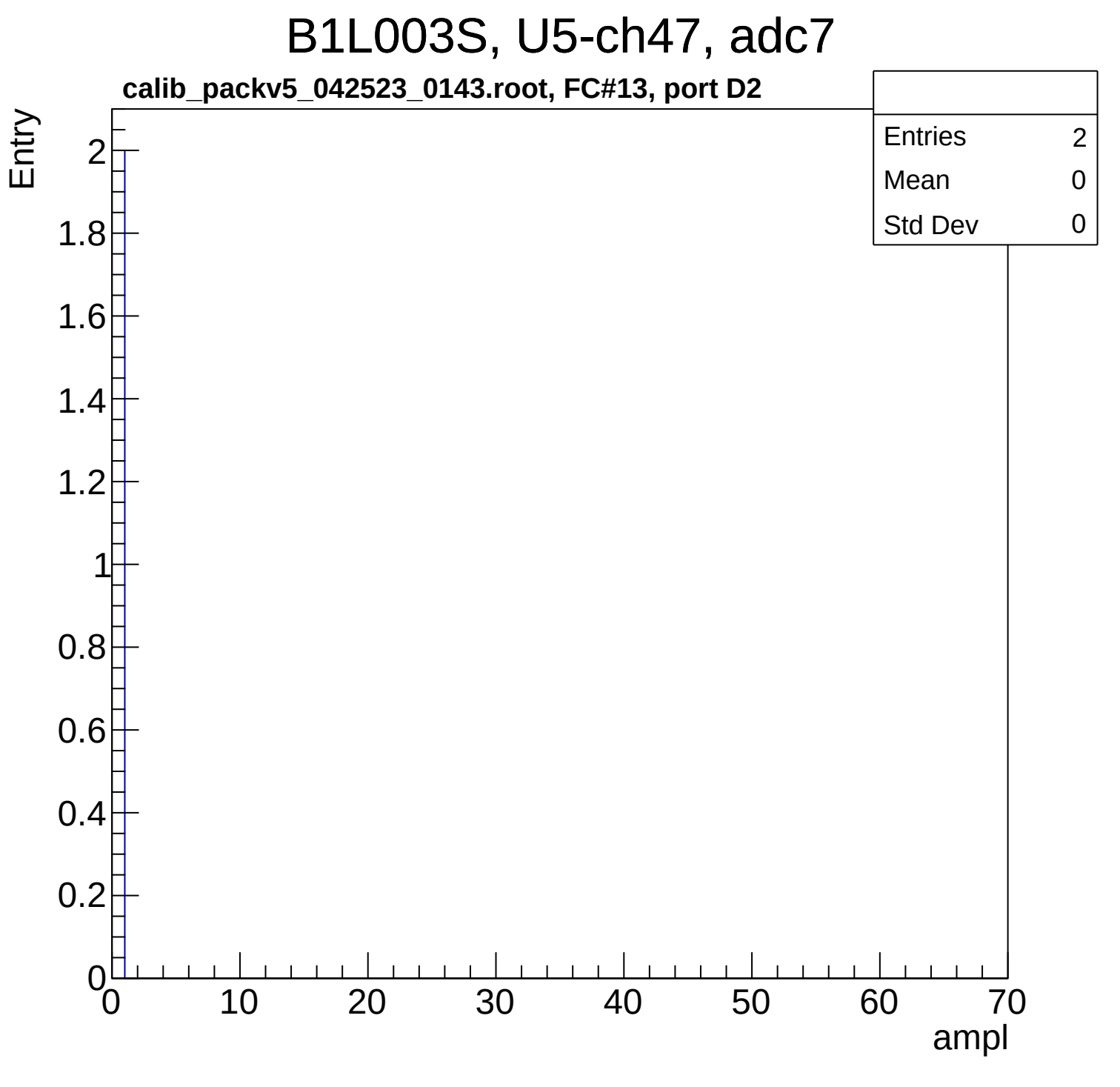
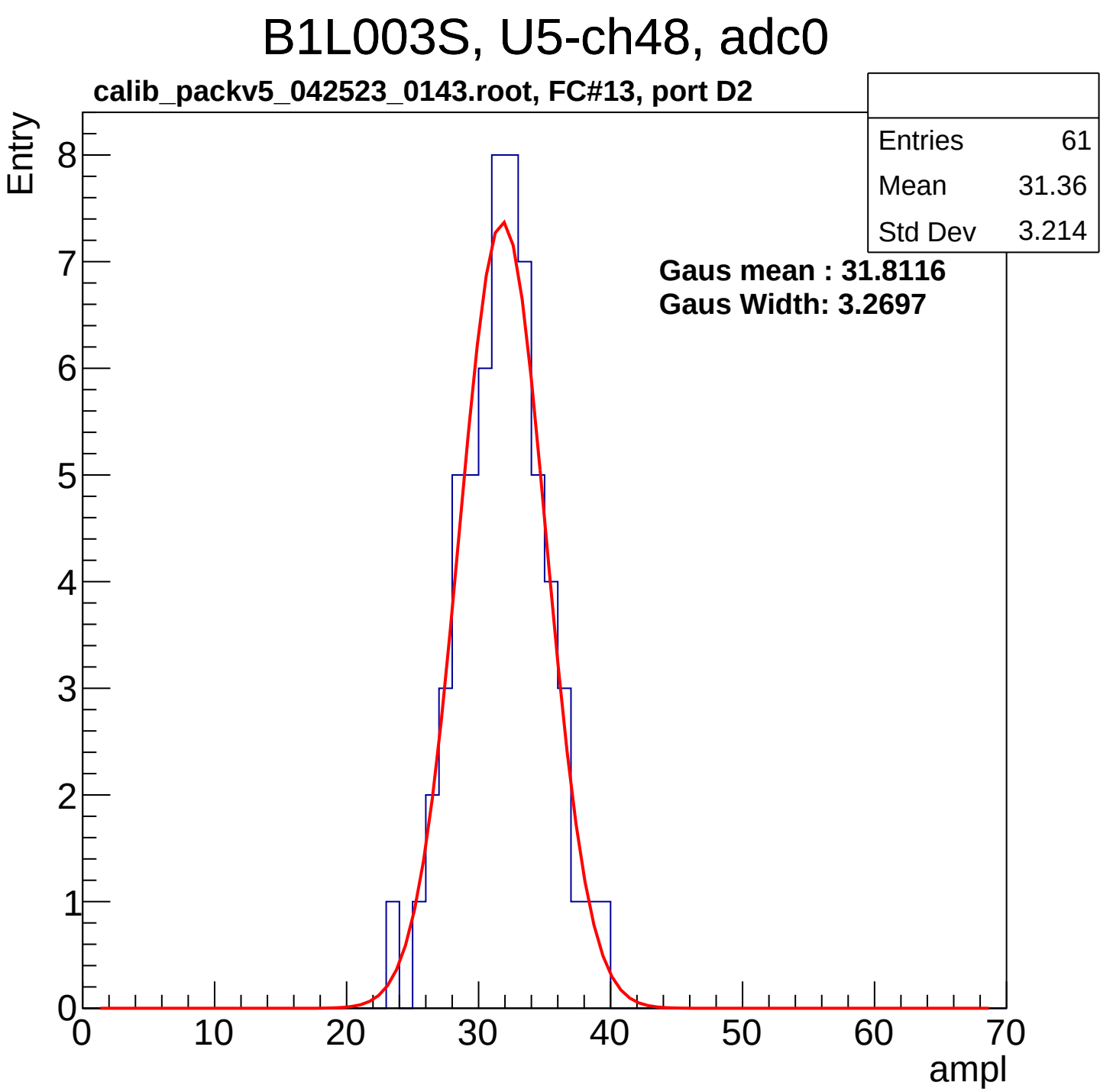
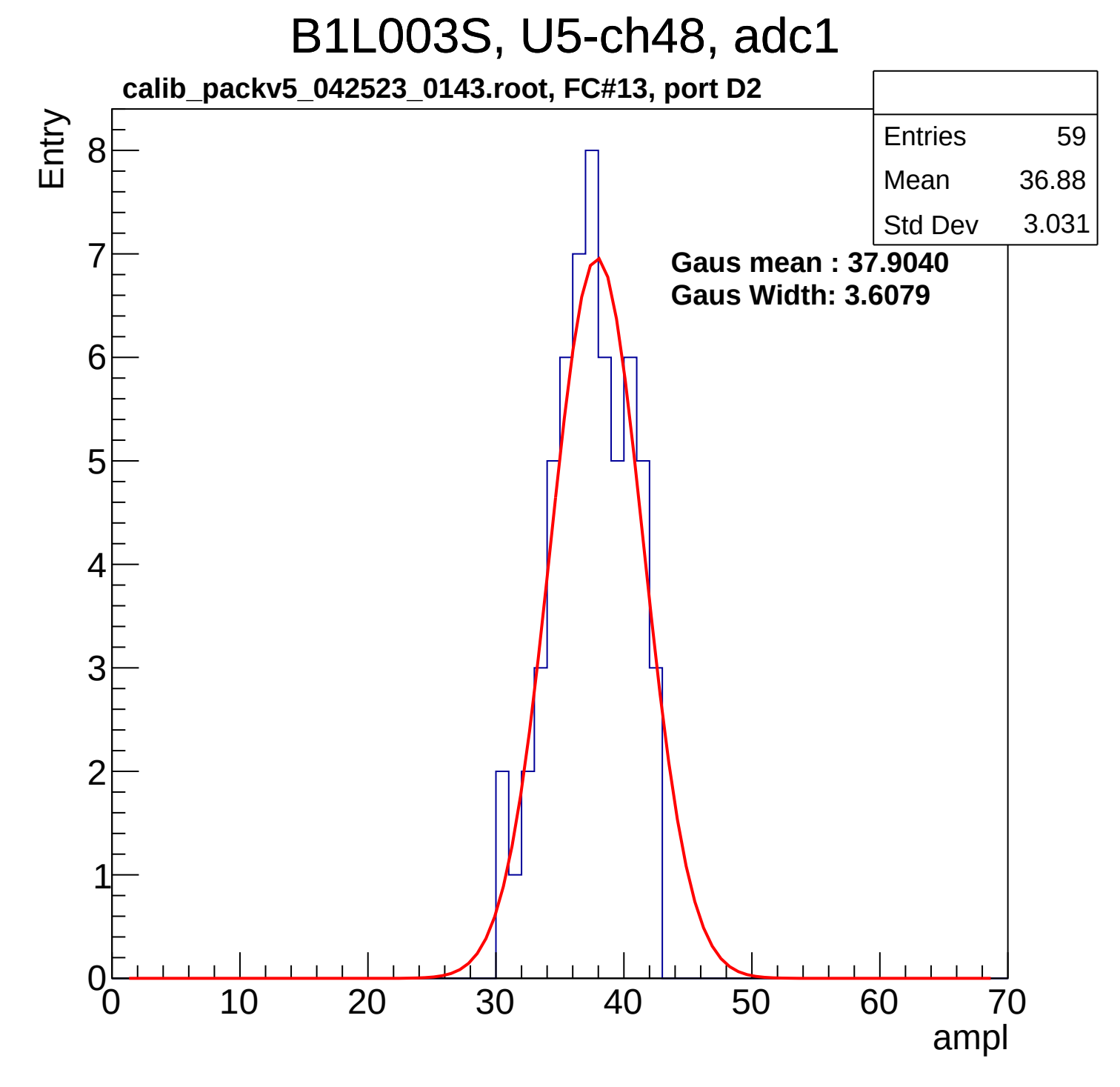
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 31.36
Std Dev 3.214
Gaus mean : 31.8116
Gaus Width: 3.2697
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.88
Std Dev 3.031
Gaus mean : 37.9040
Gaus Width: 3.6079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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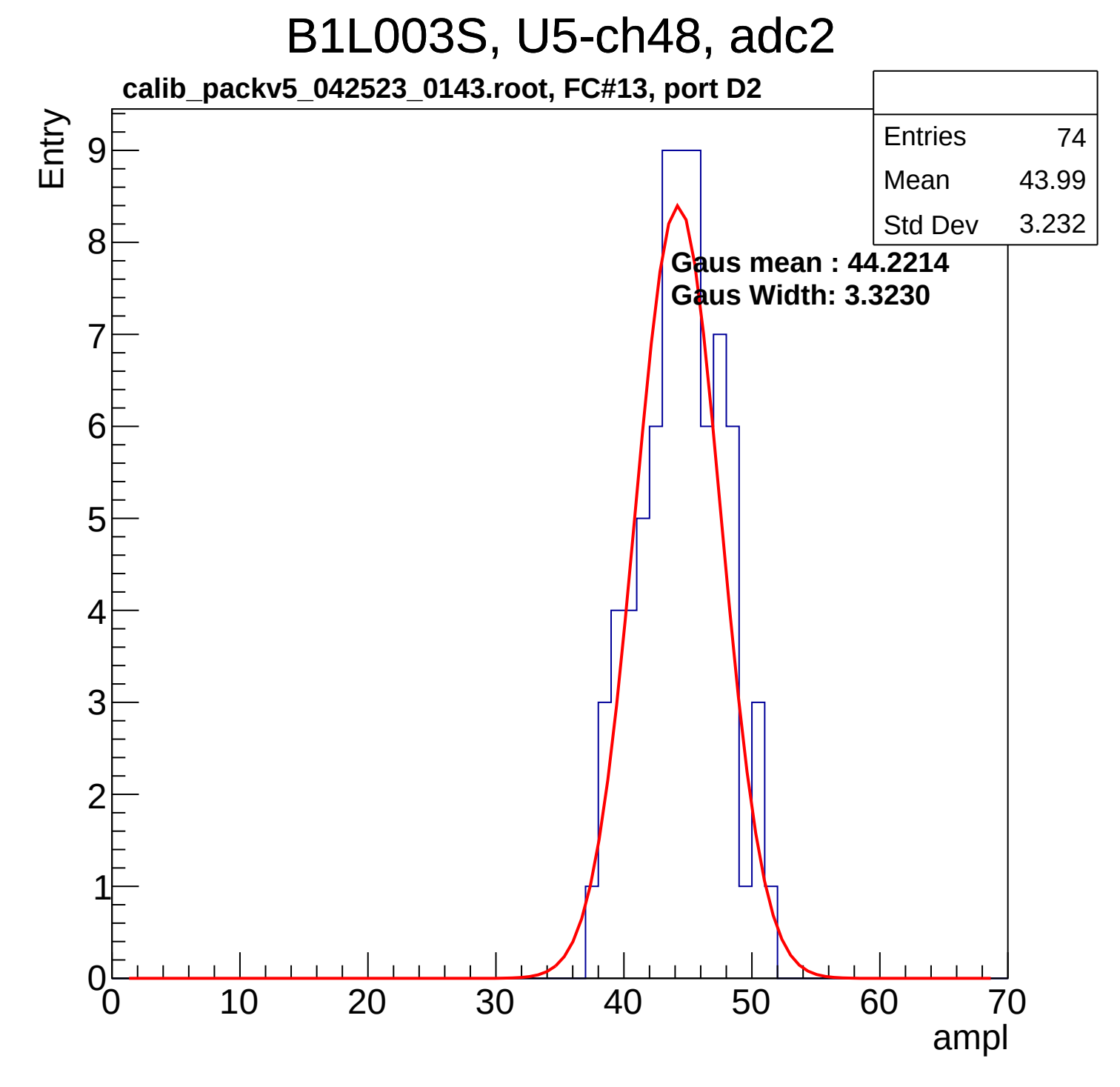
B1L003S, U5-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.99
Std Dev 3.232
Gaus mean : 44.2214
Gaus Width: 3.3230
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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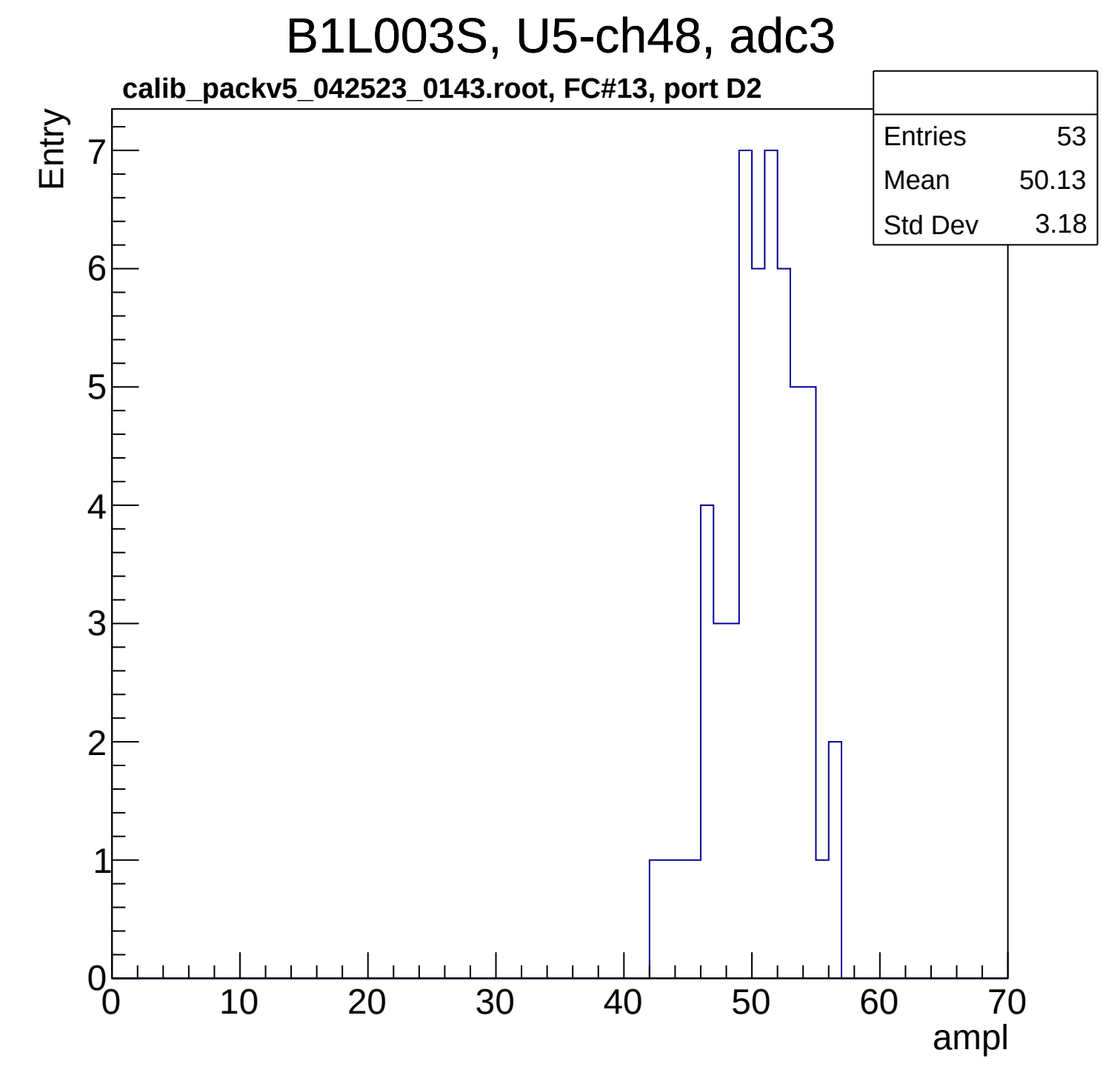
B1L003S, U5-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 50.13
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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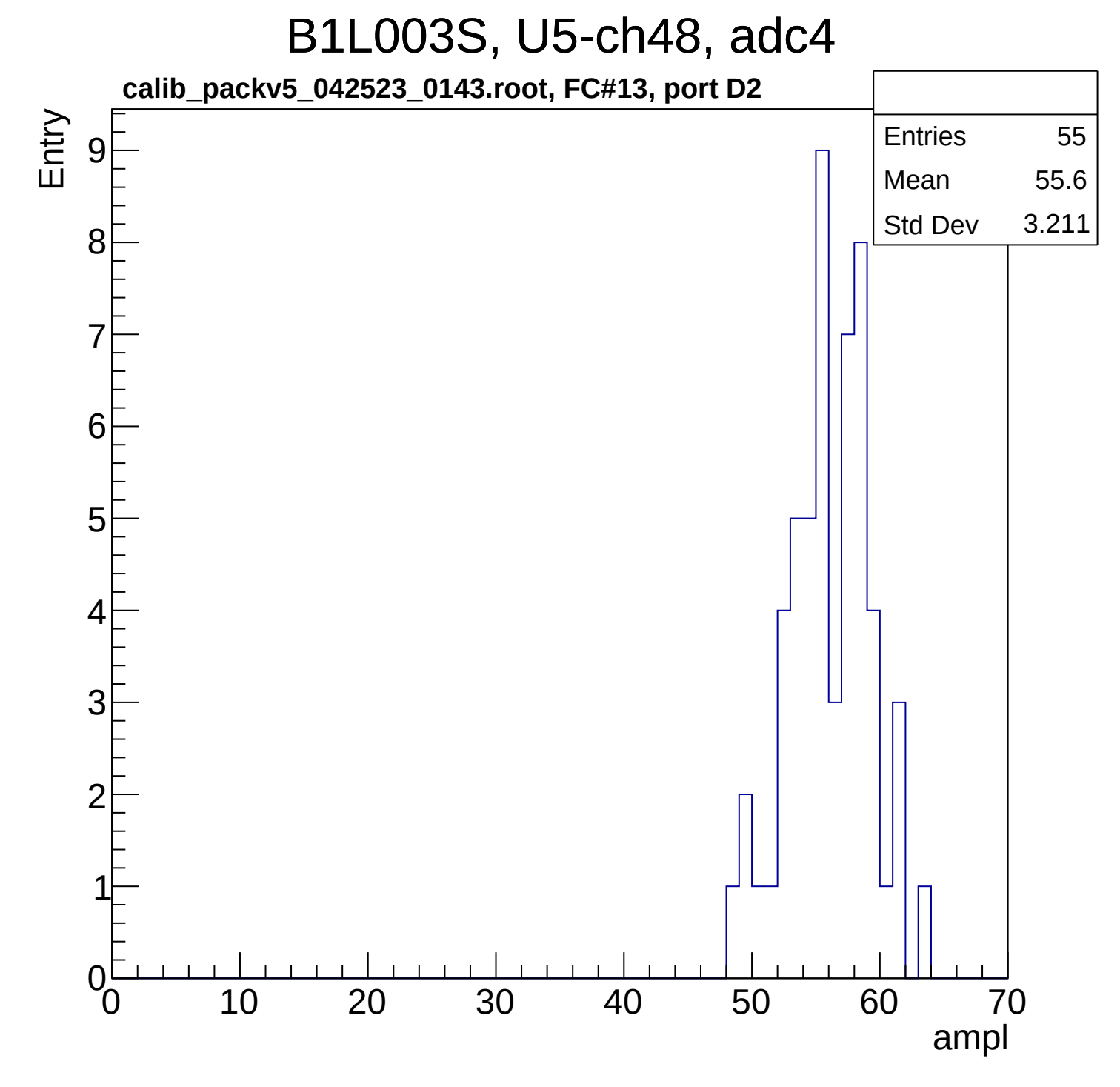
B1L003S, U5-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.6
Std Dev 3.211
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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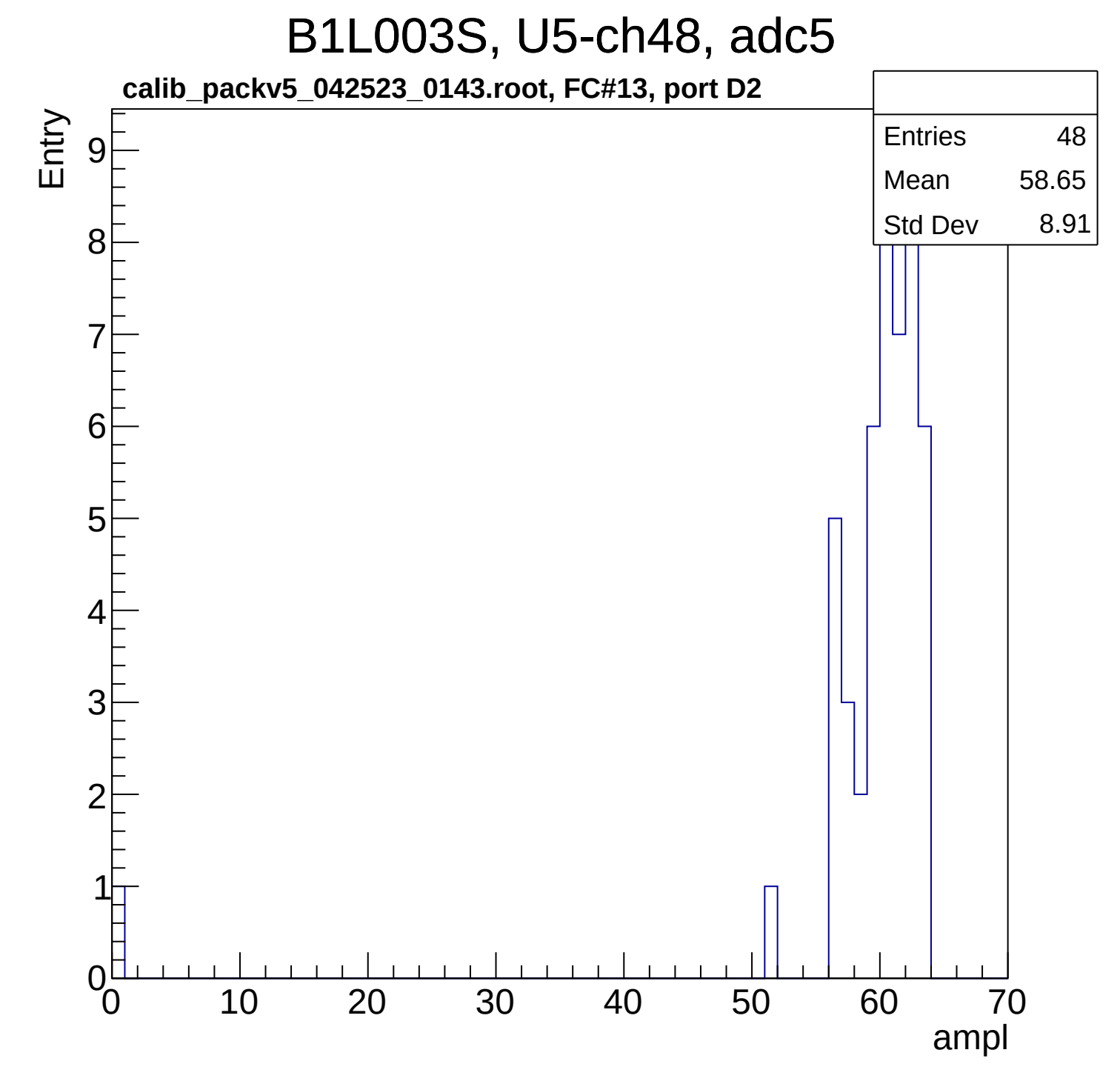
B1L003S, U5-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.65
Std Dev 8.91
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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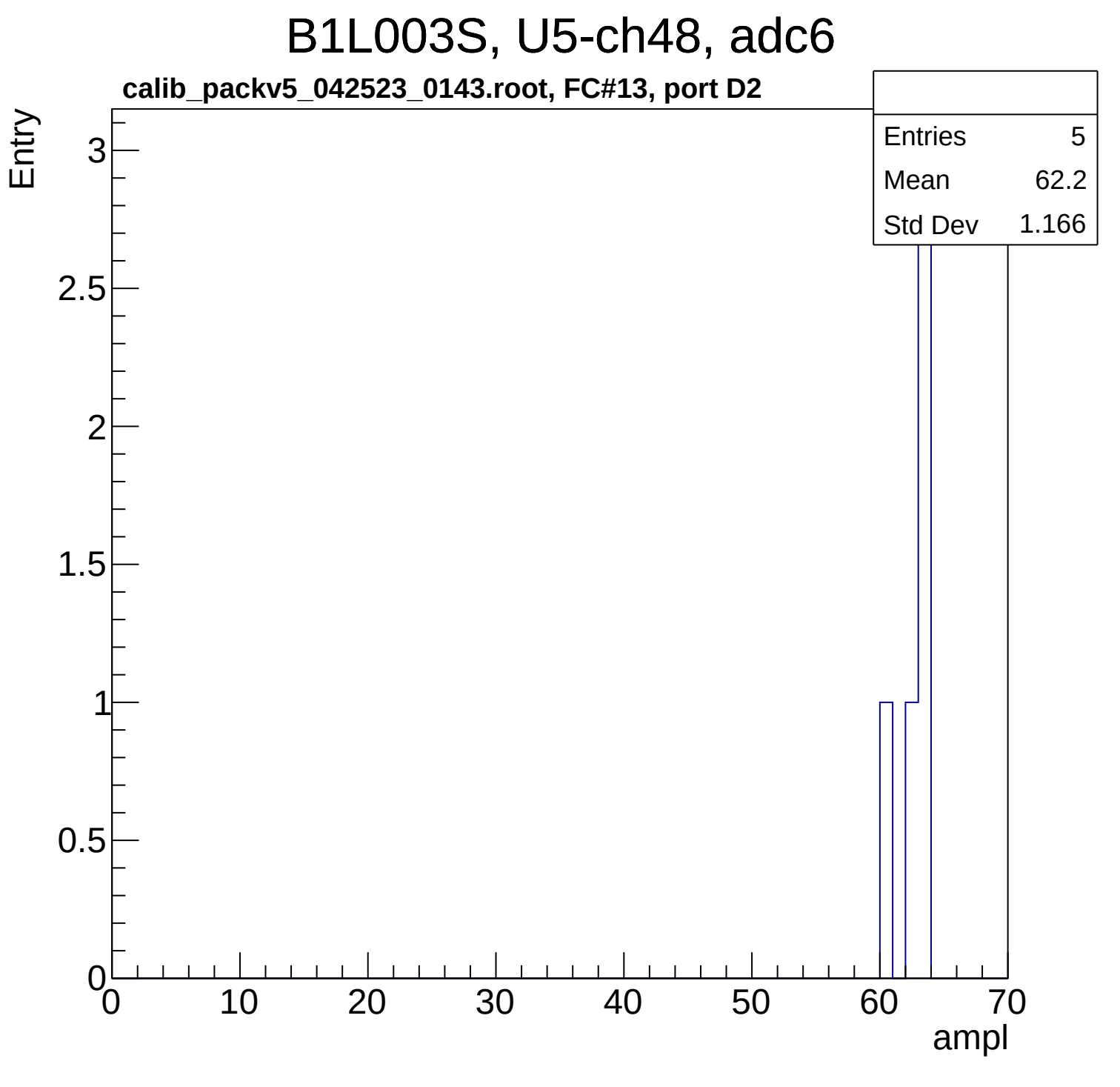
B1L003S, U5-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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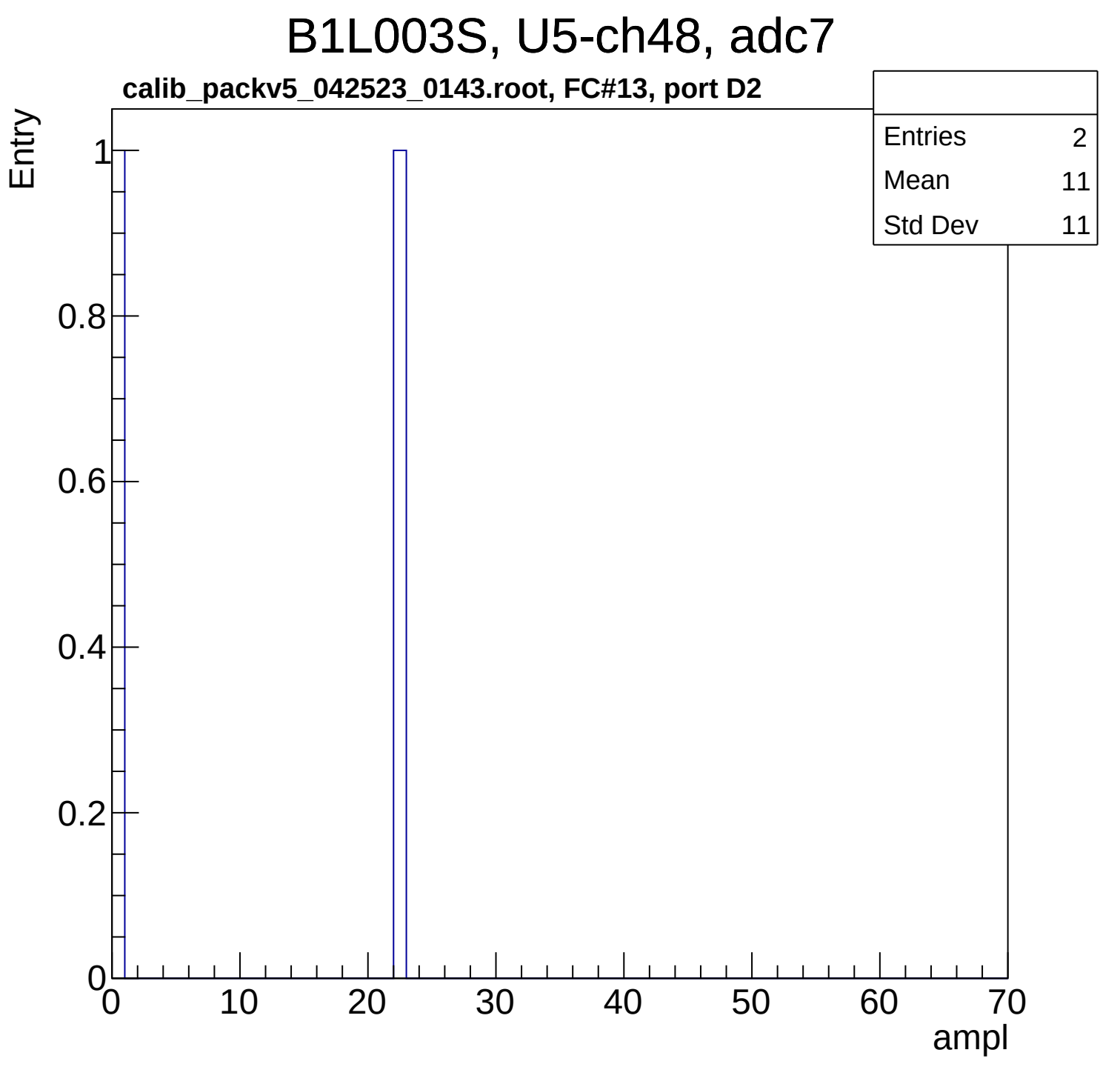
B1L003S, U5-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
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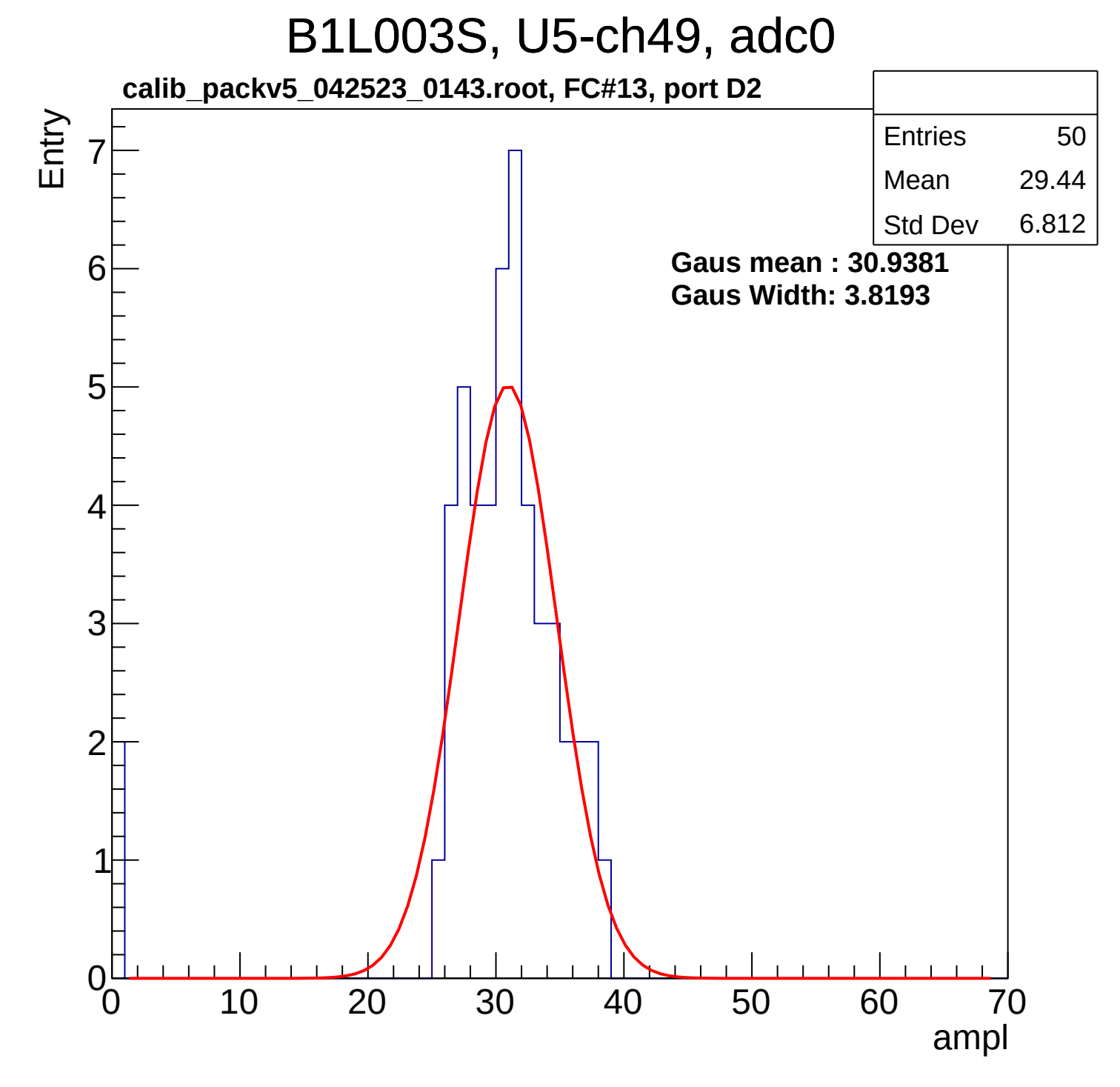
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 29.44
Std Dev 6.812
Gaus mean : 30.9381
Gaus Width: 3.8193
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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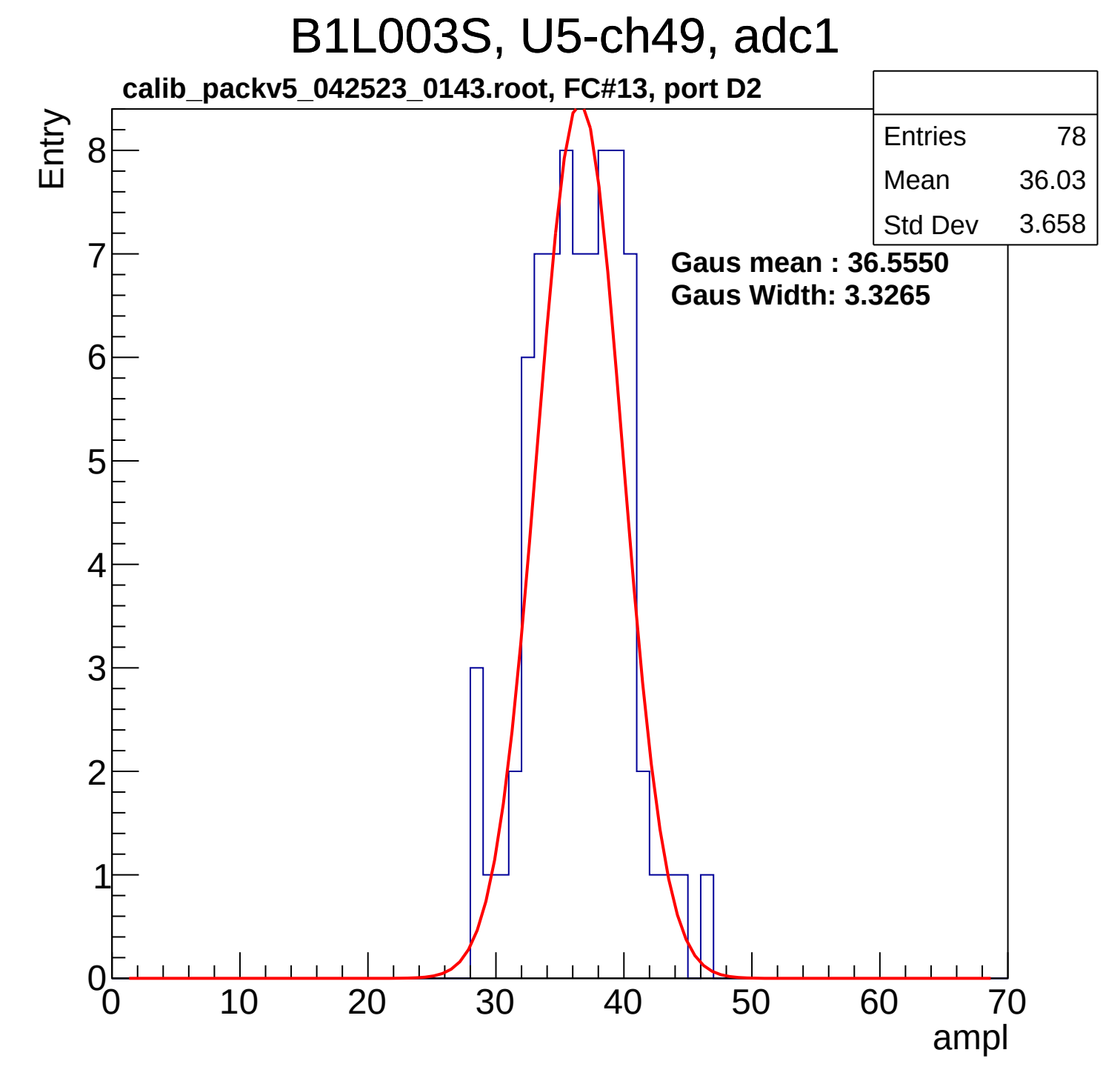
B1L003S, U5-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.03
Std Dev 3.658
Gaus mean : 36.5550
Gaus Width: 3.3265
Entry
ampl
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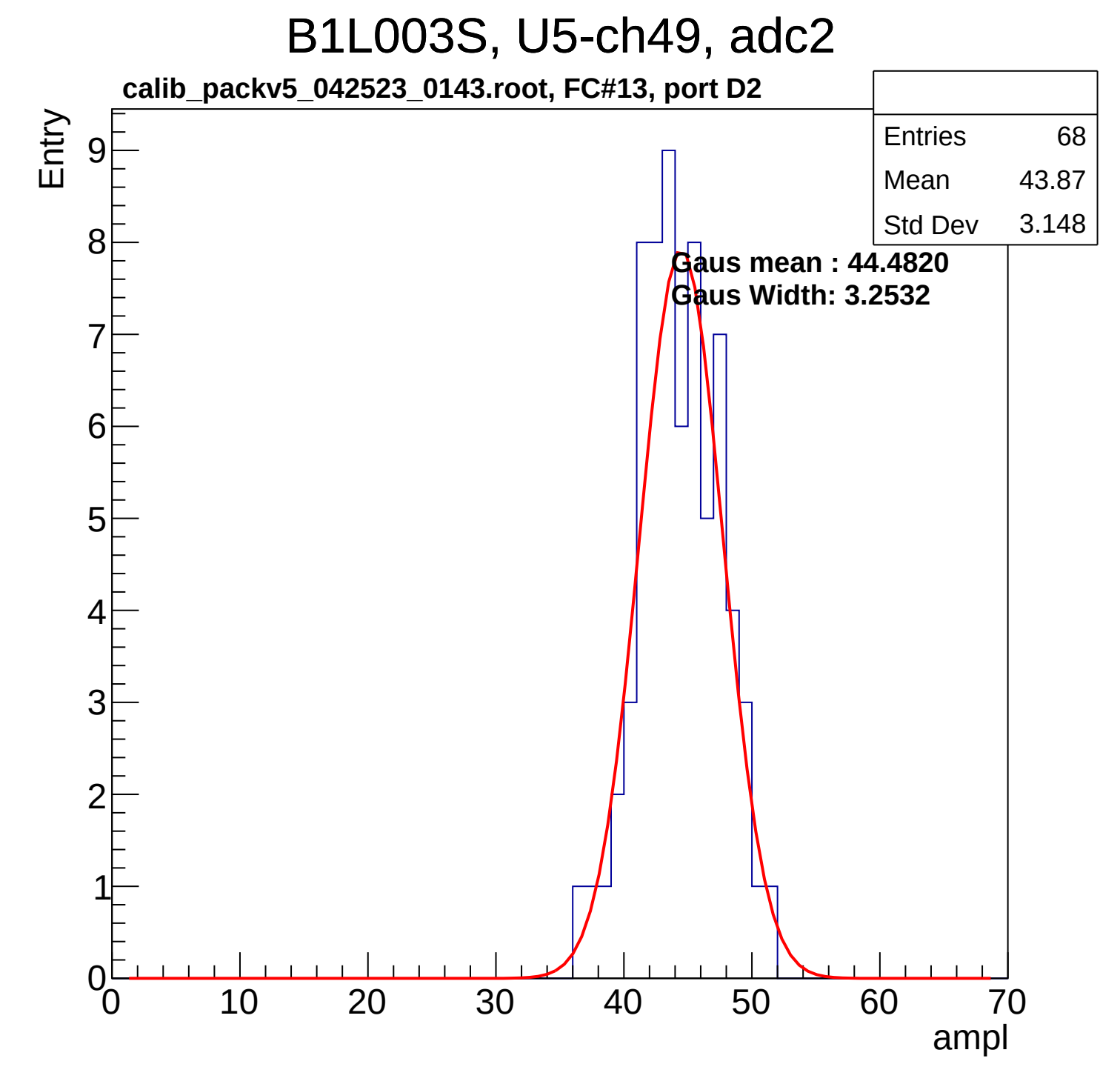
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.87
Std Dev 3.148
Gaus mean : 44.4820
Gaus Width: 3.2532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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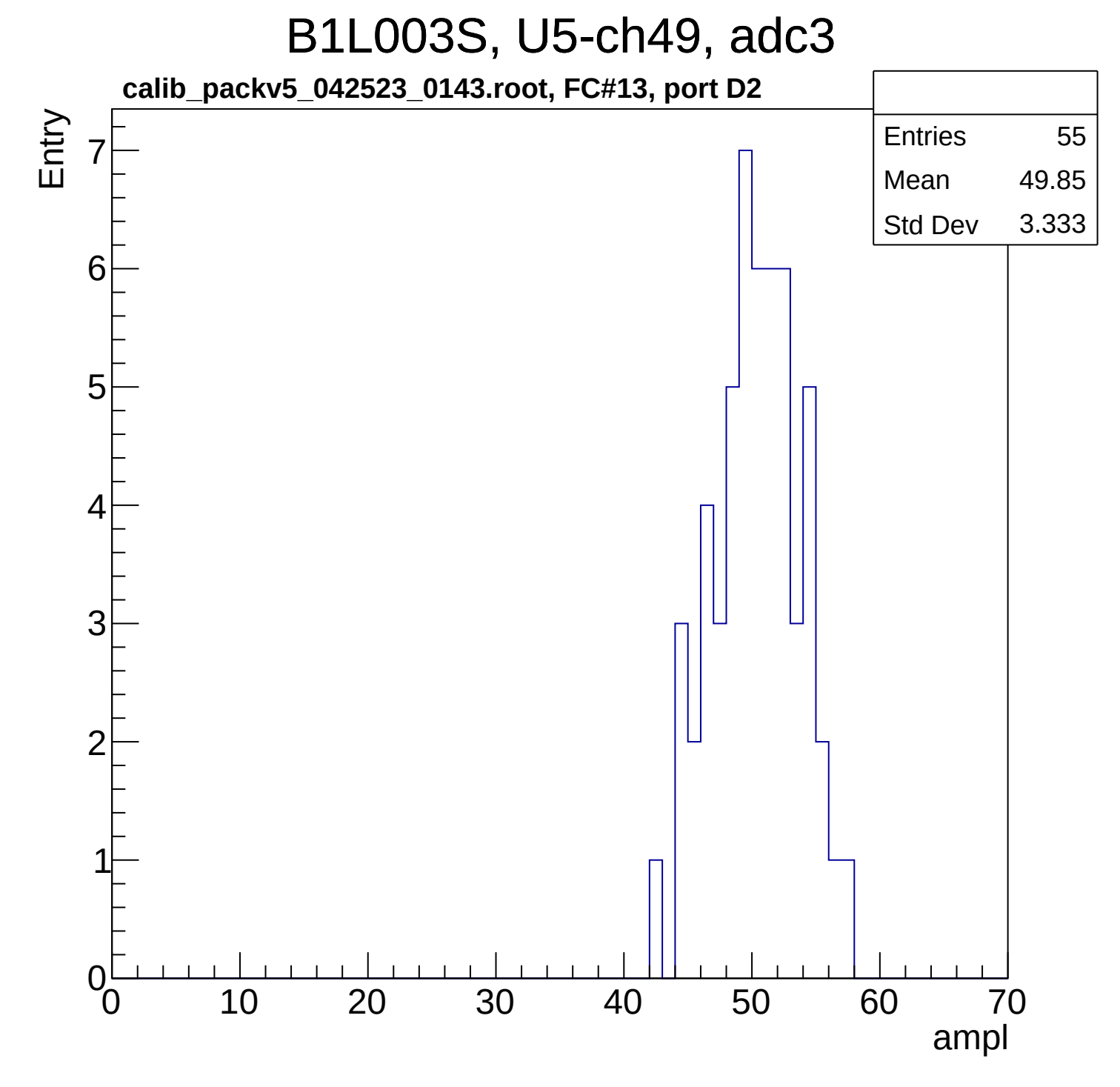
B1L003S, U5-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.85
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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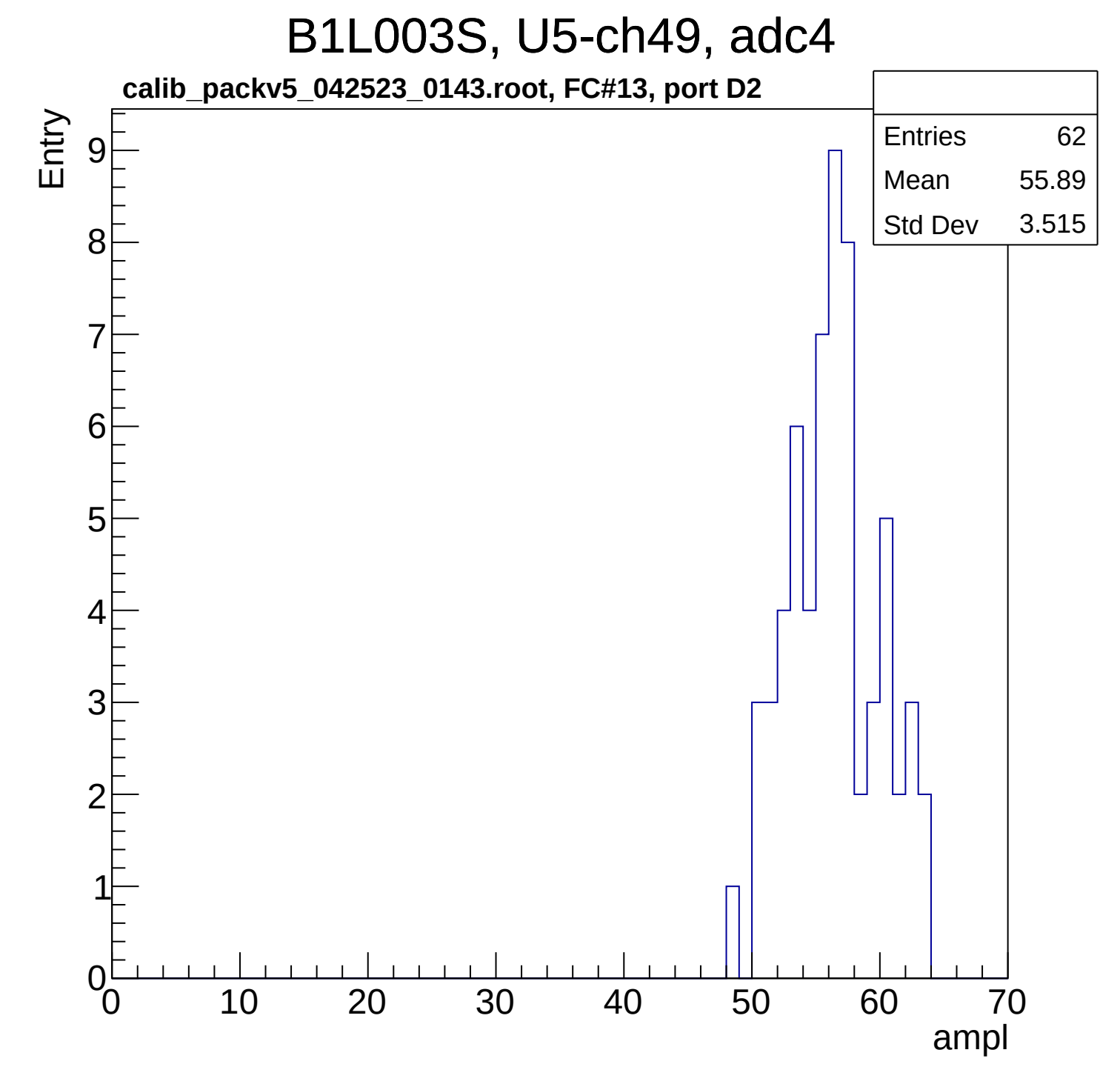
B1L003S, U5-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.89
Std Dev 3.515
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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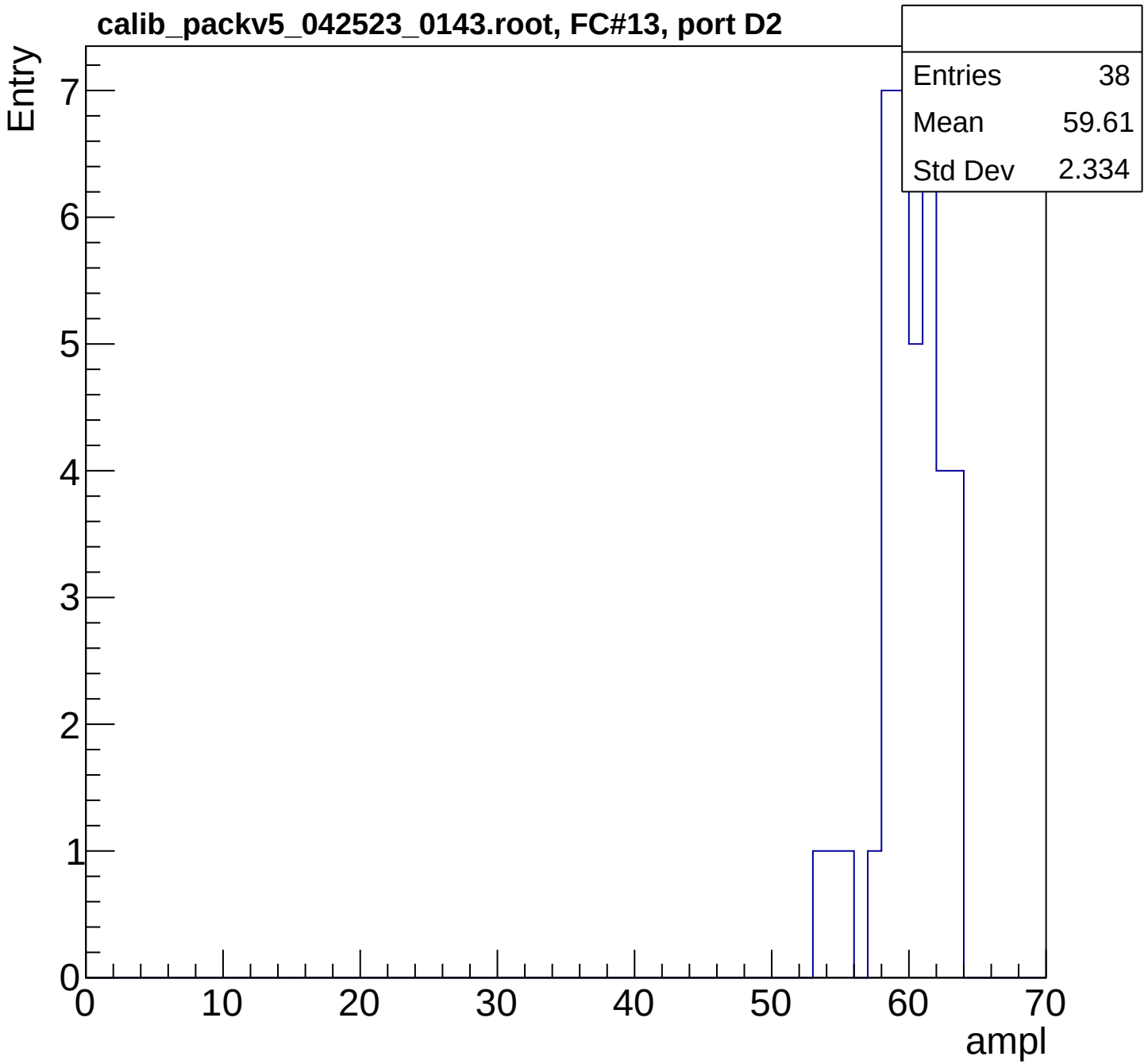
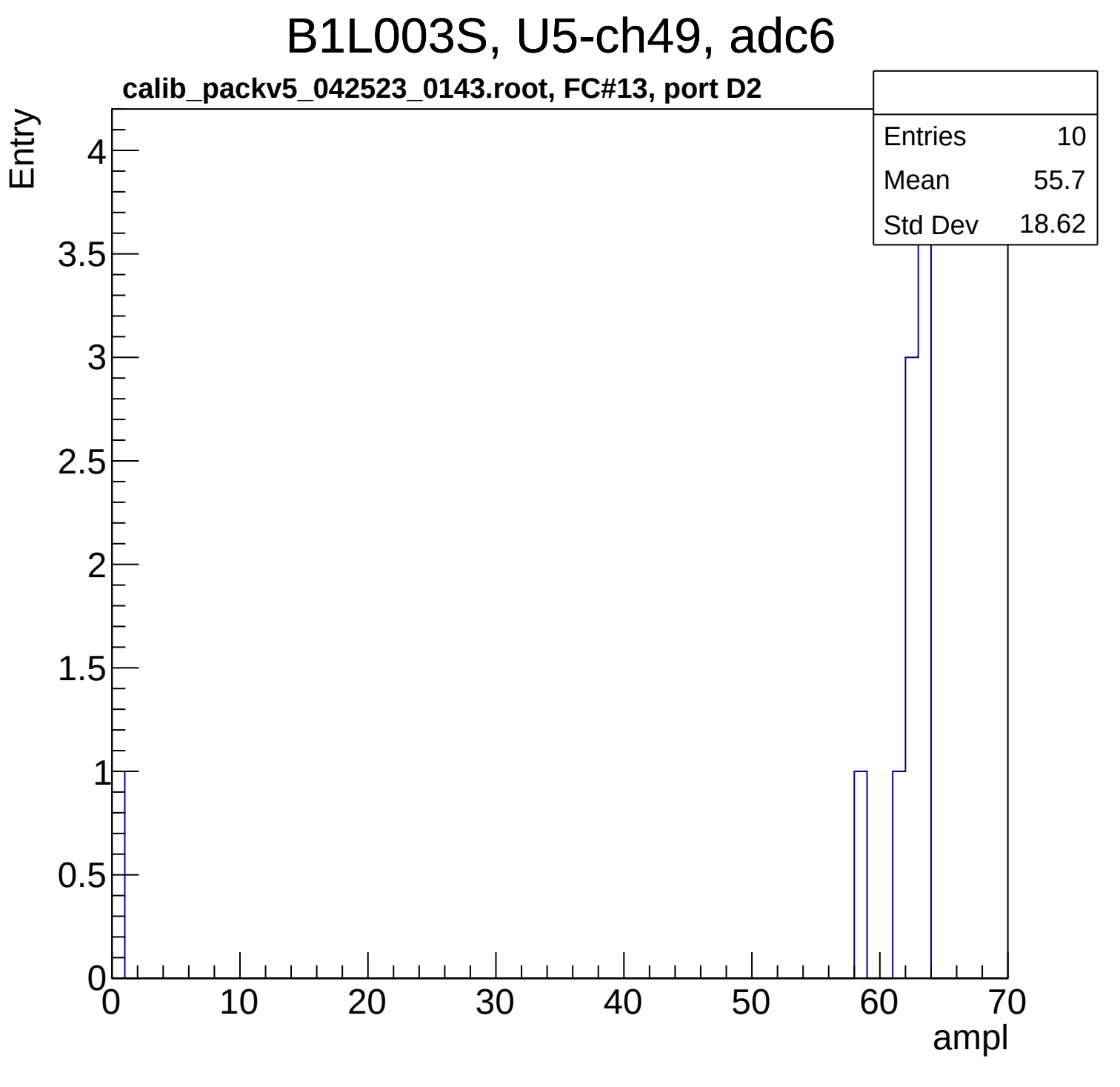

B1L003S, U5-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.61
Std Dev 2.334
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.7
Std Dev 18.62
Entry
ampl
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1.5
2
2.5
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3.5
4
0
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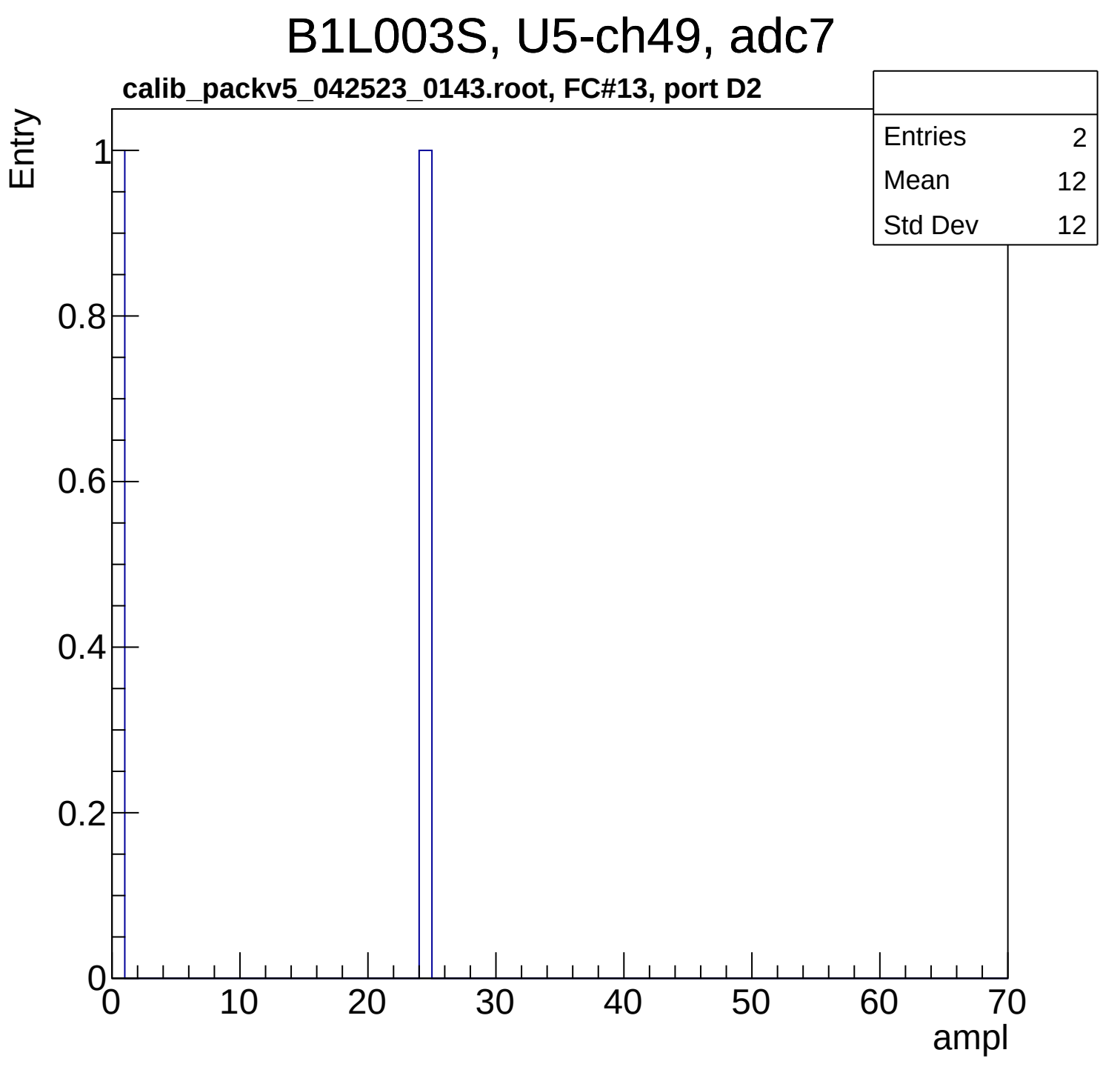
B1L003S, U5-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
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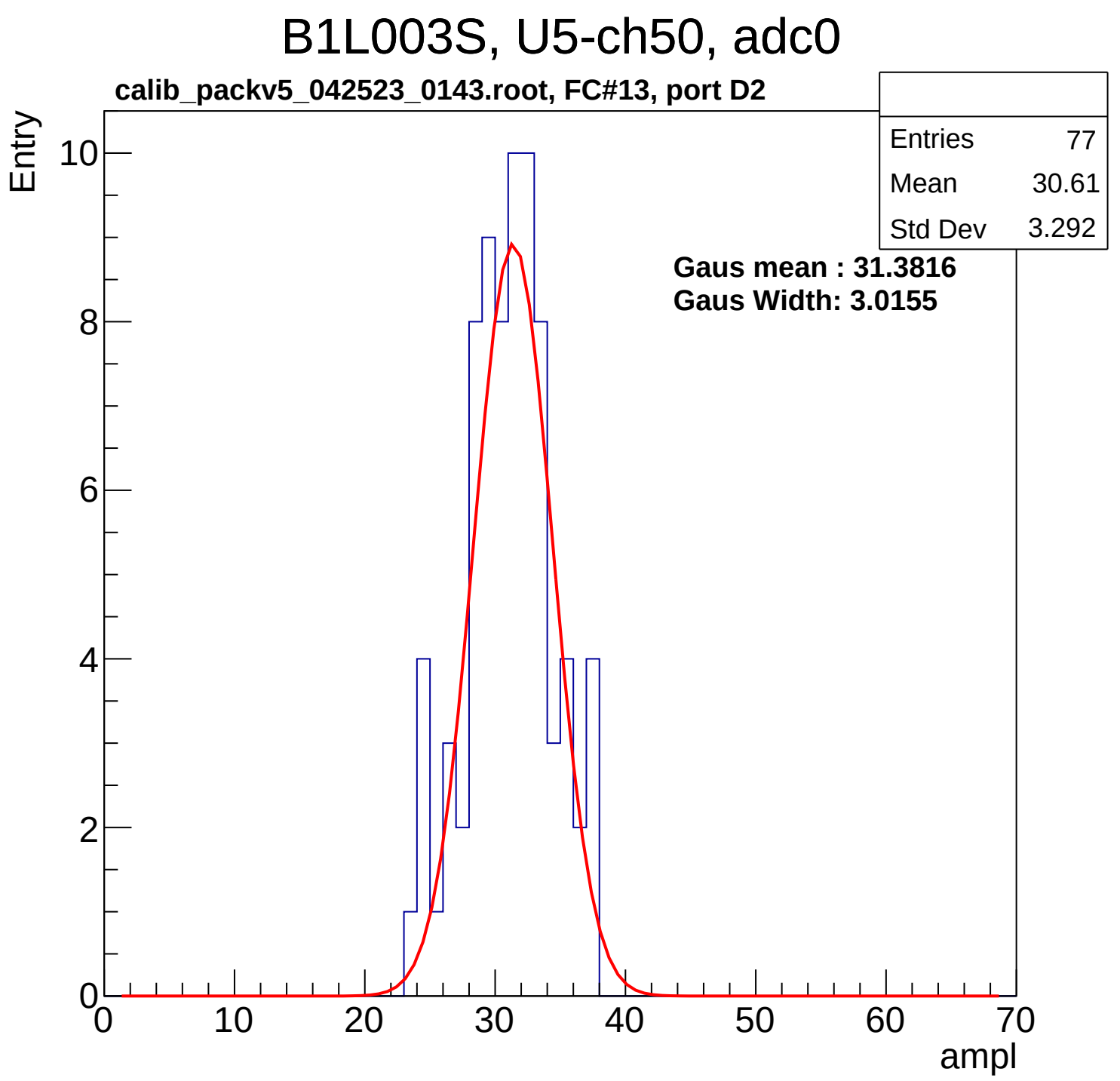
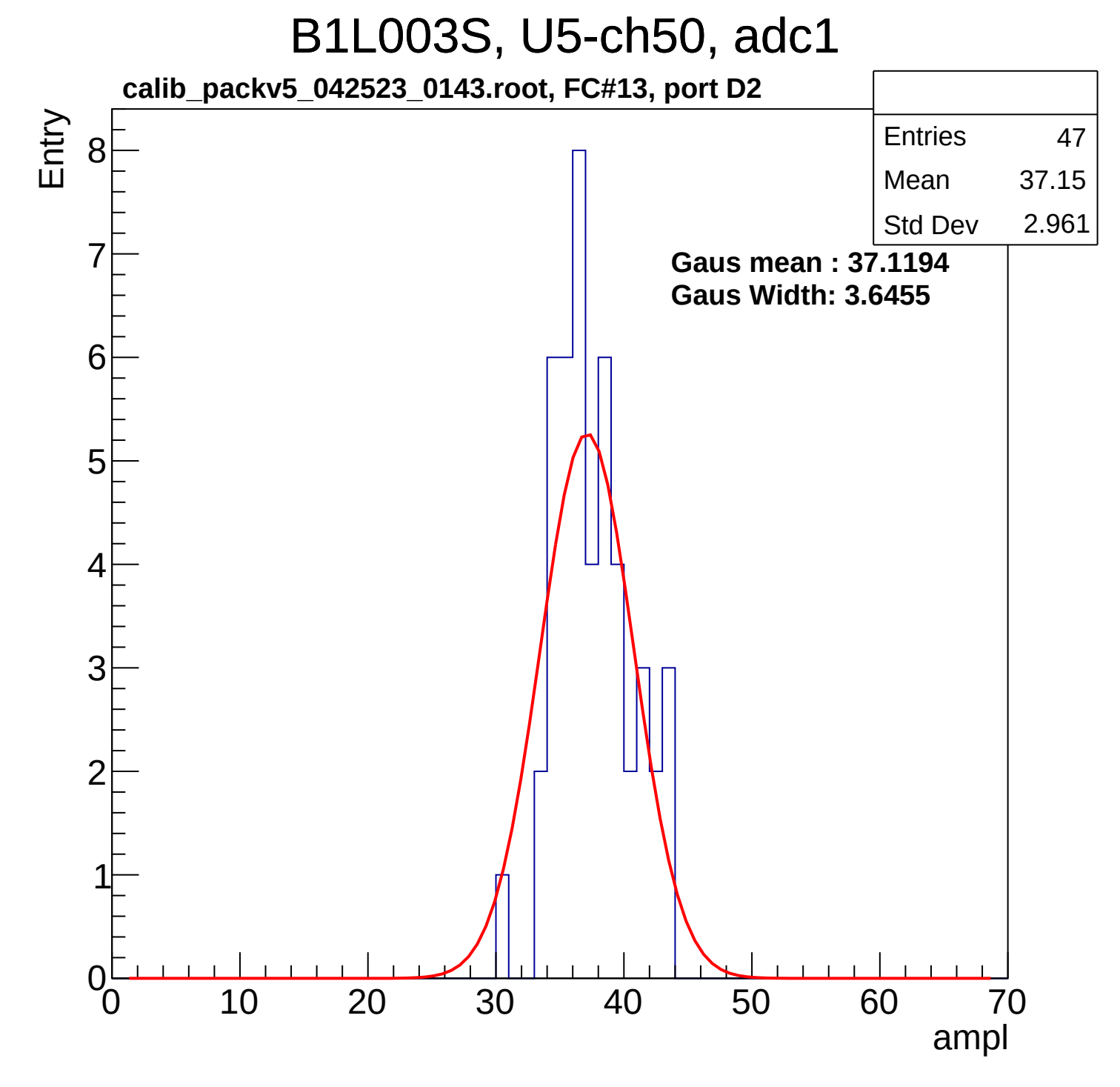
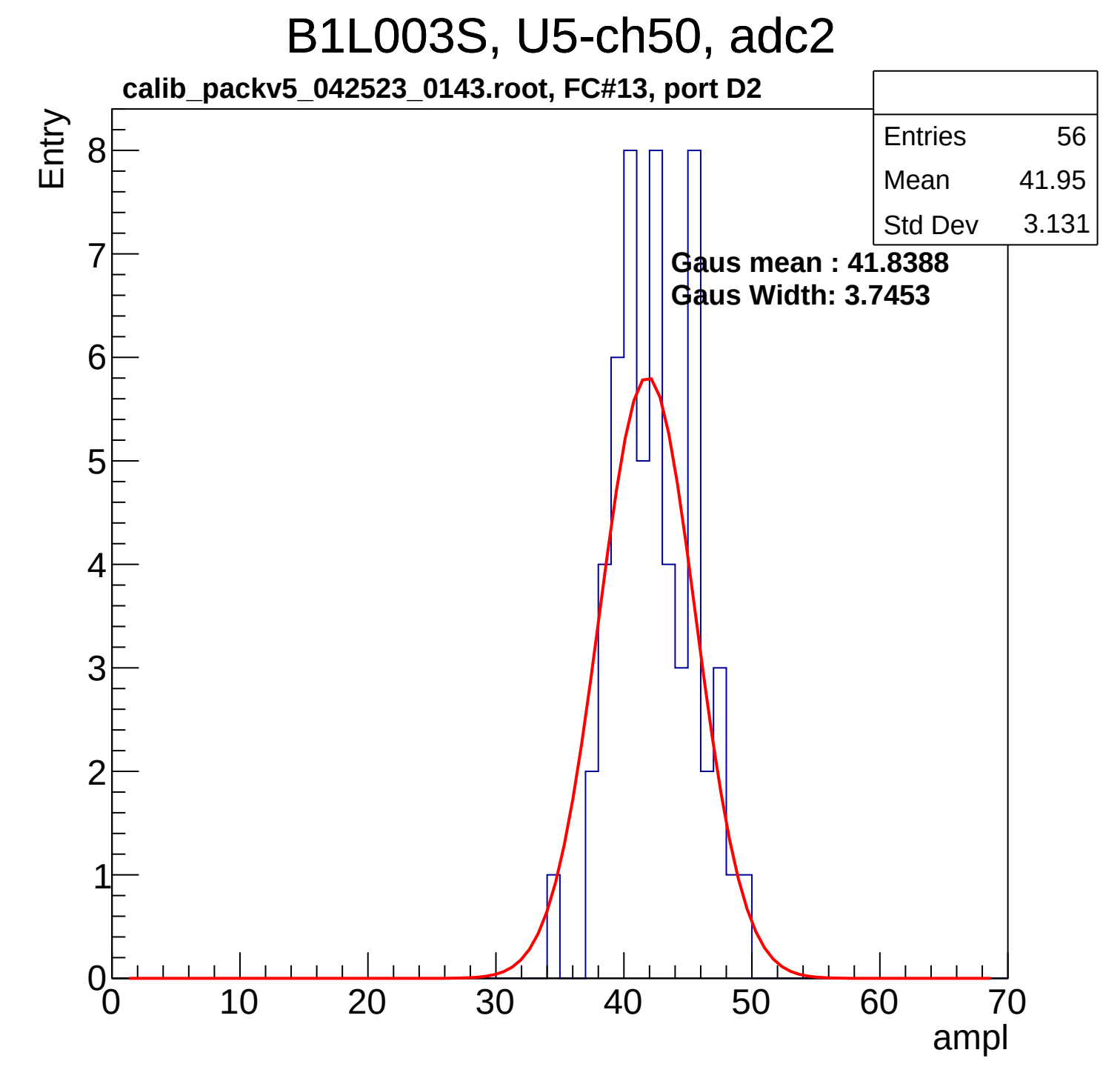
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 30.61
Std Dev 3.292
Gaus mean : 31.3816
Gaus Width: 3.0155
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 37.15
Std Dev 2.961
Gaus mean : 37.1194
Gaus Width: 3.6455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 41.95
Std Dev 3.131
Gaus mean : 41.8388
Gaus Width: 3.7453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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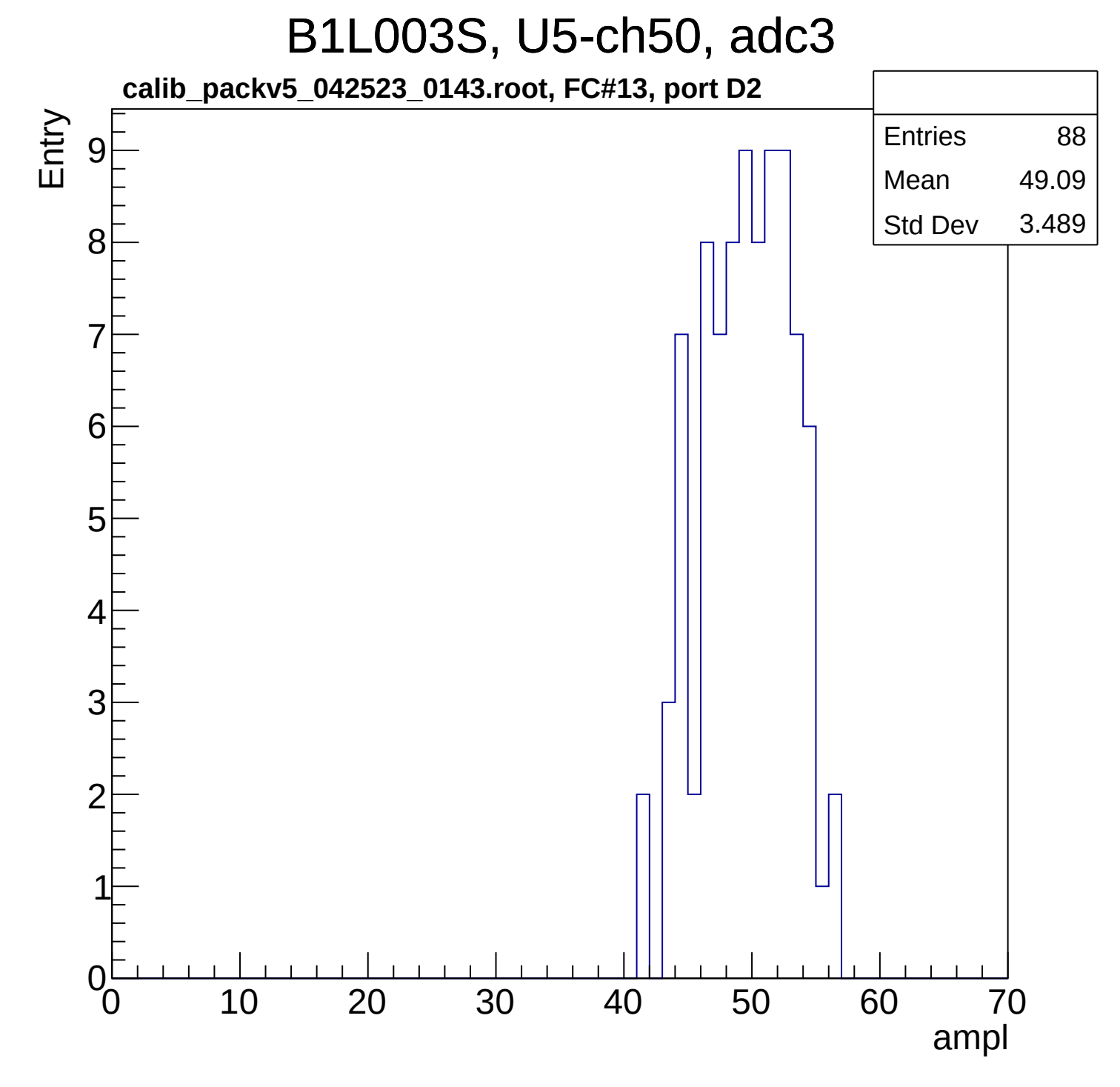
B1L003S, U5-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 88
Mean 49.09
Std Dev 3.489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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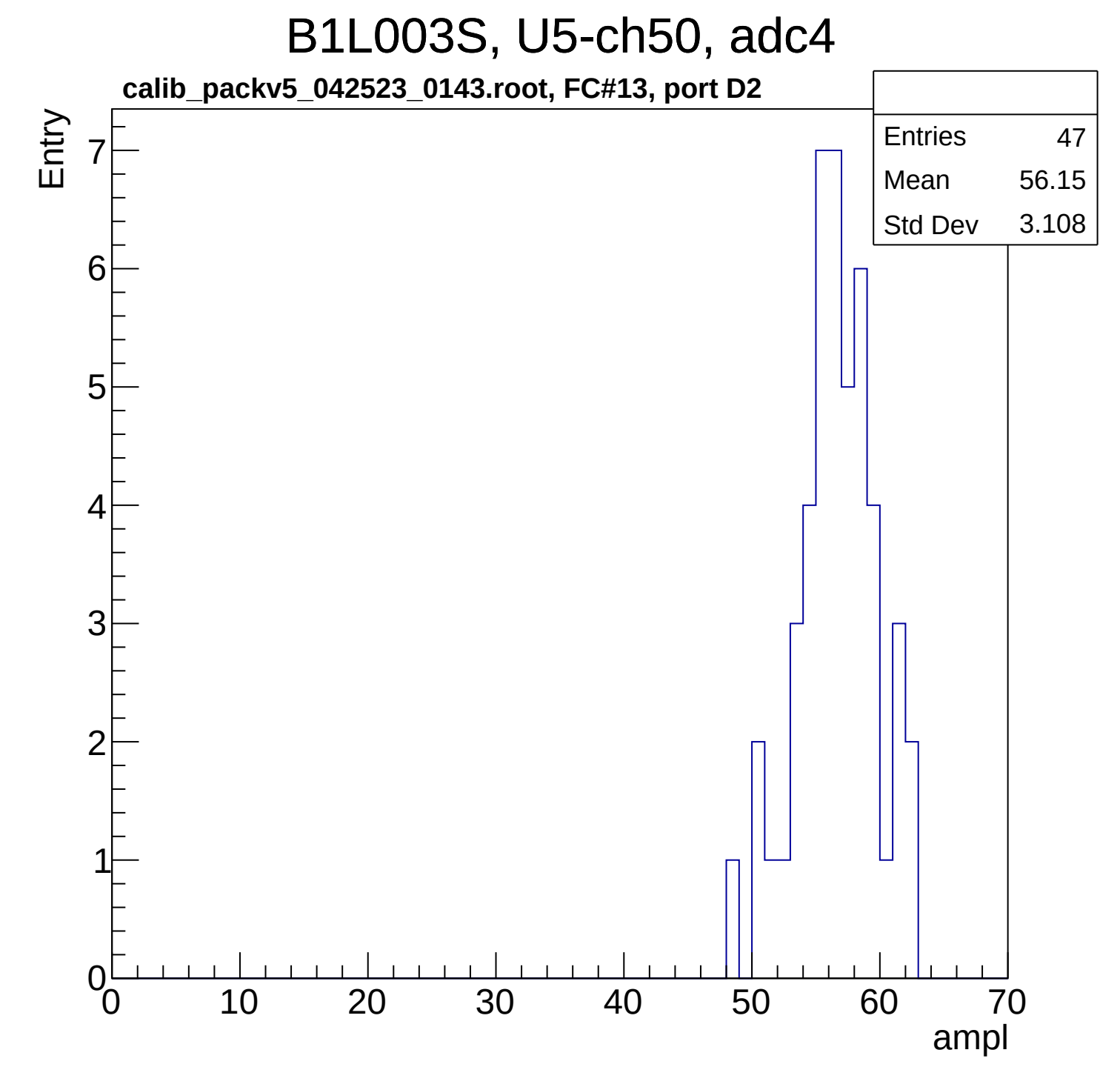
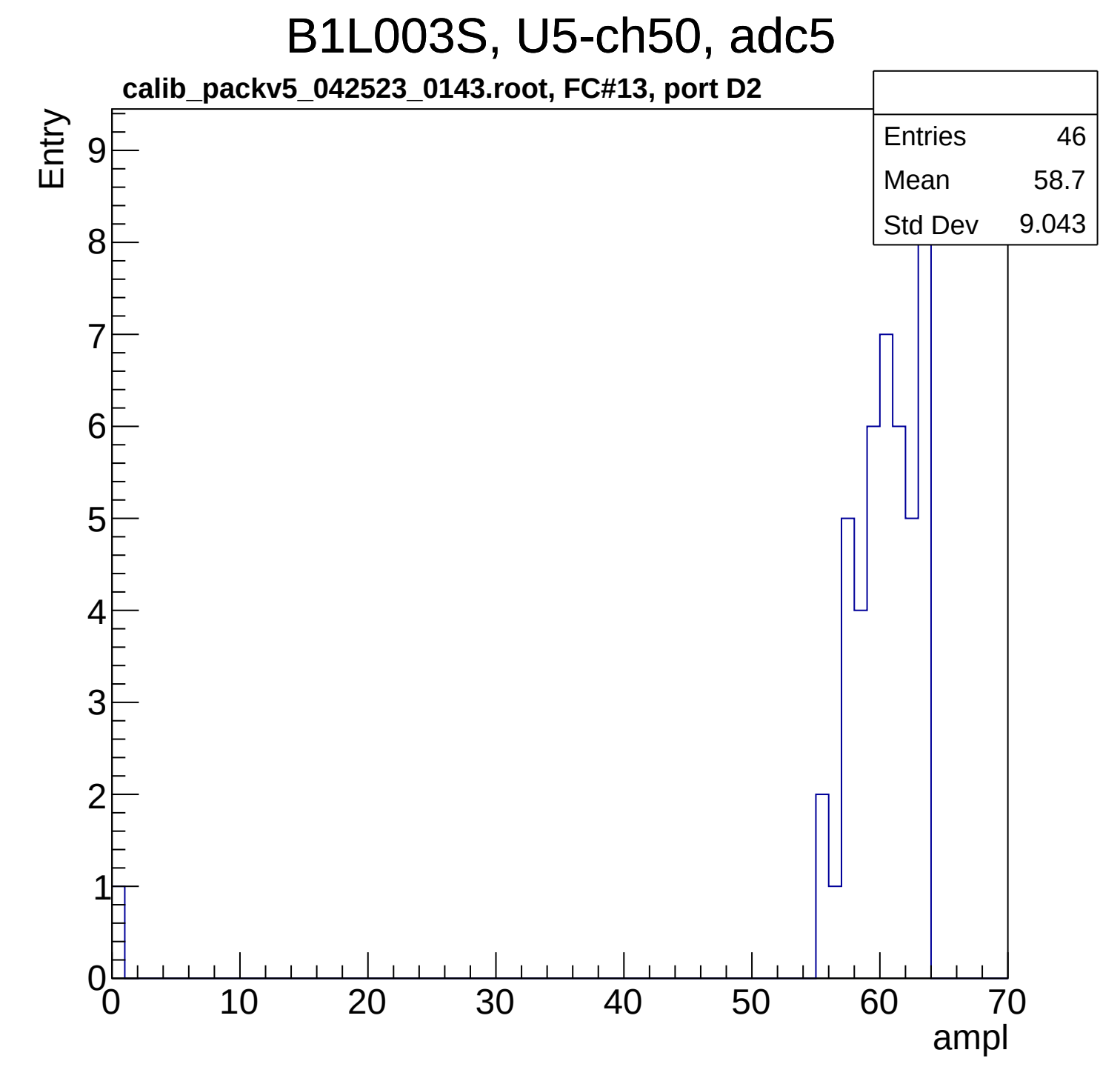

B1L003S, U5-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 56.15
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.7
Std Dev 9.043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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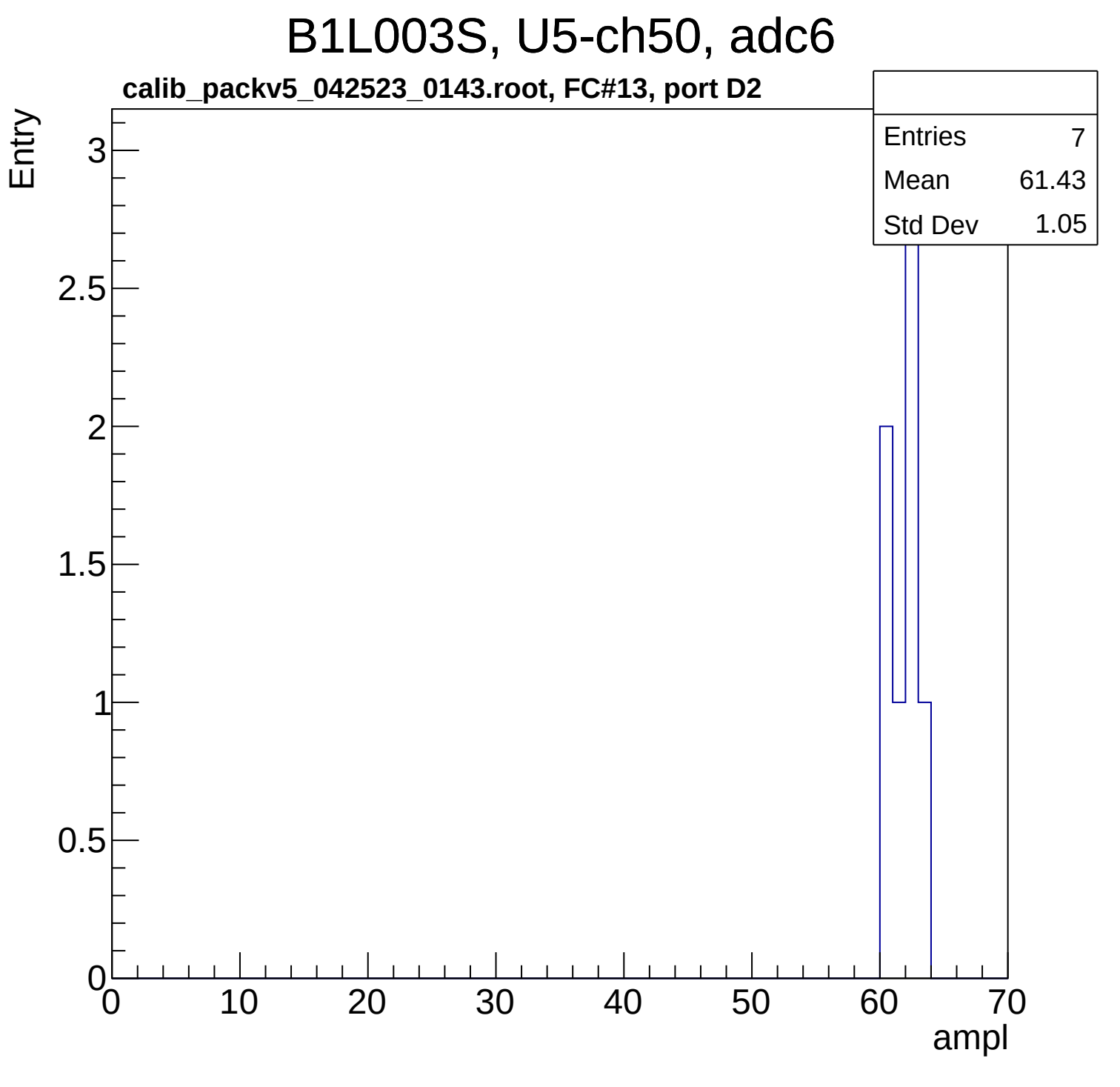
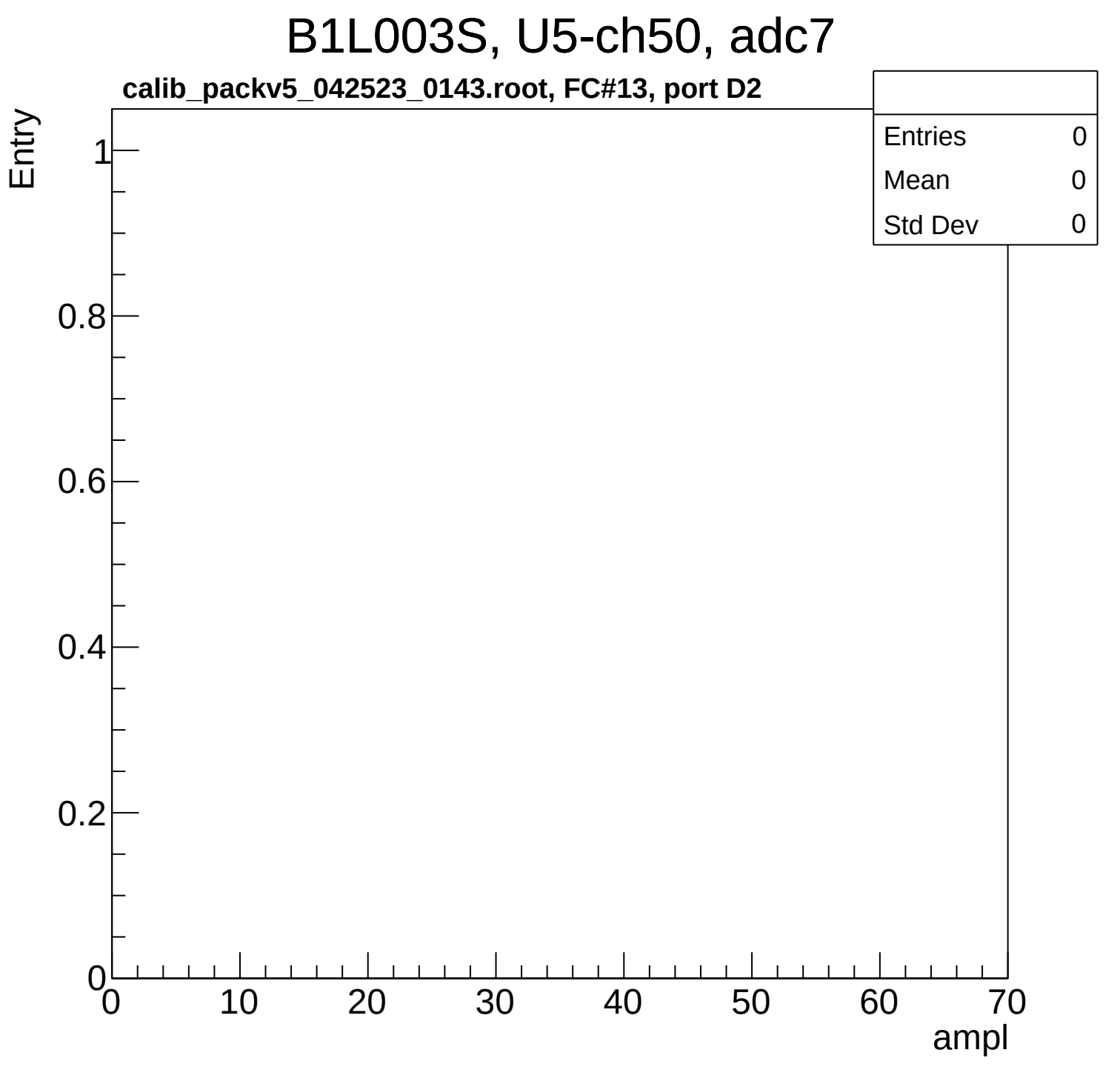
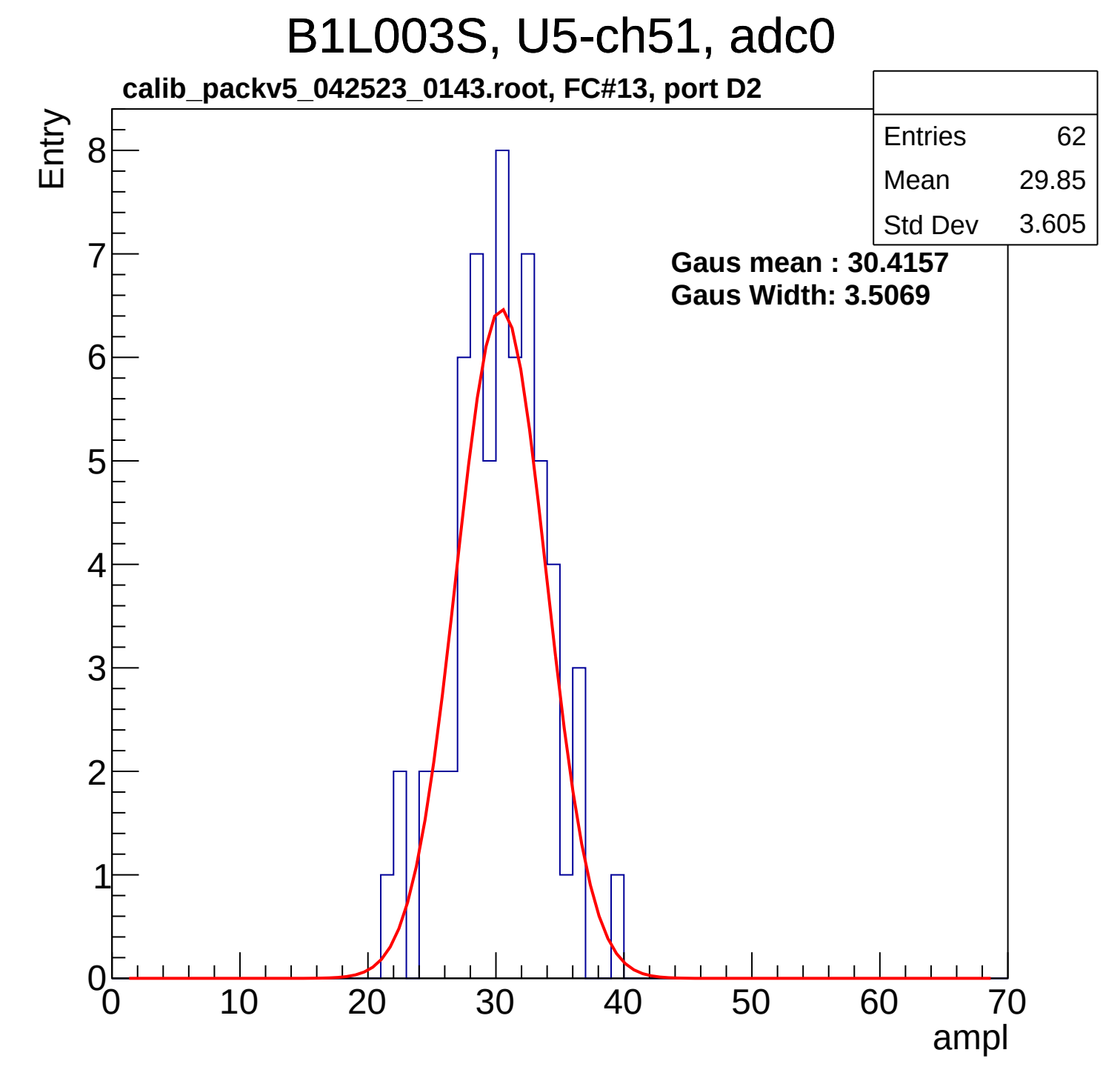
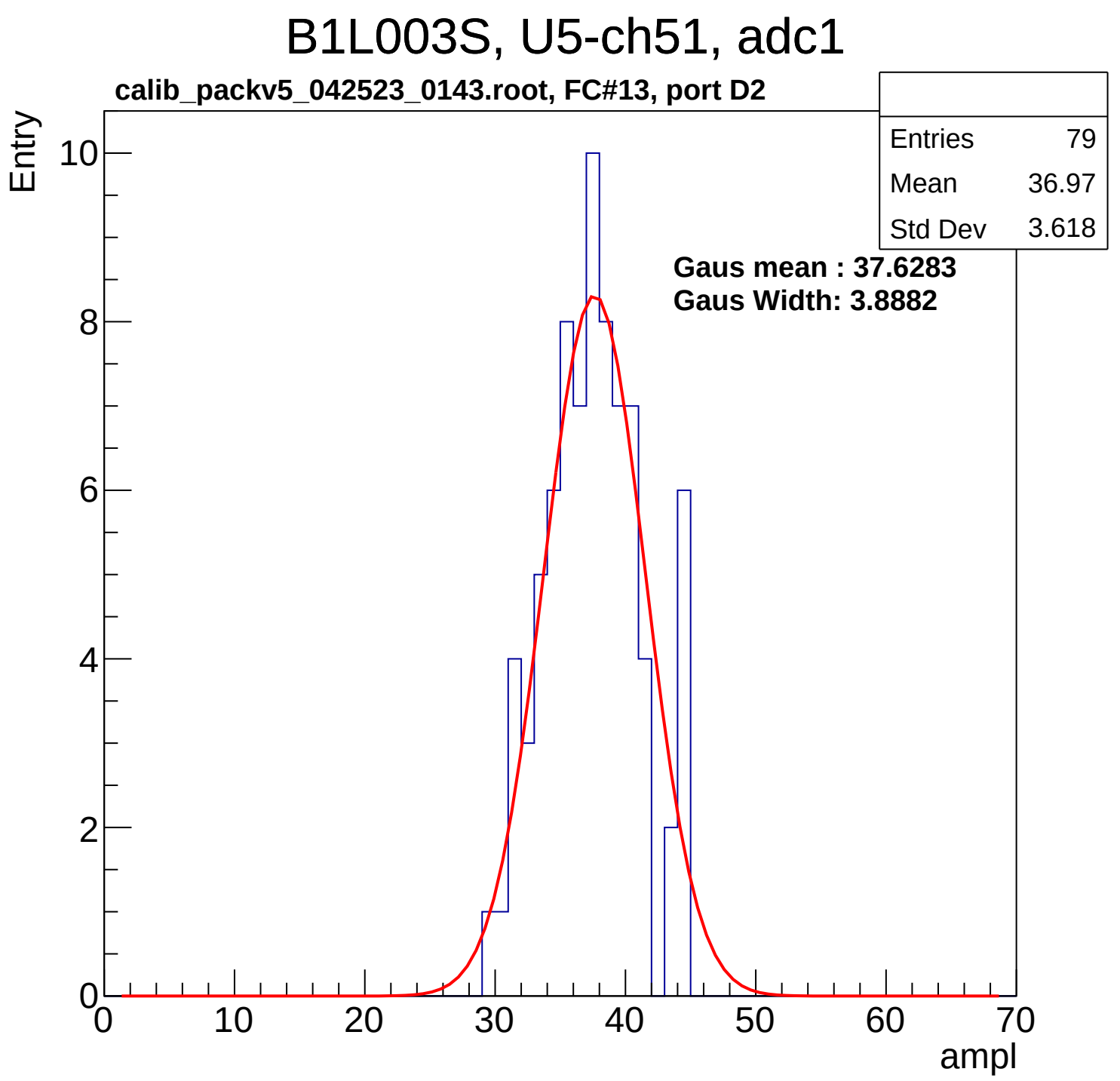
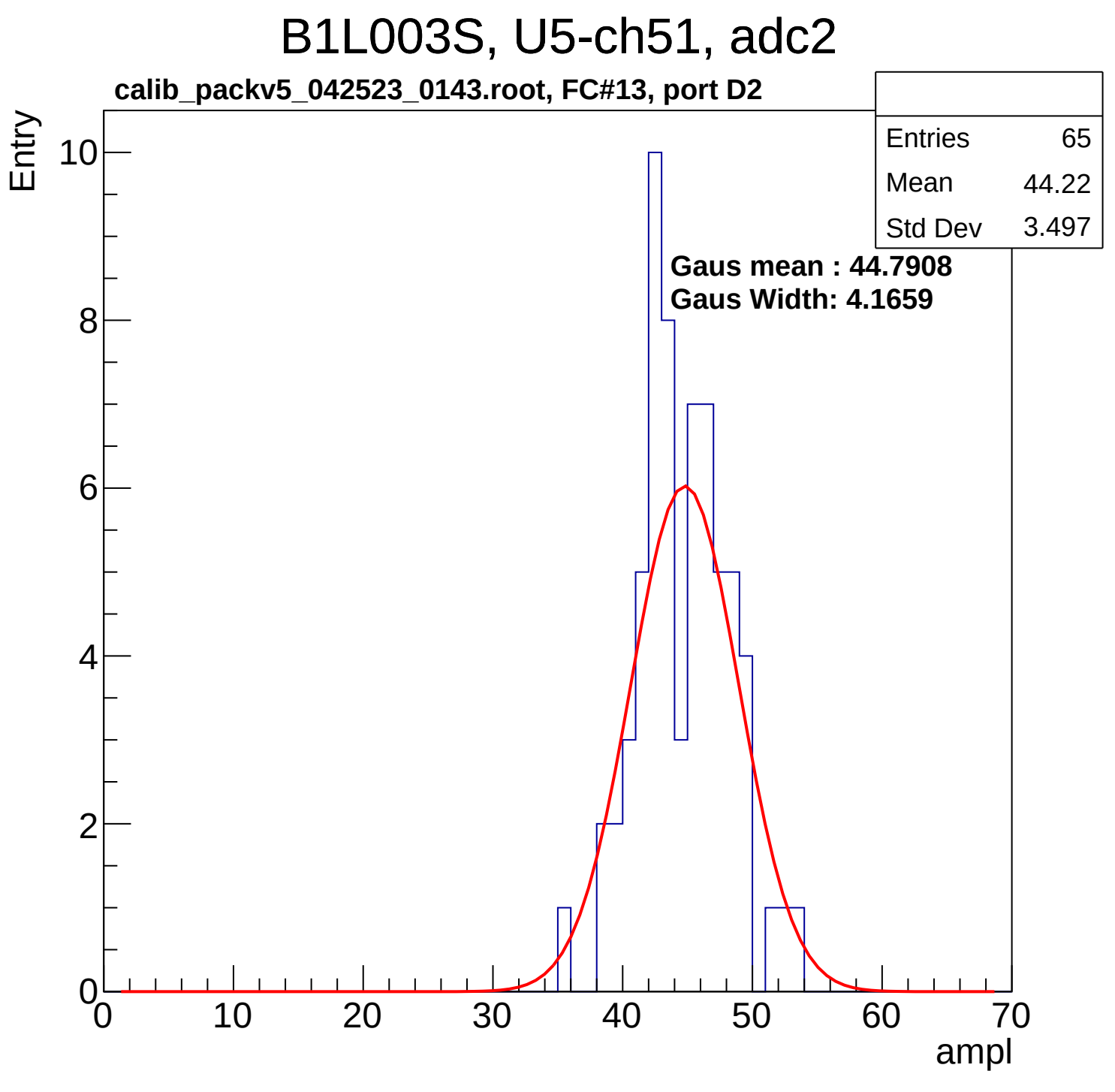
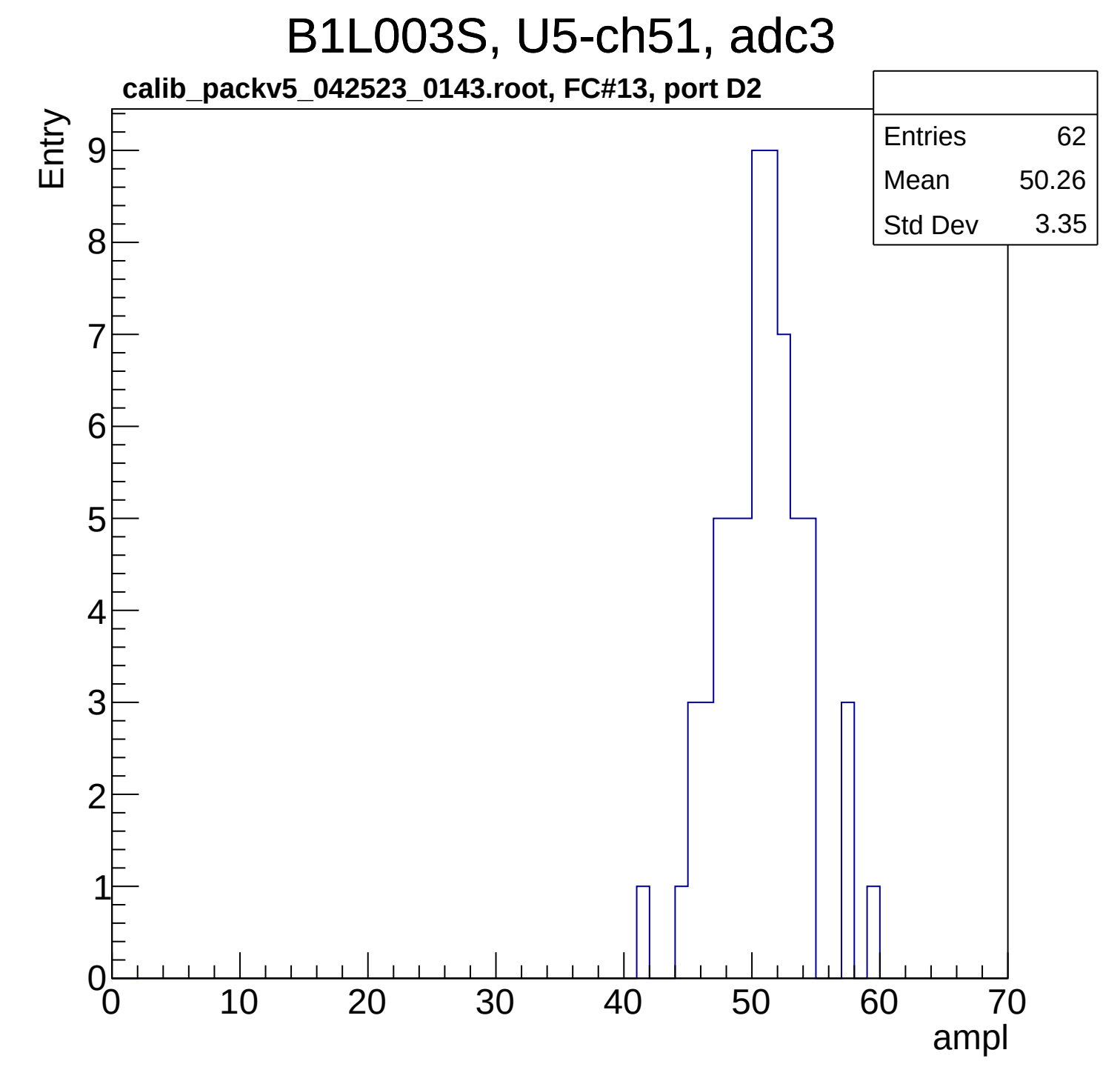

B1L003S, U5-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.05
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

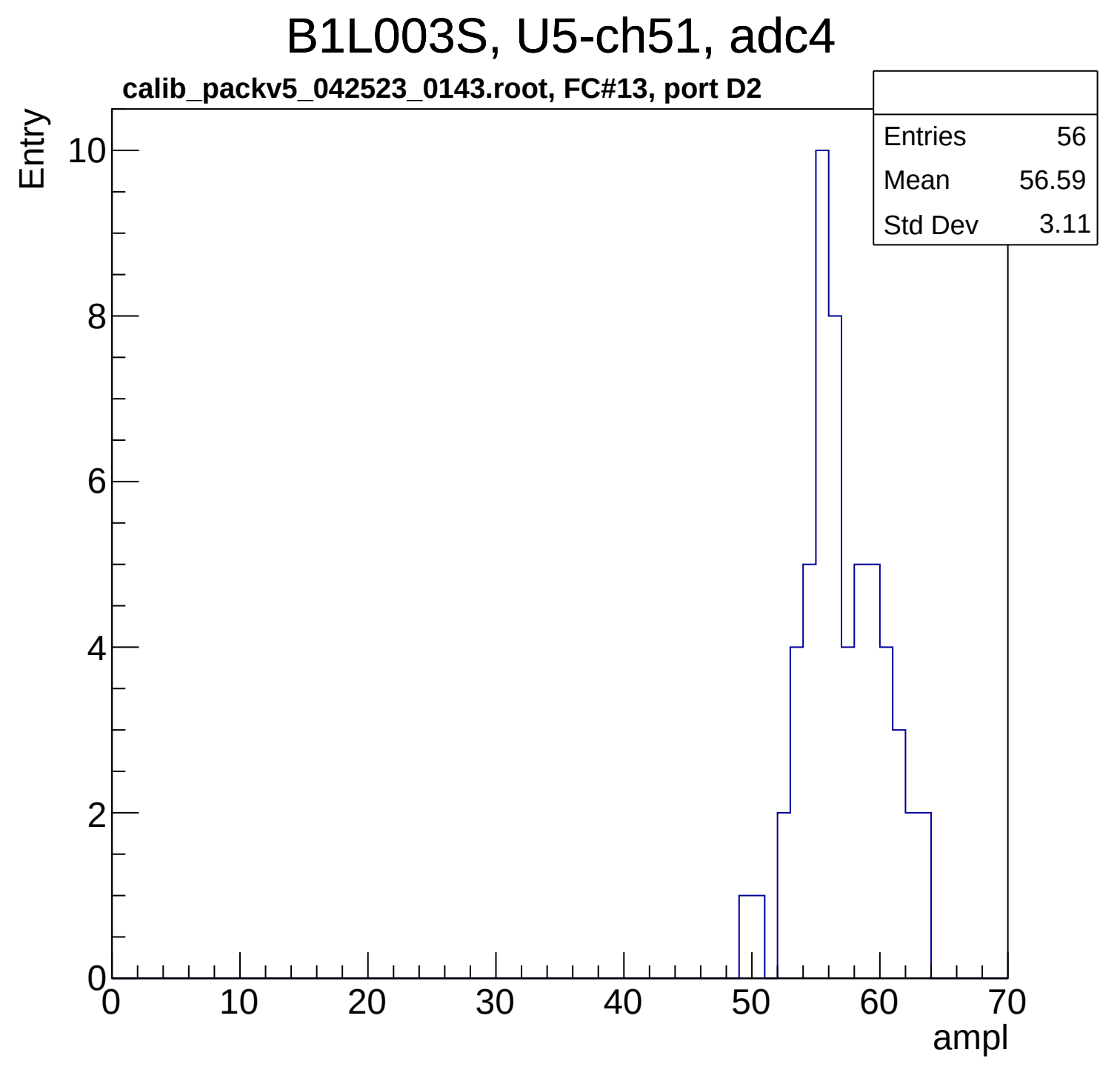

B1L003S, U5-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.85
Std Dev 3.605
Gaus mean : 30.4157
Gaus Width: 3.5069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.97
Std Dev 3.618
Gaus mean : 37.6283
Gaus Width: 3.8882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 44.22
Std Dev 3.497
Gaus mean : 44.7908
Gaus Width: 4.1659
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.26
Std Dev 3.35
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 56.59
Std Dev 3.11
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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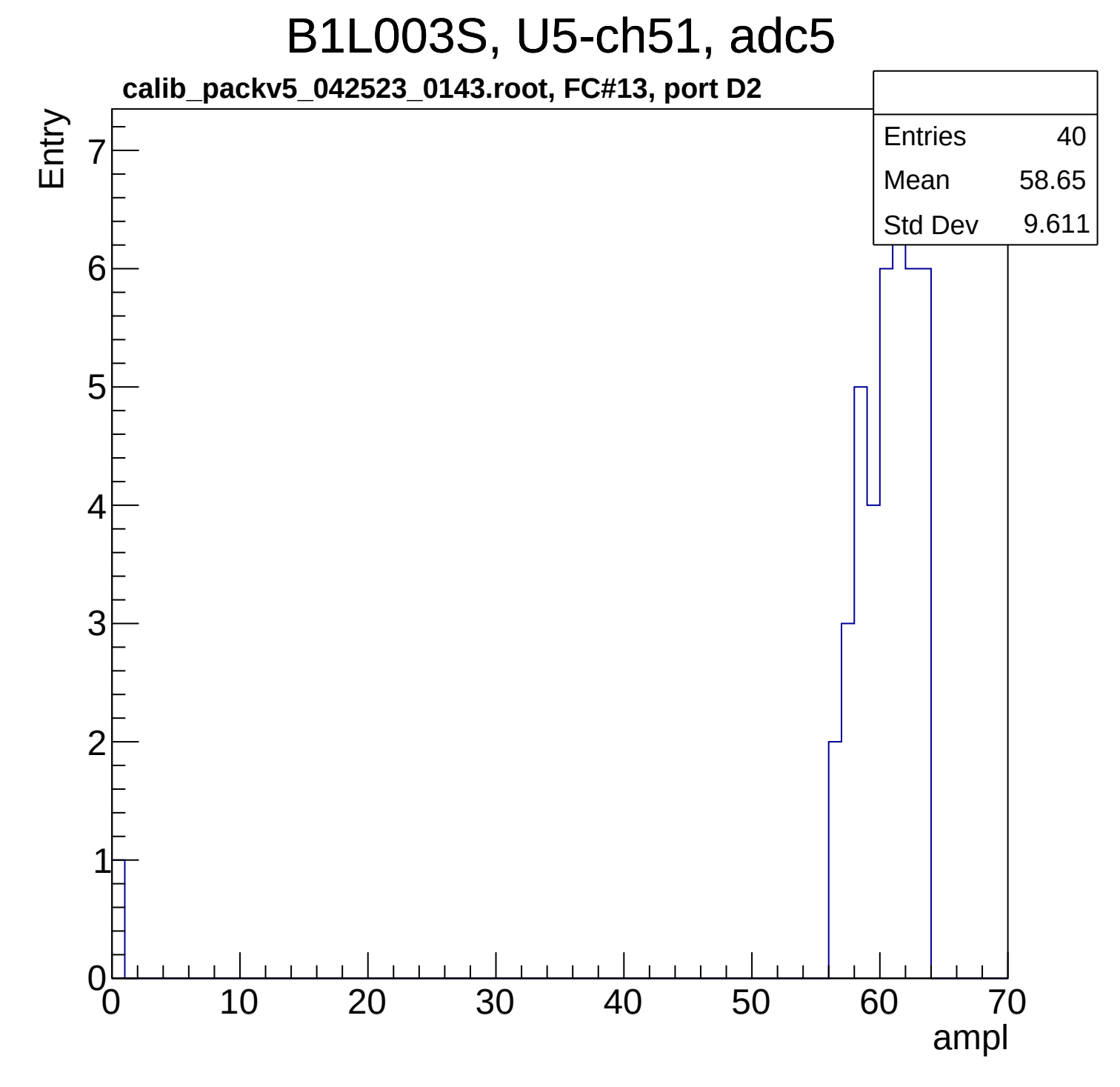
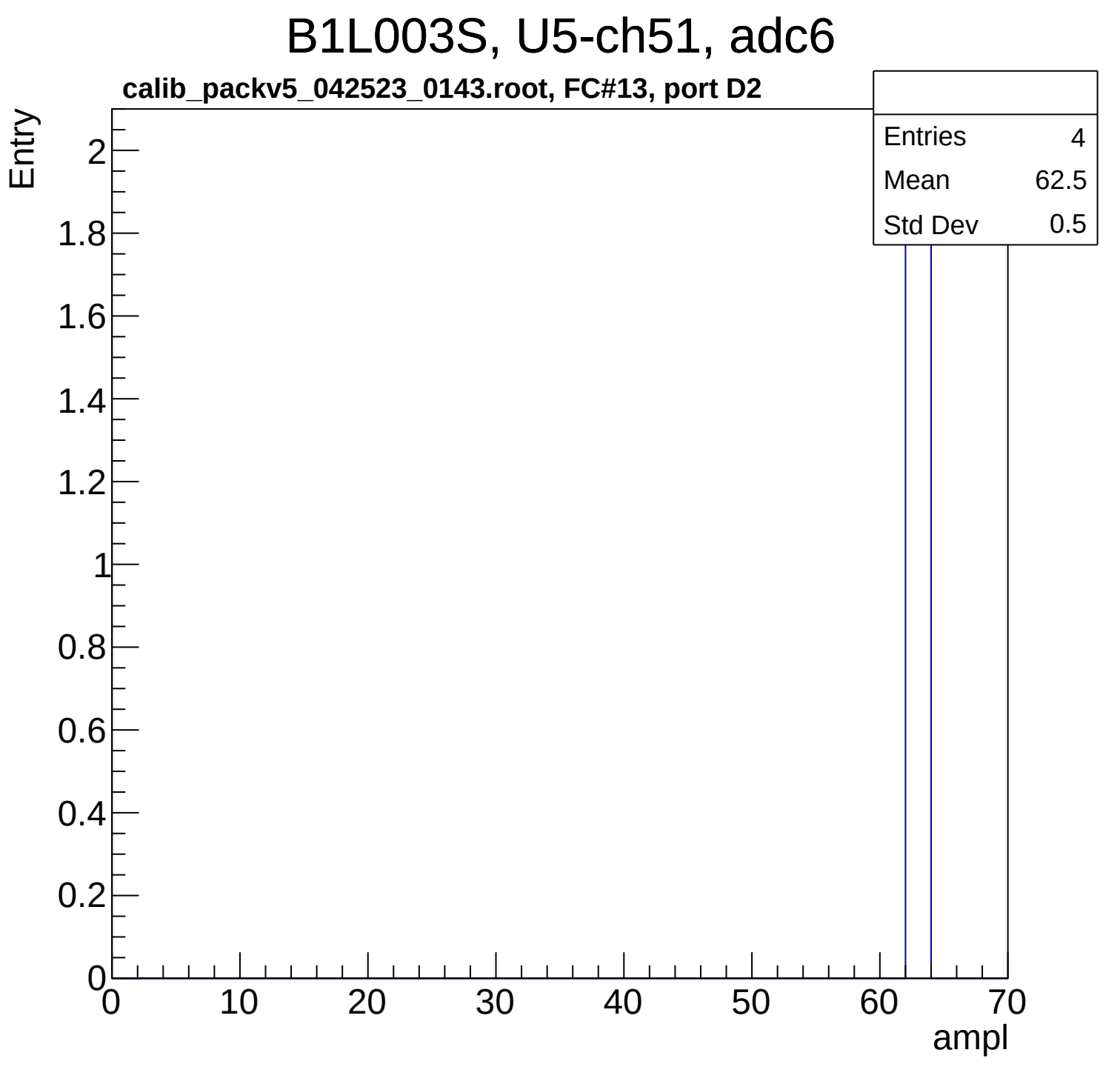
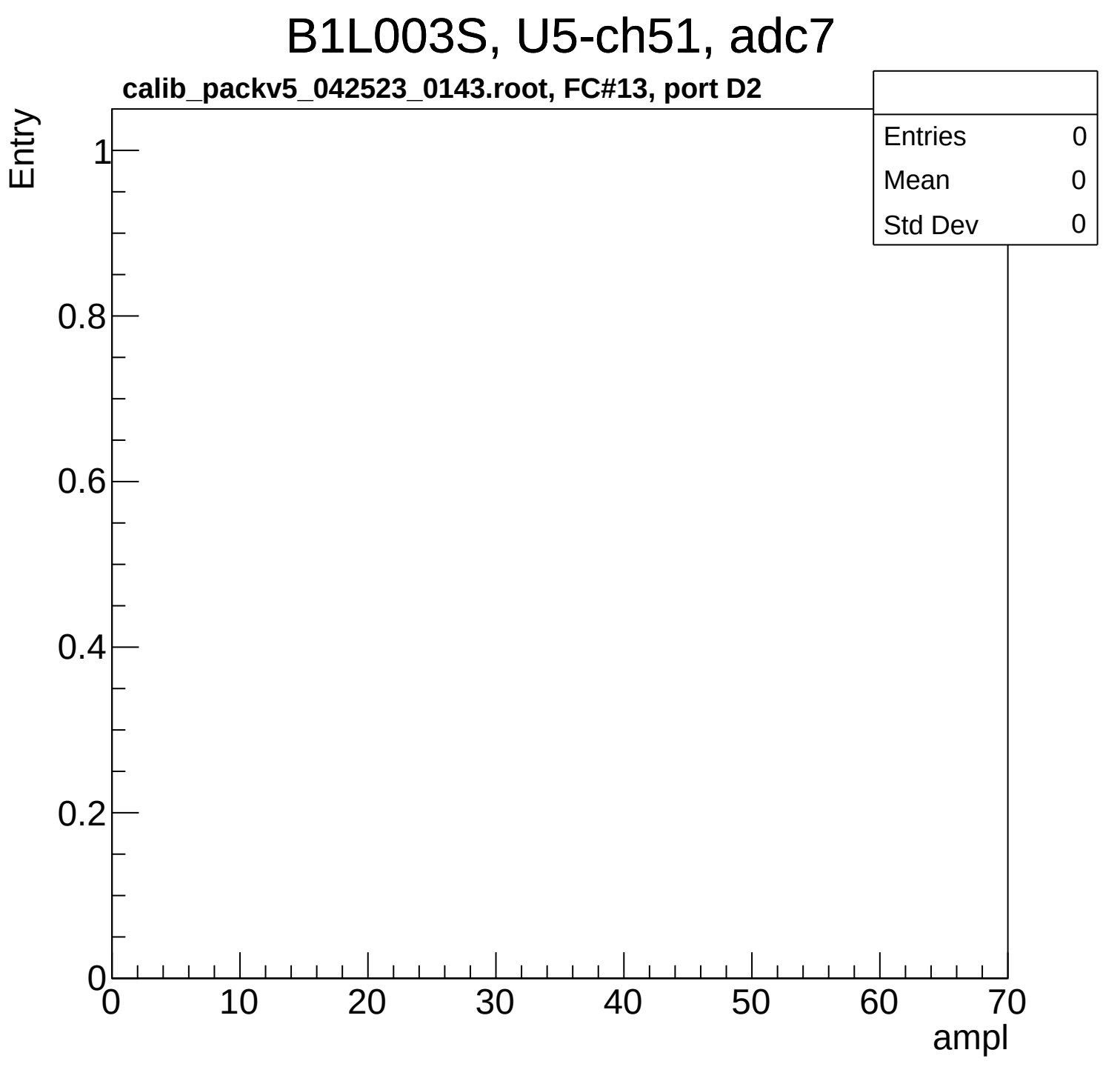
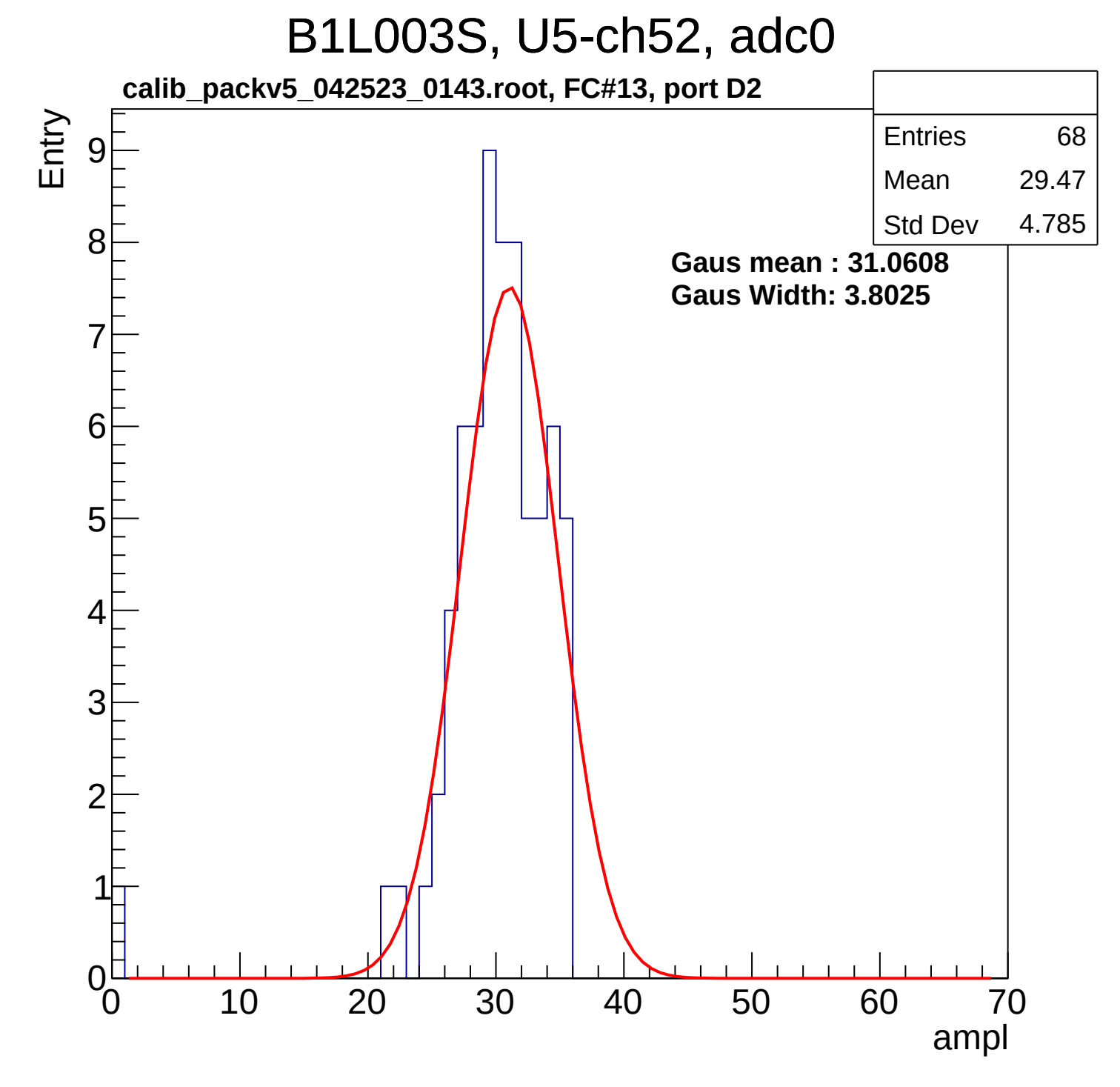
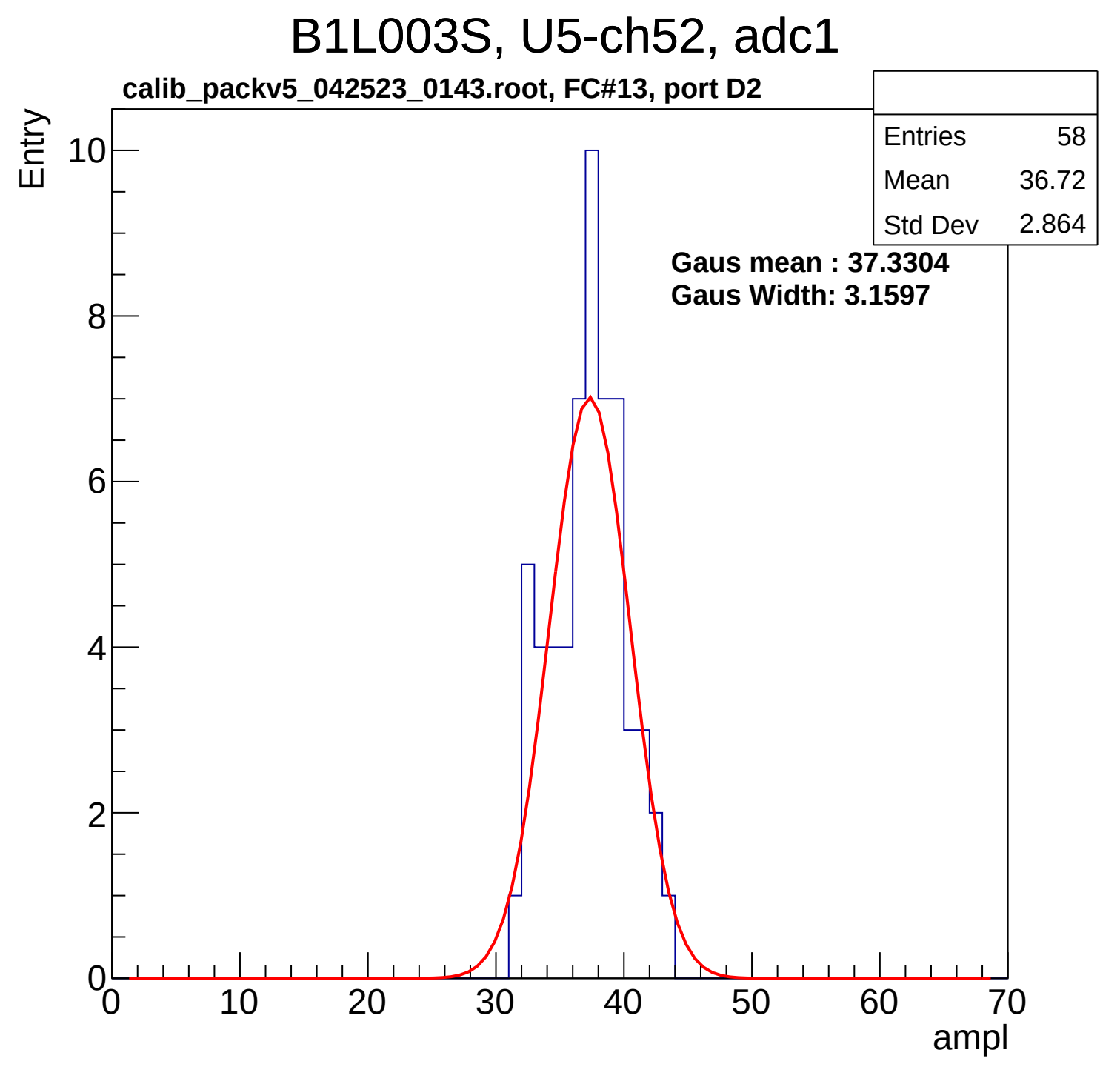
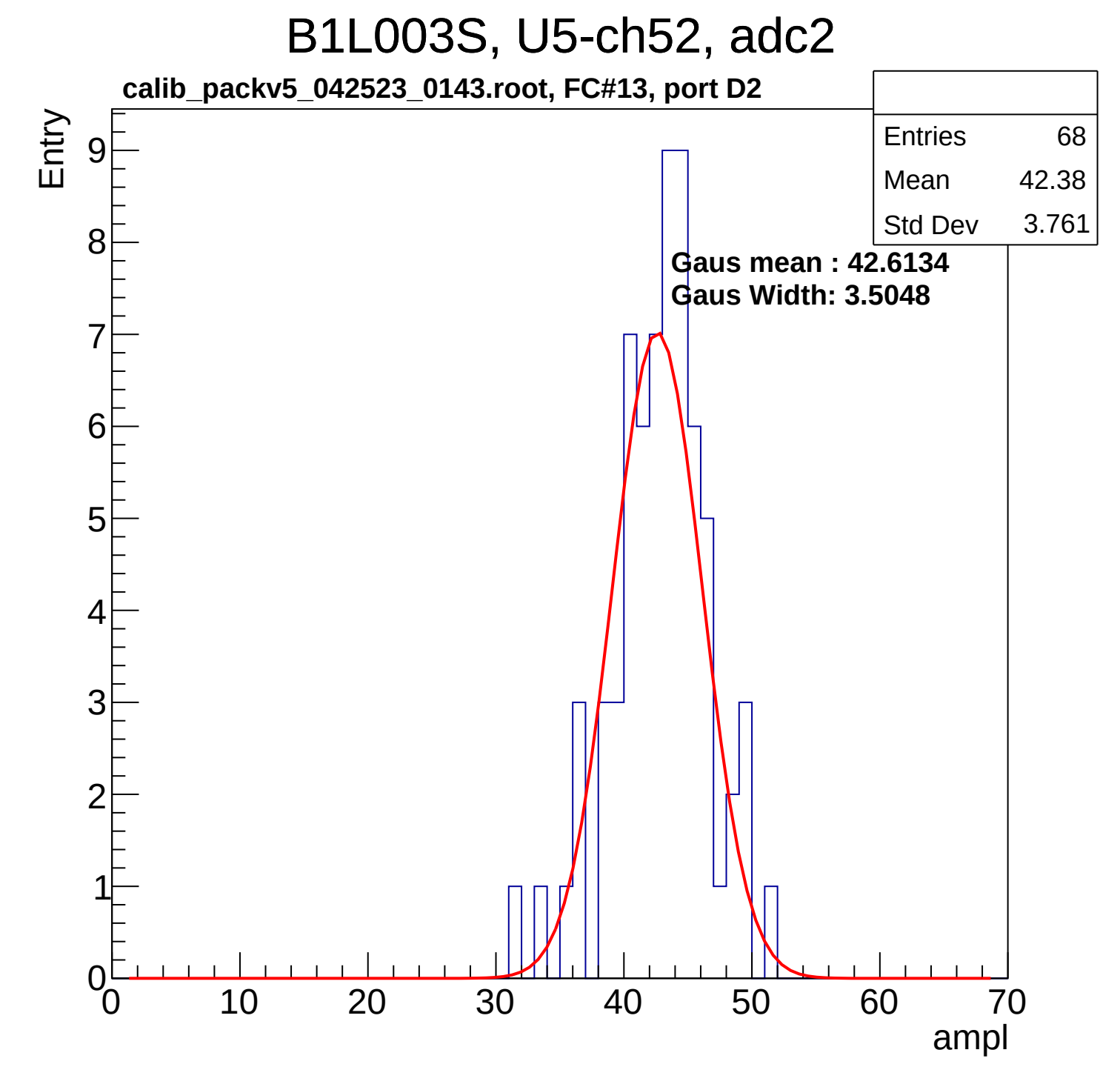

B1L003S, U5-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.65
Std Dev 9.611
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

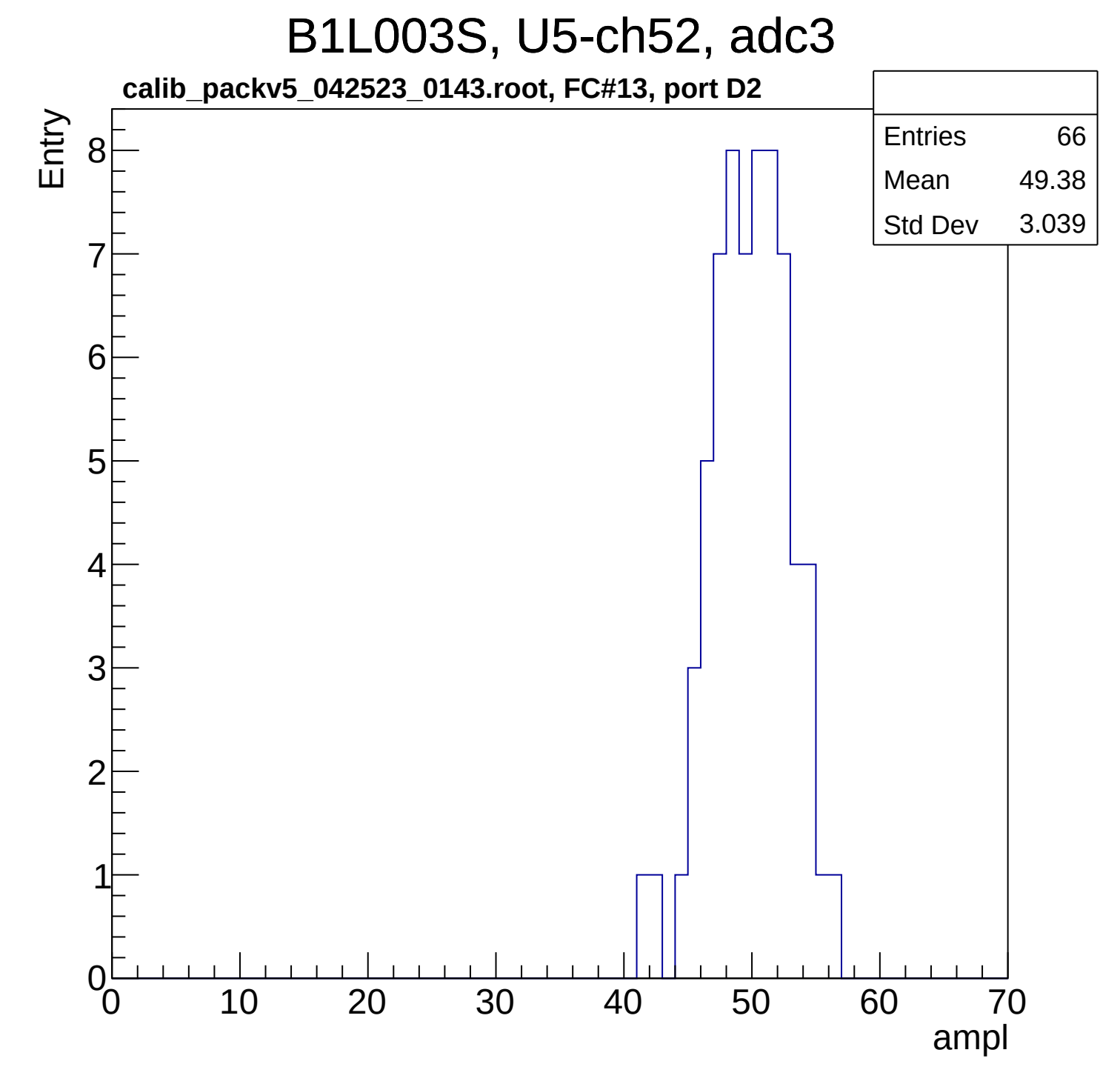

B1L003S, U5-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

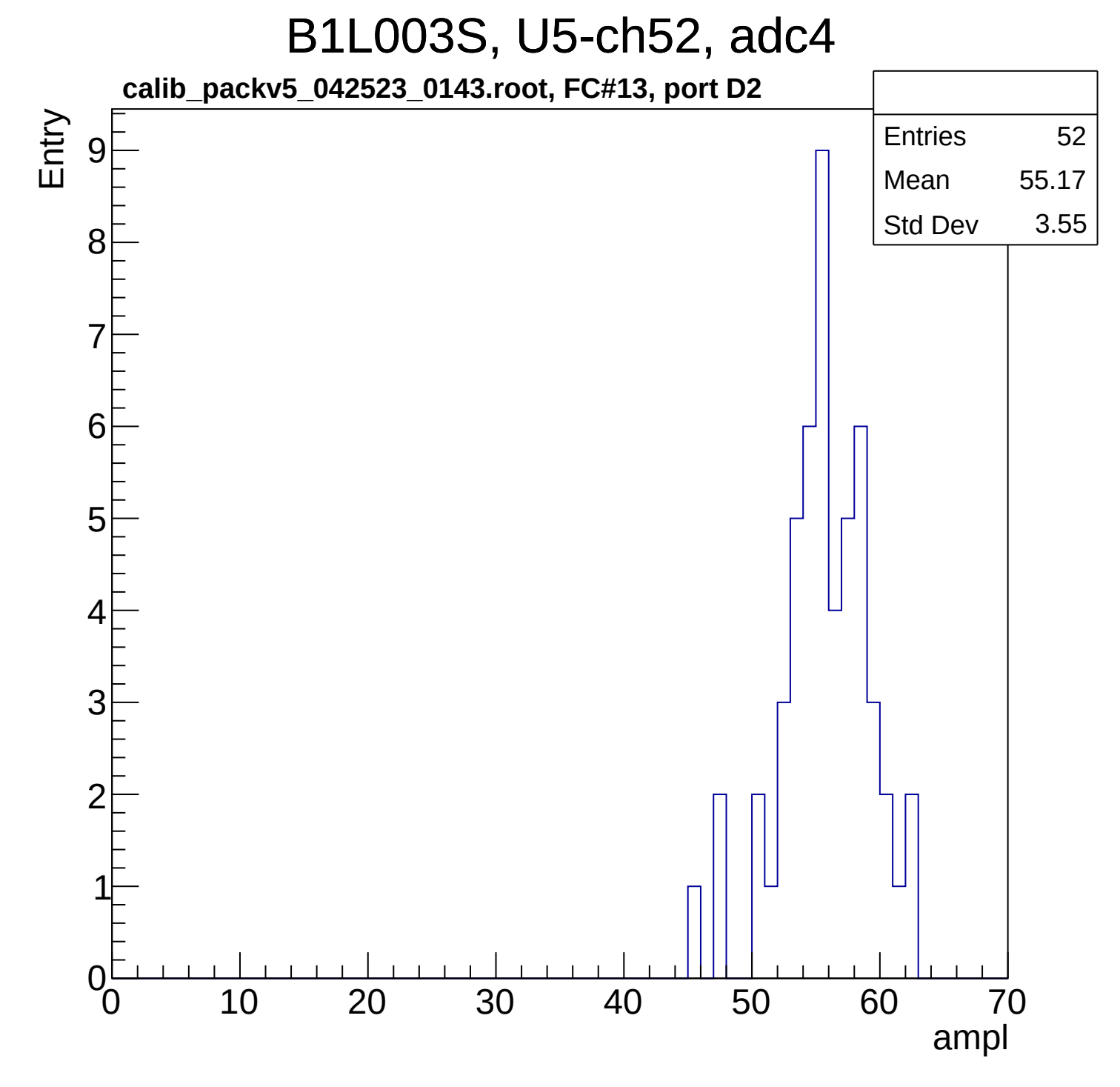

B1L003S, U5-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

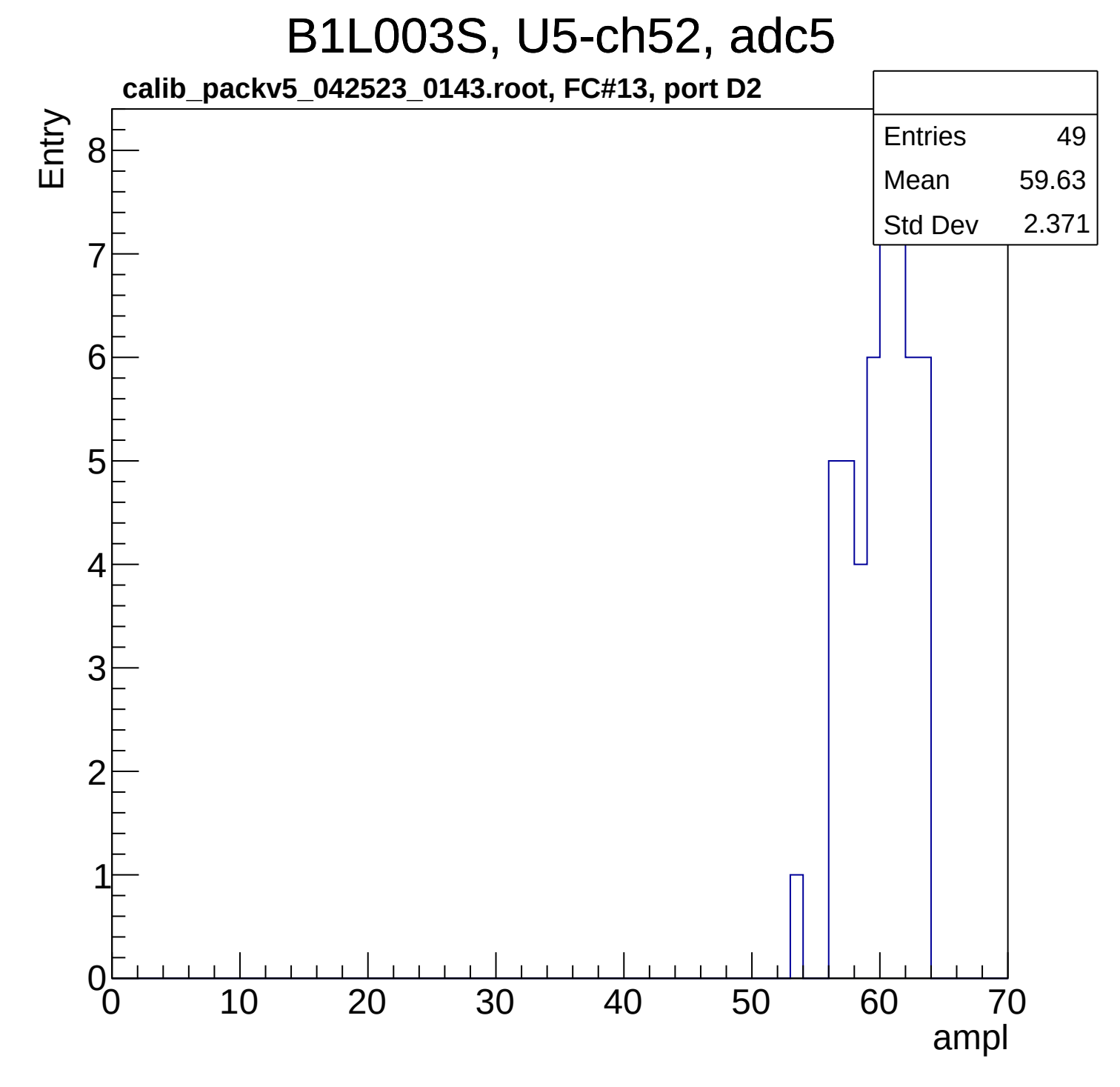

B1L003S, U5-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.47
Std Dev 4.785
Gaus mean : 31.0608
Gaus Width: 3.8025
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.72
Std Dev 2.864
Gaus mean : 37.3304
Gaus Width: 3.1597
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.38
Std Dev 3.761
Gaus mean : 42.6134
Gaus Width: 3.5048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.38
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.17
Std Dev 3.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.63
Std Dev 2.371
Entry
ampl
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2
3
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5
6
7
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0
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40
50
60
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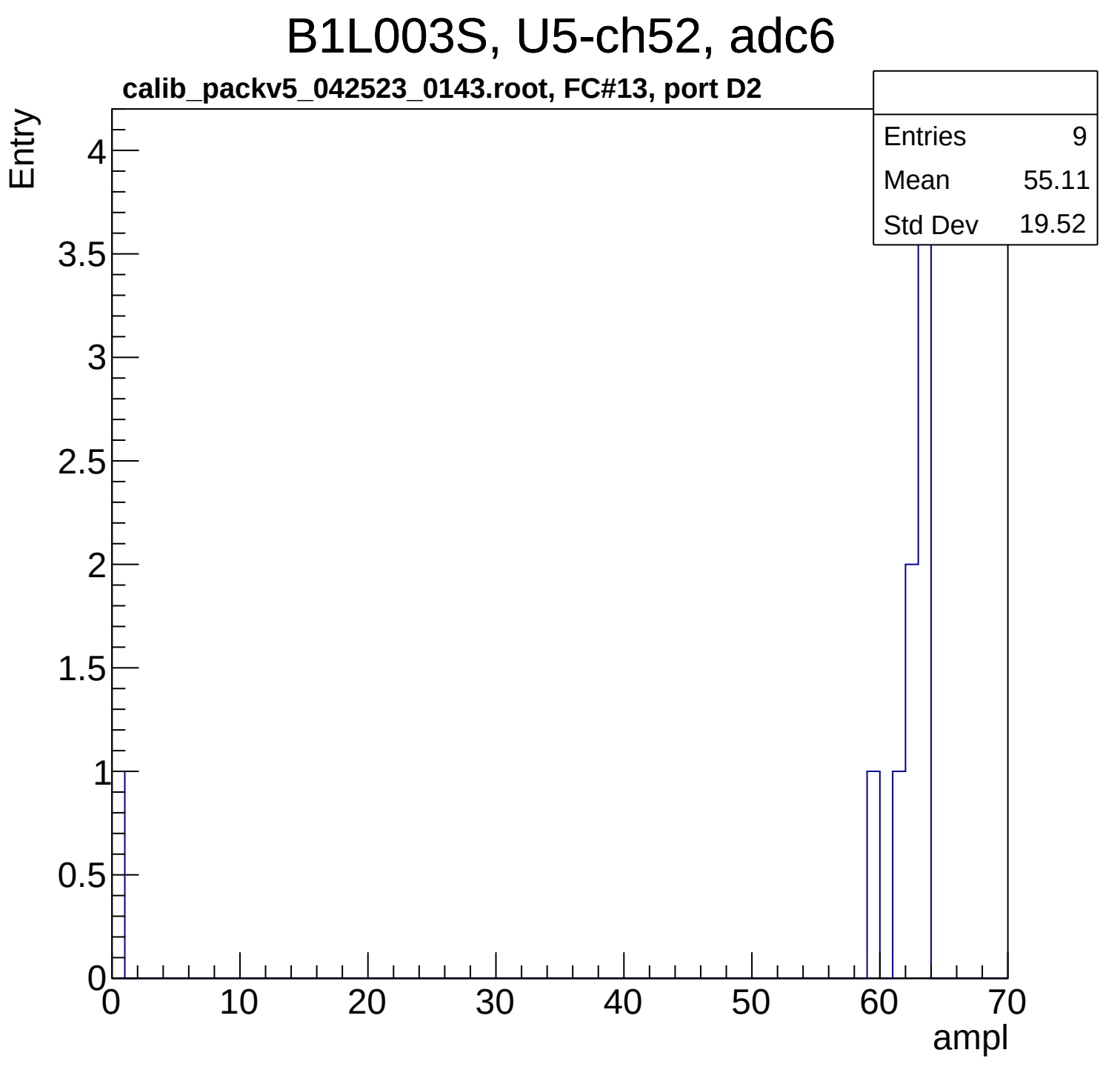
B1L003S, U5-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.11
Std Dev 19.52
Entry
ampl
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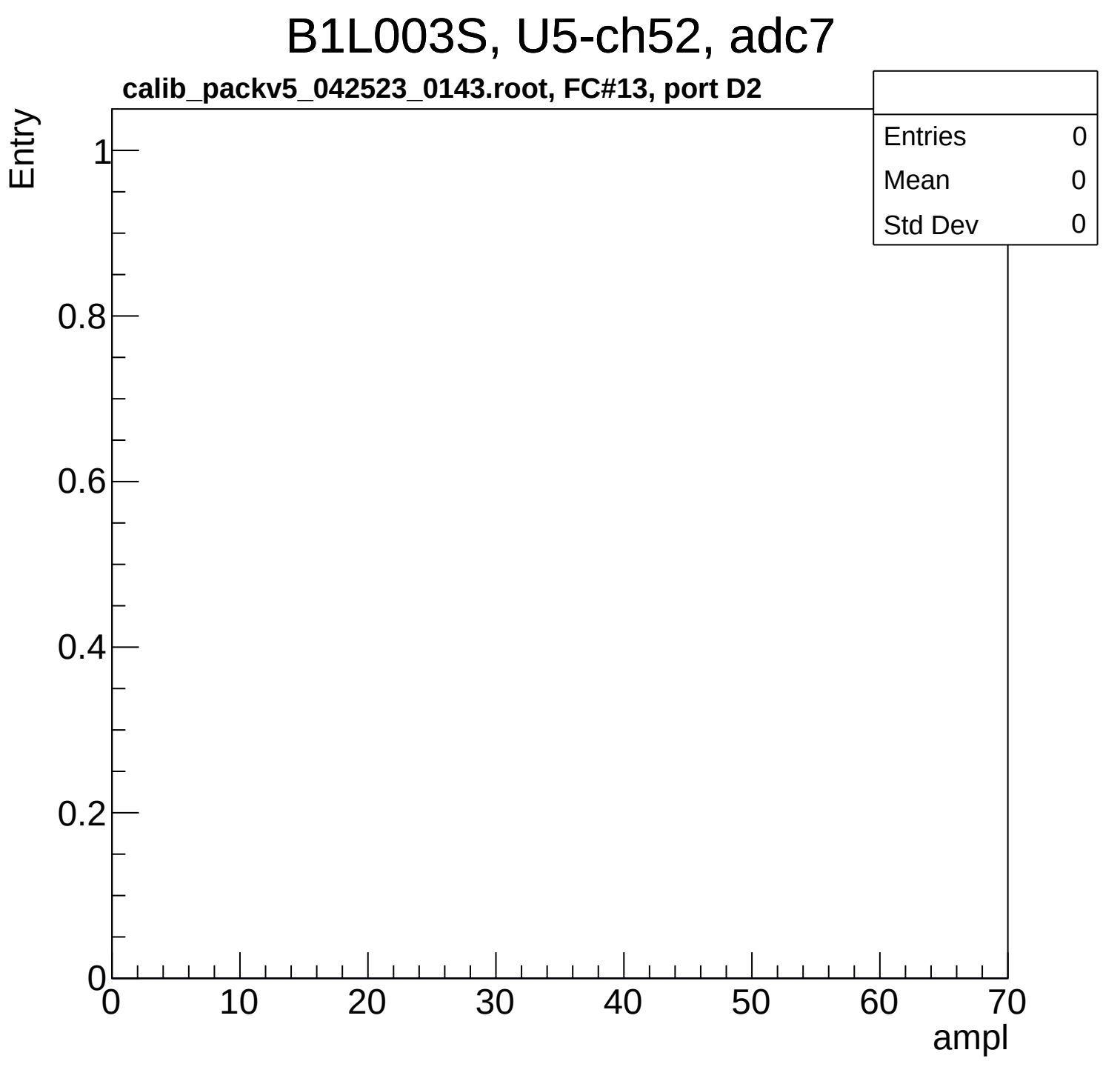
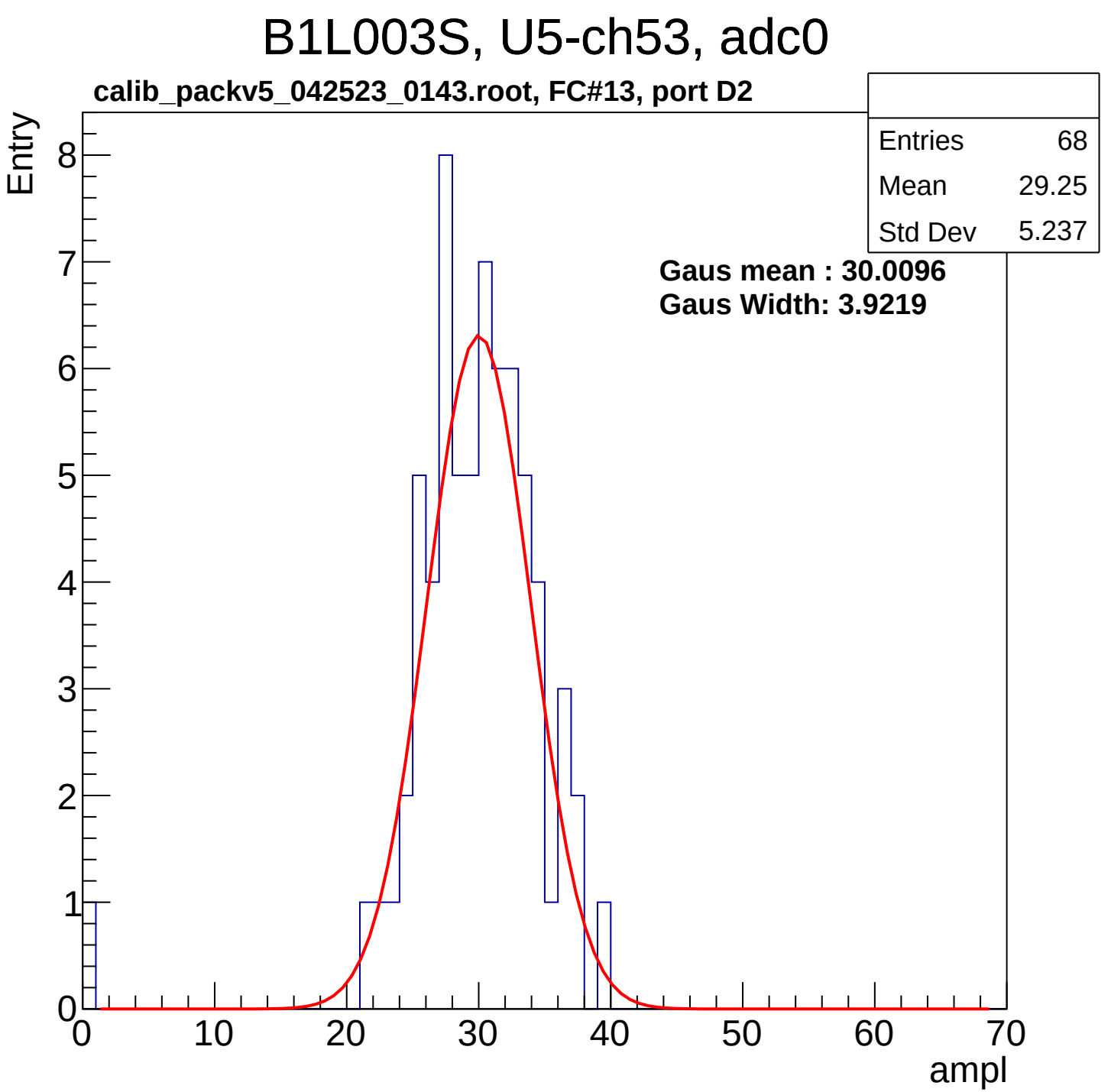
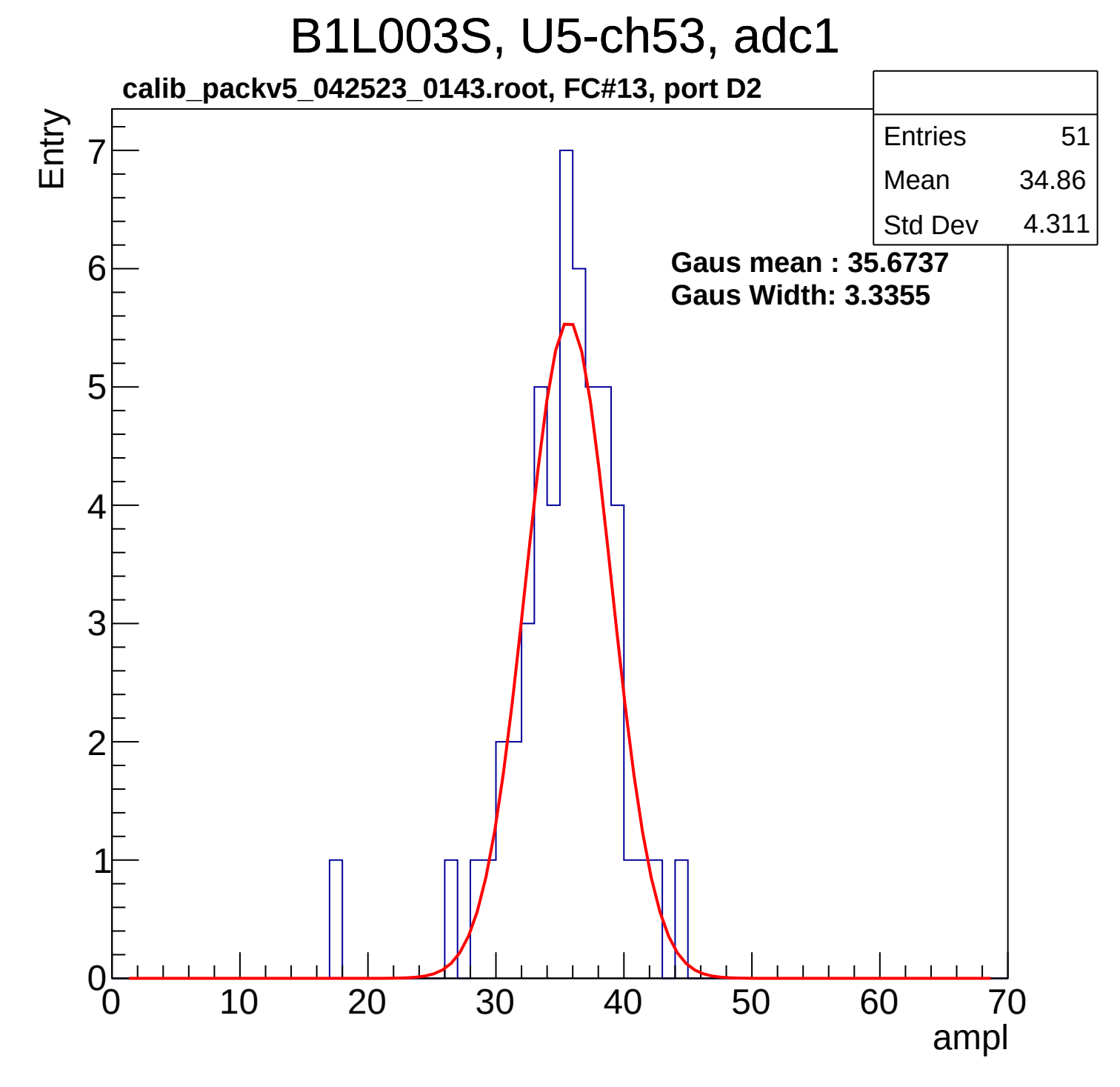
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.25
Std Dev 5.237
Gaus mean : 30.0096
Gaus Width: 3.9219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 34.86
Std Dev 4.311
Gaus mean : 35.6737
Gaus Width: 3.3355
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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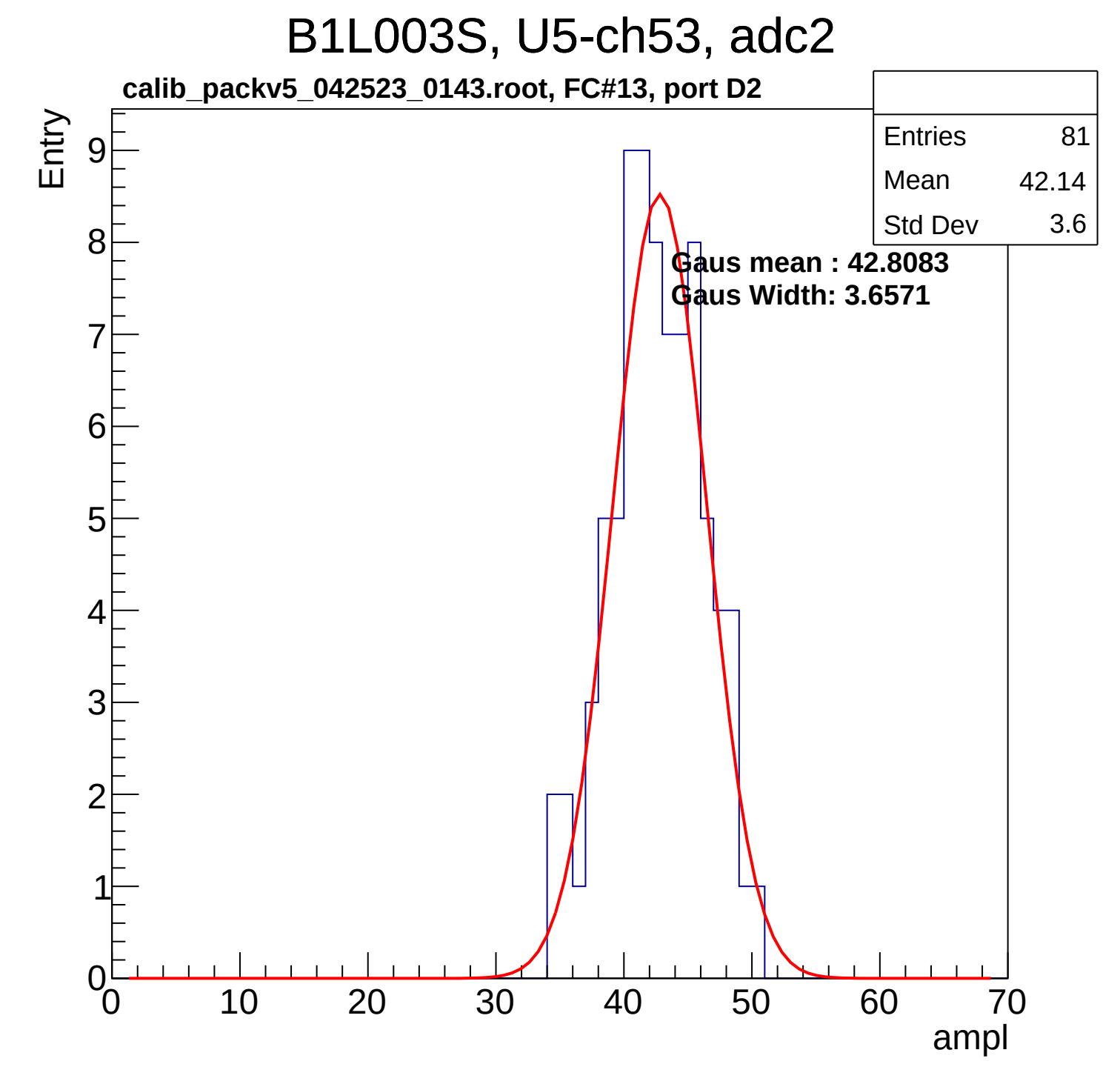
B1L003S, U5-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 42.14
Std Dev 3.6
Gaus mean : 42.8083
Gaus Width: 3.6571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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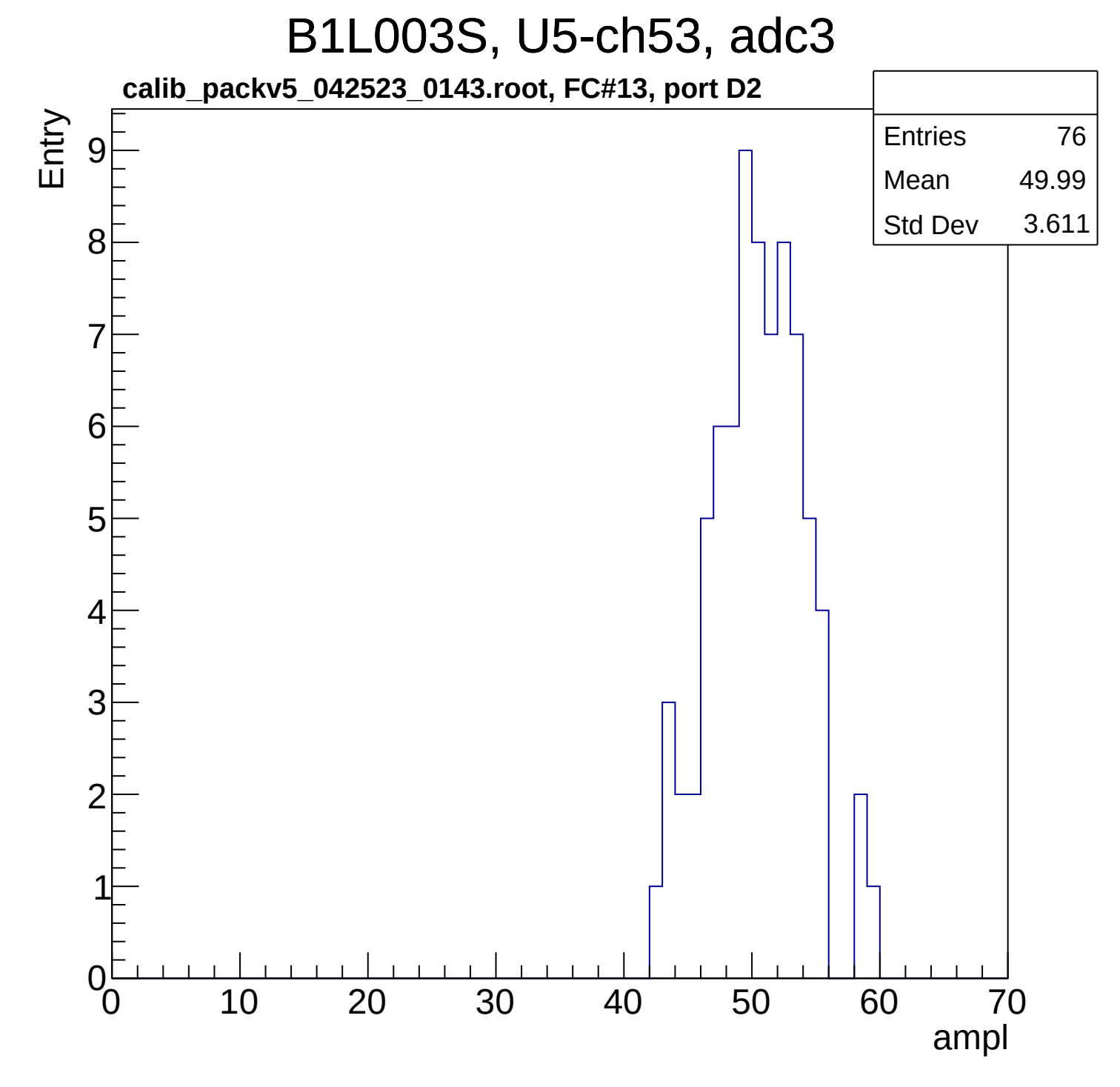
B1L003S, U5-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 49.99
Std Dev 3.611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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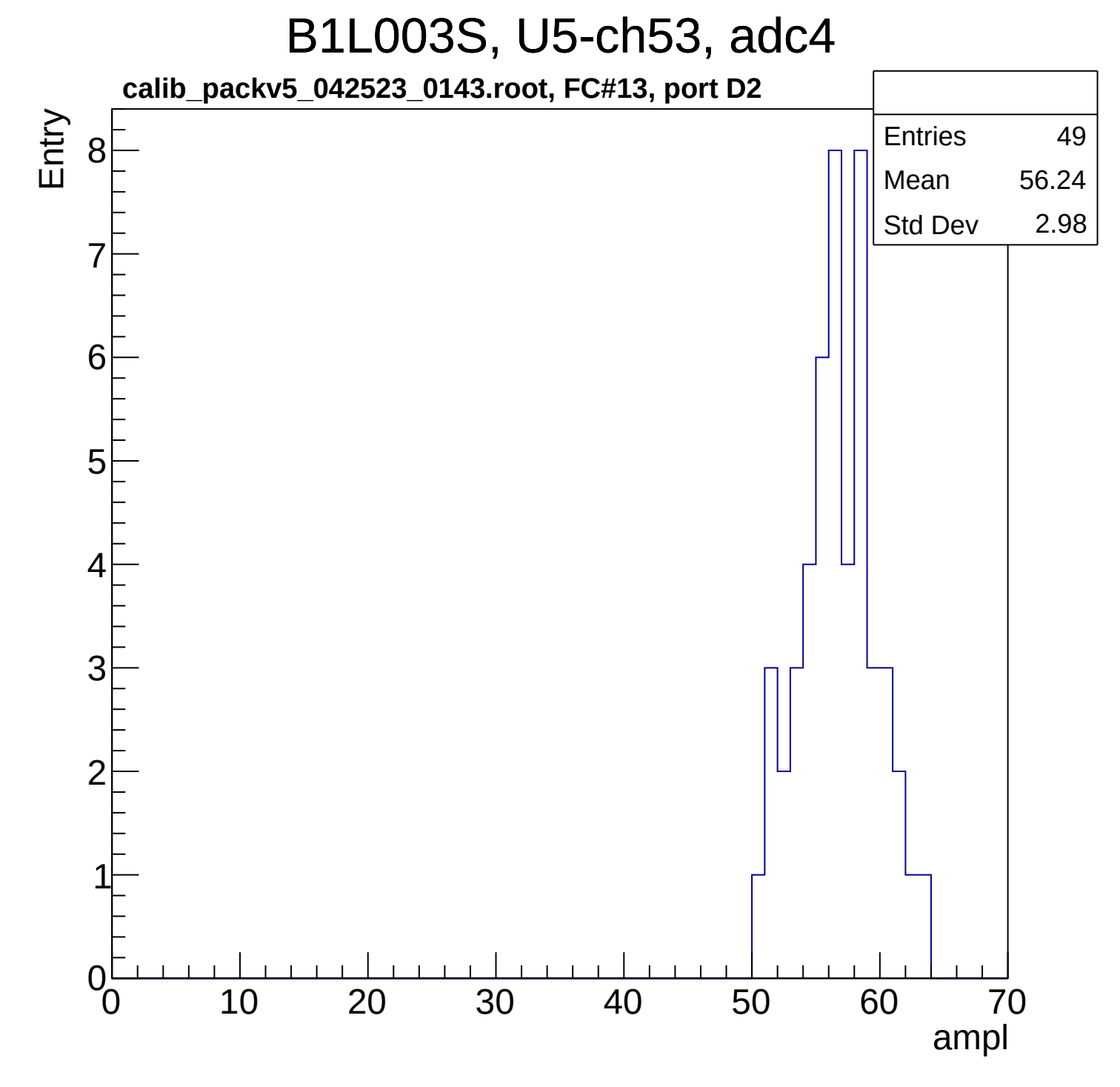
B1L003S, U5-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 56.24
Std Dev 2.98
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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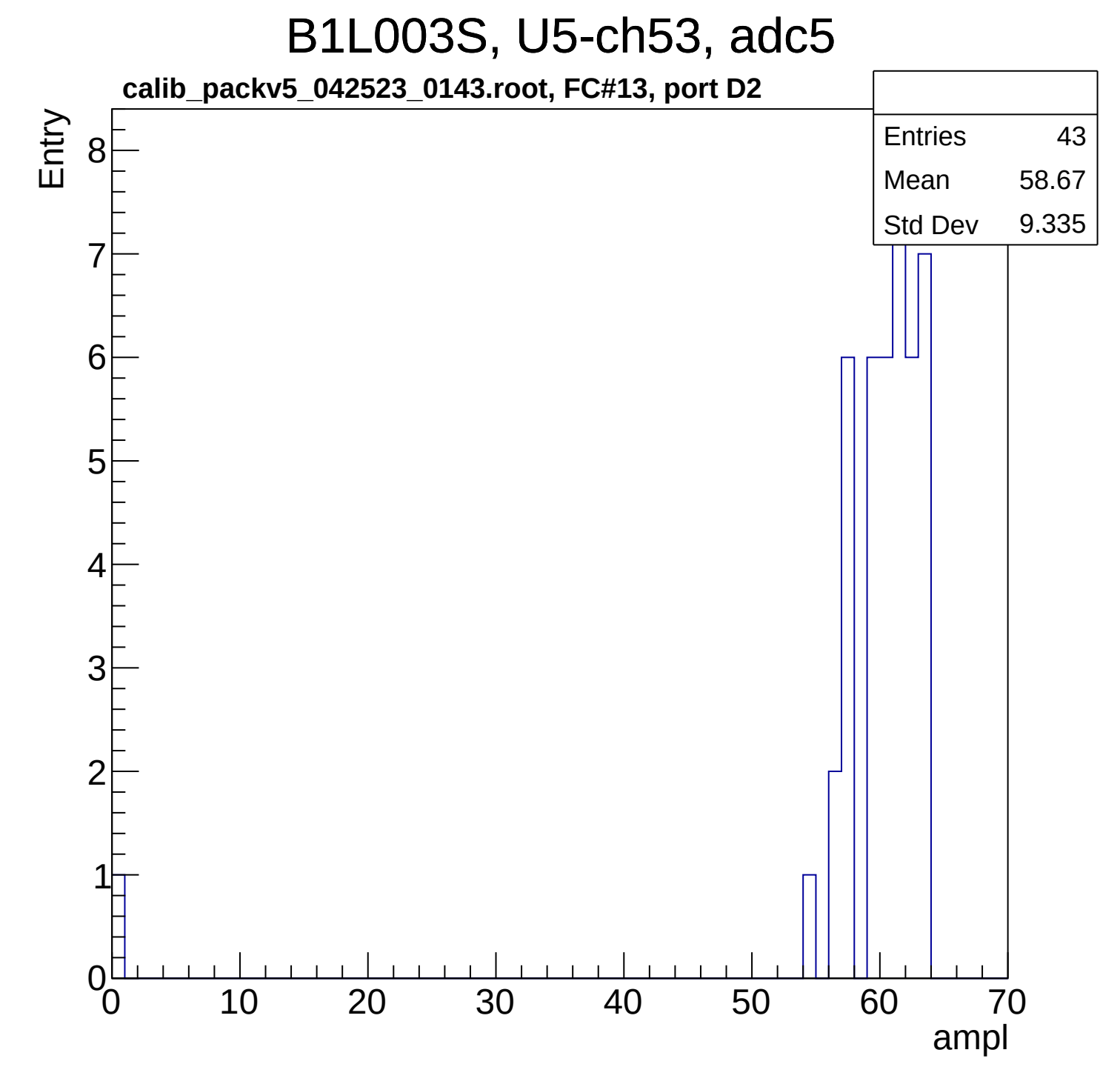
B1L003S, U5-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.67
Std Dev 9.335
Entry
ampl
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1
2
3
4
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0
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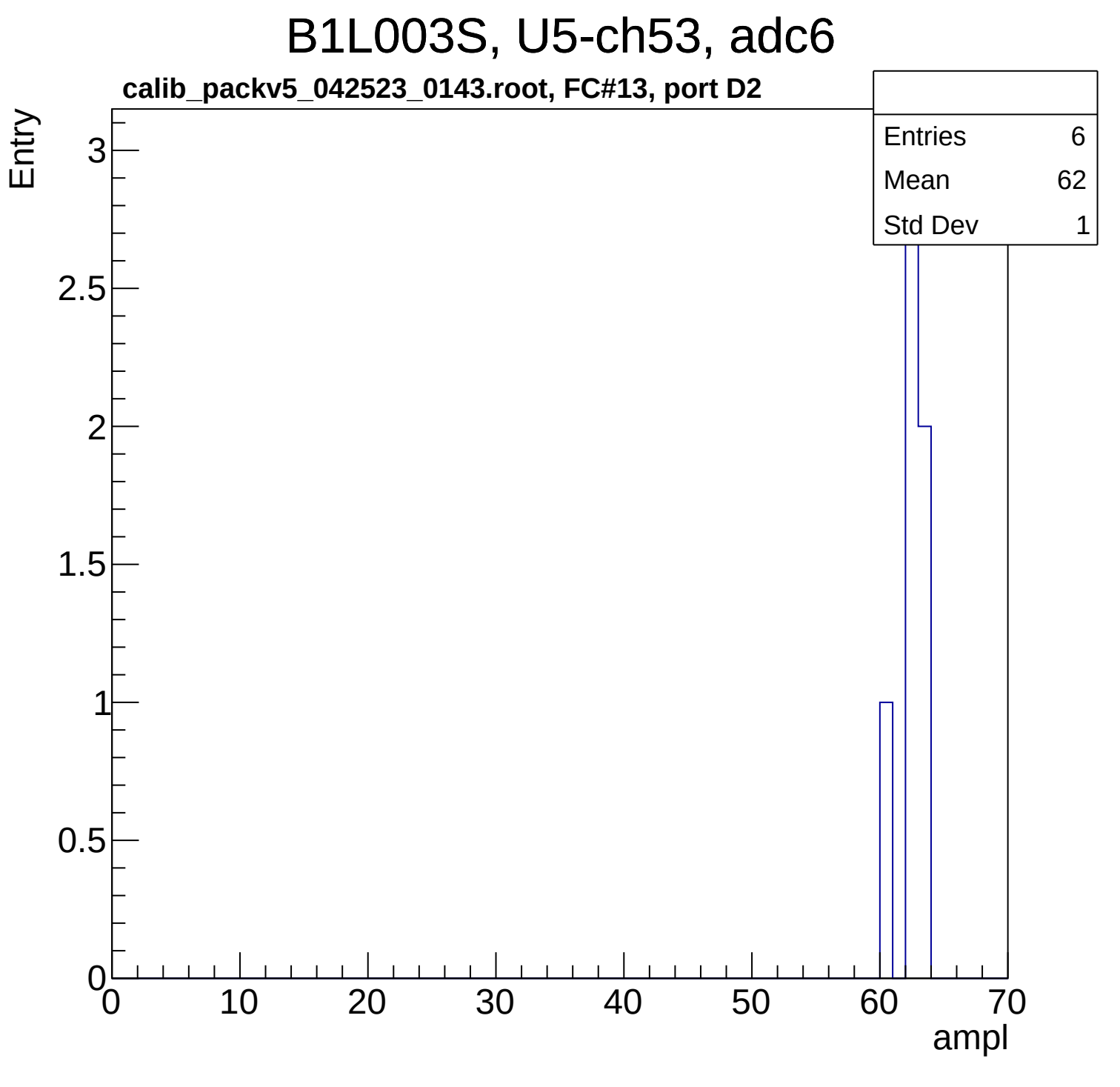
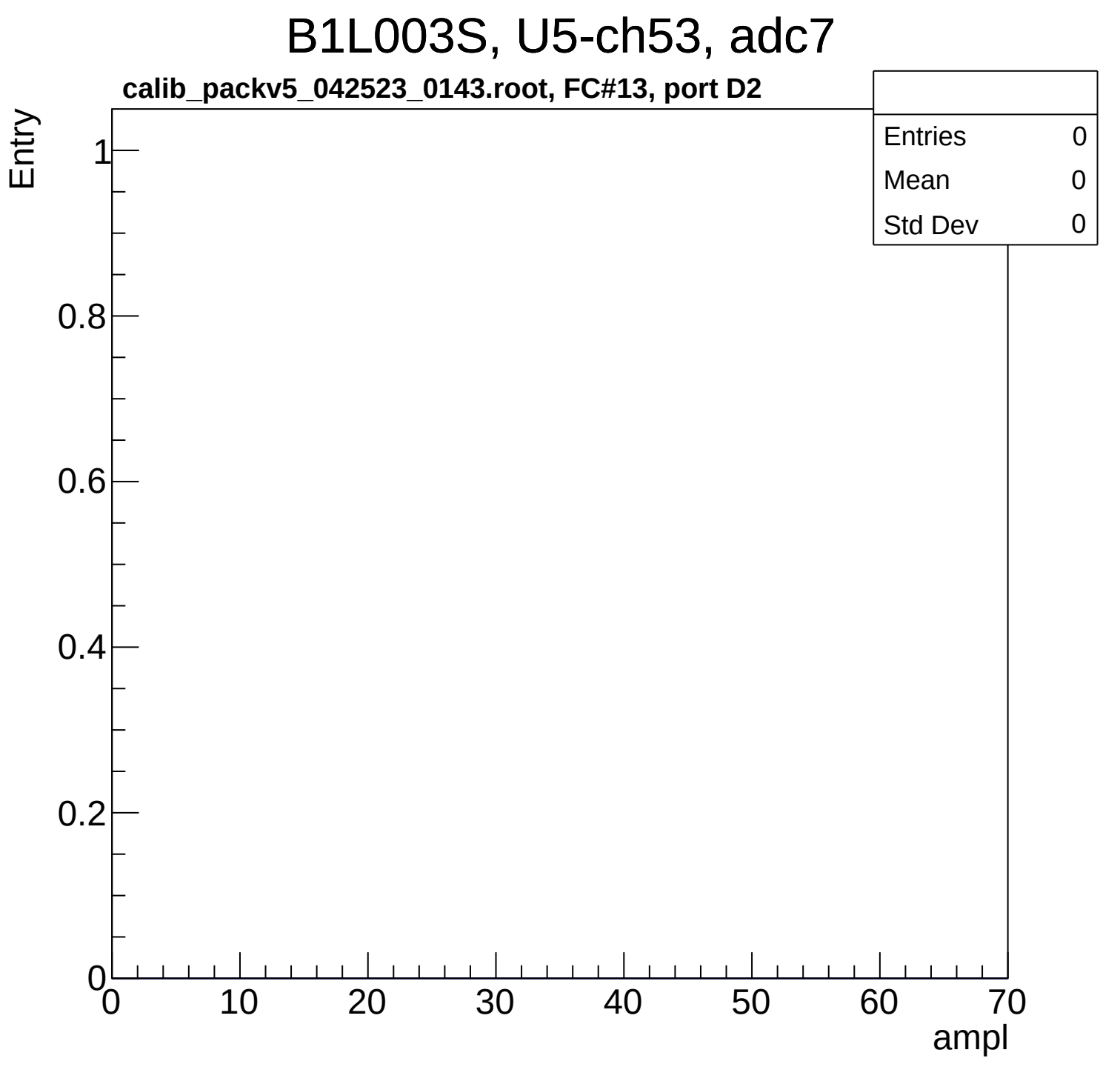
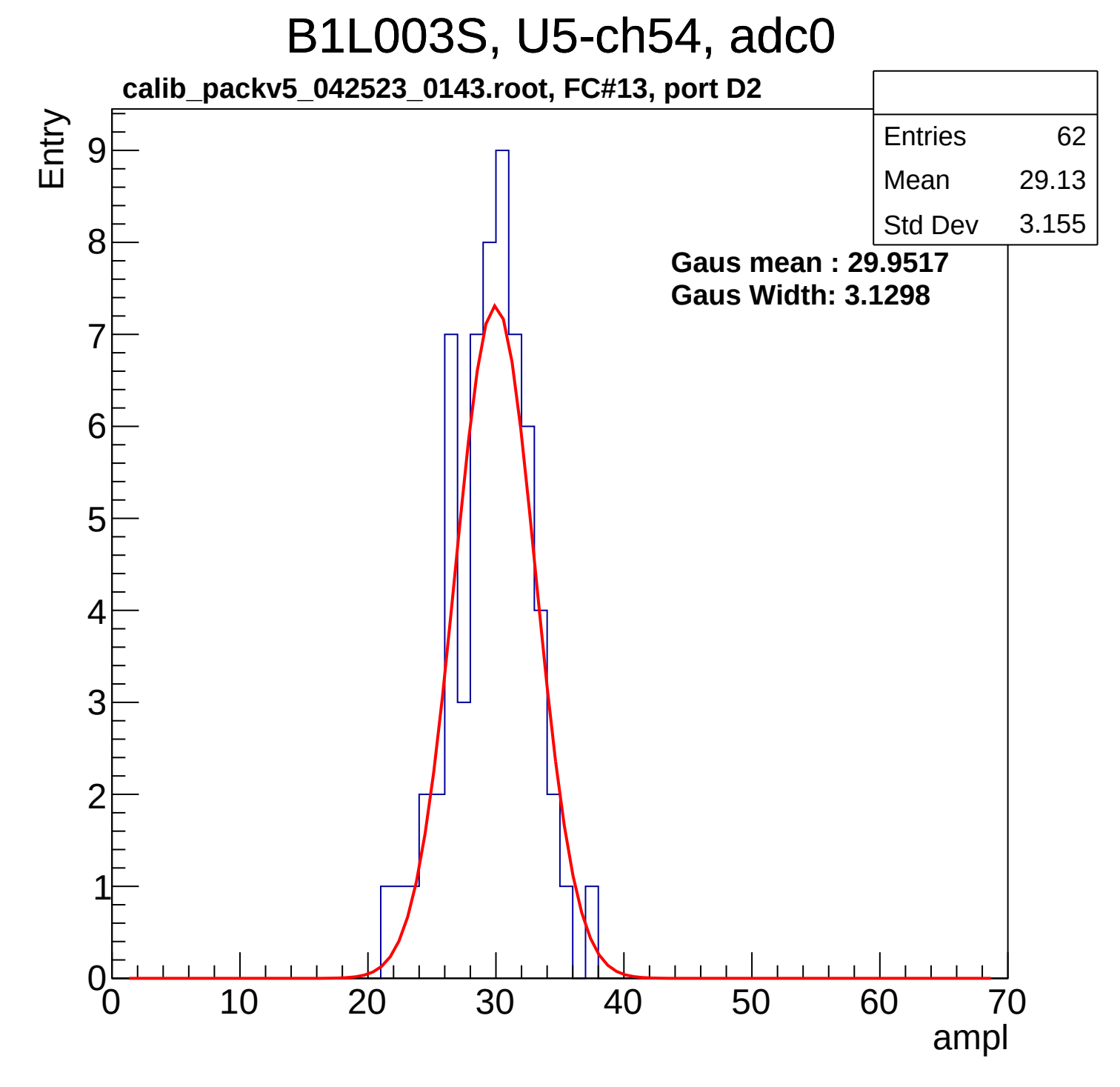
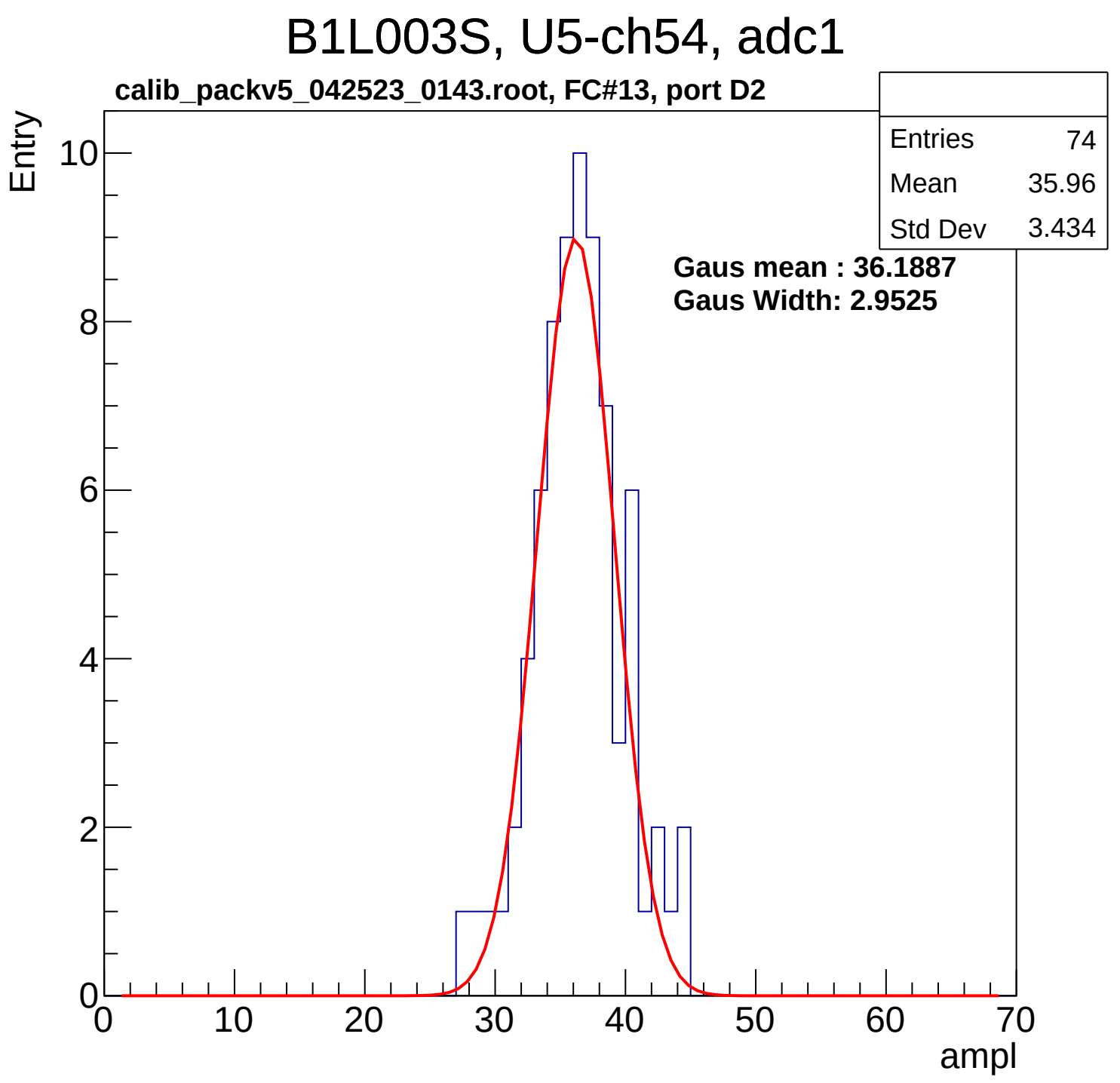

B1L003S, U5-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.13
Std Dev 3.155
Gaus mean : 29.9517
Gaus Width: 3.1298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.96
Std Dev 3.434
Gaus mean : 36.1887
Gaus Width: 2.9525
Entry
ampl
0
2
4
6
8
10
0
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40
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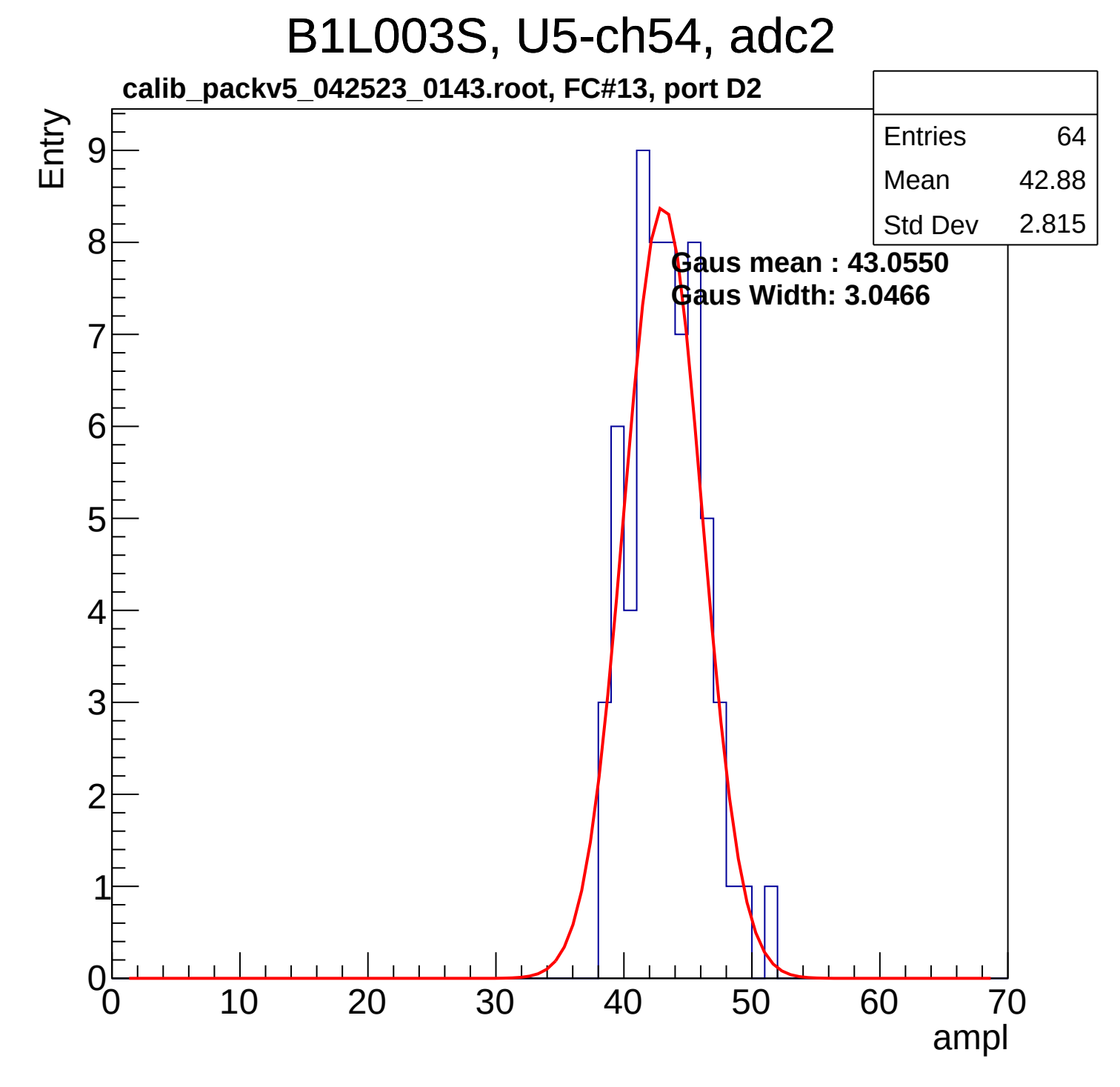
B1L003S, U5-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.88
Std Dev 2.815
Gaus mean : 43.0550
Gaus Width: 3.0466
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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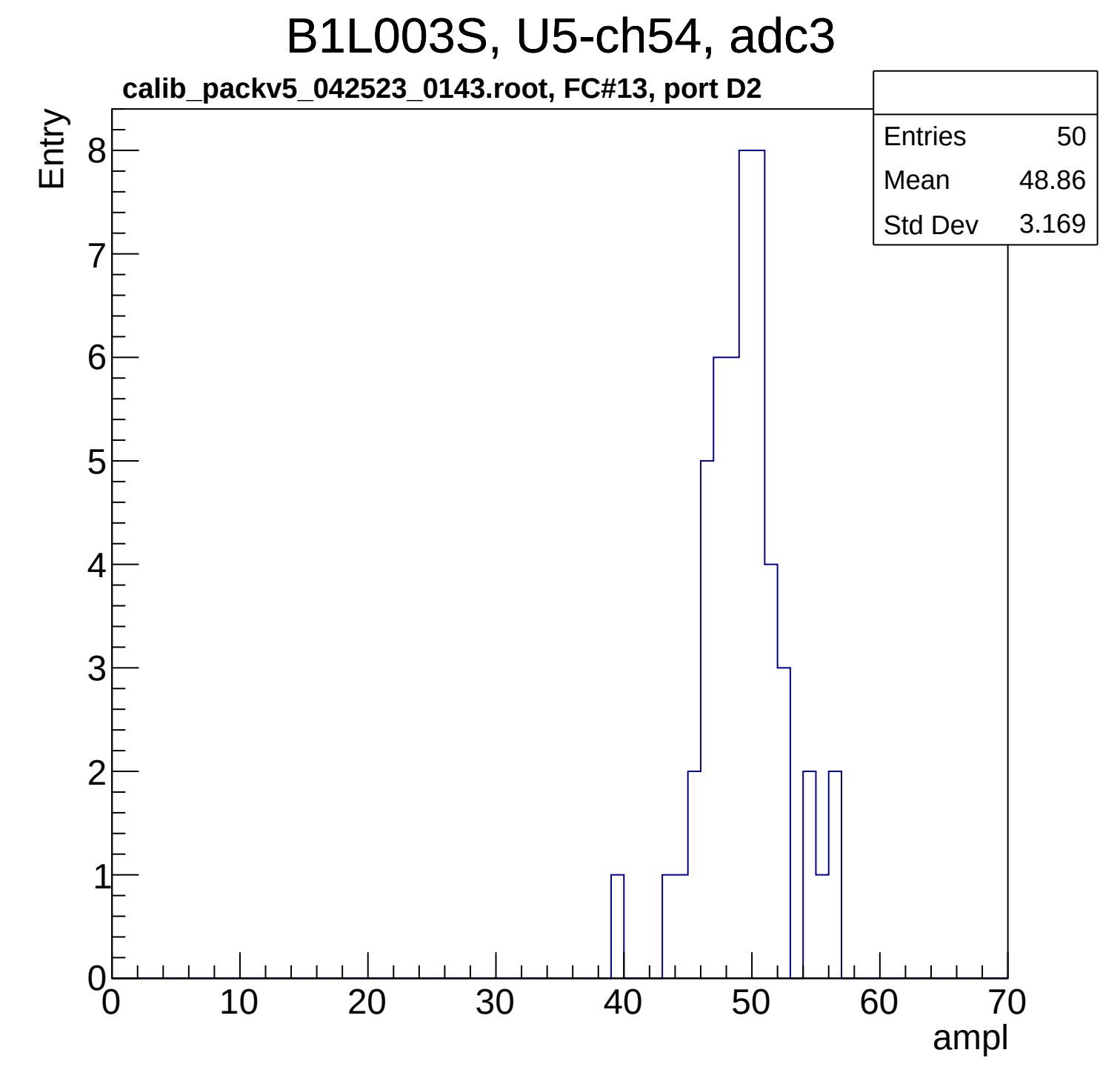
B1L003S, U5-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 48.86
Std Dev 3.169
Entry
ampl
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1
2
3
4
5
6
7
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0
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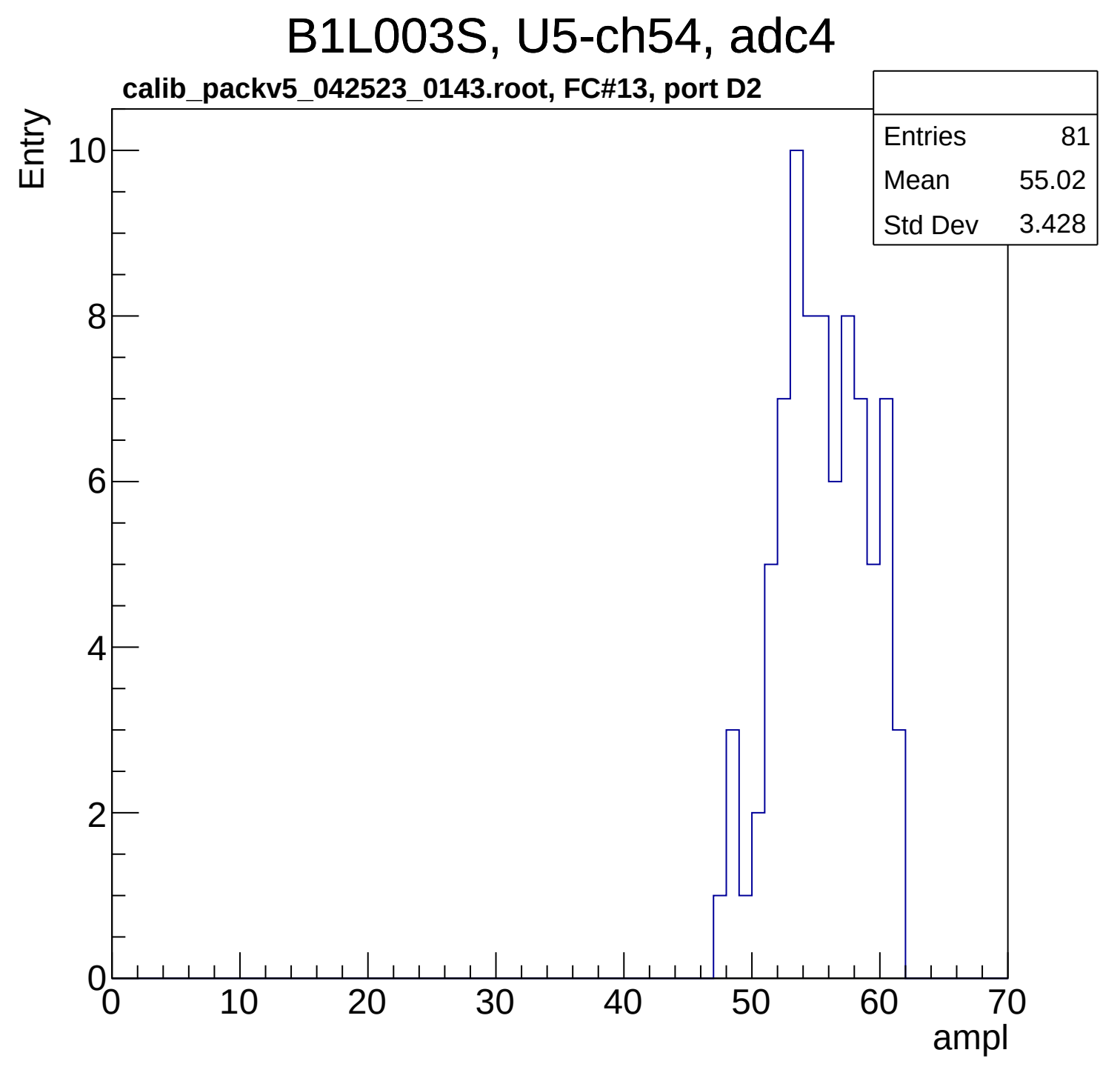
B1L003S, U5-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 55.02
Std Dev 3.428
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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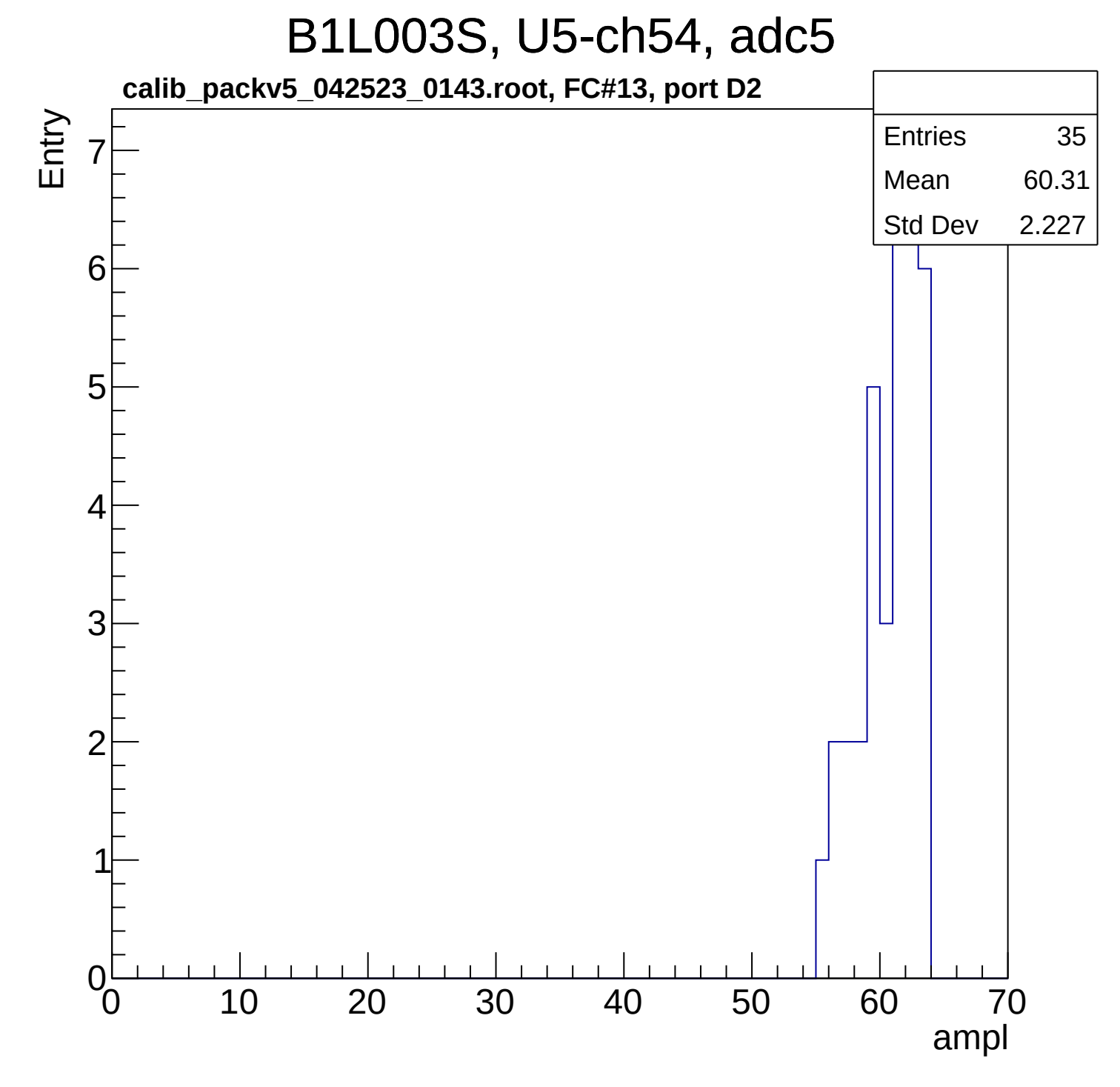
B1L003S, U5-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 60.31
Std Dev 2.227
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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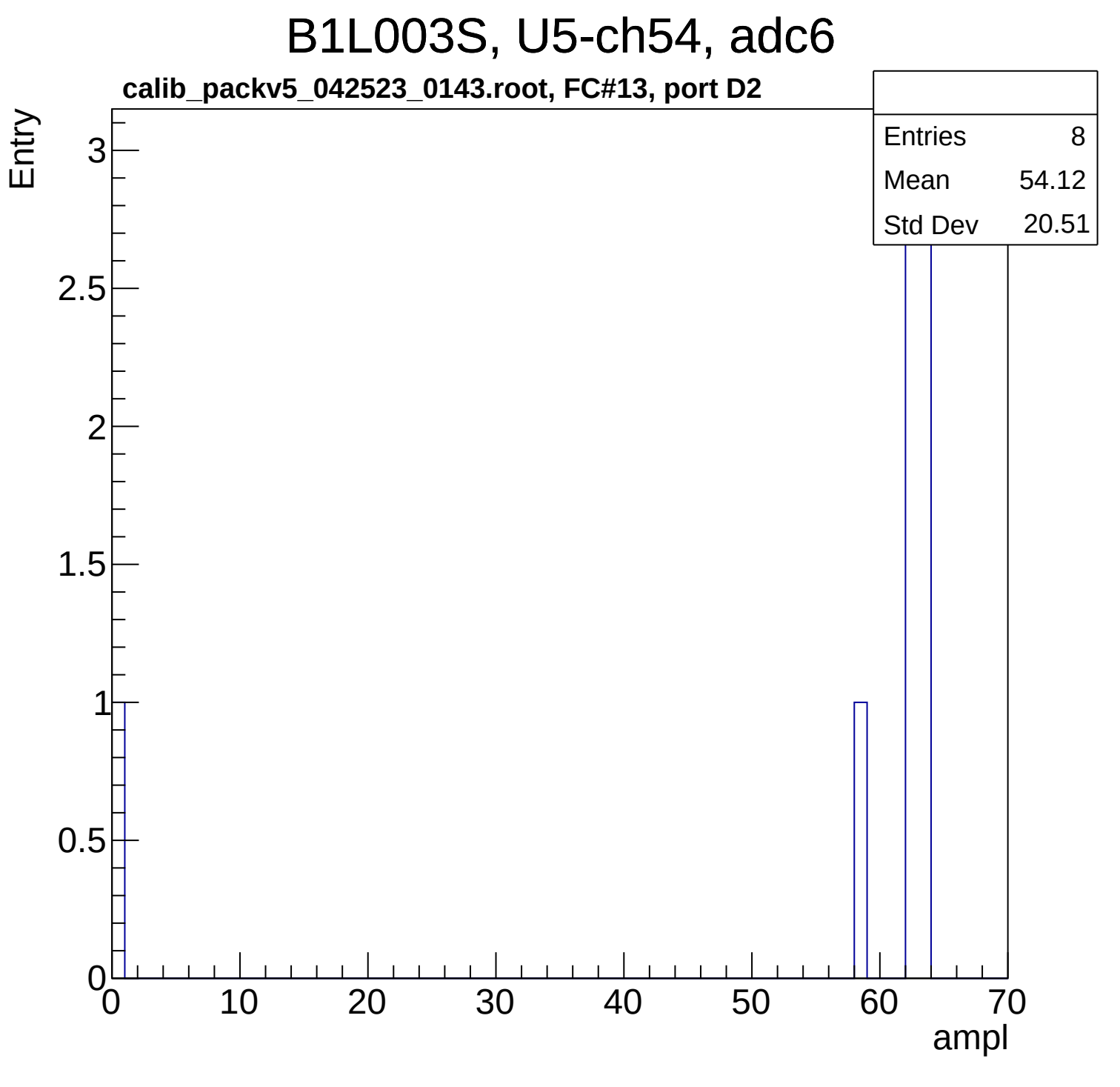
B1L003S, U5-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.12
Std Dev 20.51
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
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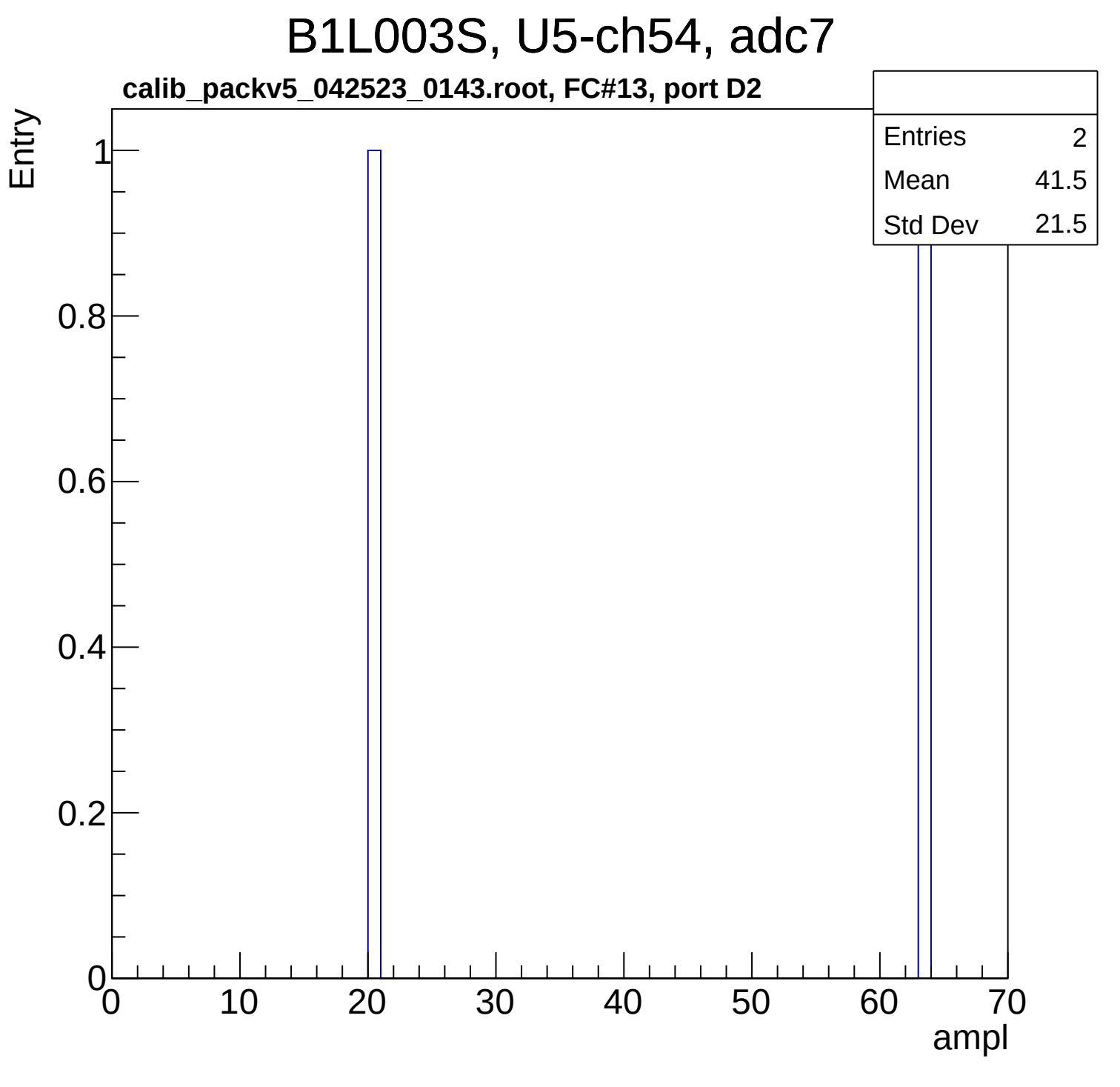
B1L003S, U5-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
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60
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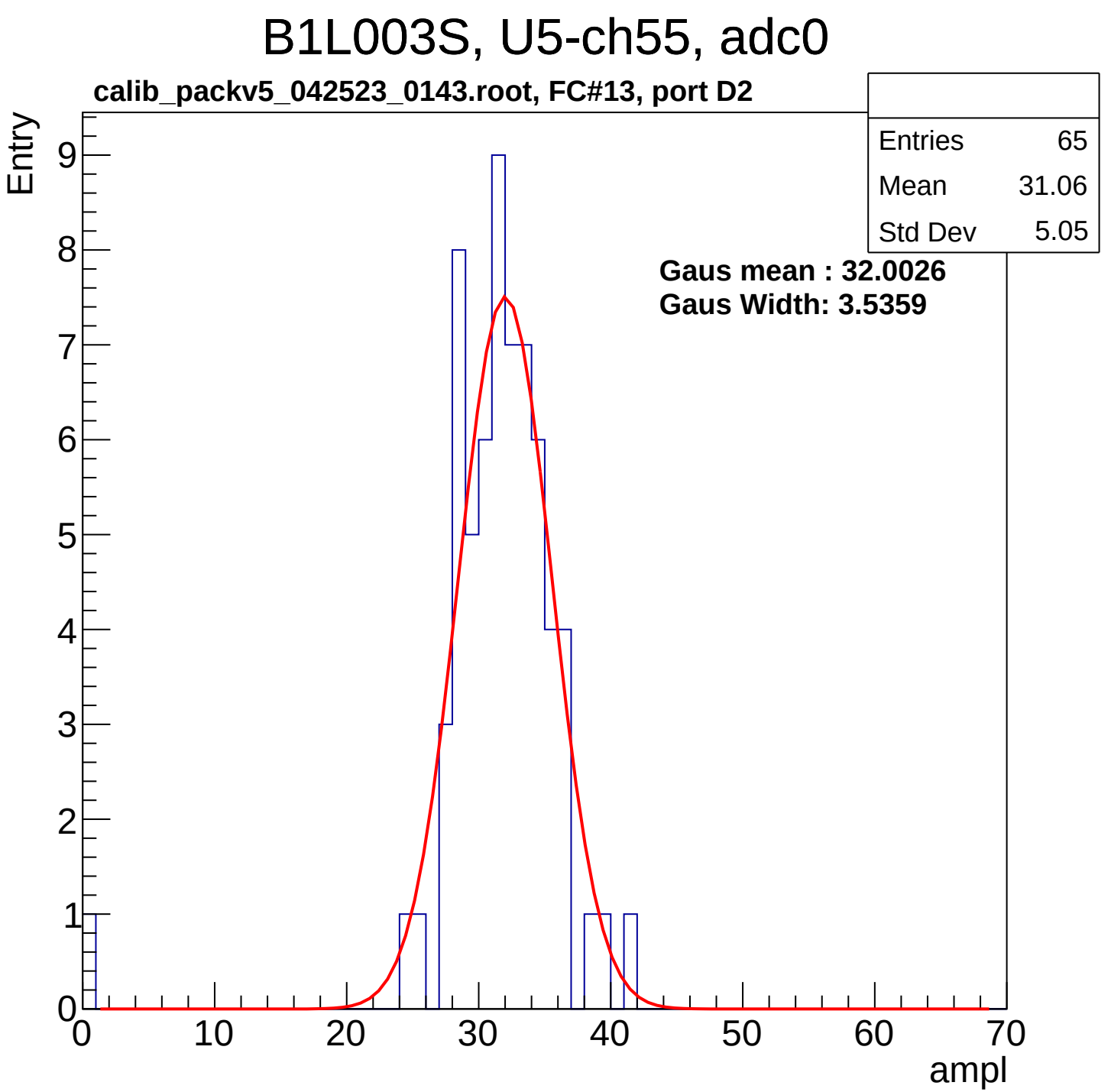
B1L003S, U5-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 31.06
Std Dev 5.05
Gaus mean : 32.0026
Gaus Width: 3.5359
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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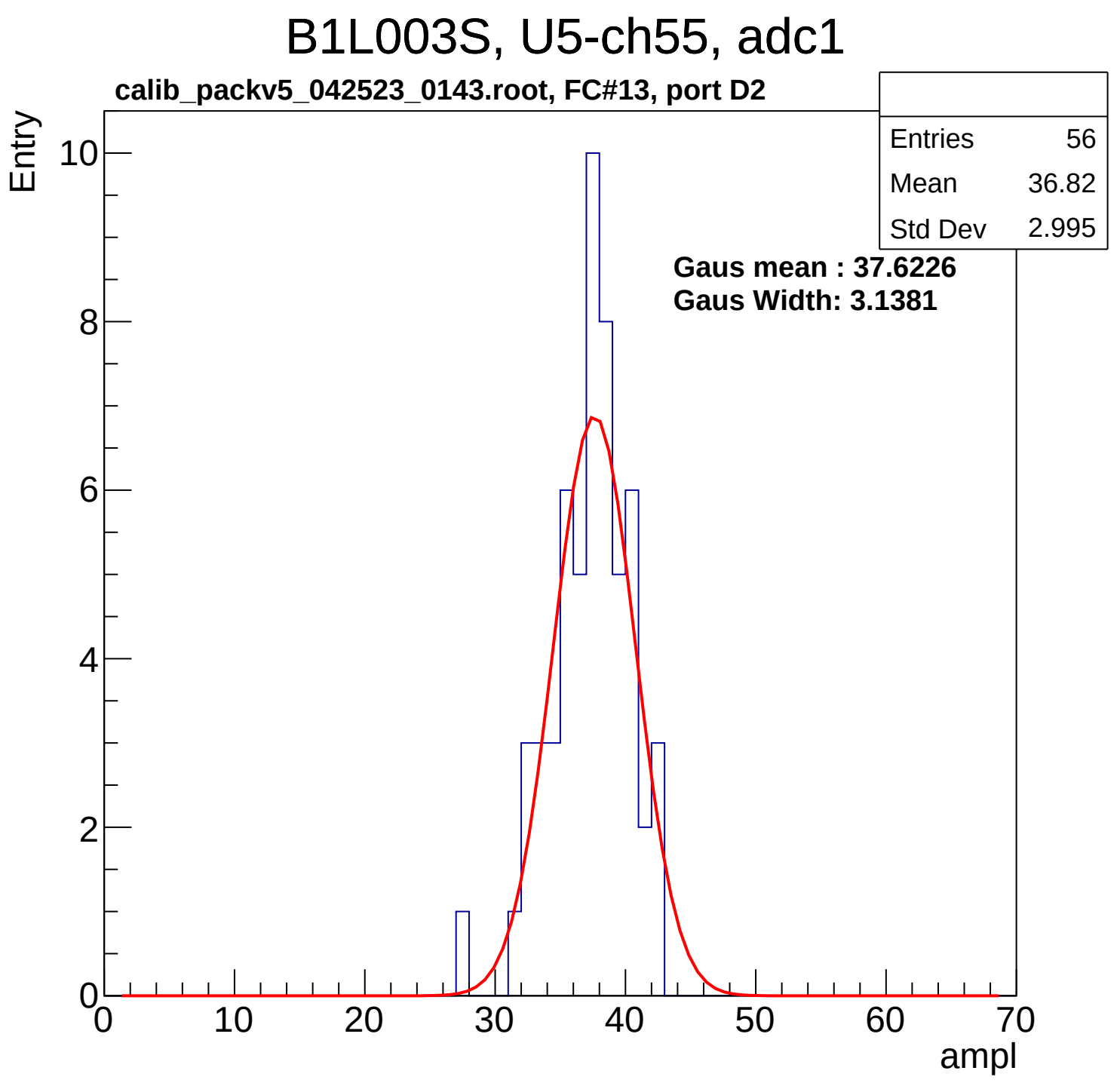
B1L003S, U5-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.82
Std Dev 2.995
Gaus mean : 37.6226
Gaus Width: 3.1381
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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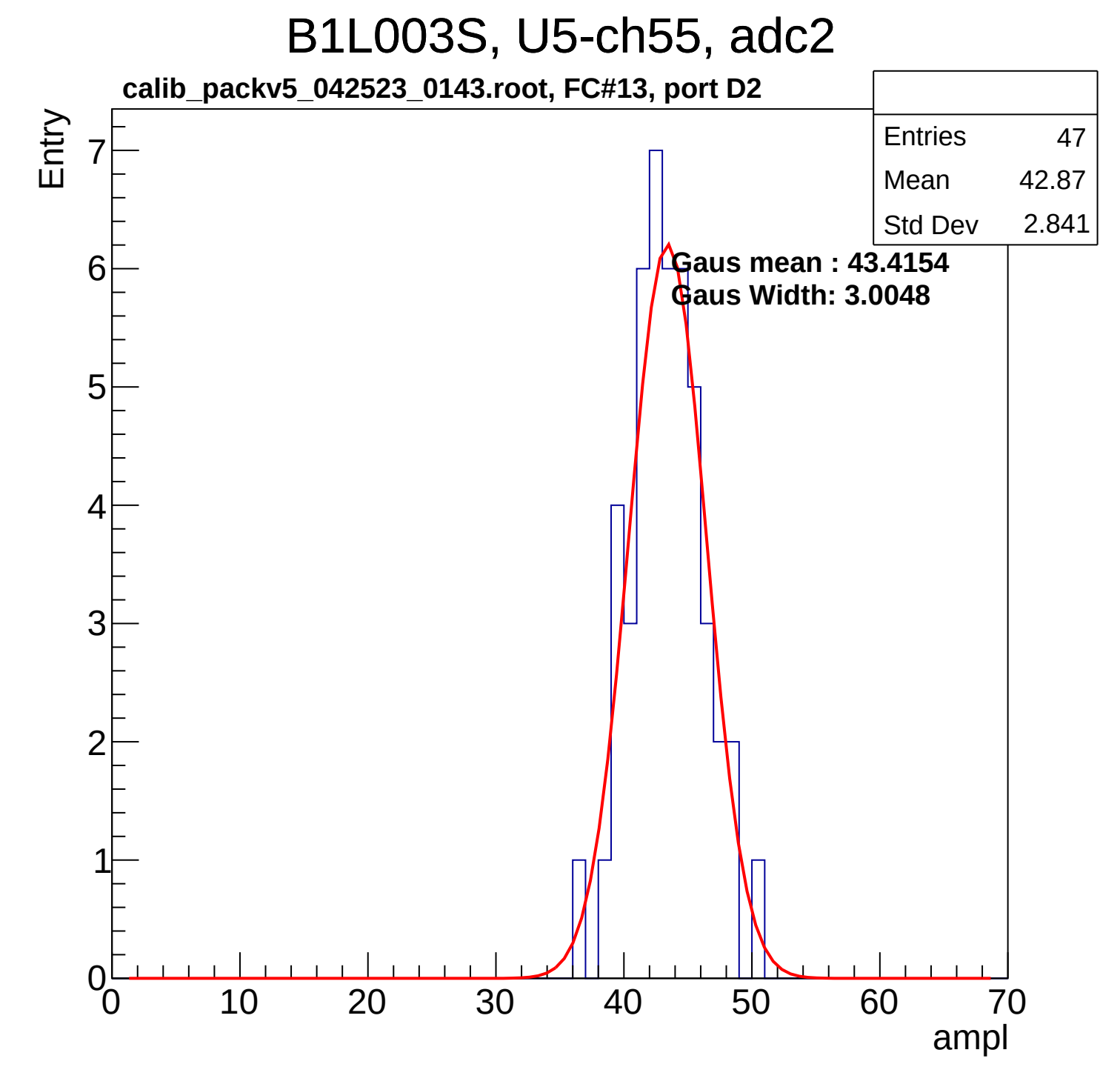
B1L003S, U5-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 42.87
Std Dev 2.841
Gaus mean : 43.4154
Gaus Width: 3.0048
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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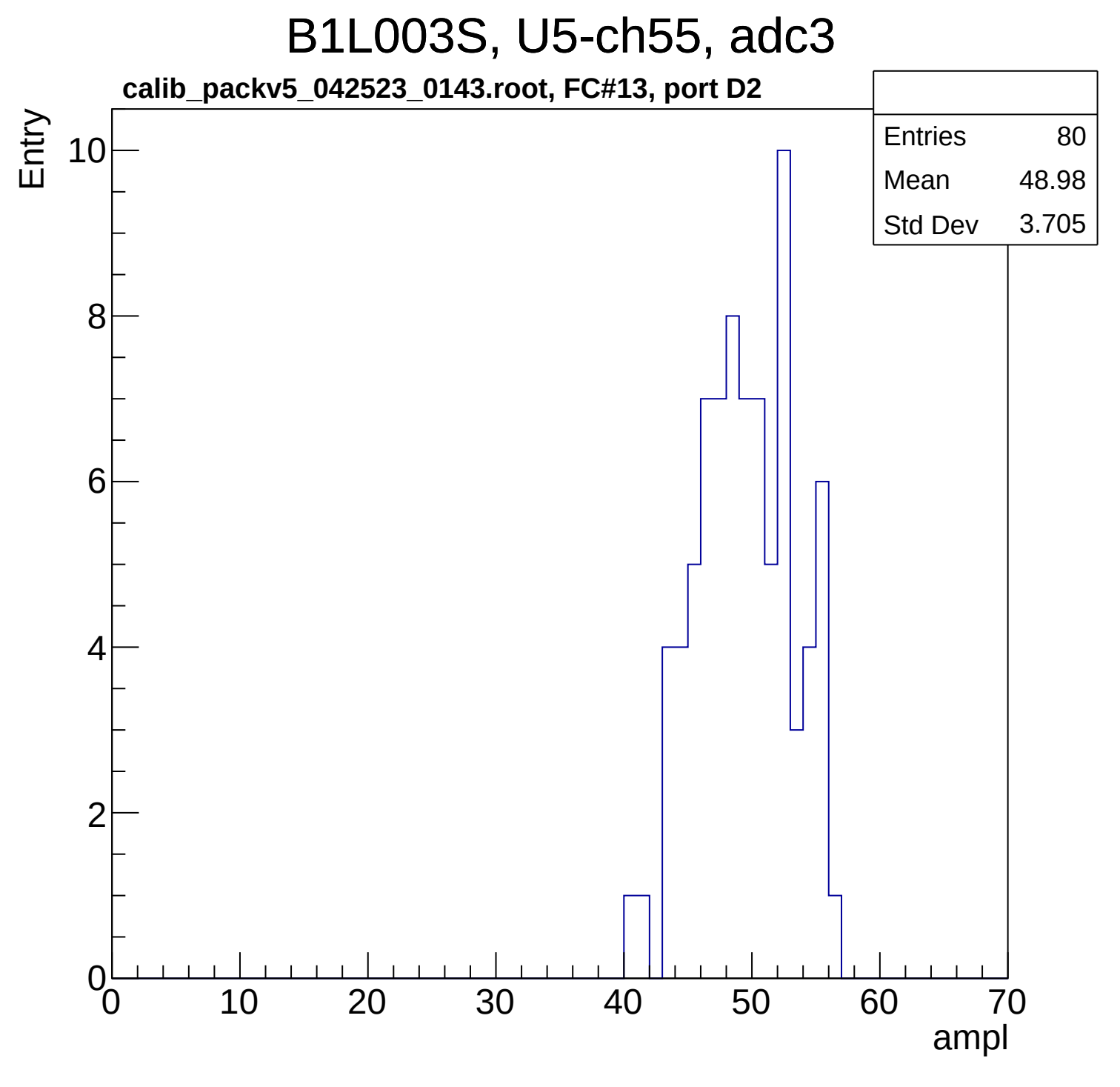
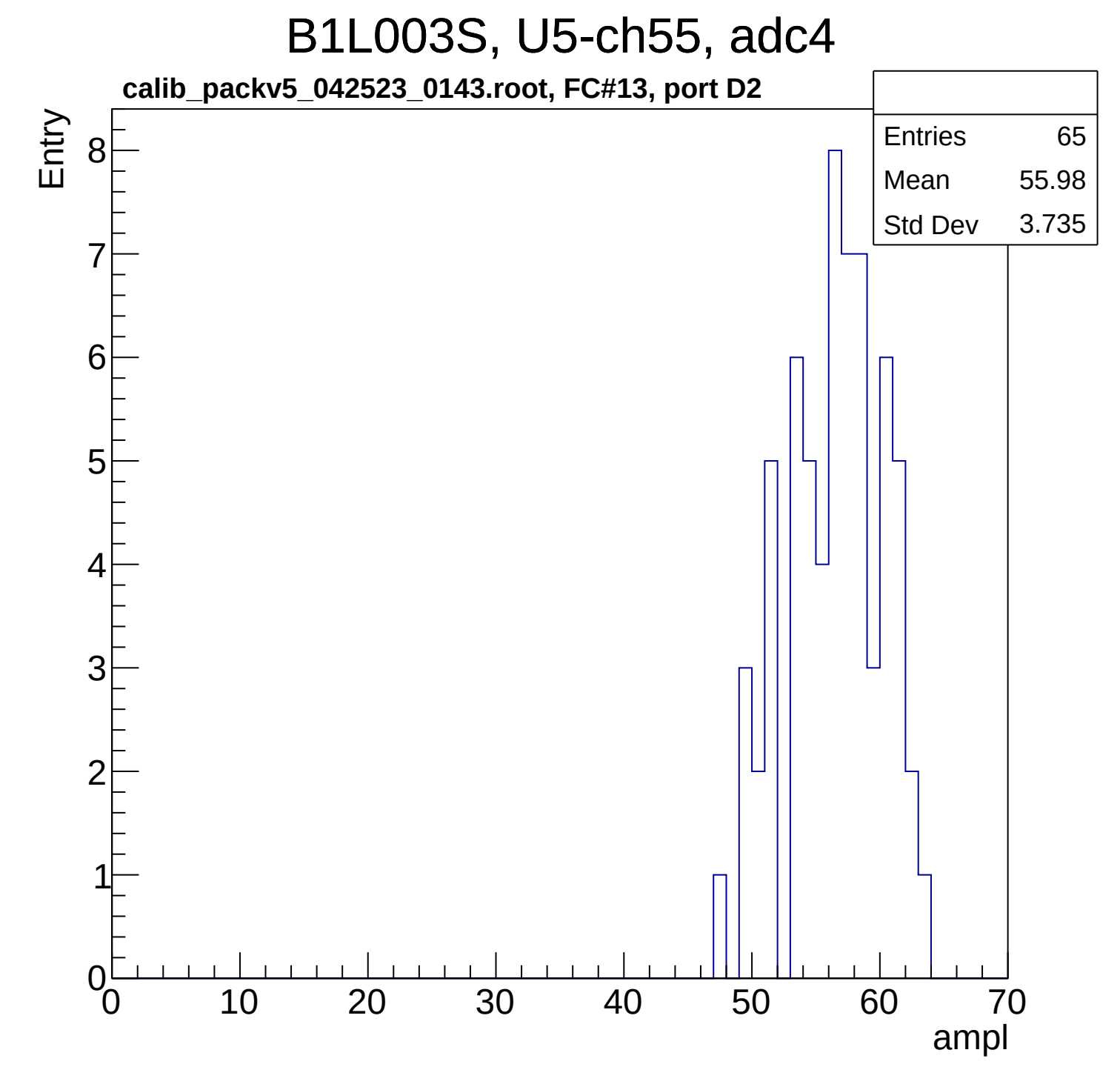

B1L003S, U5-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 48.98
Std Dev 3.705
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.98
Std Dev 3.735
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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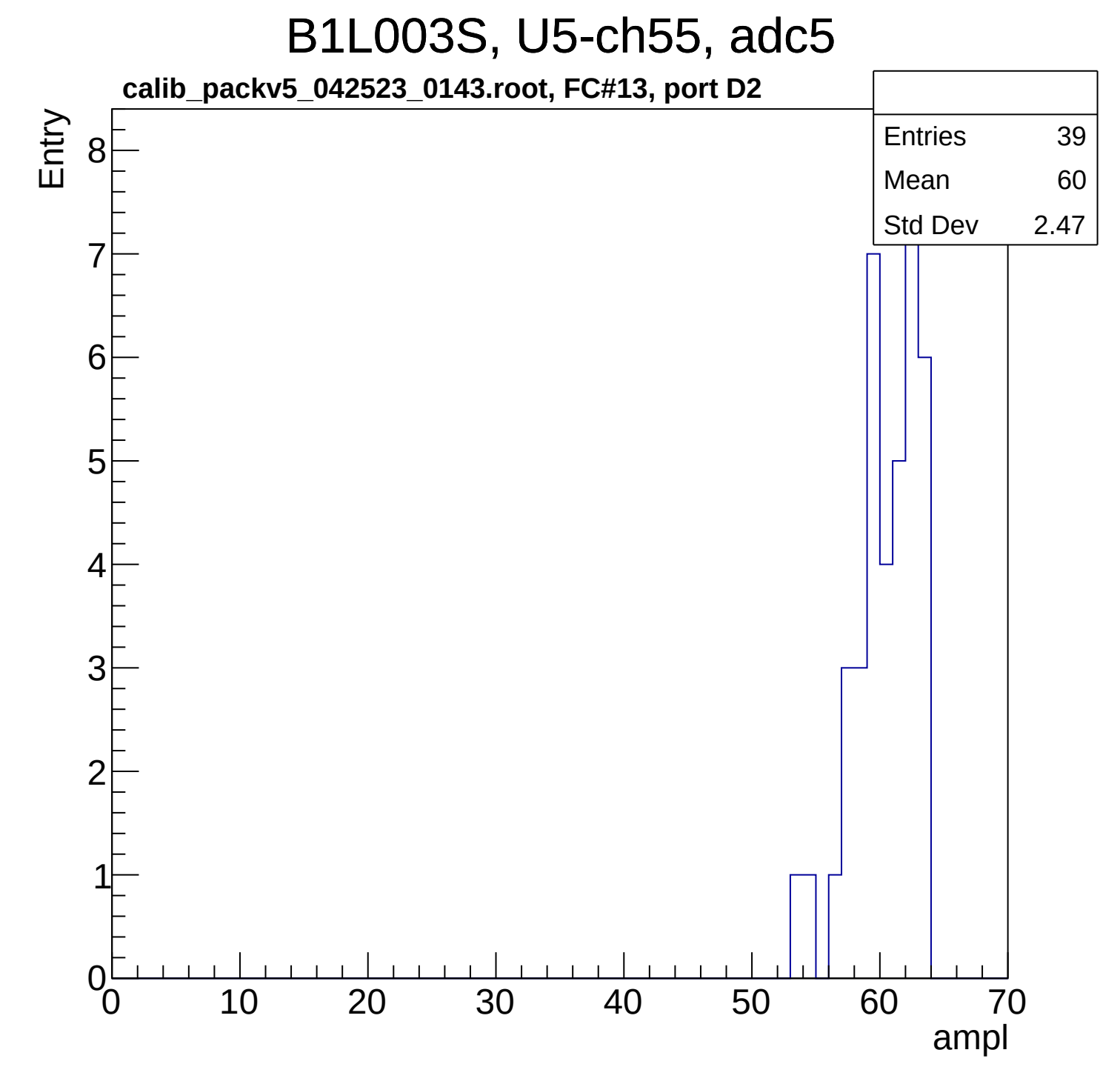
B1L003S, U5-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 60
Std Dev 2.47
Entry
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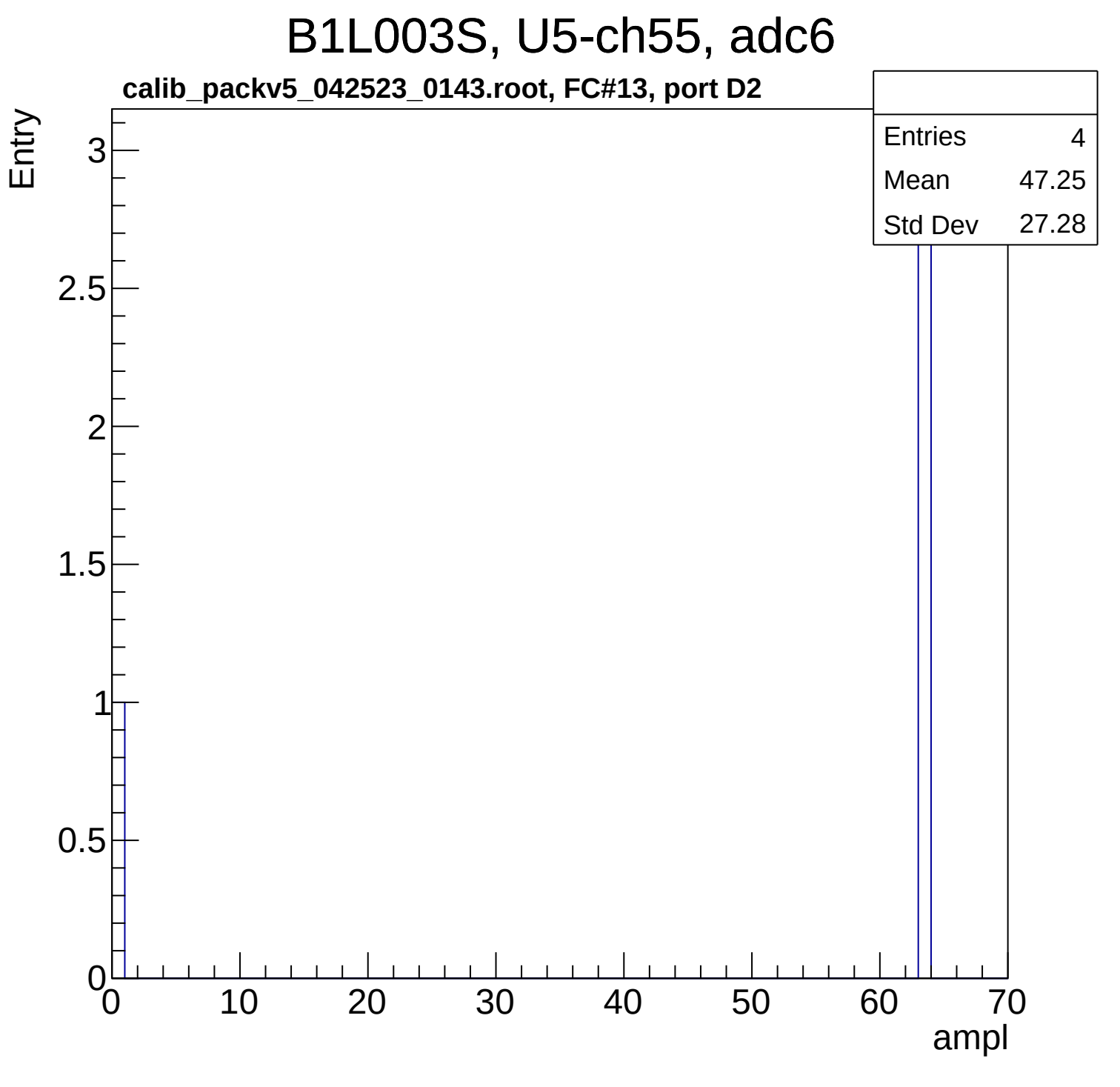
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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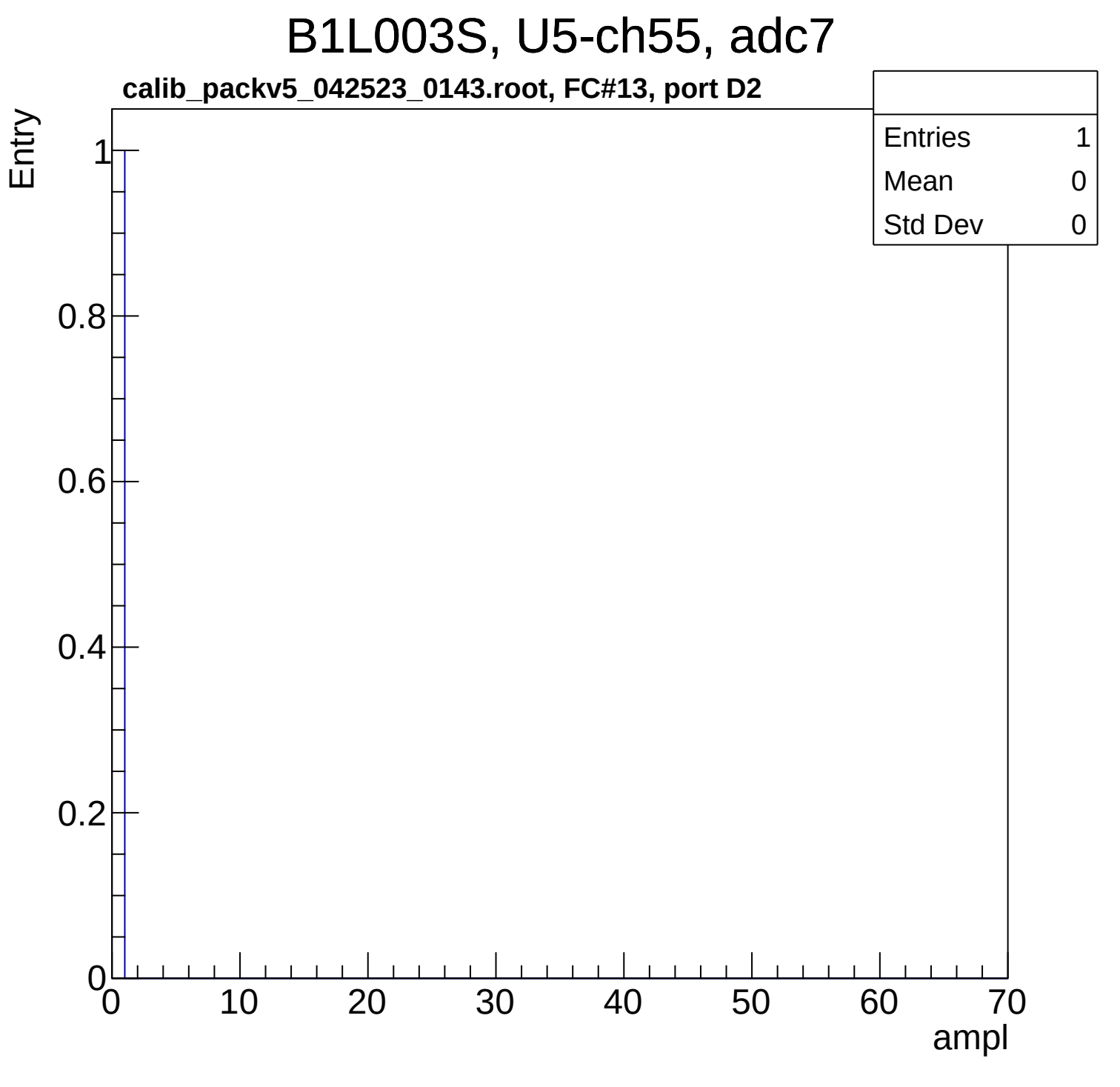
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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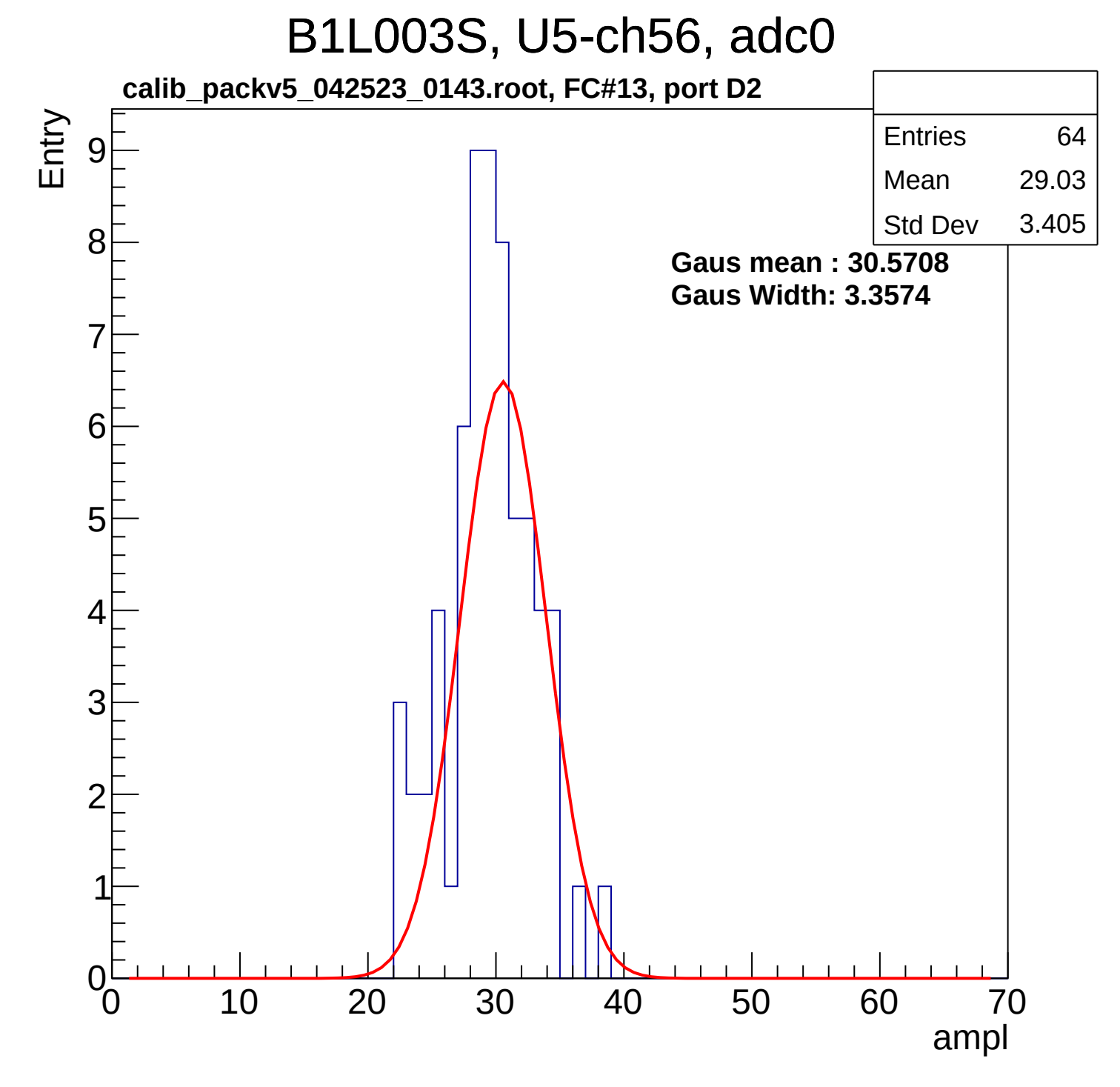
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.03
Std Dev 3.405
Gaus mean : 30.5708
Gaus Width: 3.3574
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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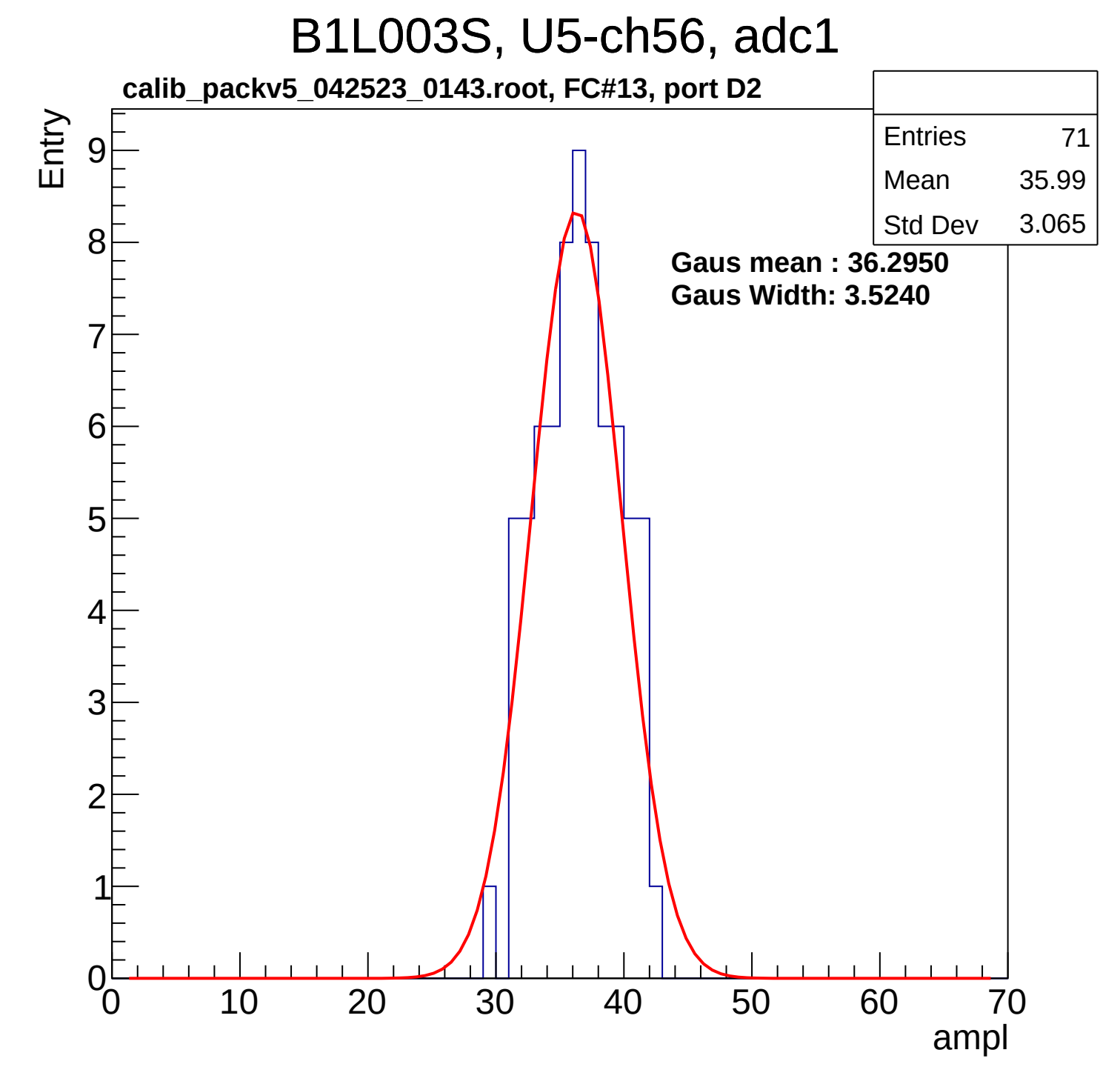
B1L003S, U5-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.99
Std Dev 3.065
Gaus mean : 36.2950
Gaus Width: 3.5240
Entry
ampl
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10
20
30
40
50
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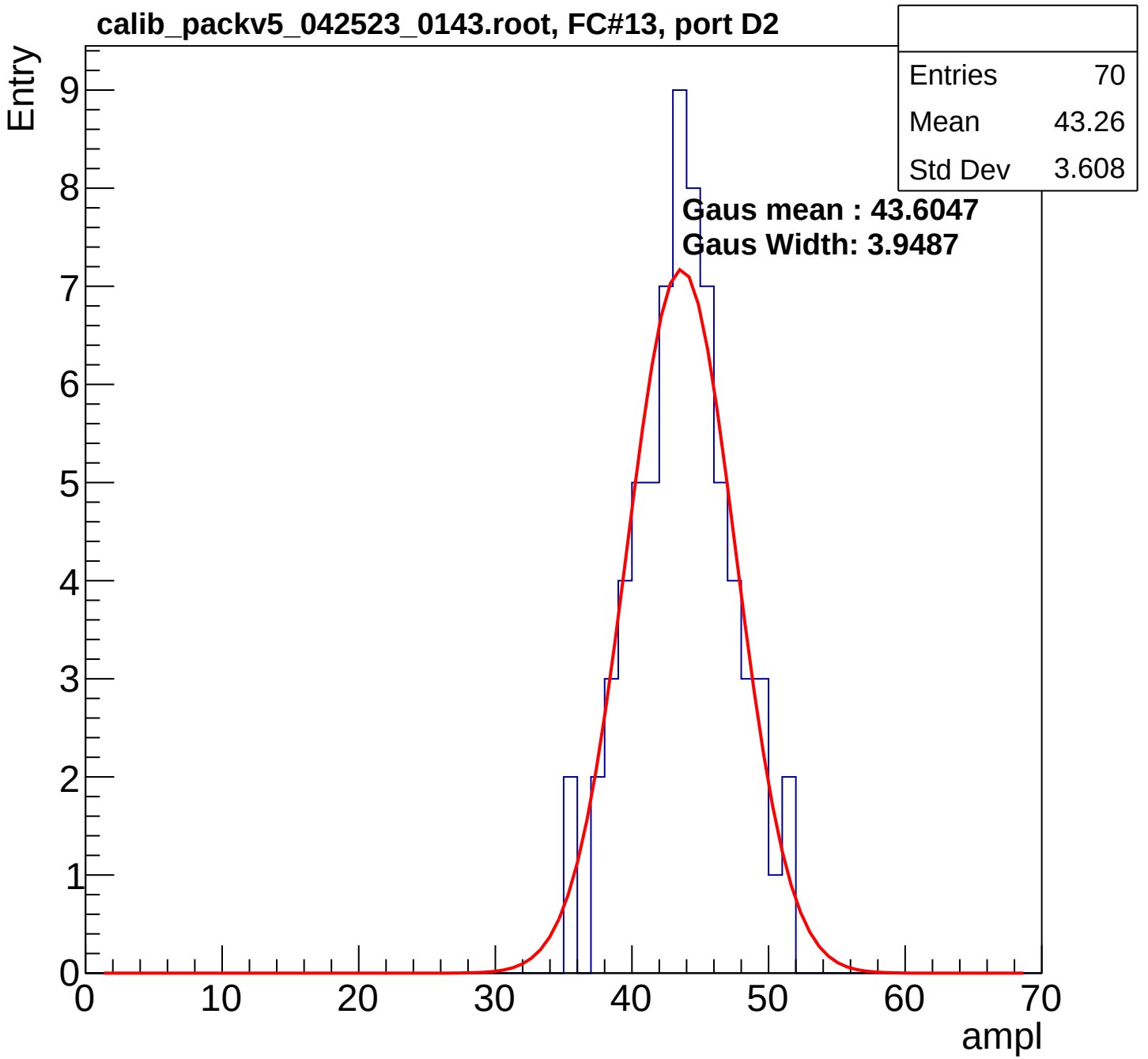
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.26
Std Dev 3.608
Gaus mean : 43.6047
Gaus Width: 3.9487
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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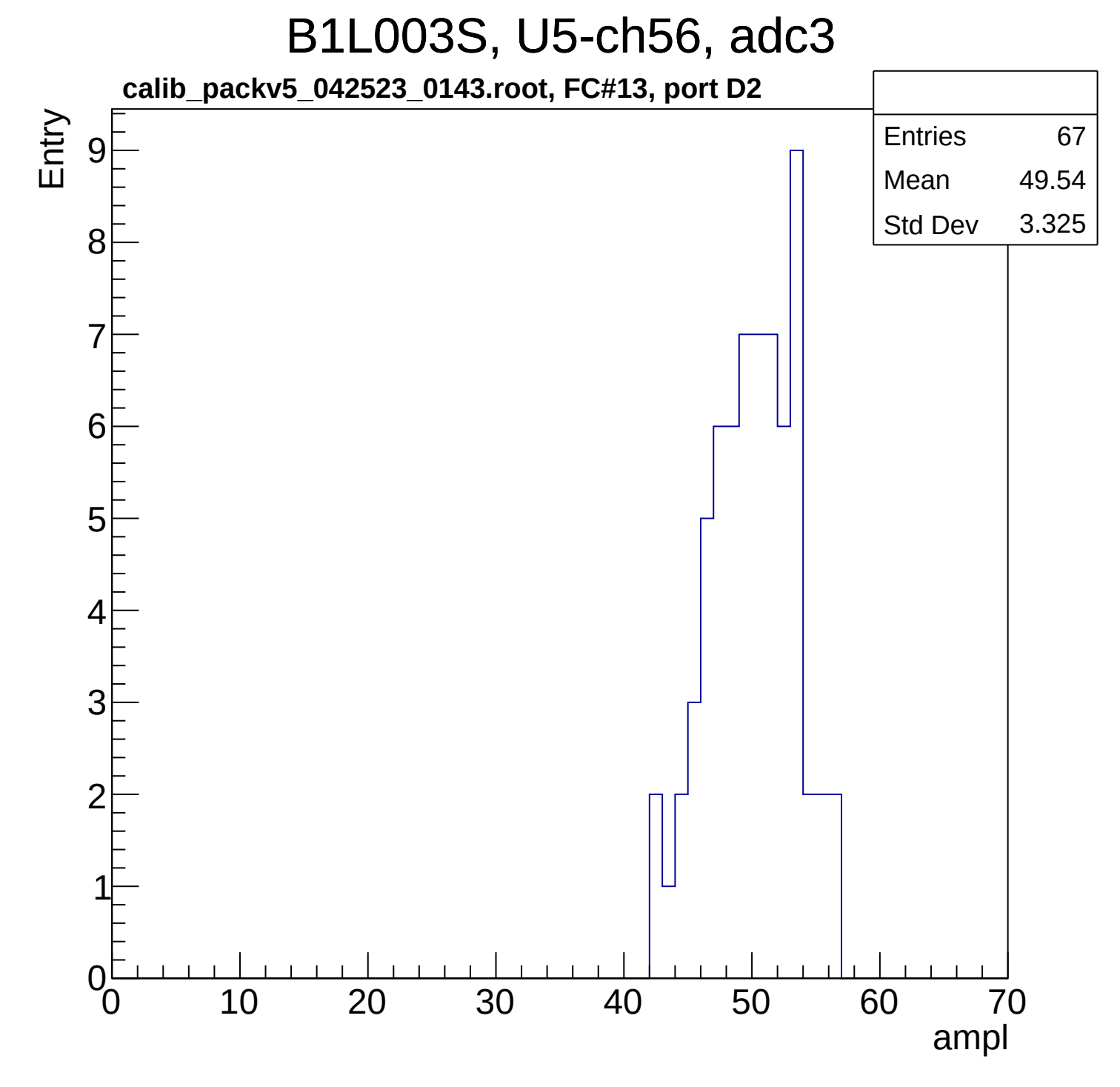
B1L003S, U5-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.54
Std Dev 3.325
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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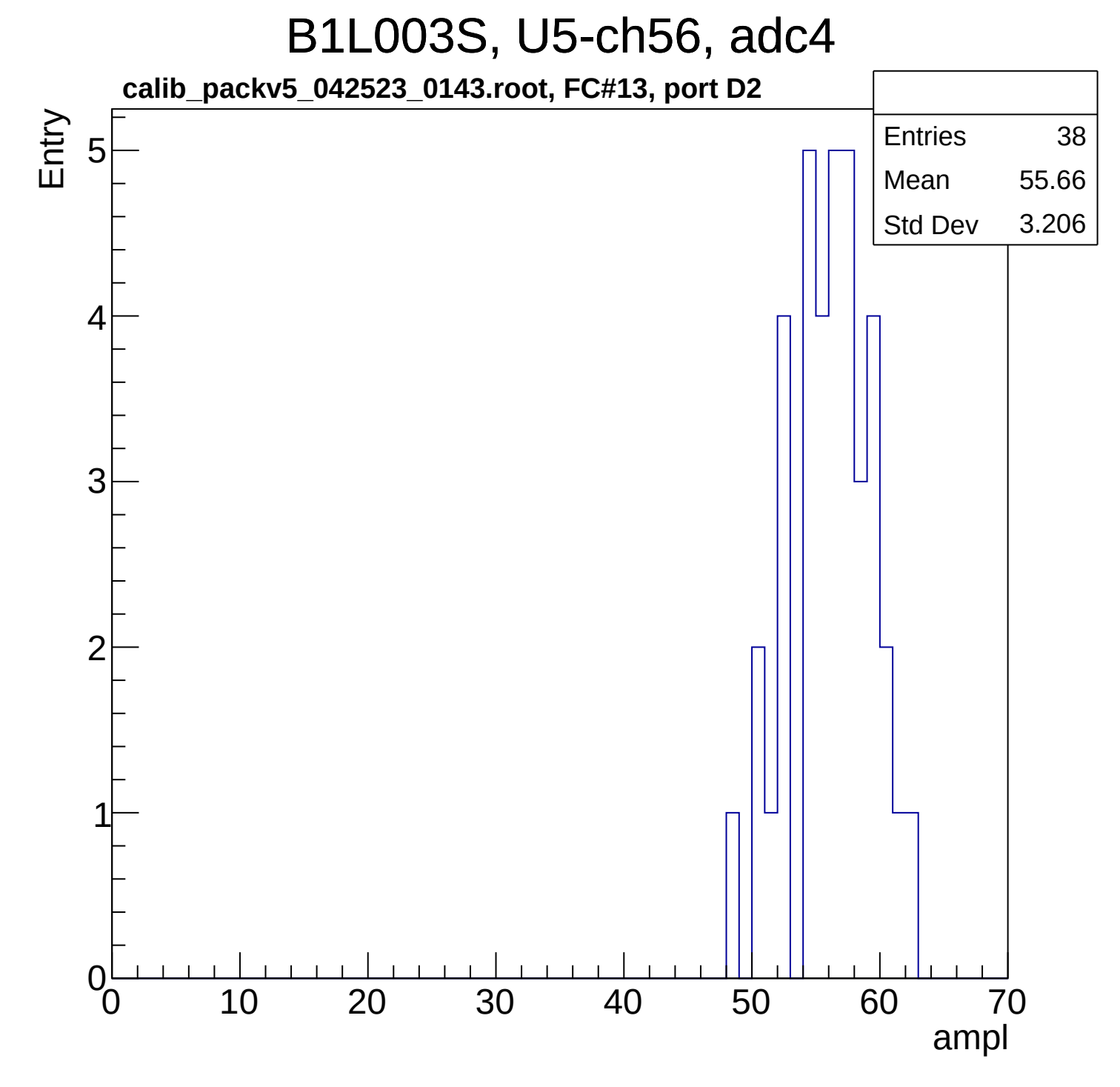
B1L003S, U5-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 55.66
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
0
10
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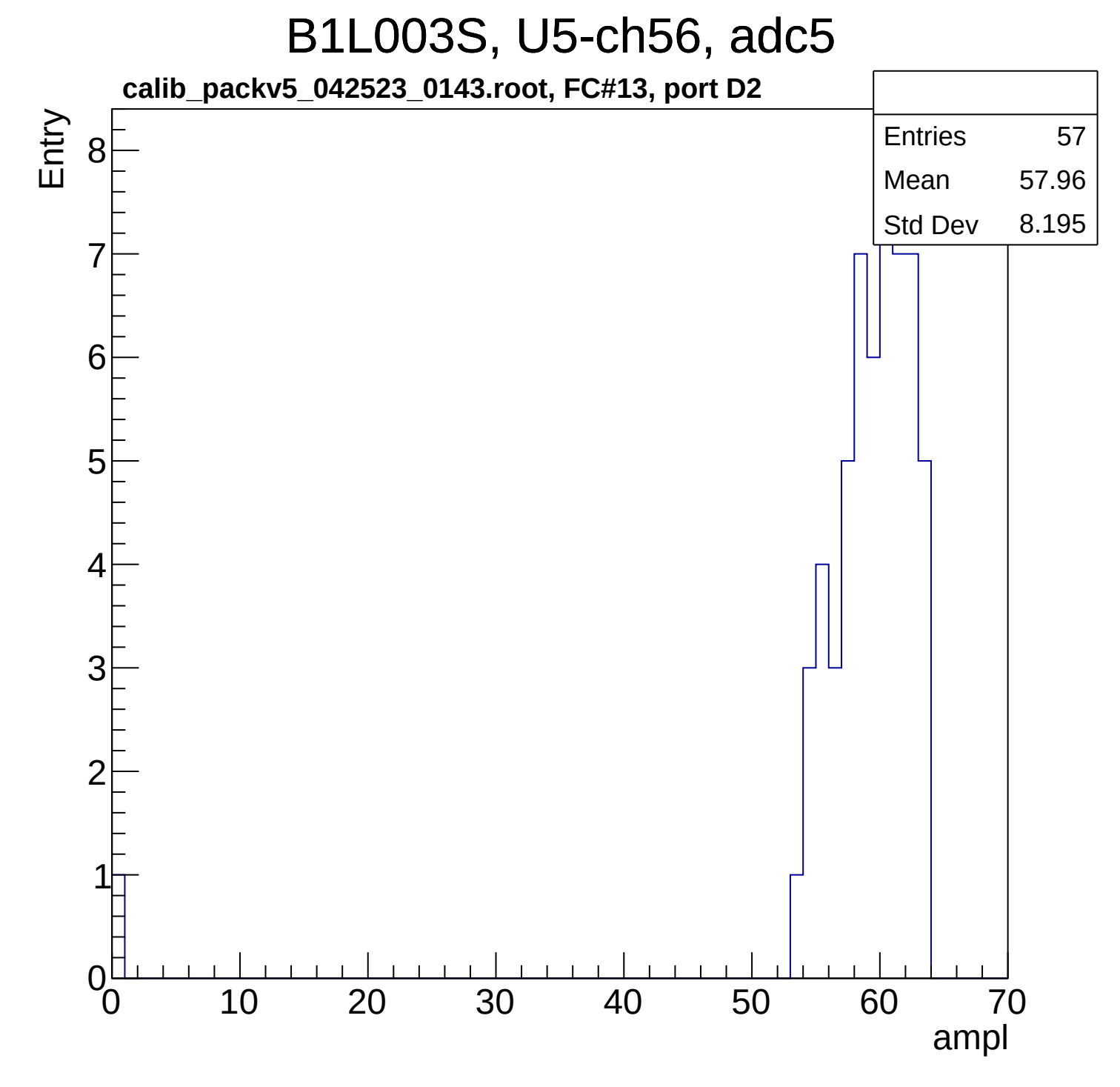
30
40
50
60
70


B1L003S, U5-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.96
Std Dev 8.195
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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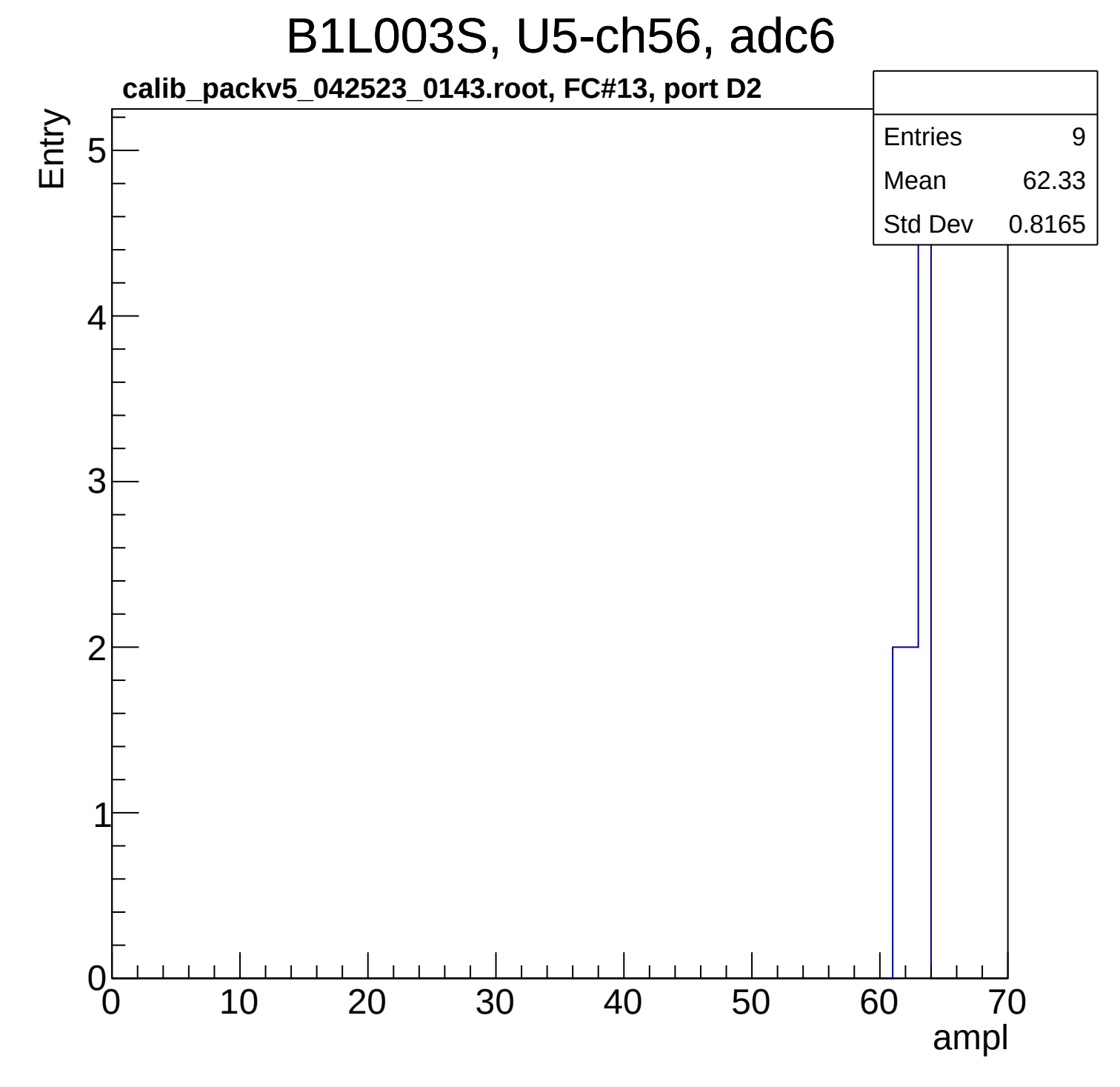
B1L003S, U5-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.33
Std Dev 0.8165
Entry
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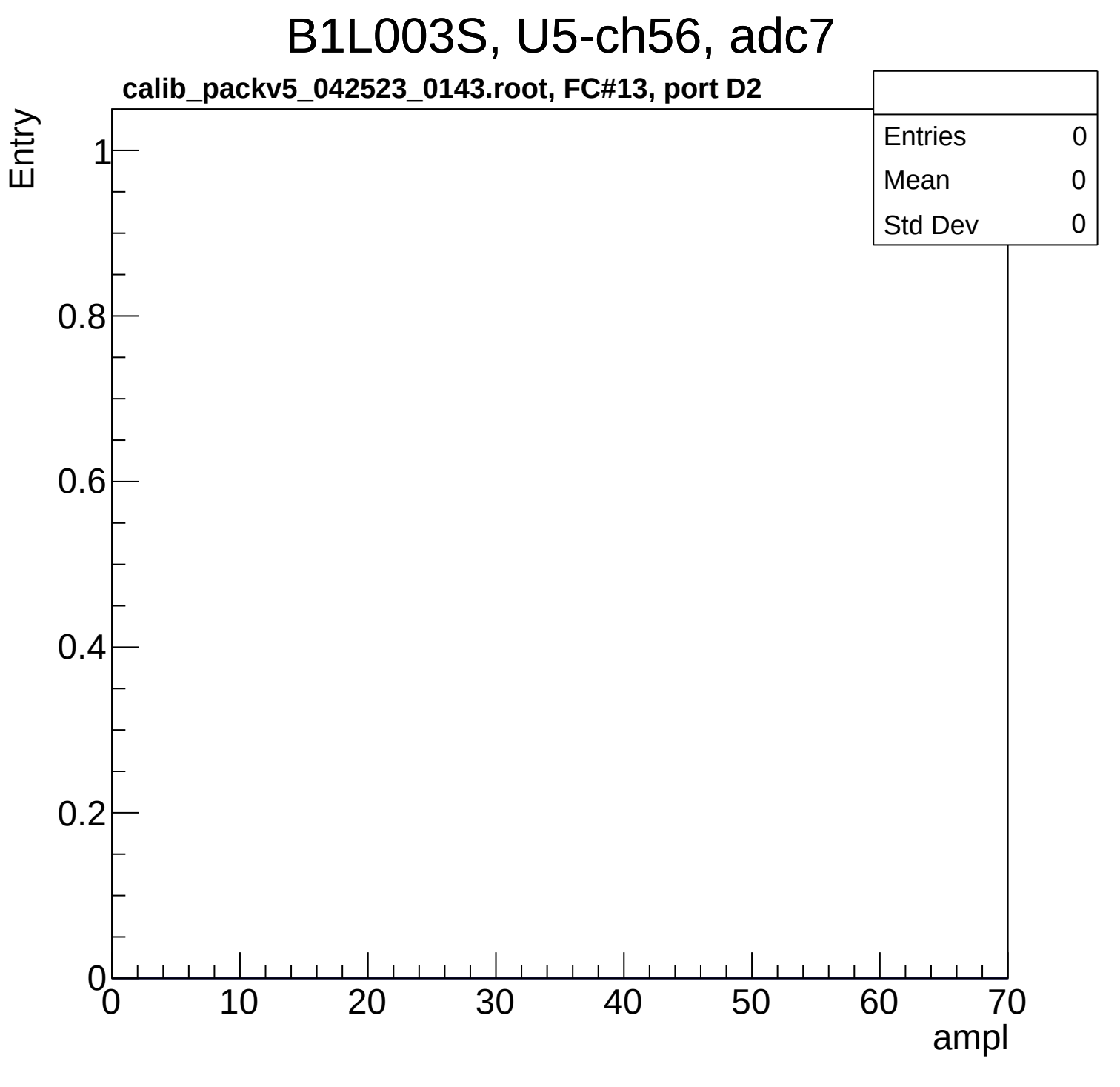
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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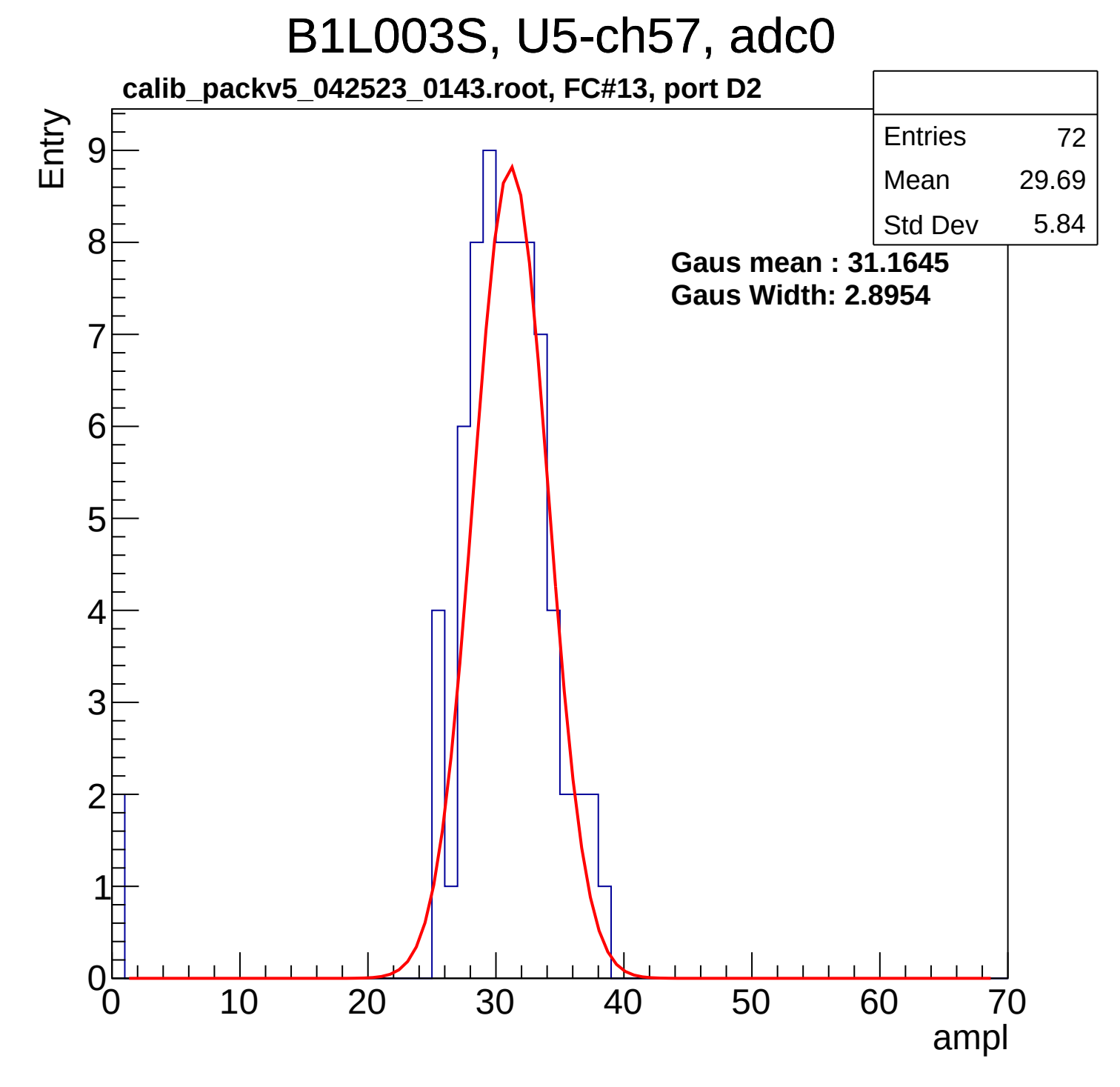
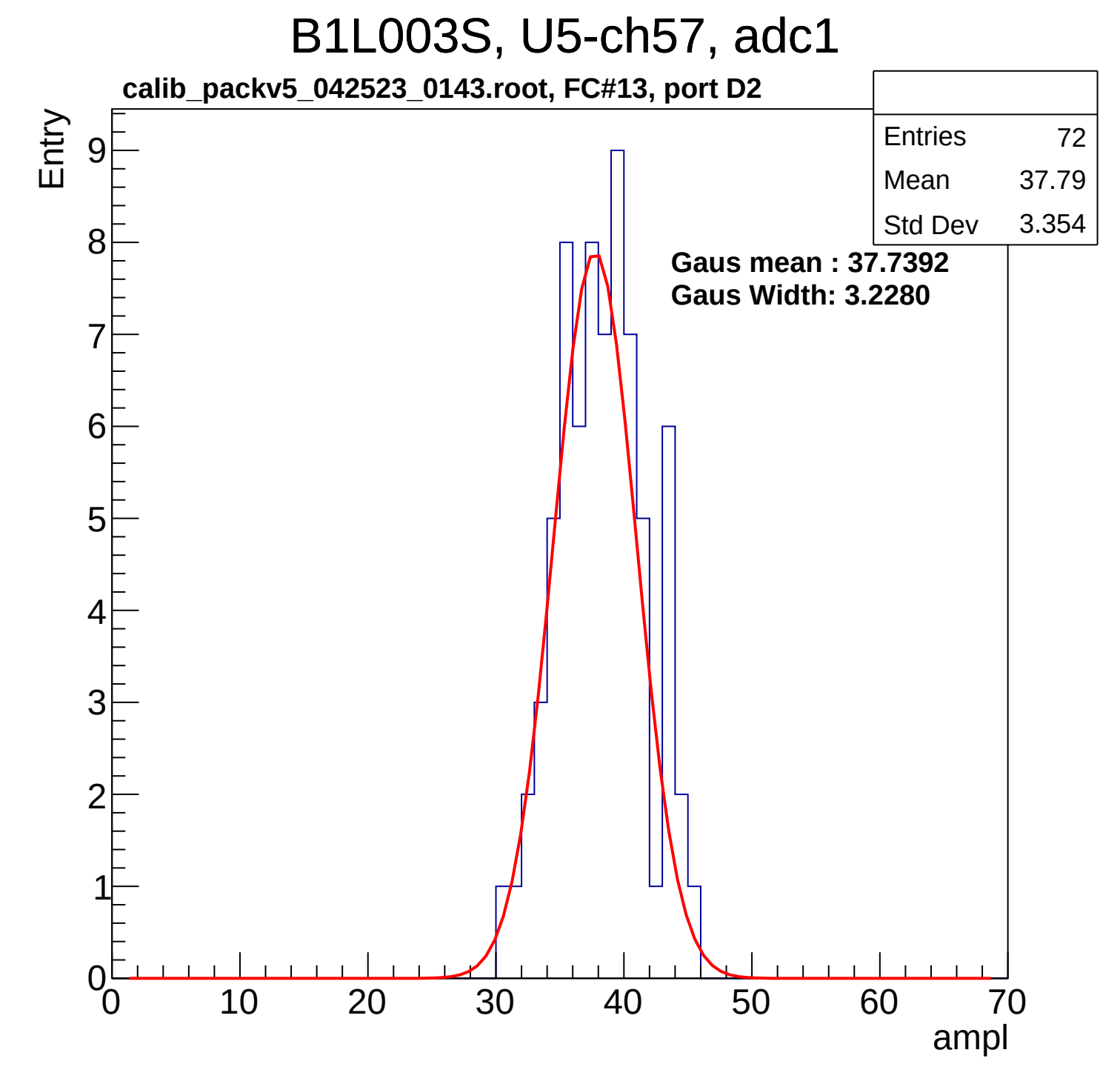
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.69
Std Dev 5.84
Gaus mean : 31.1645
Gaus Width: 2.8954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.79
Std Dev 3.354
Gaus mean : 37.7392
Gaus Width: 3.2280
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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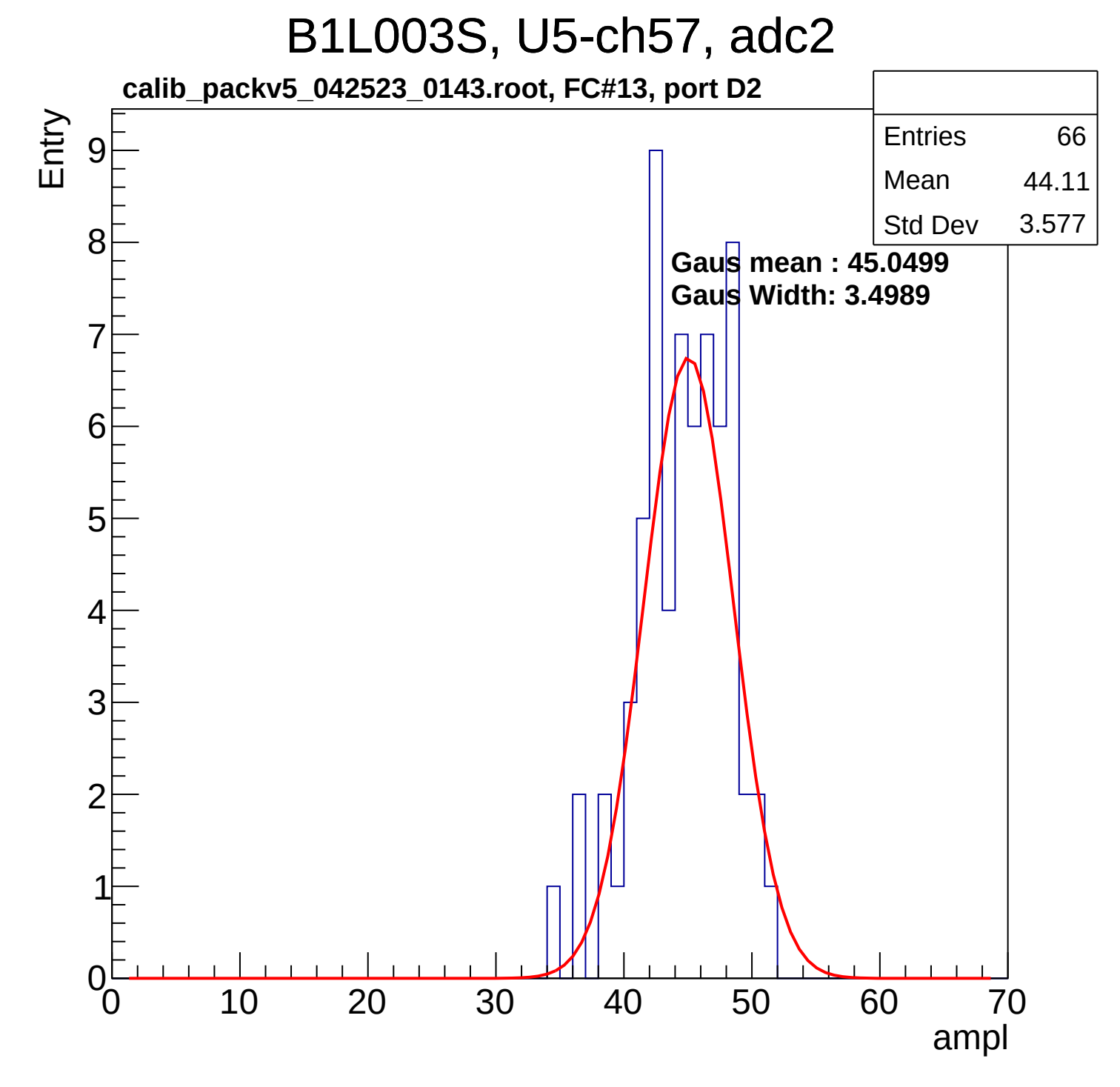
B1L003S, U5-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 44.11
Std Dev 3.577
Gaus mean : 45.0499
Gaus Width: 3.4989
Entry
ampl
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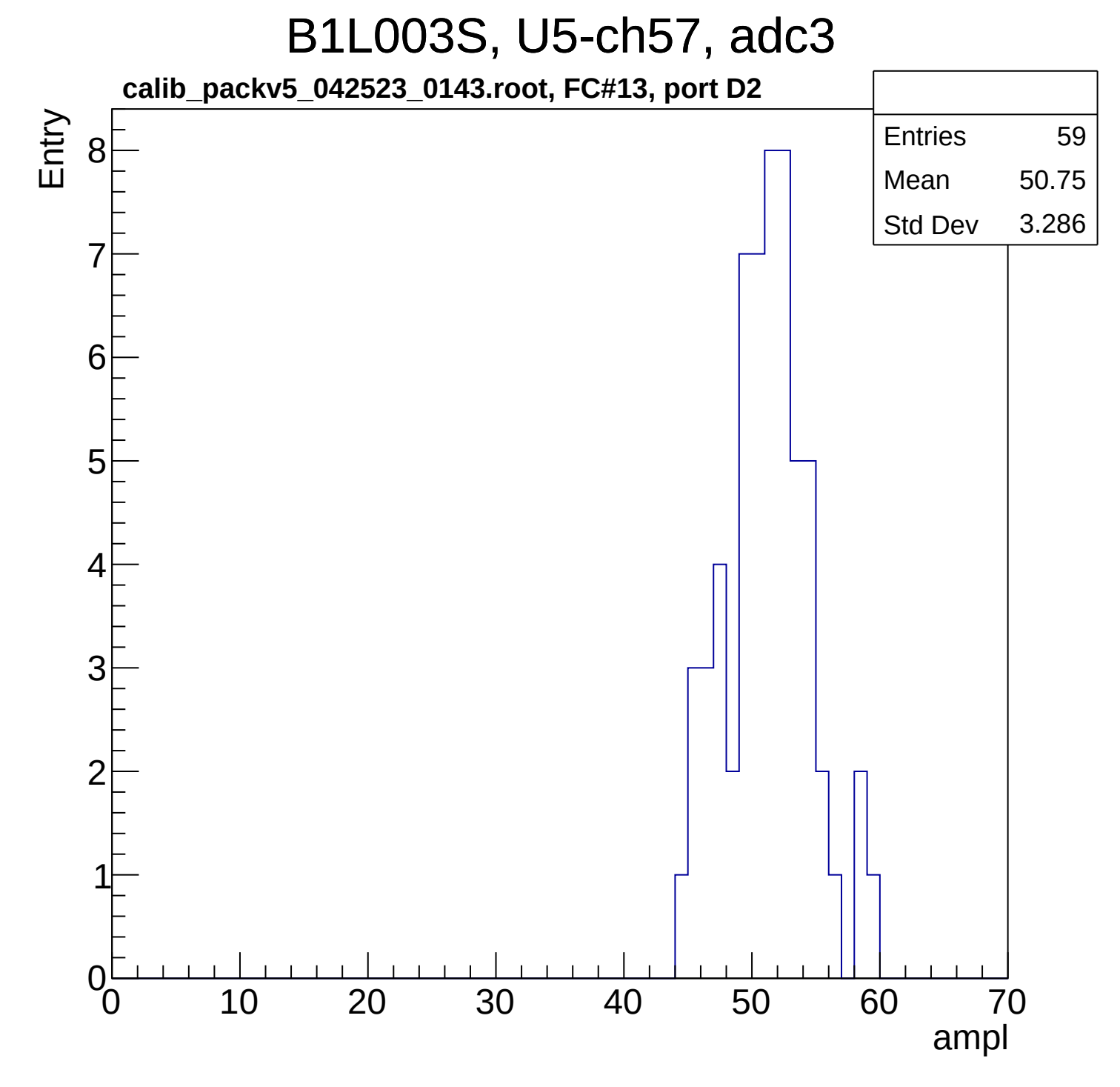
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 50.75
Std Dev 3.286
Entry
ampl
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3
4
5
6
7
8
0
10
20
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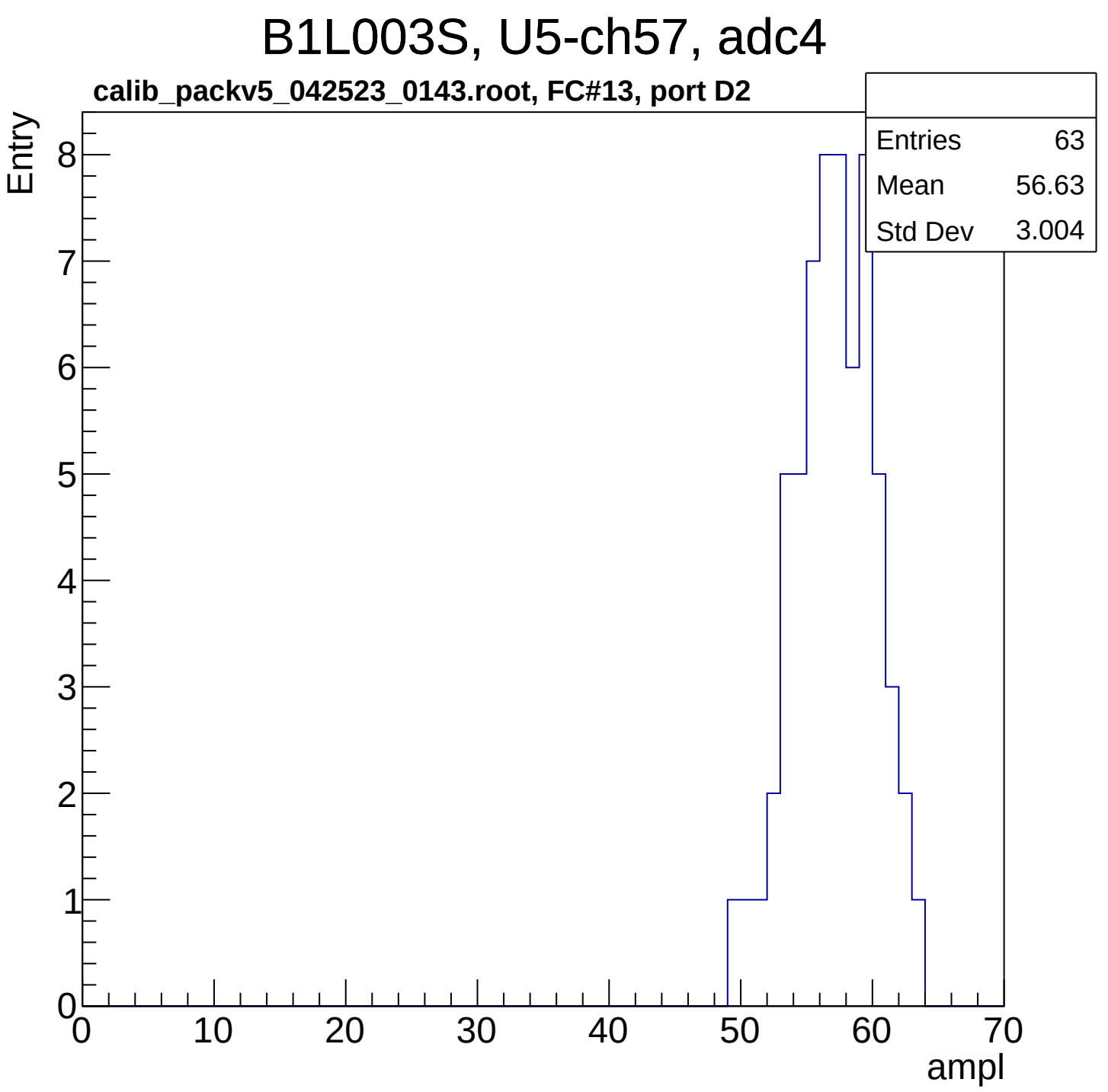
40
50
60
70


B1L003S, U5-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 56.63
Std Dev 3.004
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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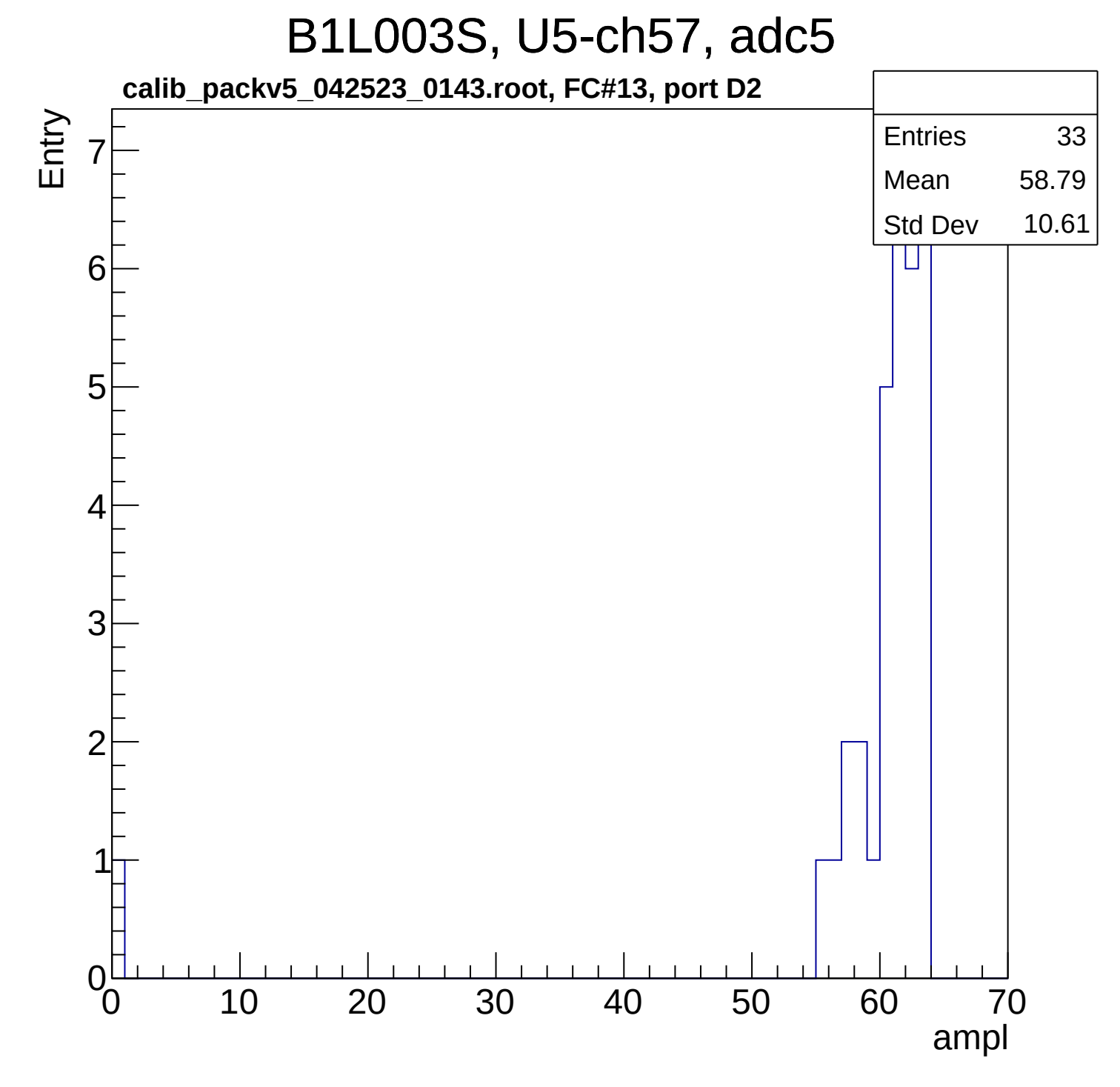
B1L003S, U5-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 58.79
Std Dev 10.61
Entry
ampl
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3
4
5
6
7
0
10
20
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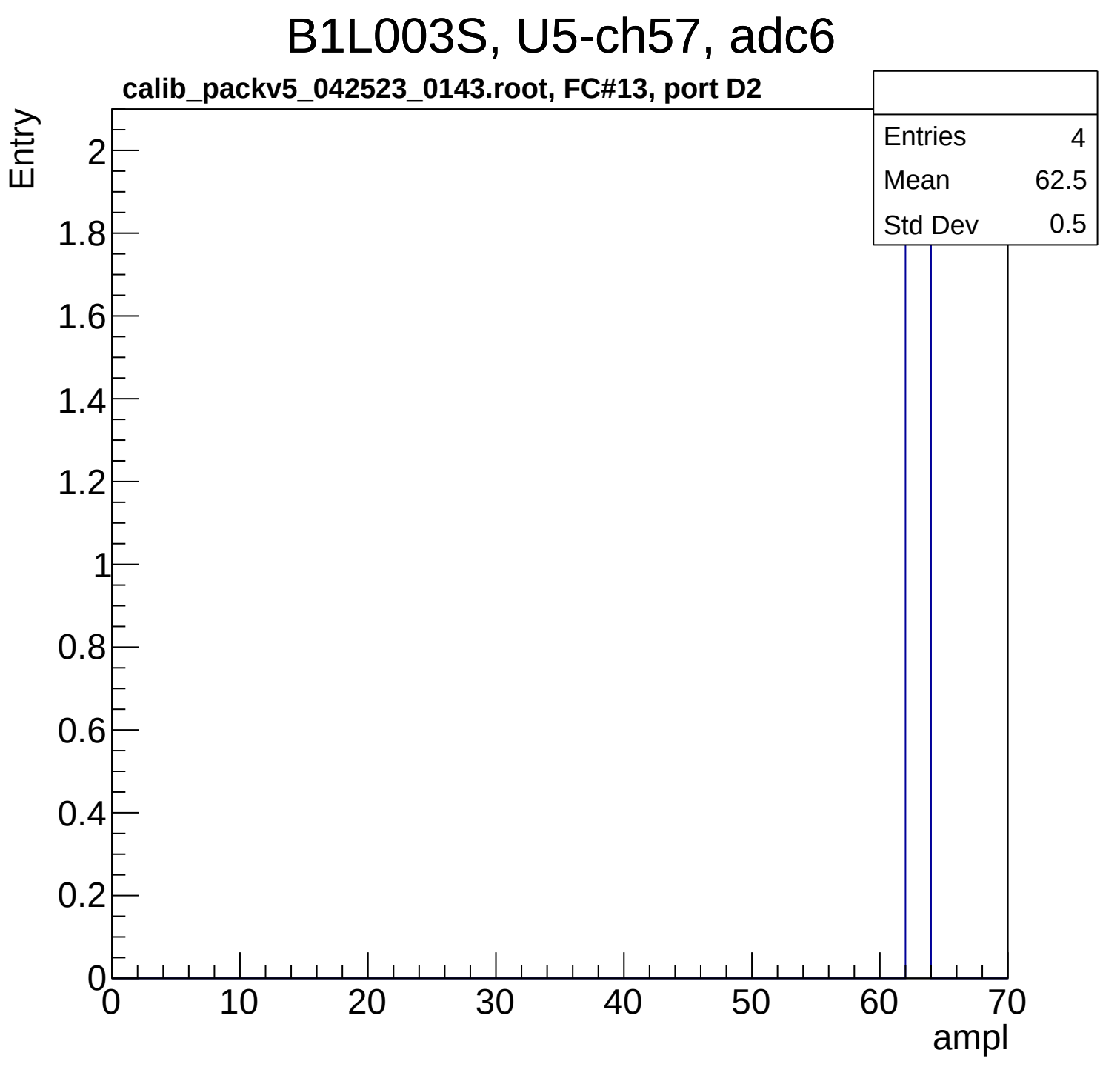
40
50
60
70


B1L003S, U5-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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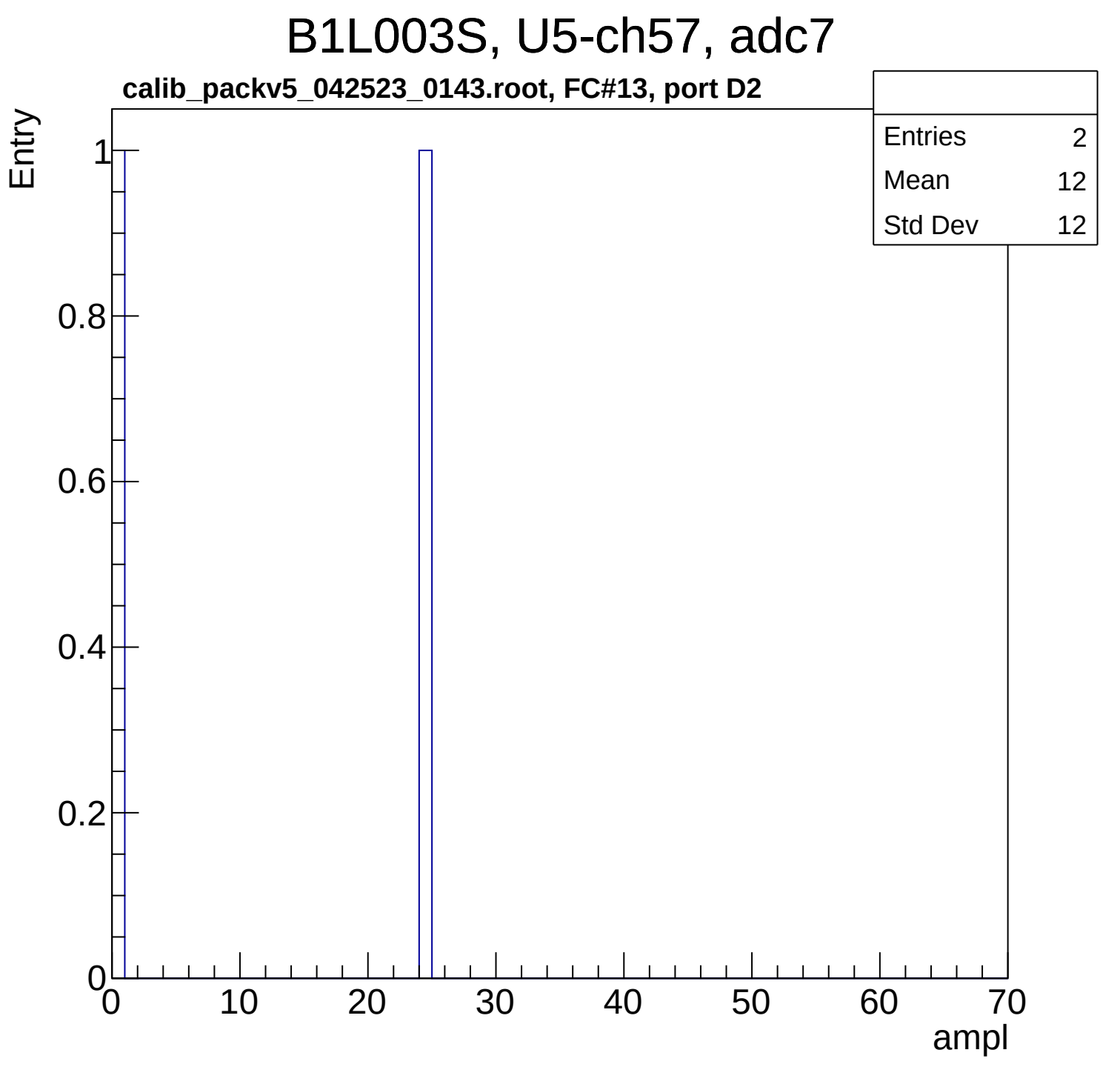
B1L003S, U5-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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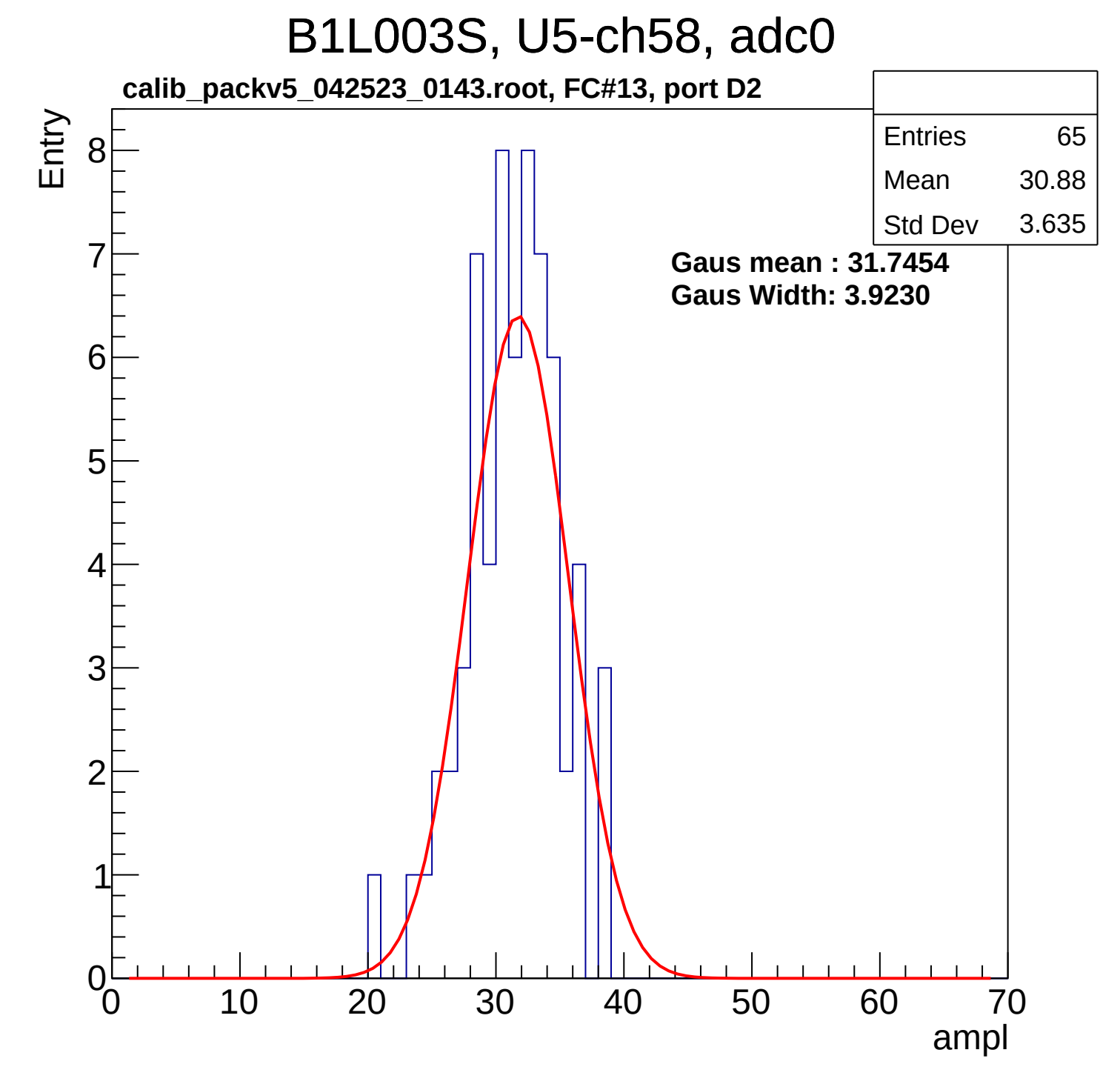
40
50
60
70
ampl


B1L003S, U5-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.88
Std Dev 3.635
Gaus mean : 31.7454
Gaus Width: 3.9230
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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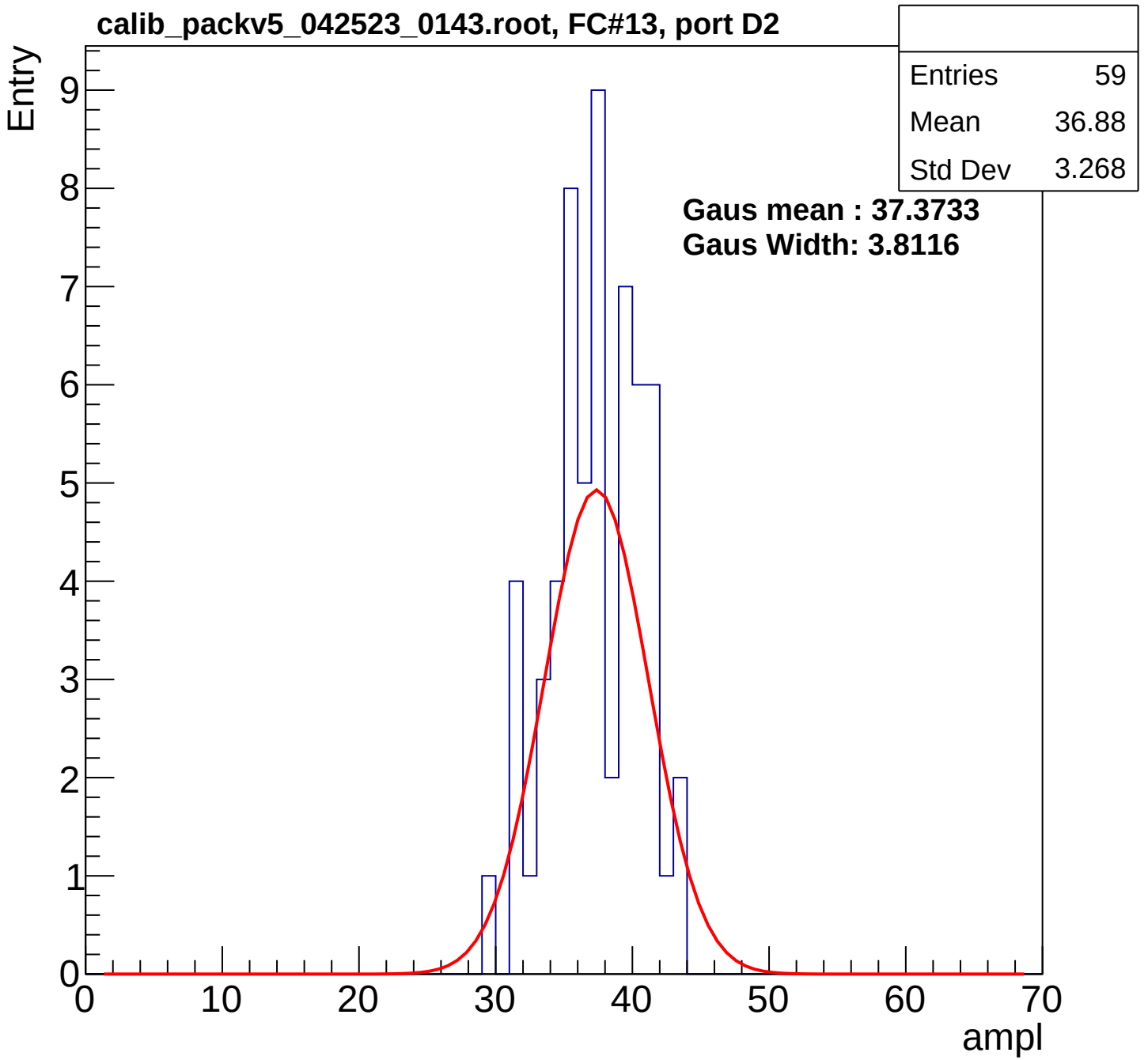
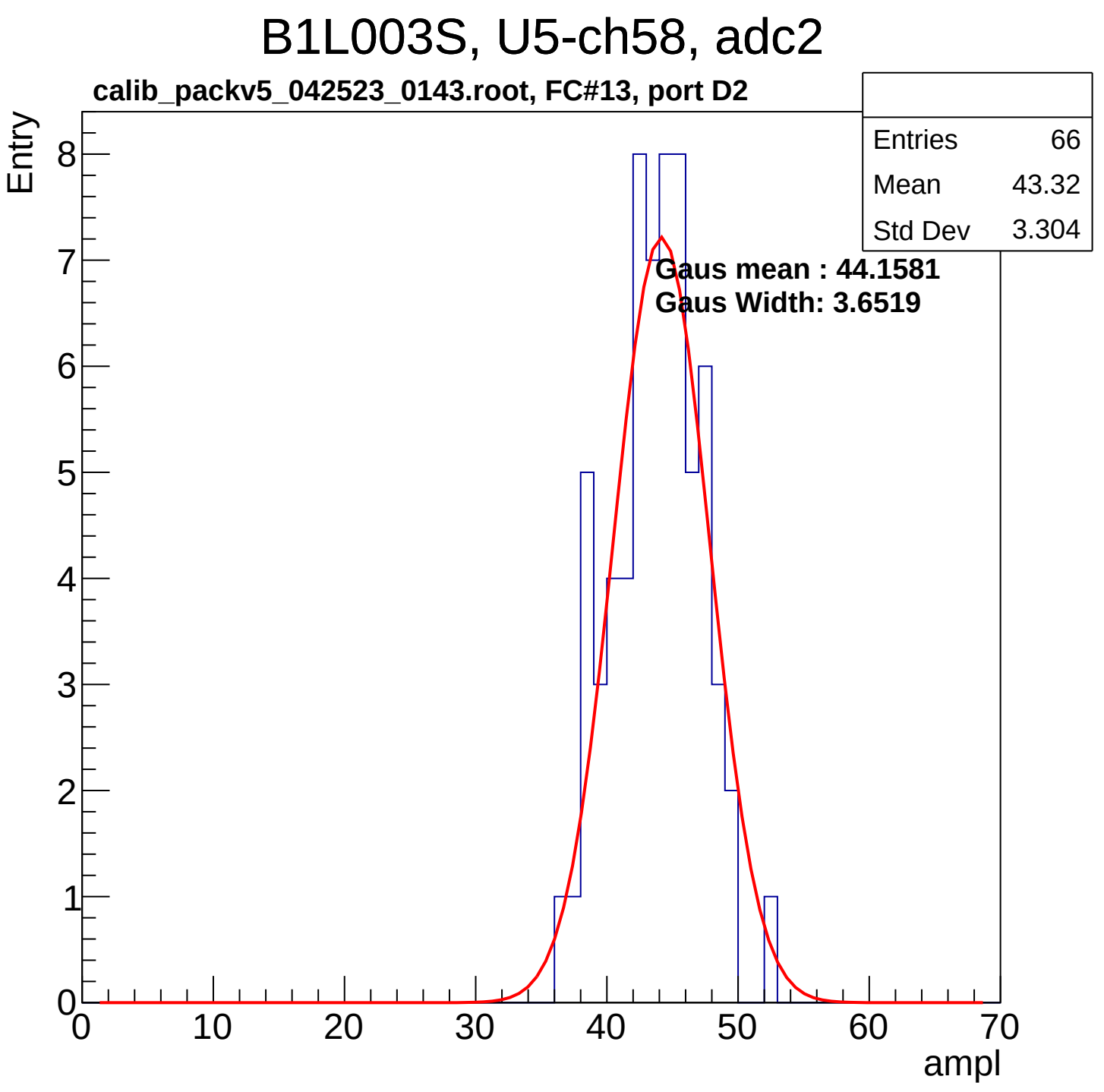

B1L003S, U5-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.88
Std Dev 3.268
Gaus mean : 37.3733
Gaus Width: 3.8116
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.32
Std Dev 3.304
Gaus mean : 44.1581
Gaus Width: 3.6519
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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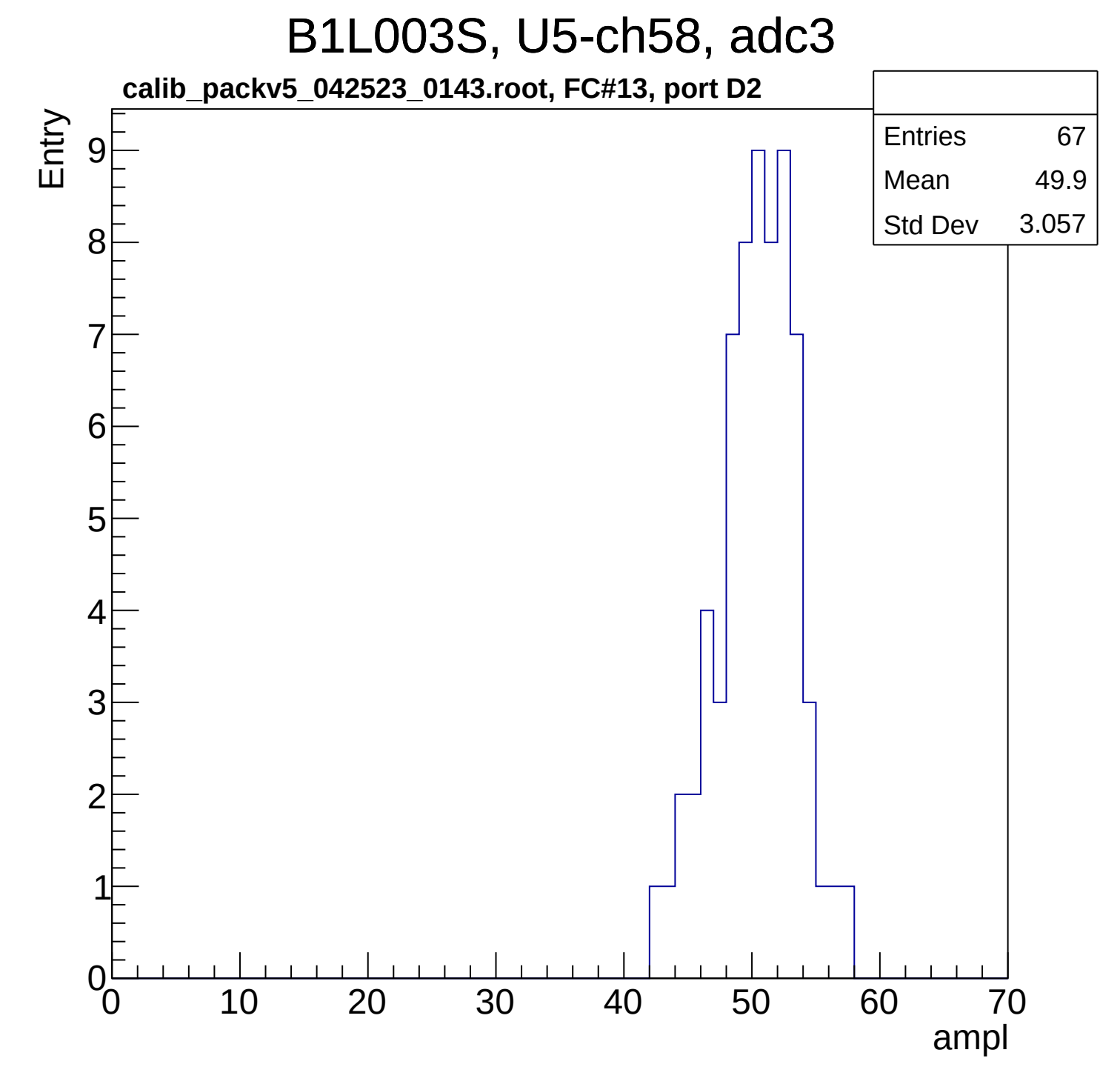
B1L003S, U5-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.9
Std Dev 3.057
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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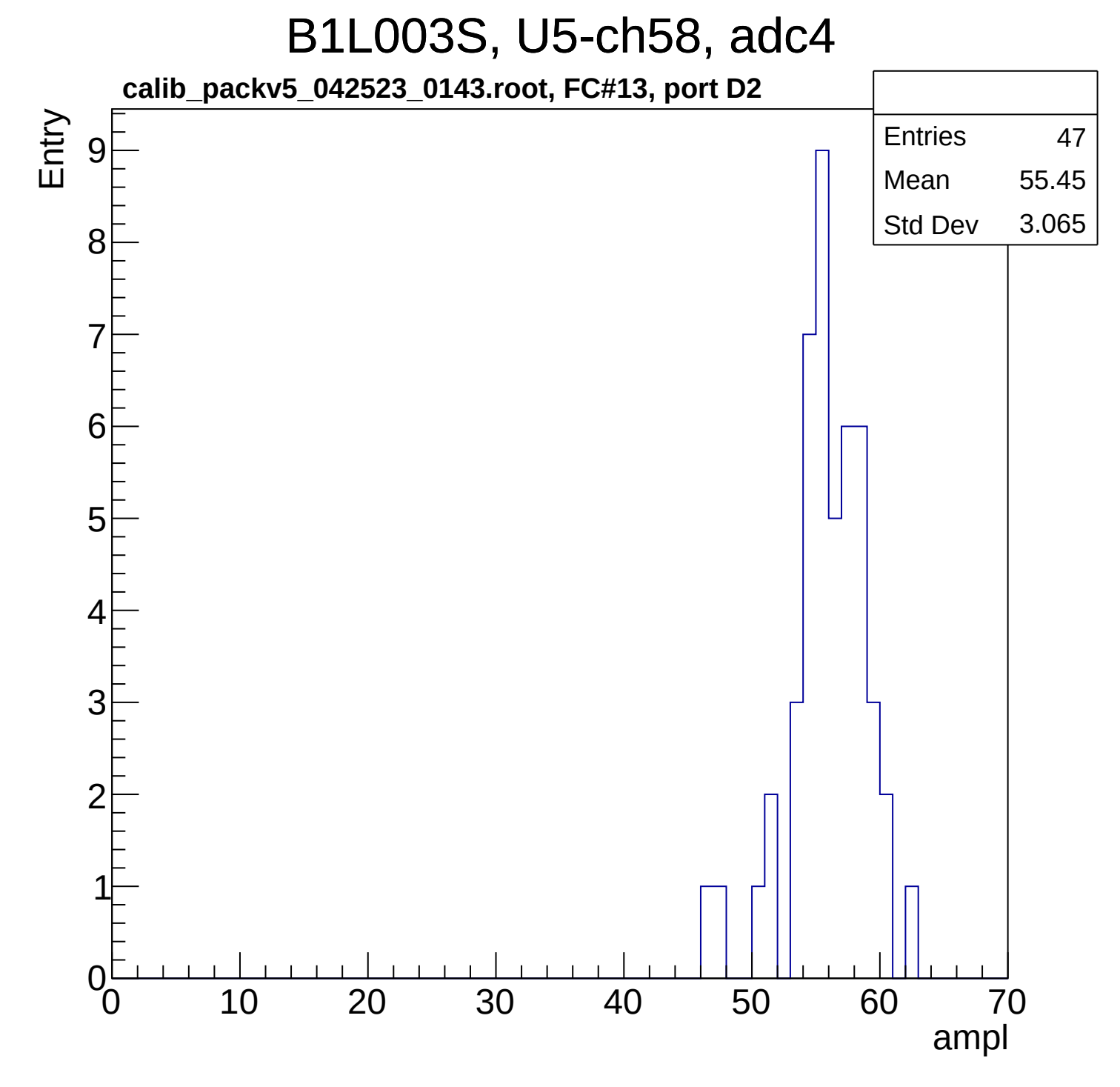
B1L003S, U5-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.45
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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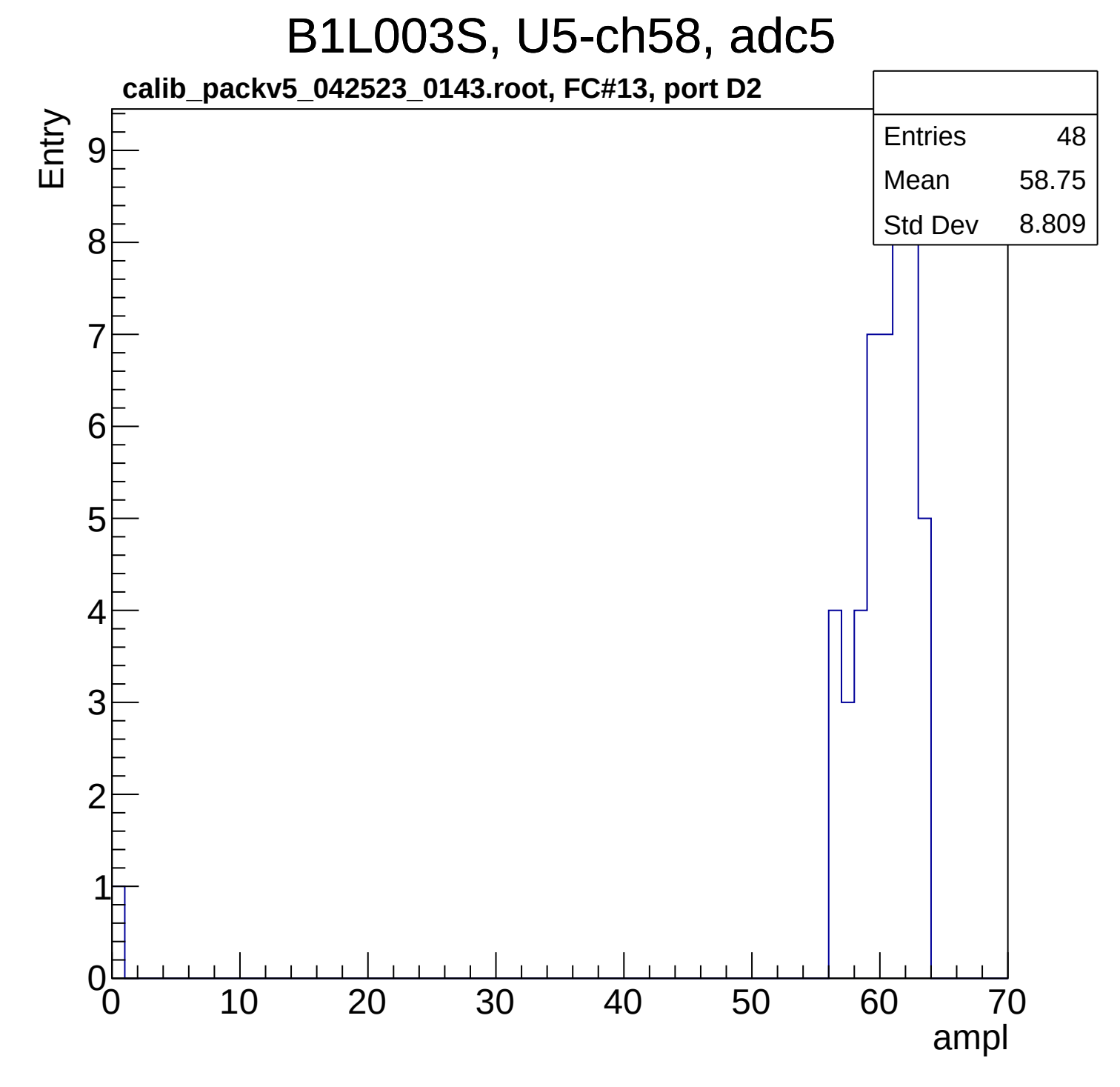
B1L003S, U5-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.75
Std Dev 8.809
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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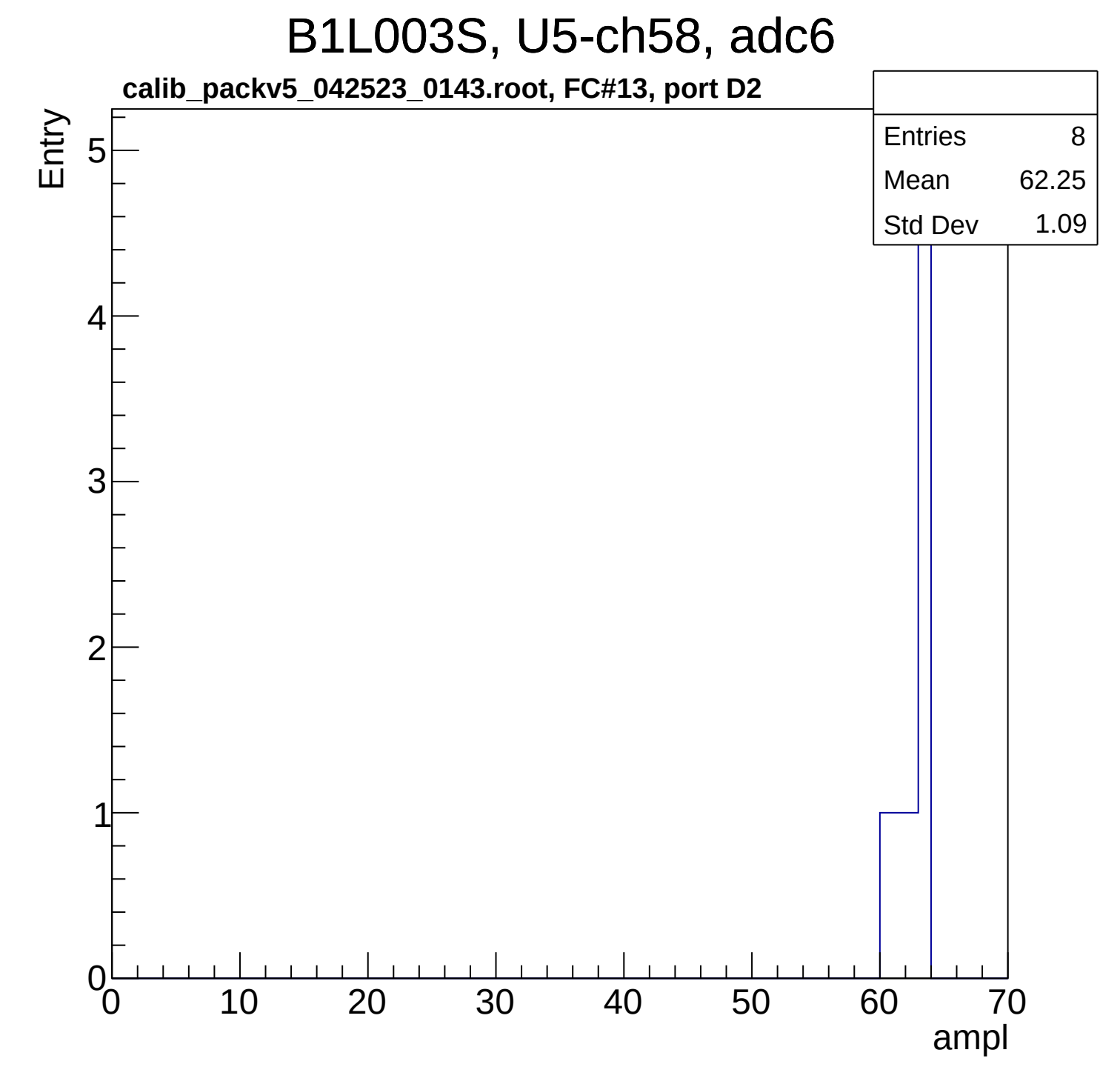
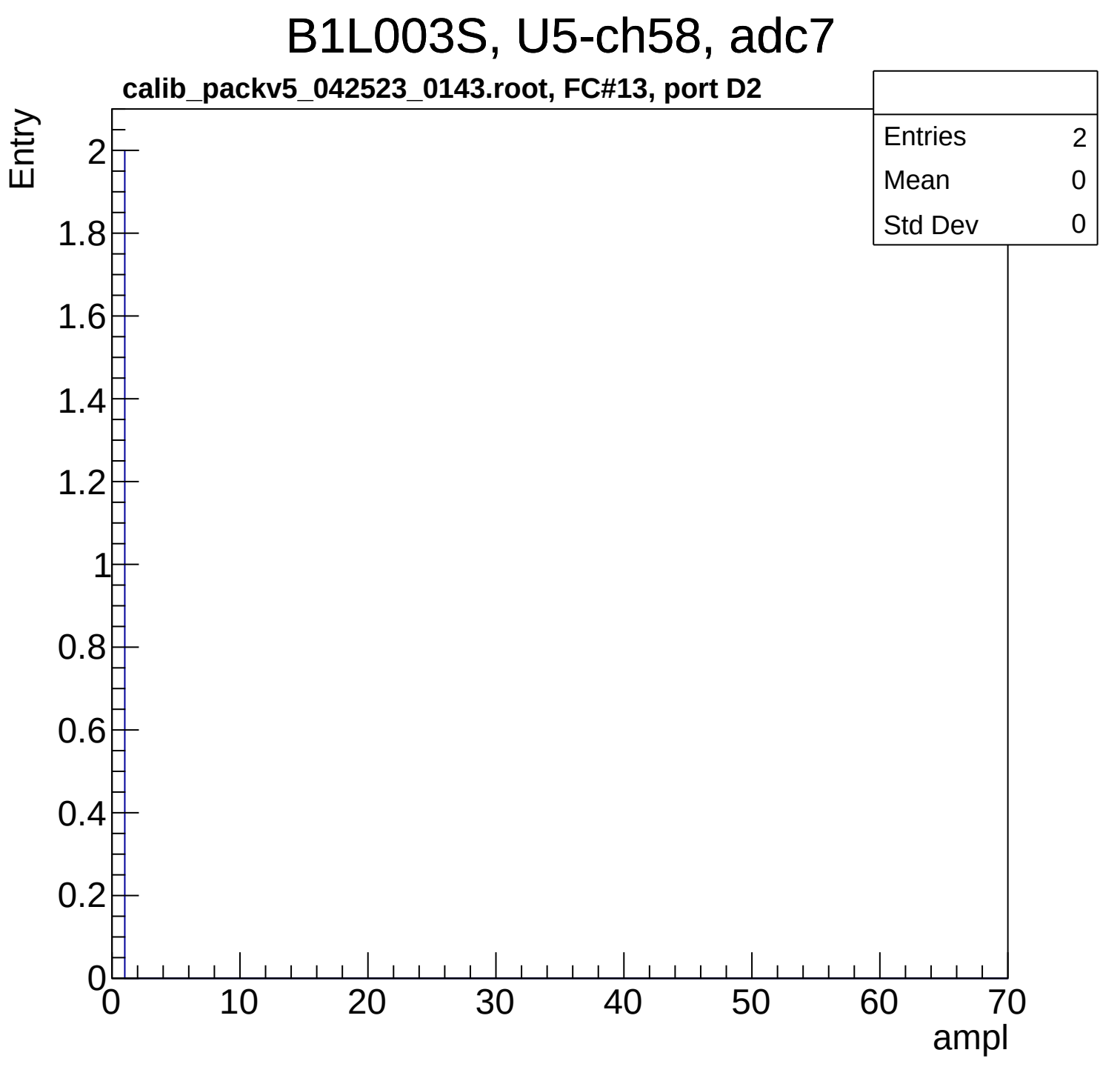
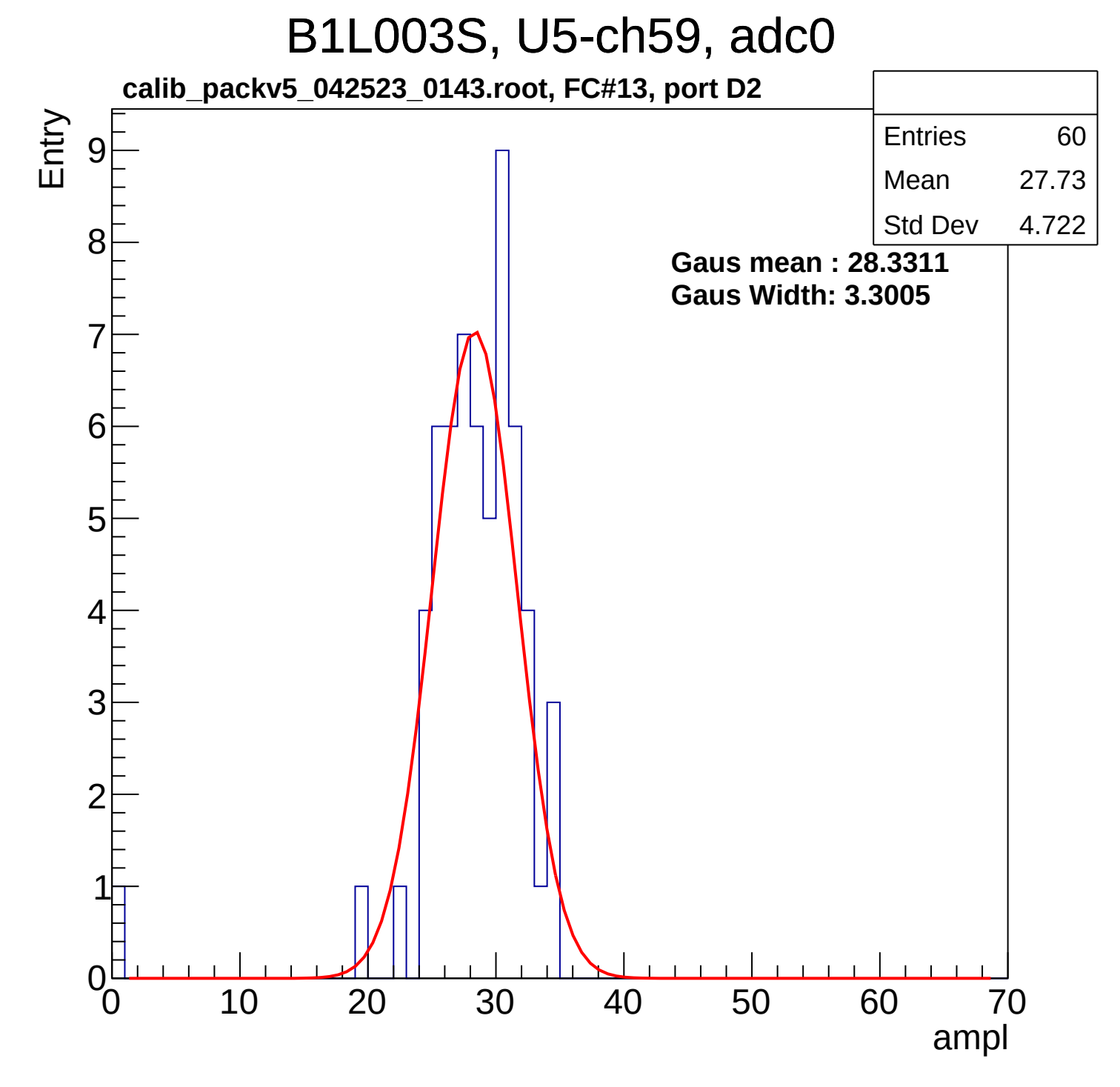

B1L003S, U5-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.25
Std Dev 1.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 27.73
Std Dev 4.722
Gaus mean : 28.3311
Gaus Width: 3.3005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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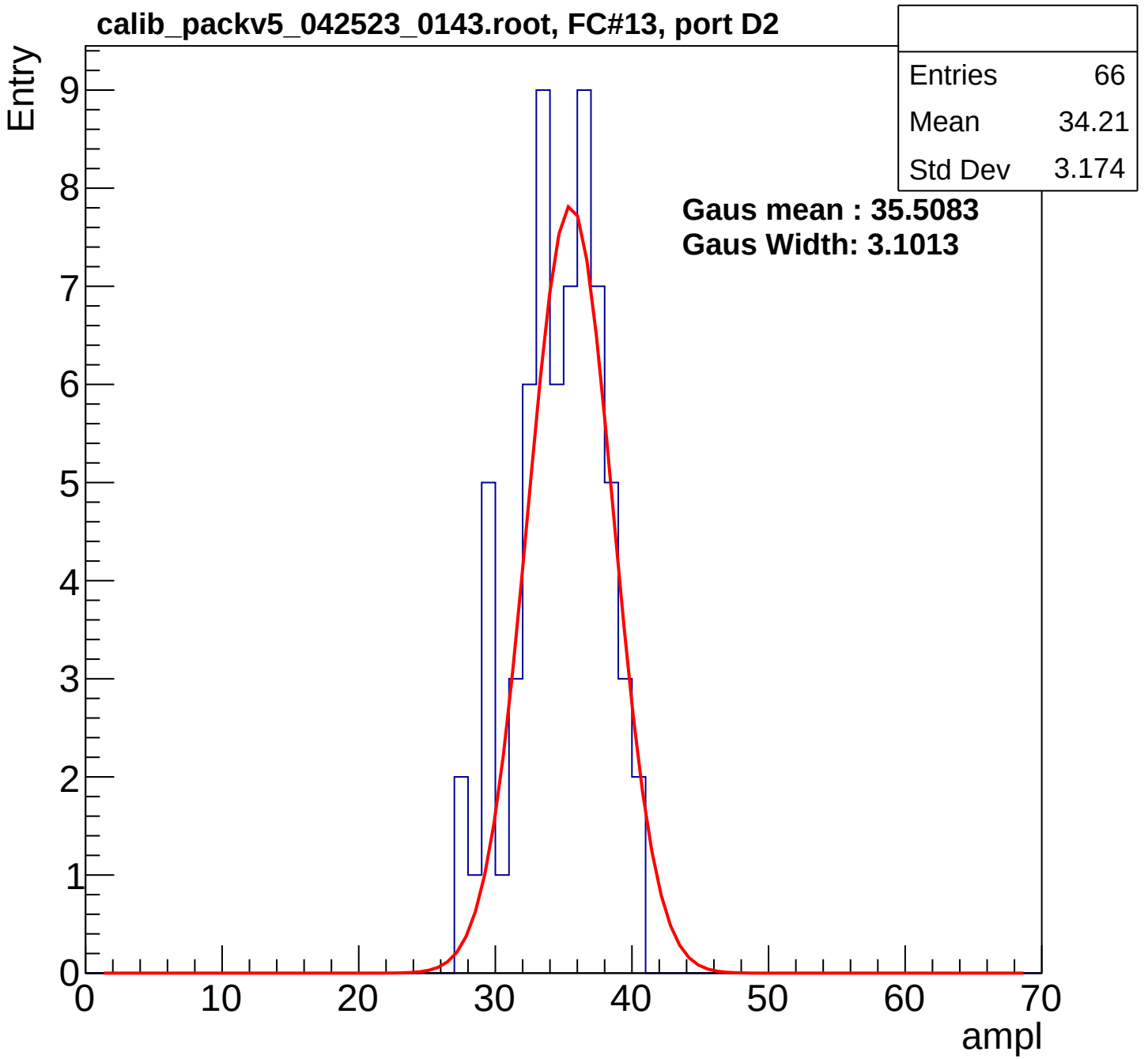
B1L003S, U5-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 34.21
Std Dev 3.174
Gaus mean : 35.5083
Gaus Width: 3.1013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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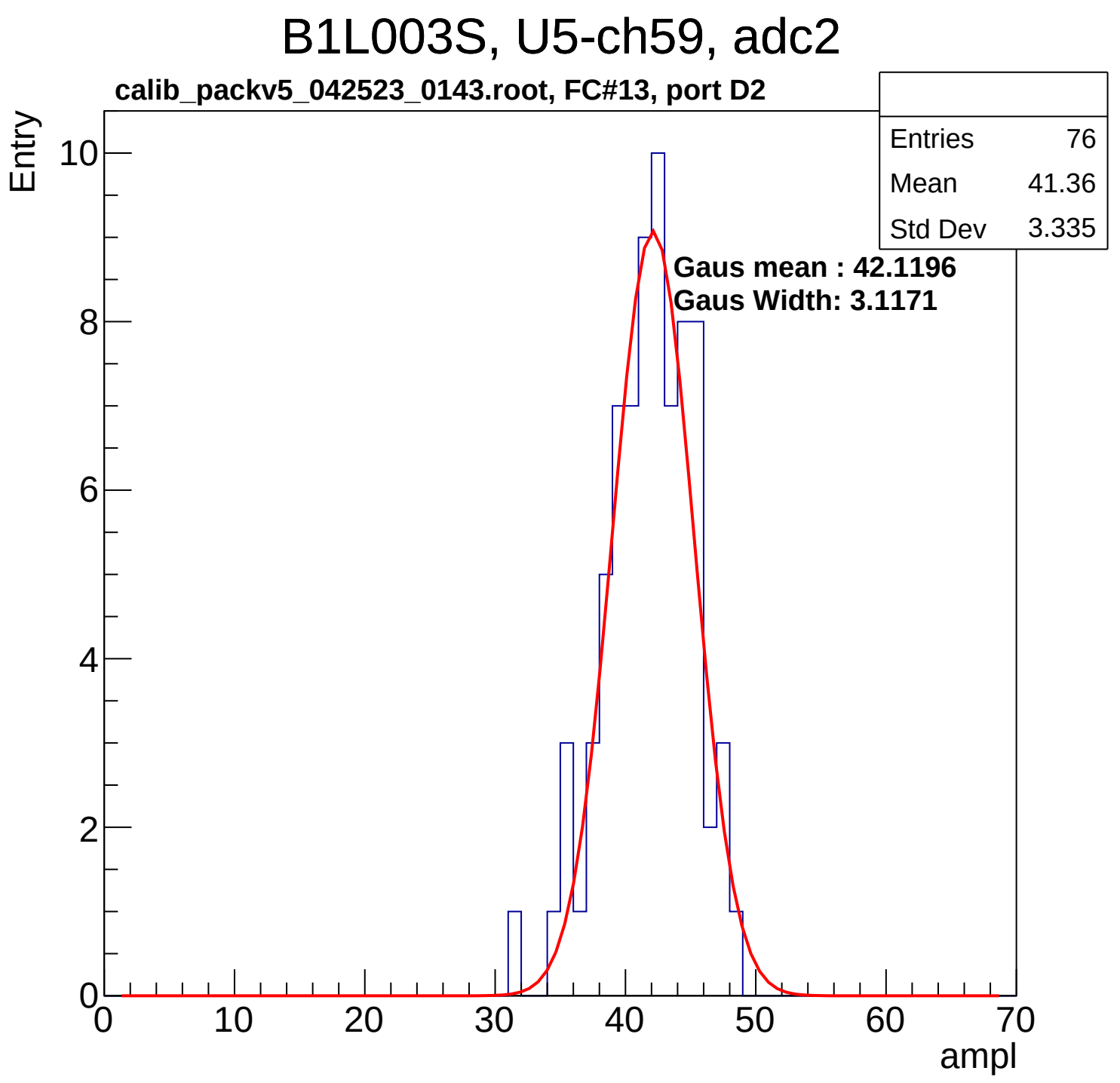
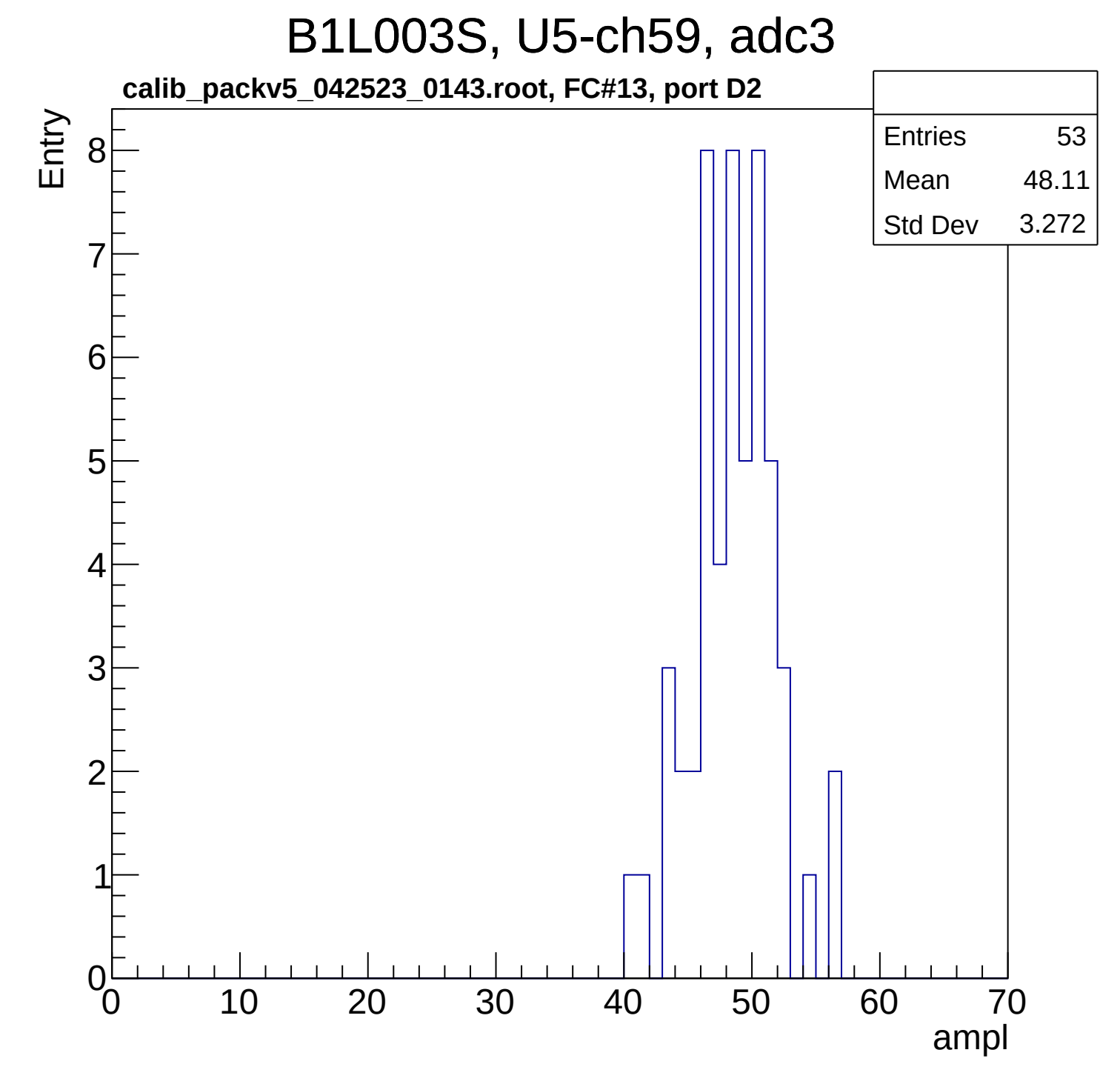

B1L003S, U5-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 41.36
Std Dev 3.335
Gaus mean : 42.1196
Gaus Width: 3.1171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.11
Std Dev 3.272
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
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60
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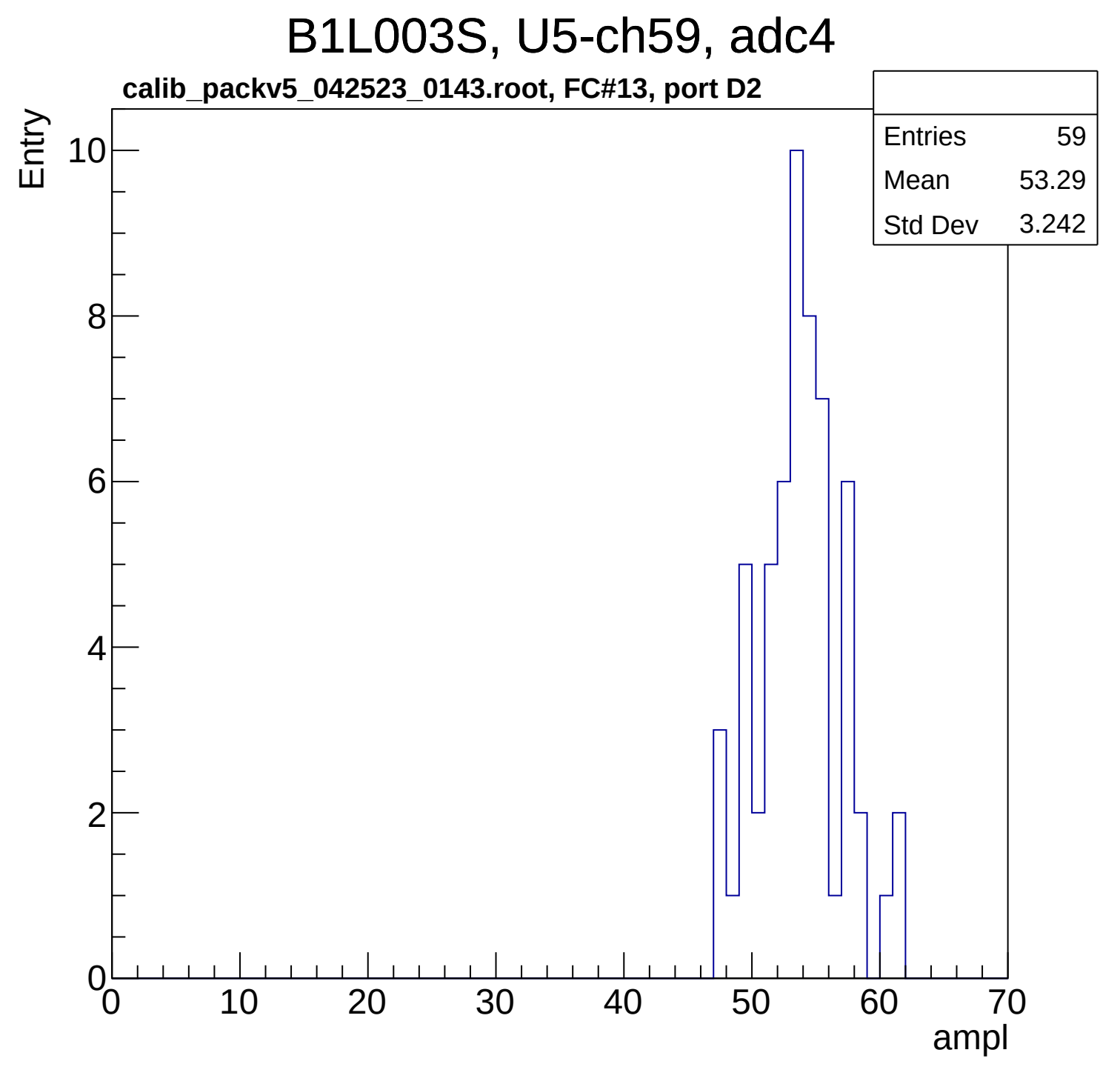
B1L003S, U5-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 53.29
Std Dev 3.242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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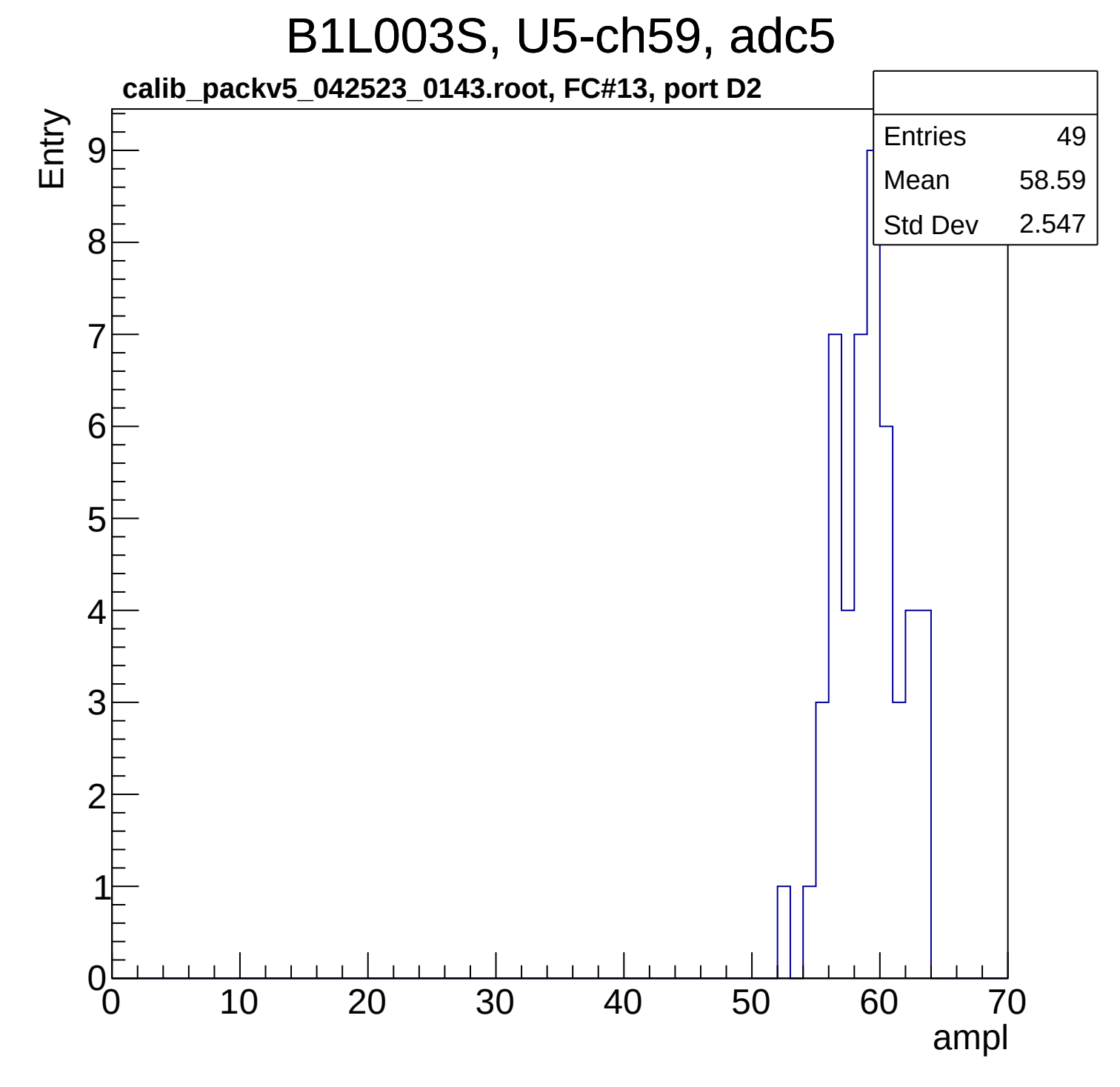
B1L003S, U5-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.59
Std Dev 2.547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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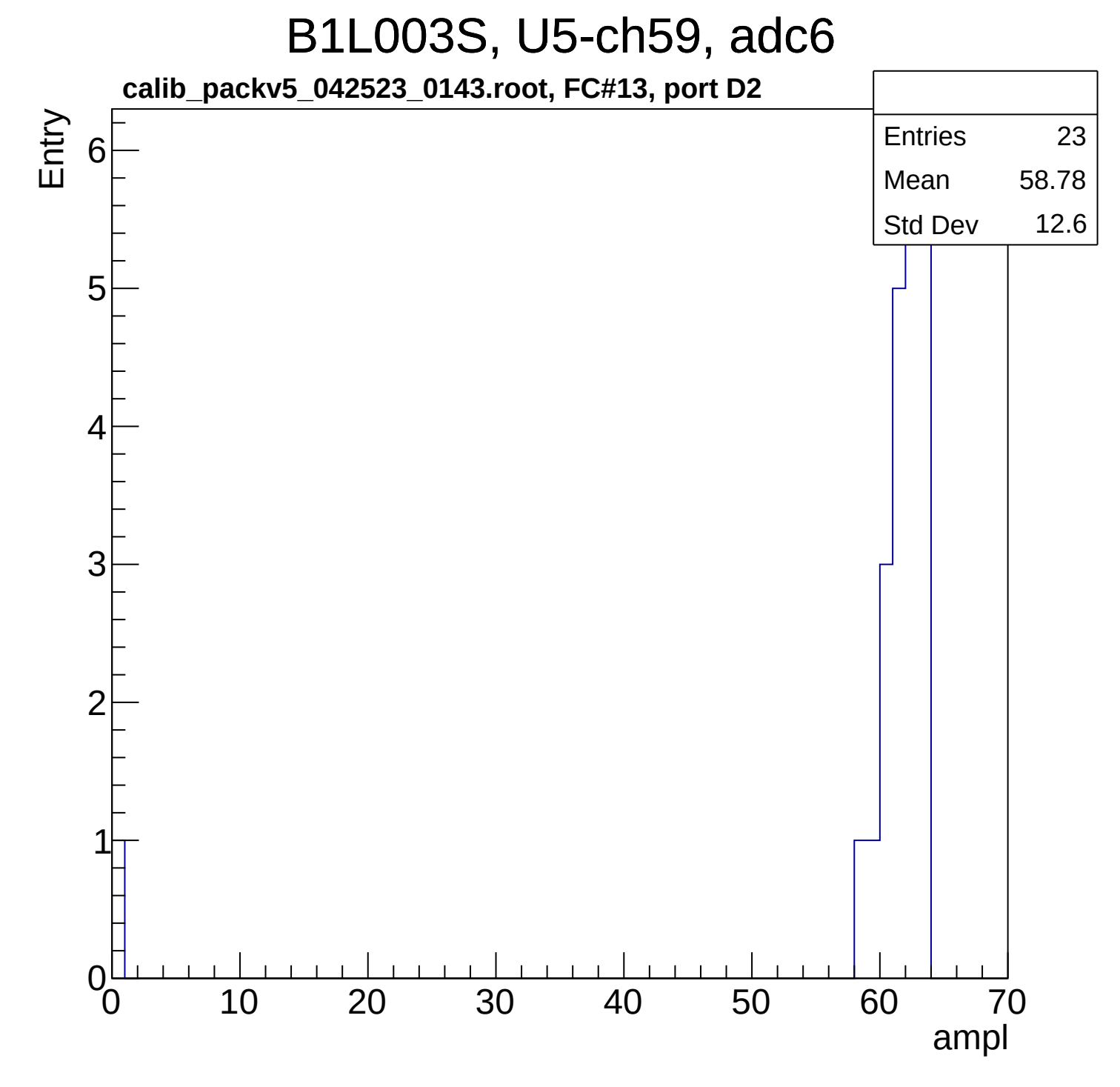
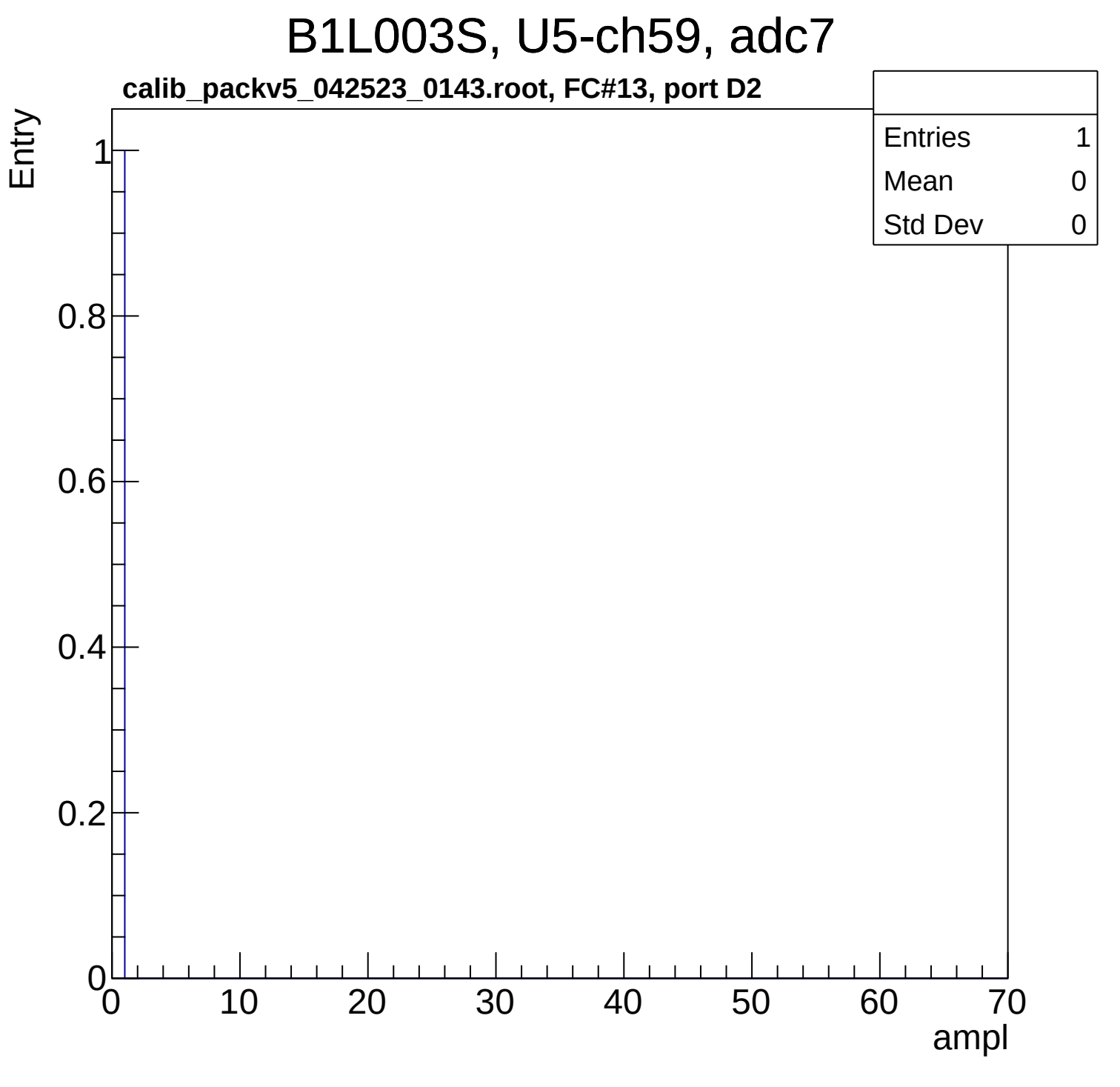

B1L003S, U5-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 58.78
Std Dev 12.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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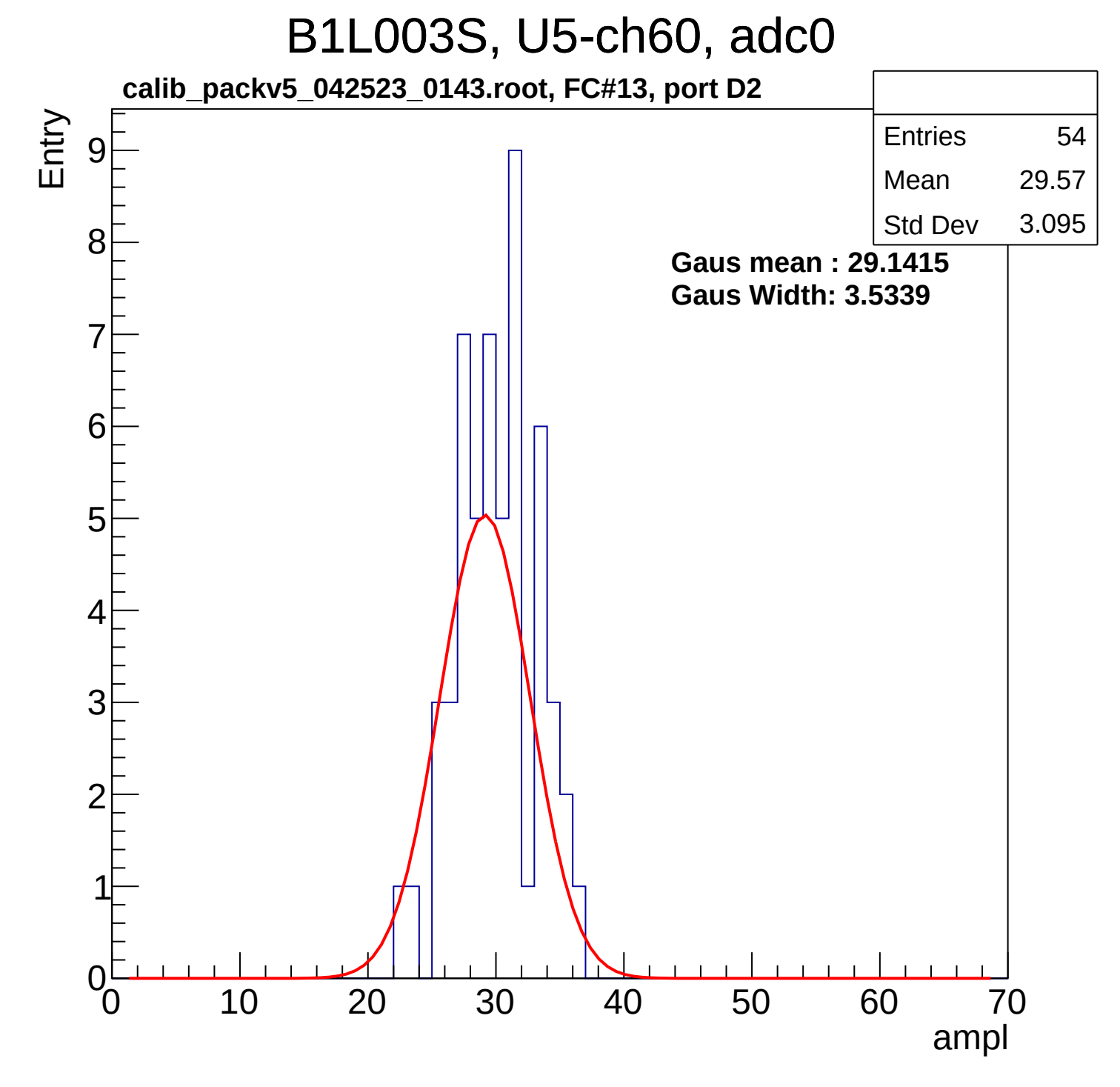
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.57
Std Dev 3.095
Gaus mean : 29.1415
Gaus Width: 3.5339
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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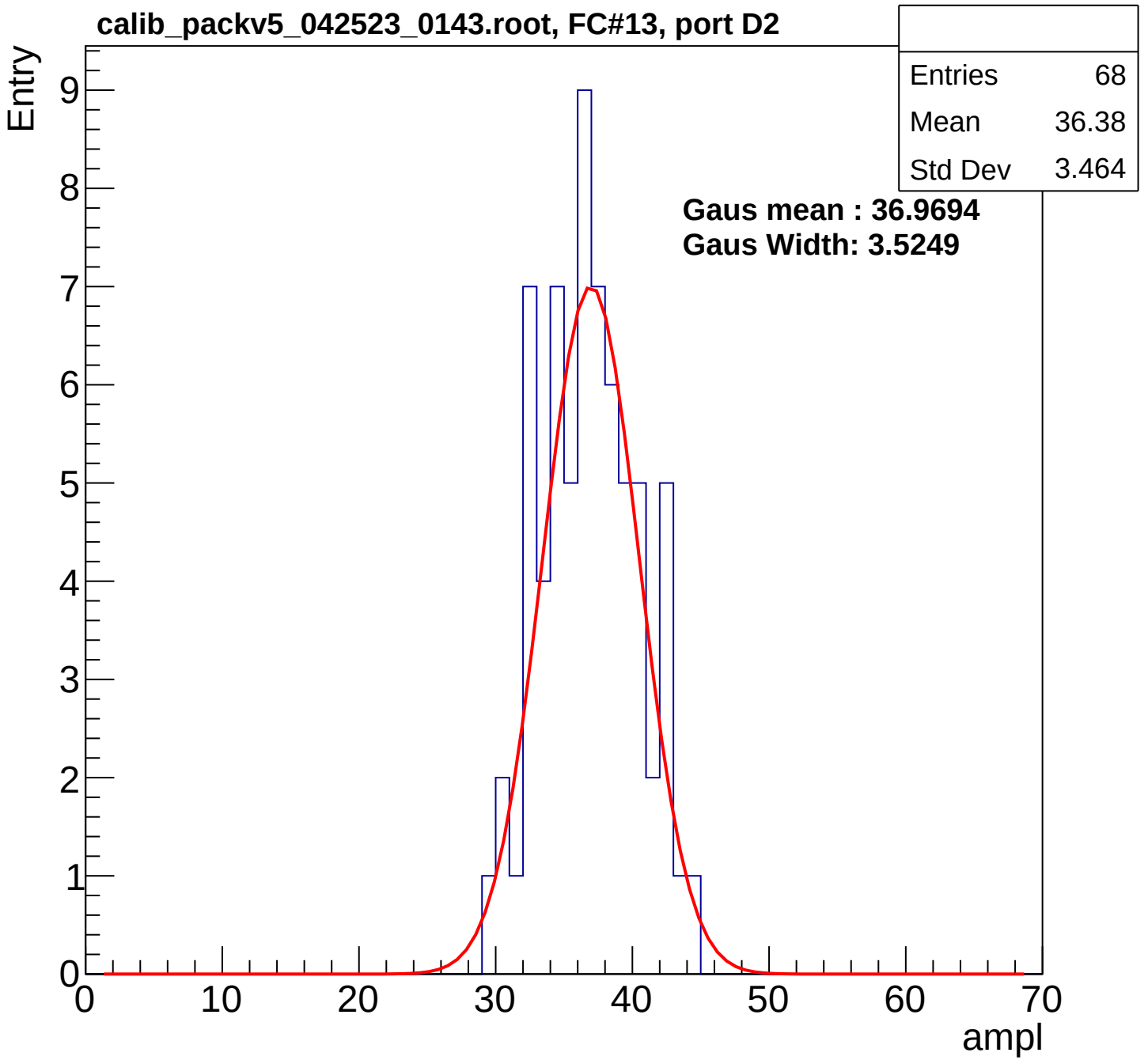
60
70


B1L003S, U5-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.38
Std Dev 3.464
Gaus mean : 36.9694
Gaus Width: 3.5249
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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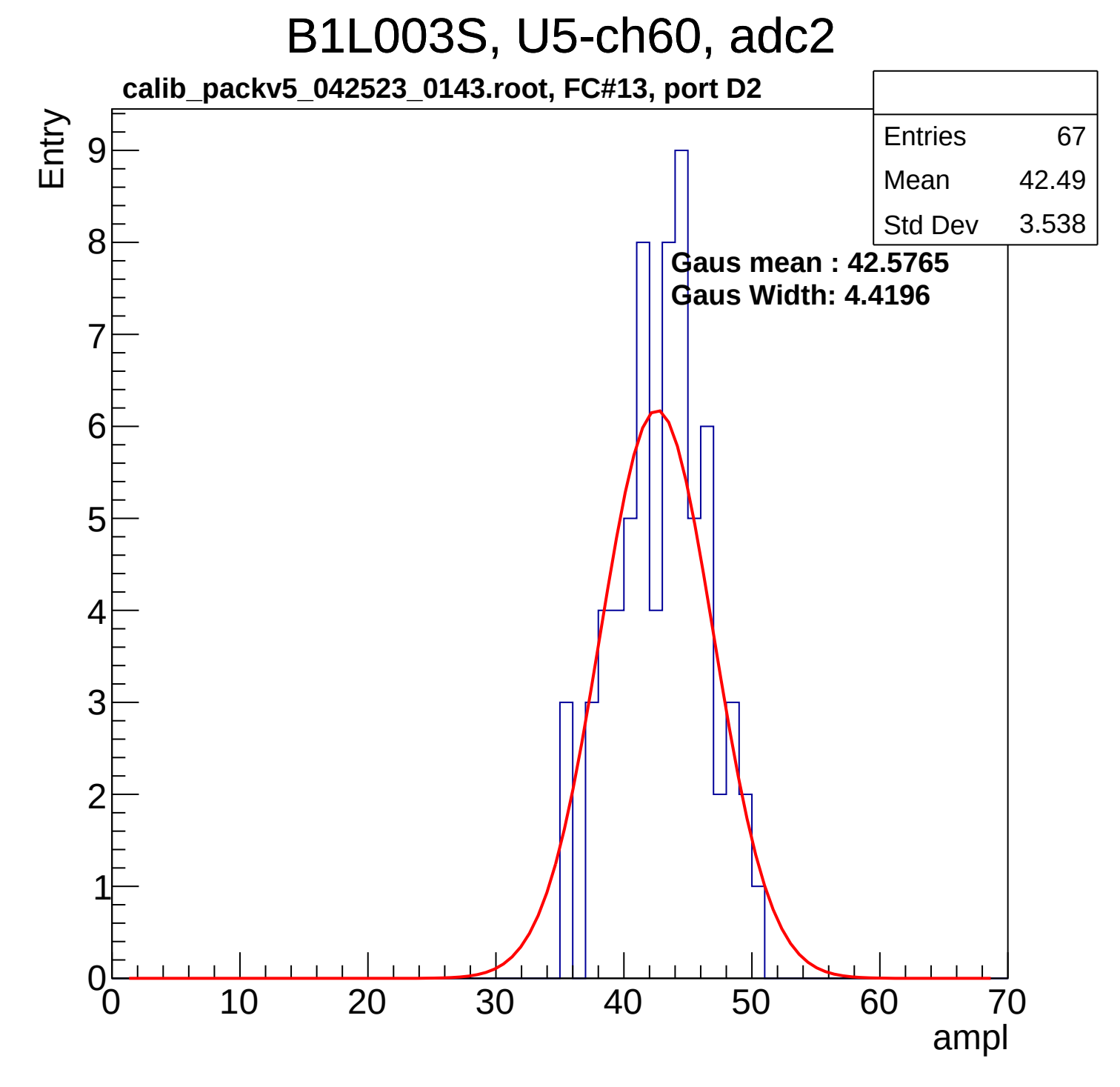
B1L003S, U5-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.49
Std Dev 3.538
Gaus mean : 42.5765
Gaus Width: 4.4196
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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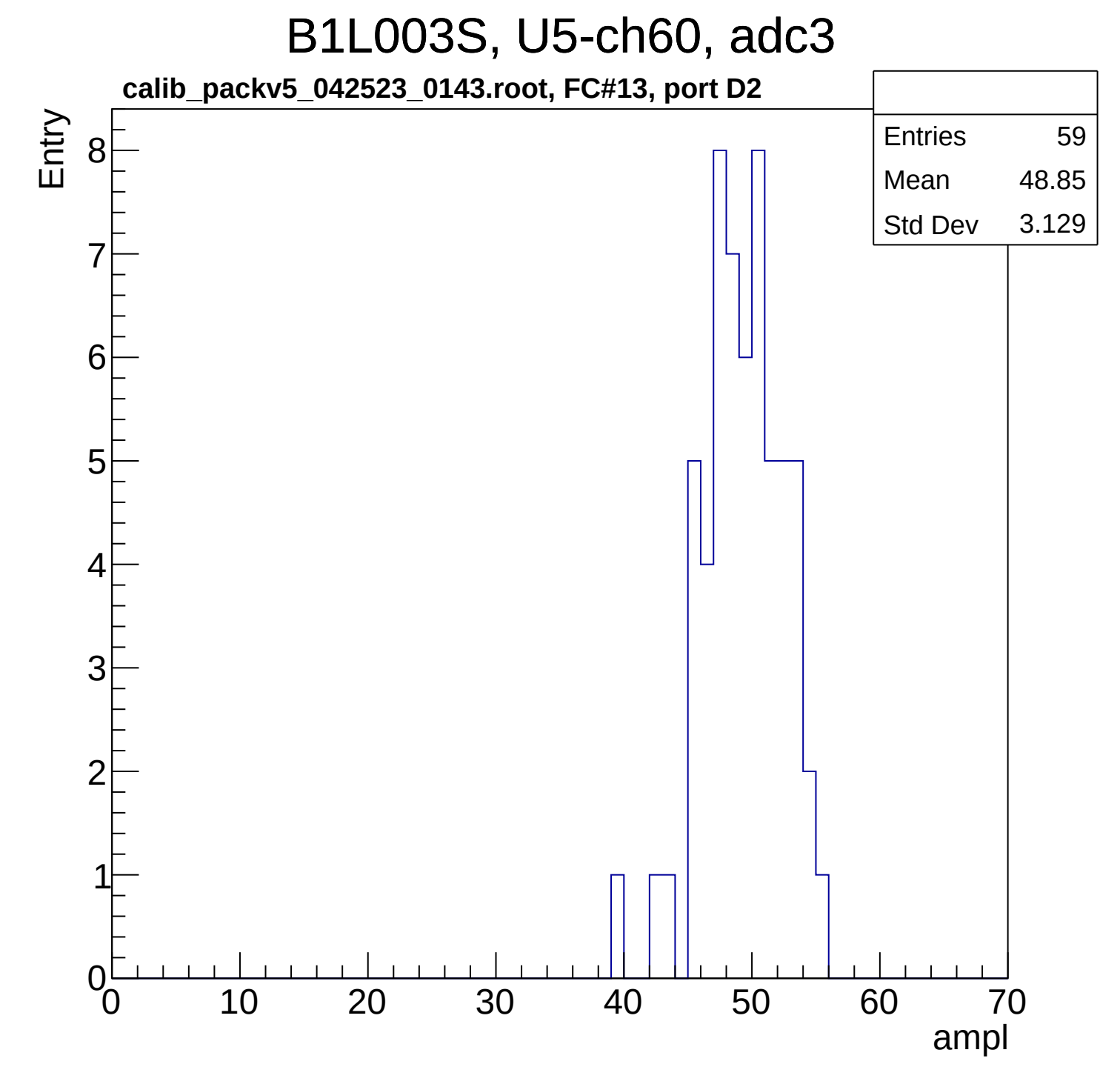
B1L003S, U5-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.85
Std Dev 3.129
Entry
ampl
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60
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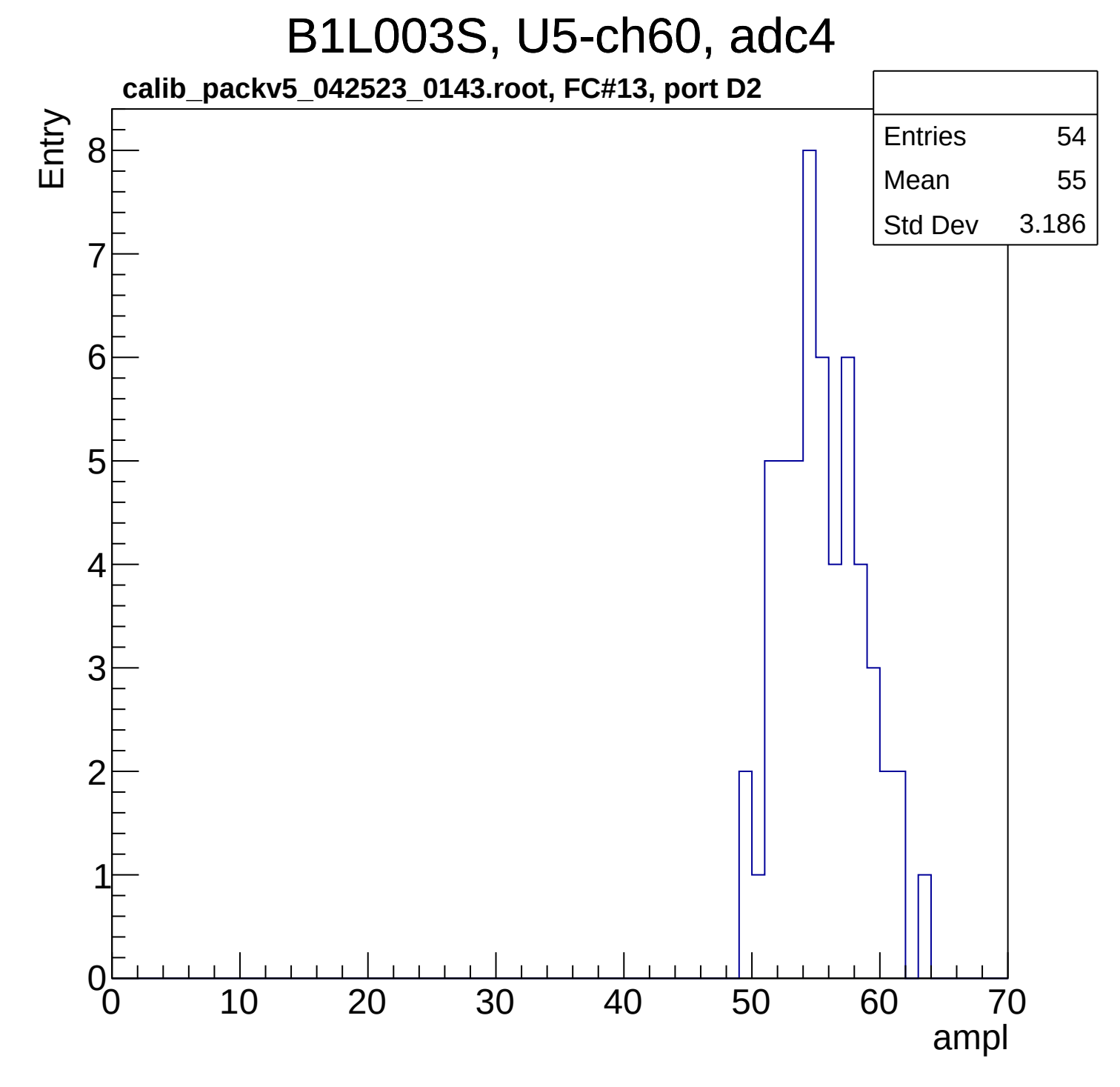
B1L003S, U5-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55
Std Dev 3.186
Entry
ampl
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0
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60
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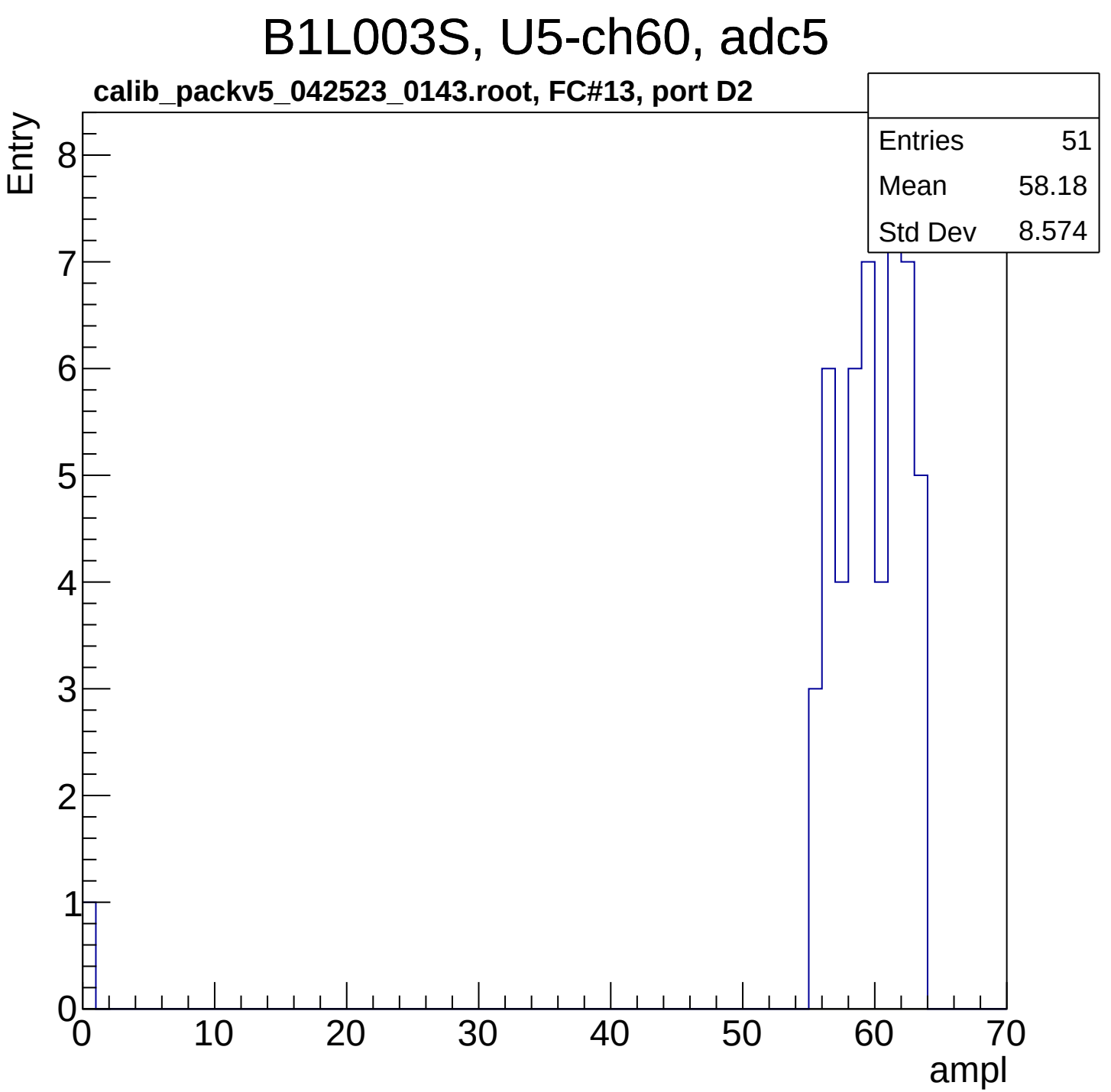
B1L003S, U5-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.18
Std Dev 8.574
Entry
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2
3
4
5
6
7
8
0
10
20
30
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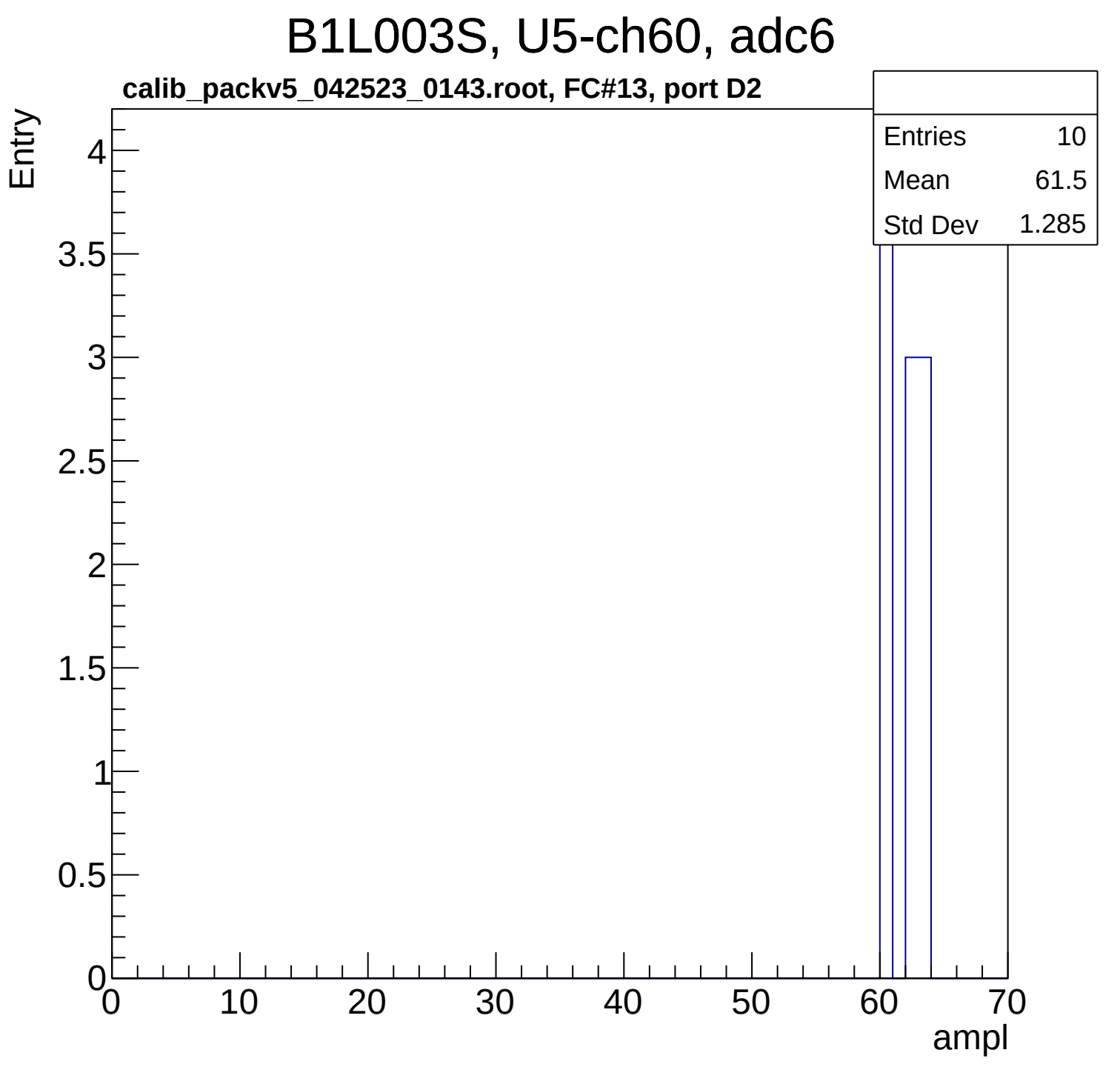
50
60
70
ampl


B1L003S, U5-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.5
Std Dev 1.285
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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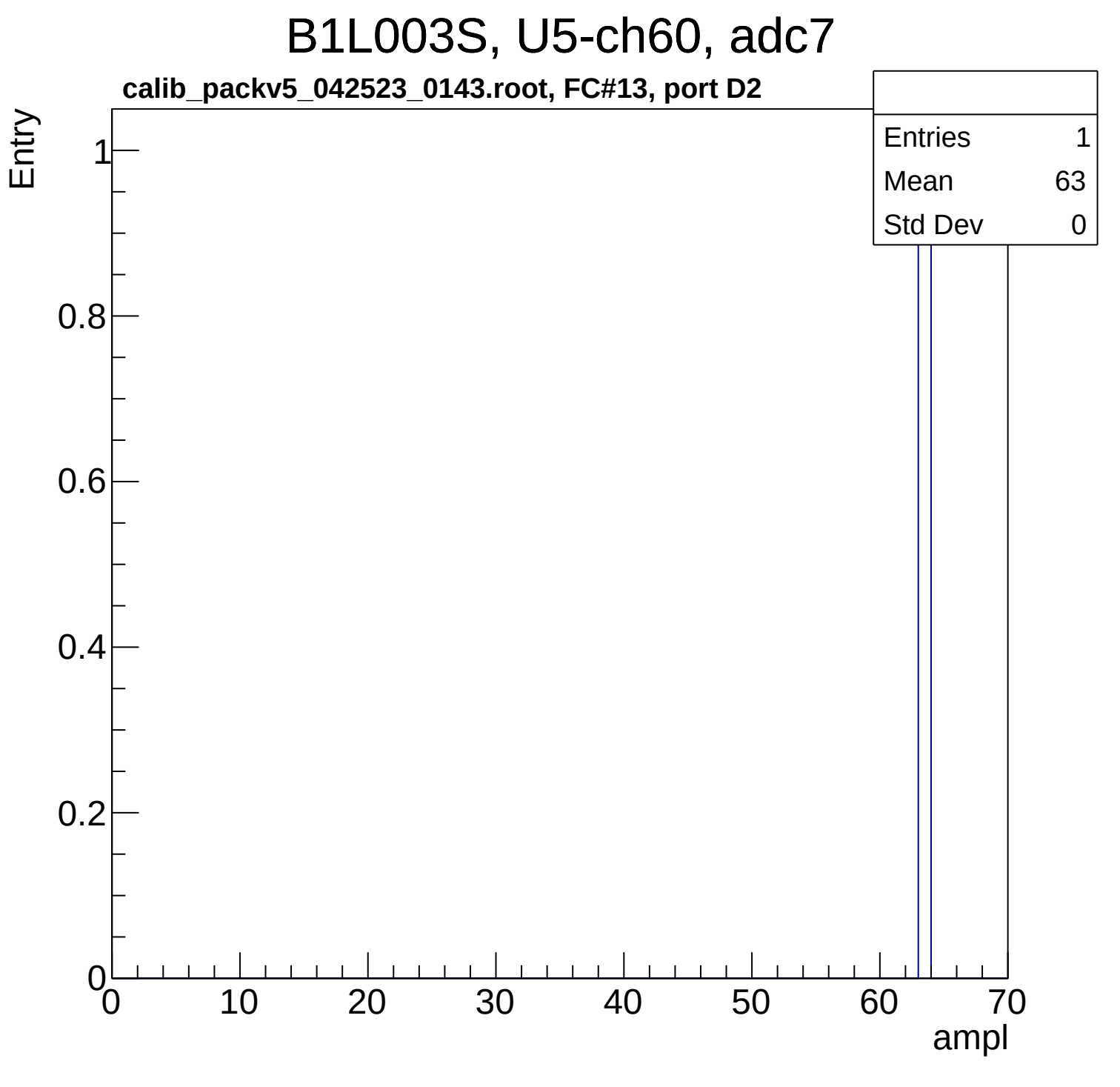
B1L003S, U5-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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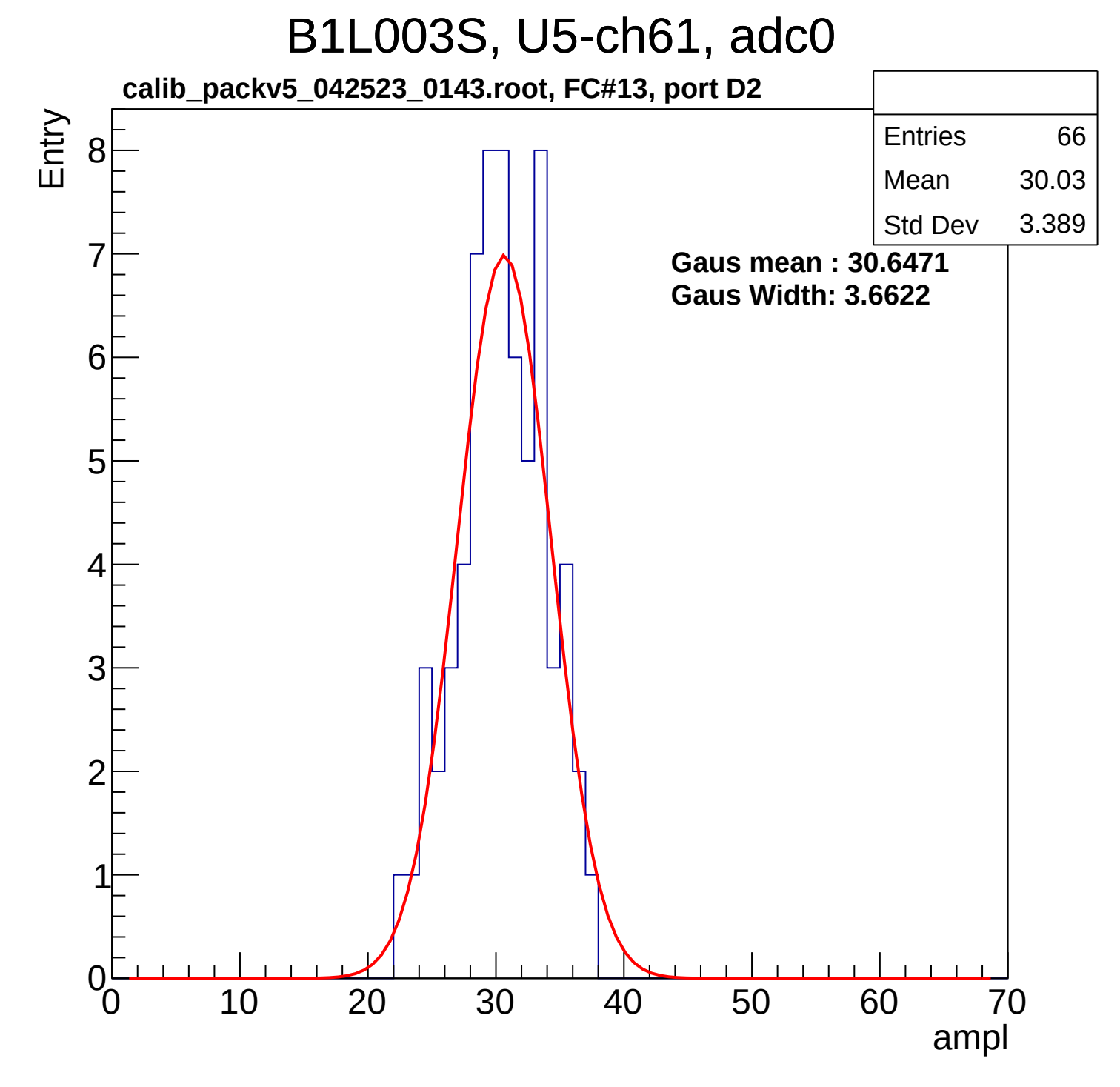
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.03
Std Dev 3.389
Gaus mean : 30.6471
Gaus Width: 3.6622
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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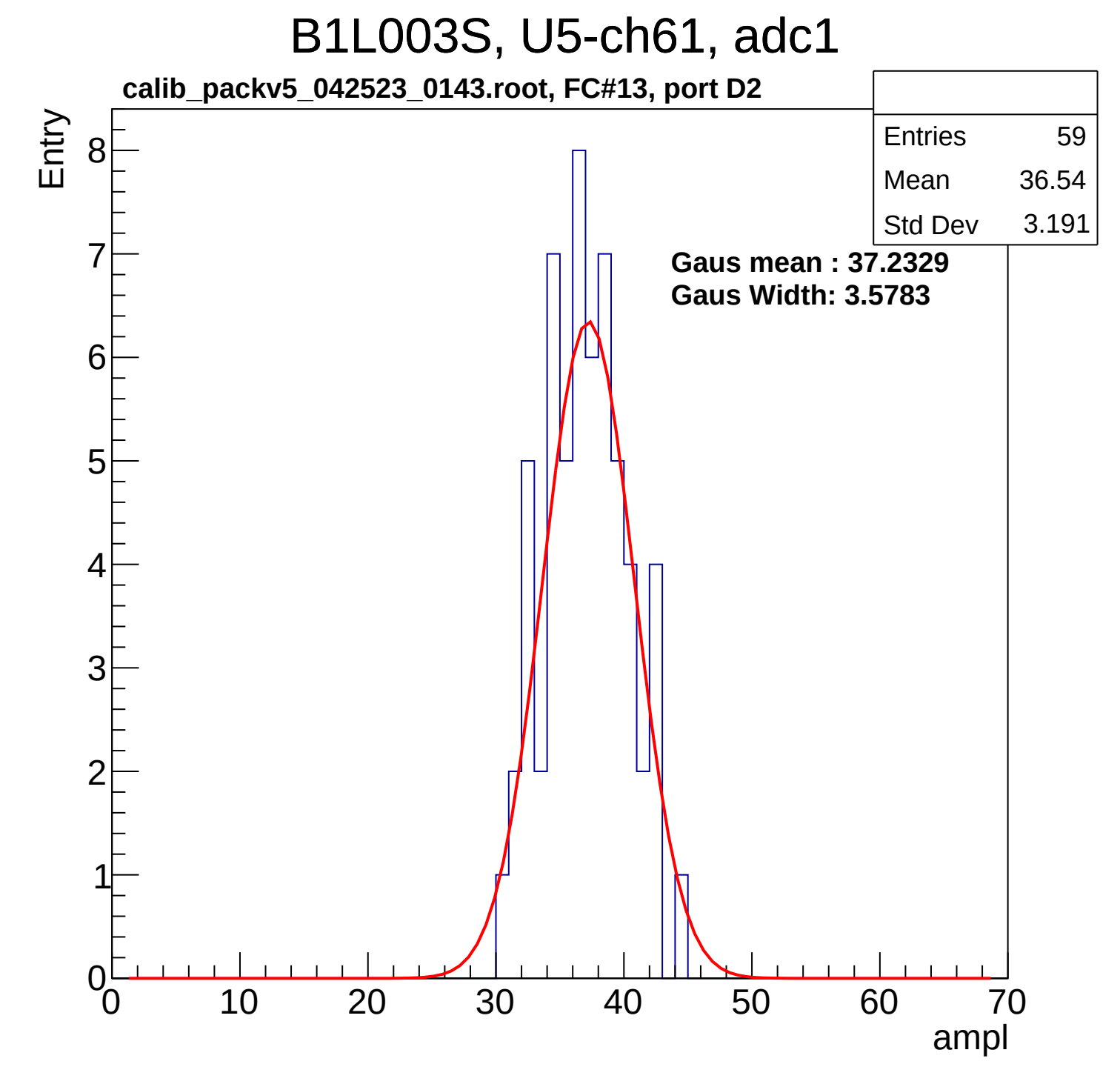
40
50
60
70


B1L003S, U5-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.54
Std Dev 3.191
Gaus mean : 37.2329
Gaus Width: 3.5783
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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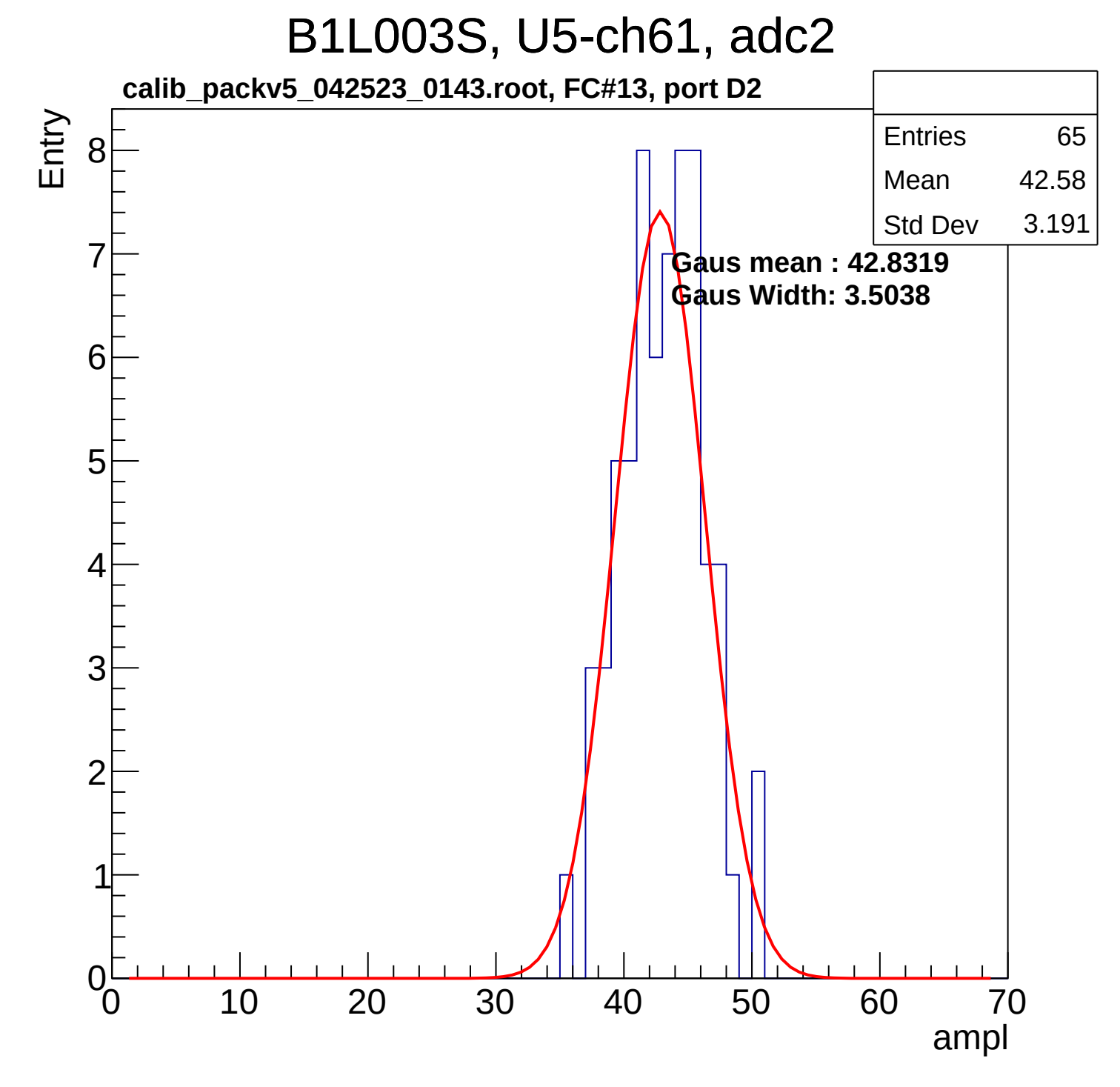
B1L003S, U5-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.58
Std Dev 3.191
Gaus mean : 42.8319
Gaus Width: 3.5038
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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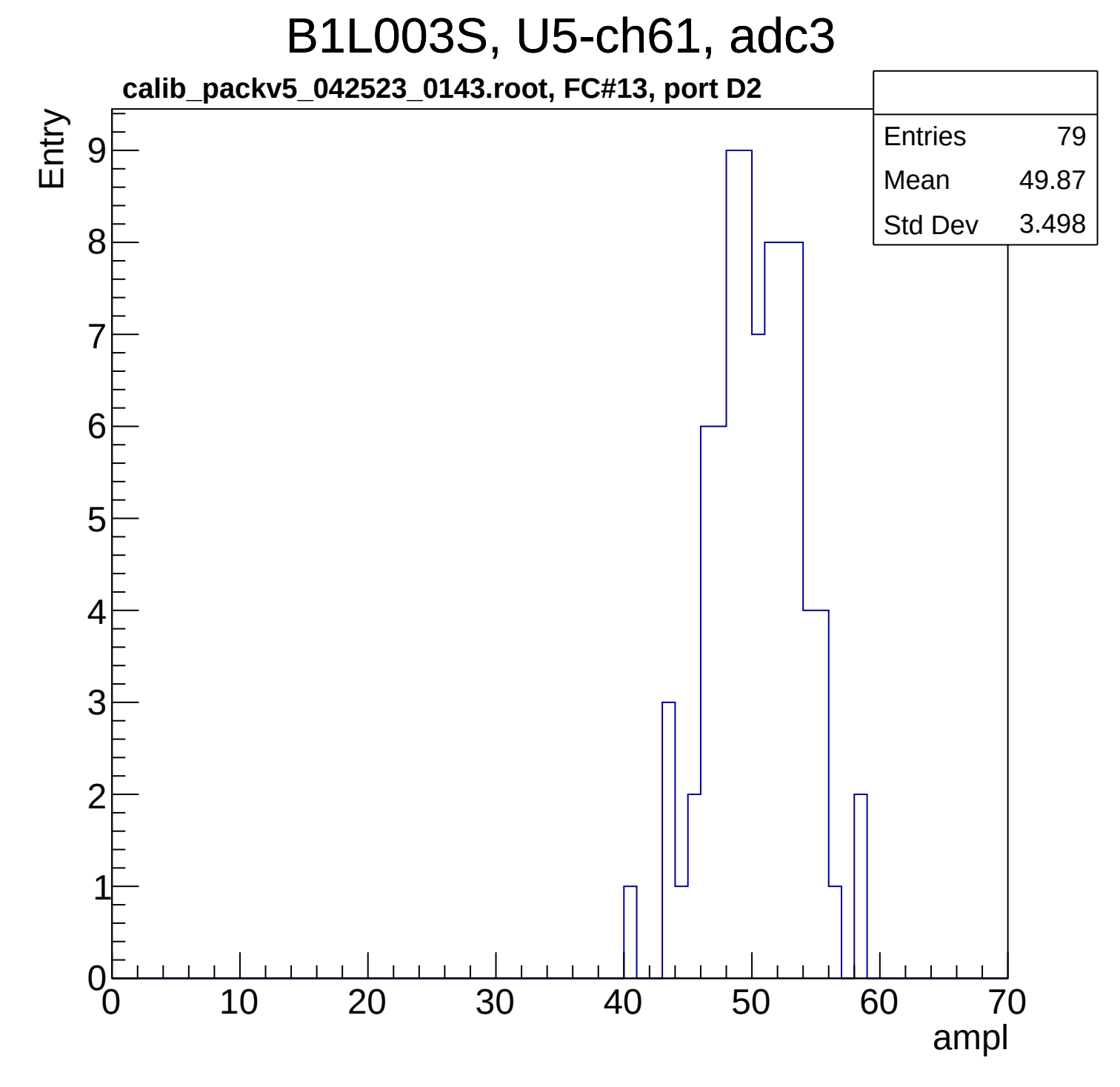
B1L003S, U5-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.87
Std Dev 3.498
Entry
ampl
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3
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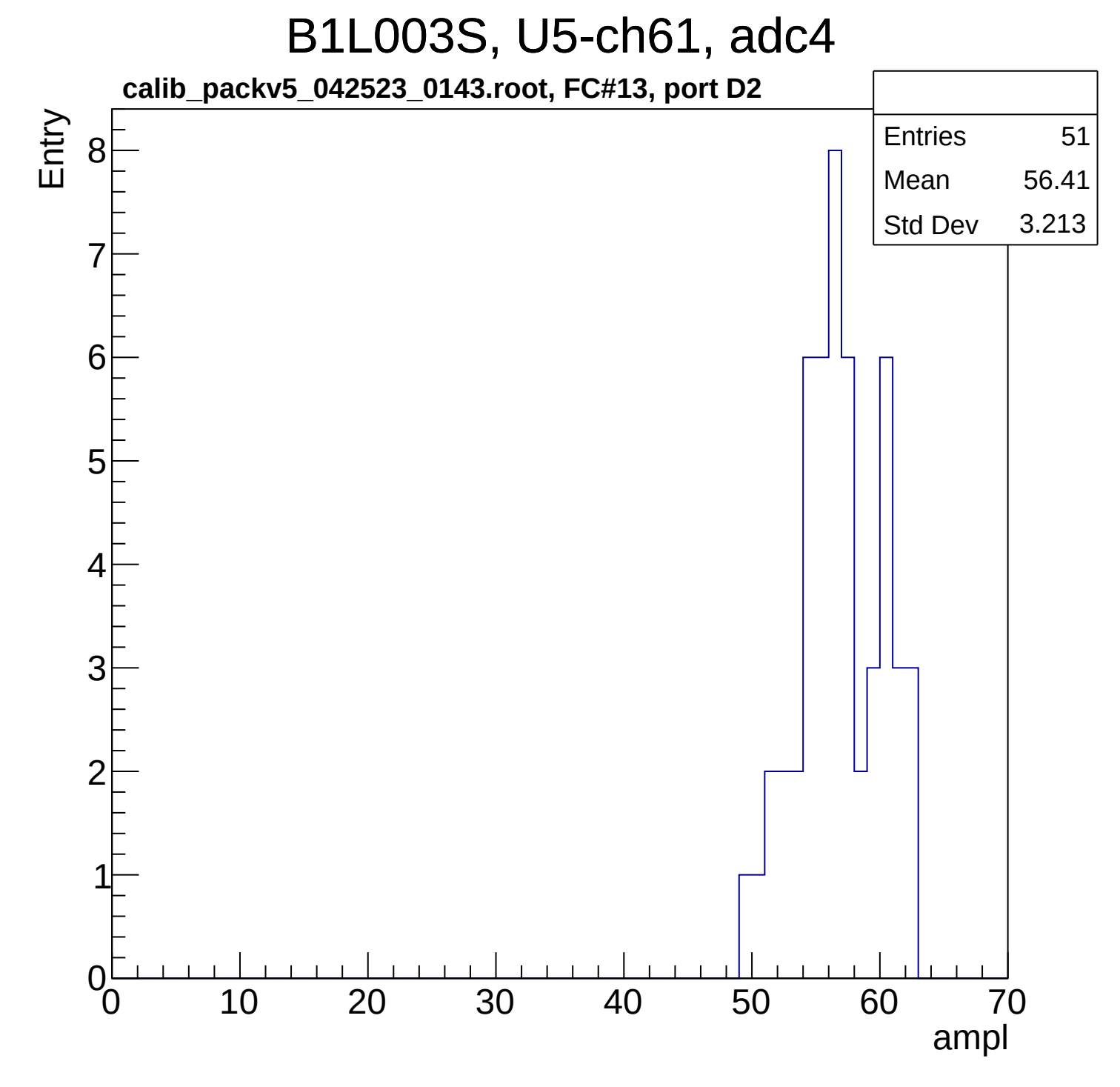
B1L003S, U5-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 56.41
Std Dev 3.213
Entry
ampl
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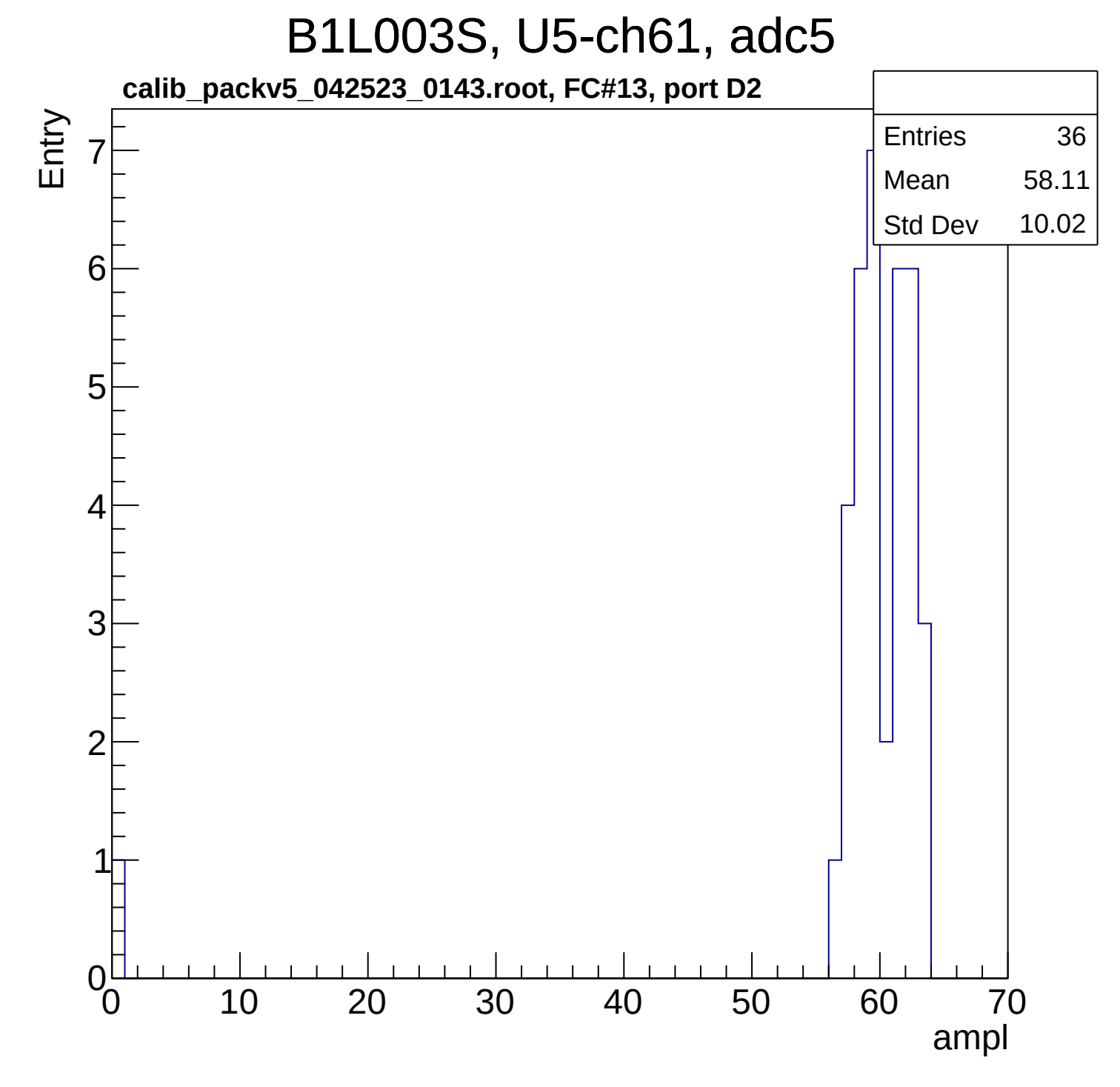
B1L003S, U5-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.11
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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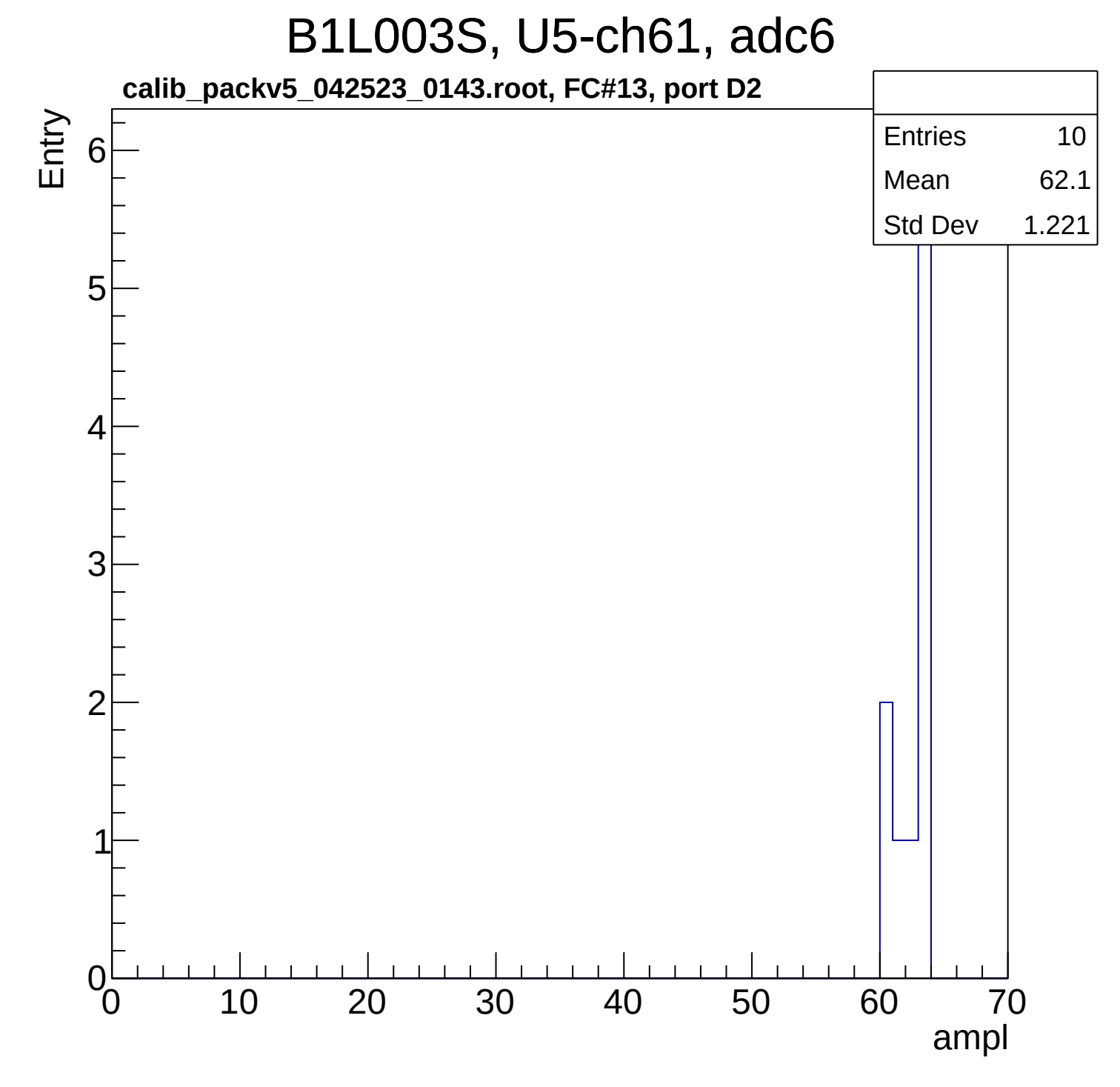
B1L003S, U5-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.1
Std Dev 1.221
Entry
0
1
2
3
4
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6
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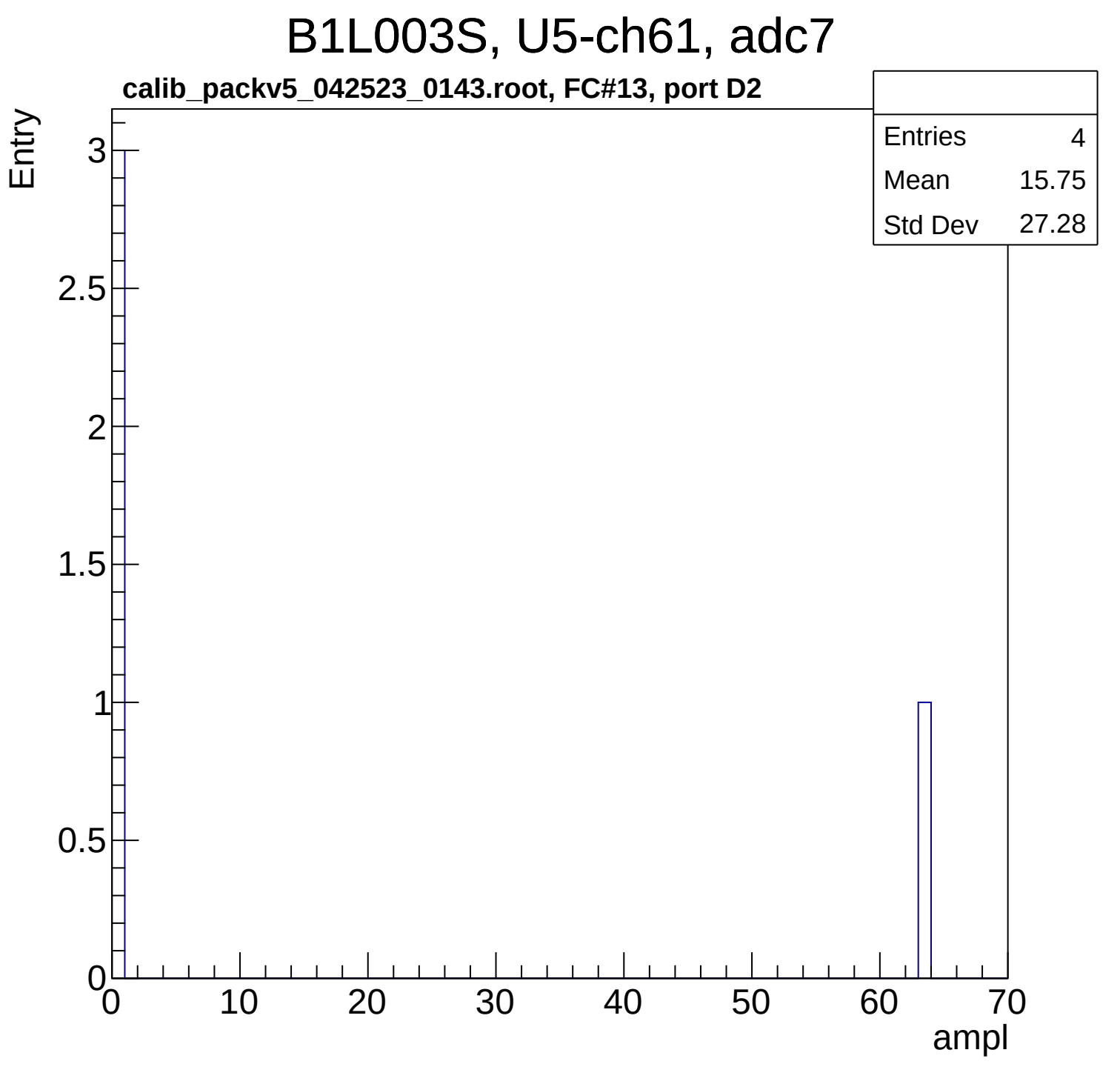
70
ampl


B1L003S, U5-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
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10
20
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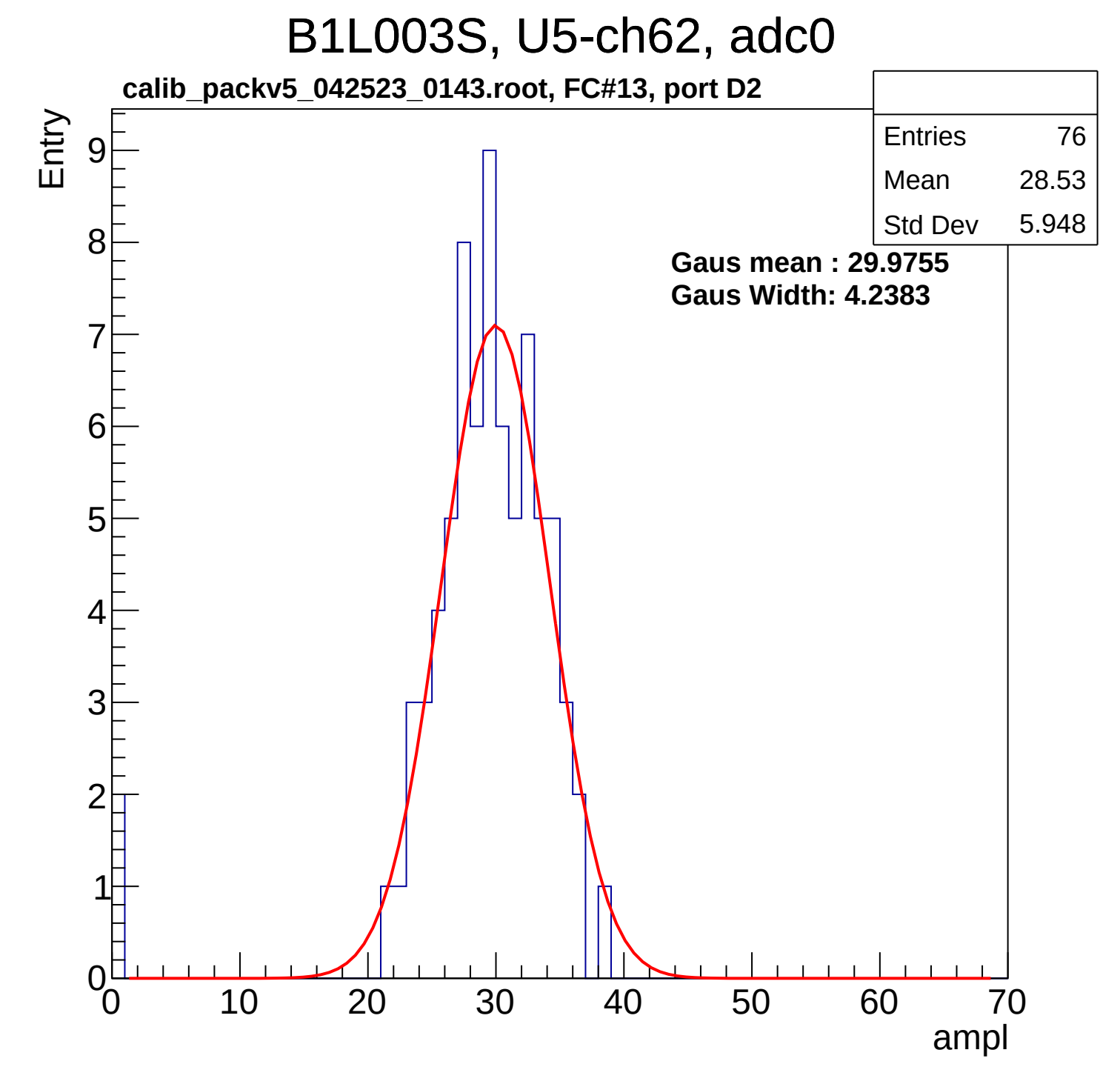
50
60
70
ampl


B1L003S, U5-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.53
Std Dev 5.948
Gaus mean : 29.9755
Gaus Width: 4.2383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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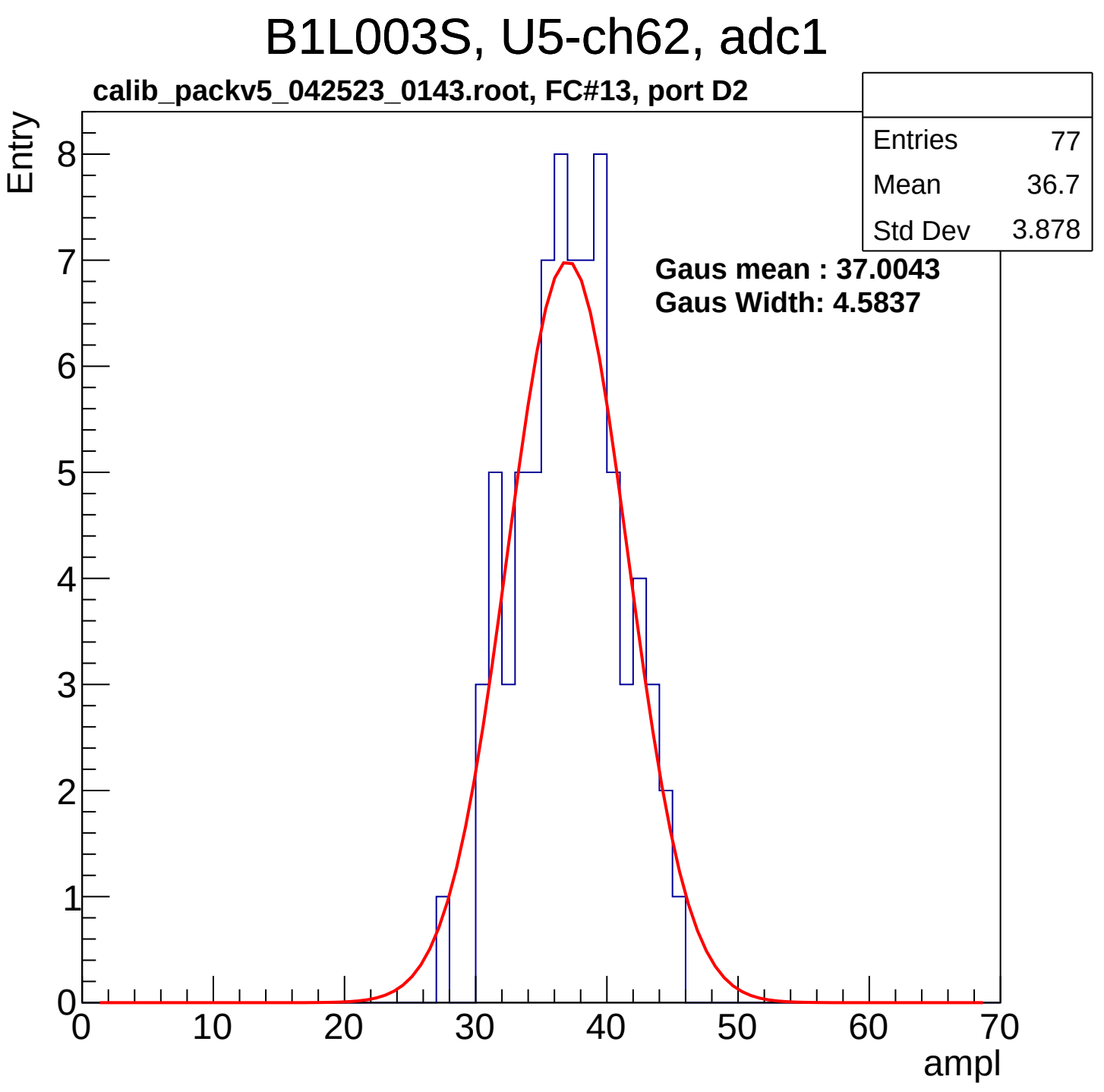
B1L003S, U5-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.7
Std Dev 3.878
Gaus mean : 37.0043
Gaus Width: 4.5837
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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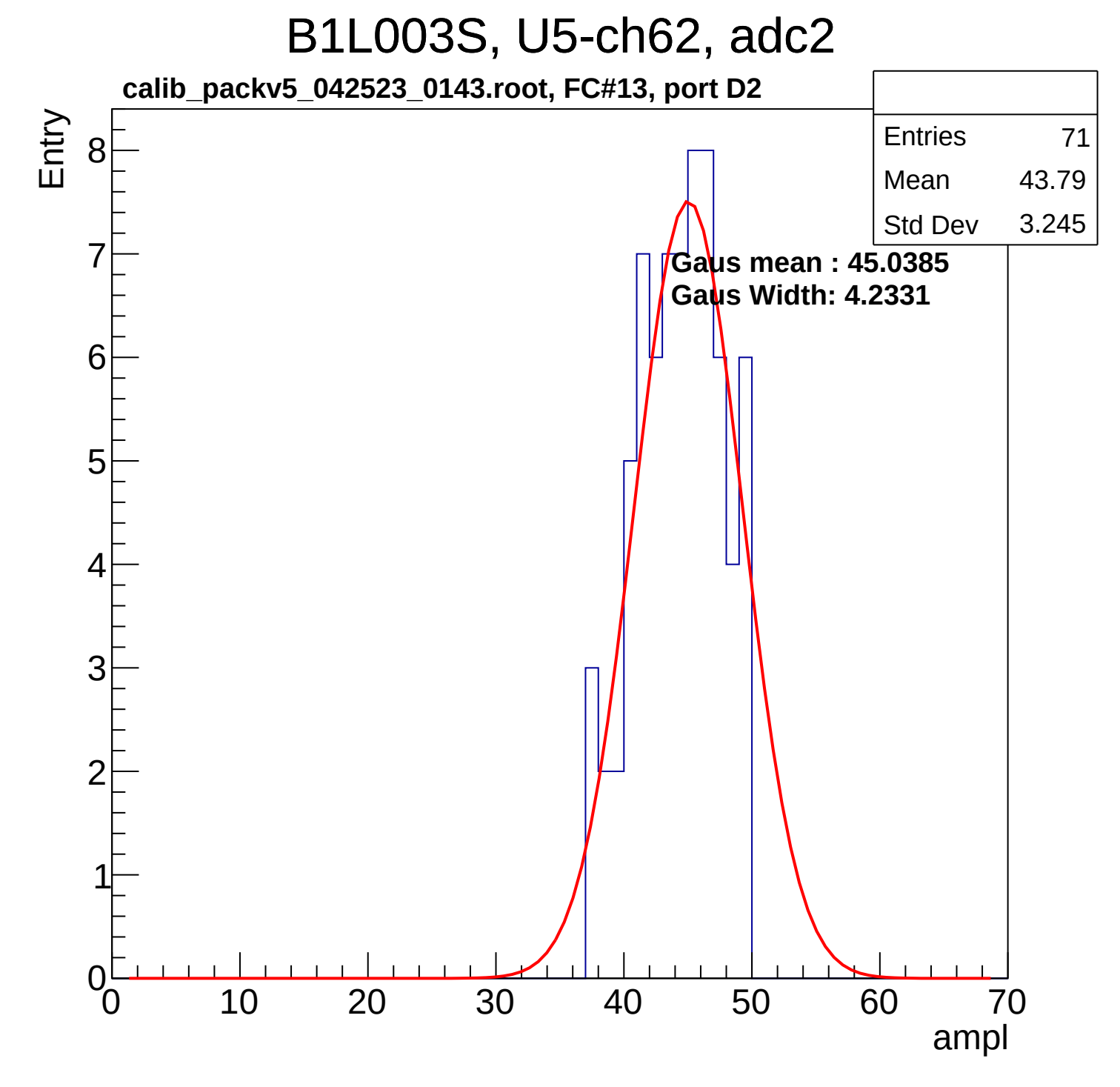
B1L003S, U5-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.79
Std Dev 3.245
Gaus mean : 45.0385
Gaus Width: 4.2331
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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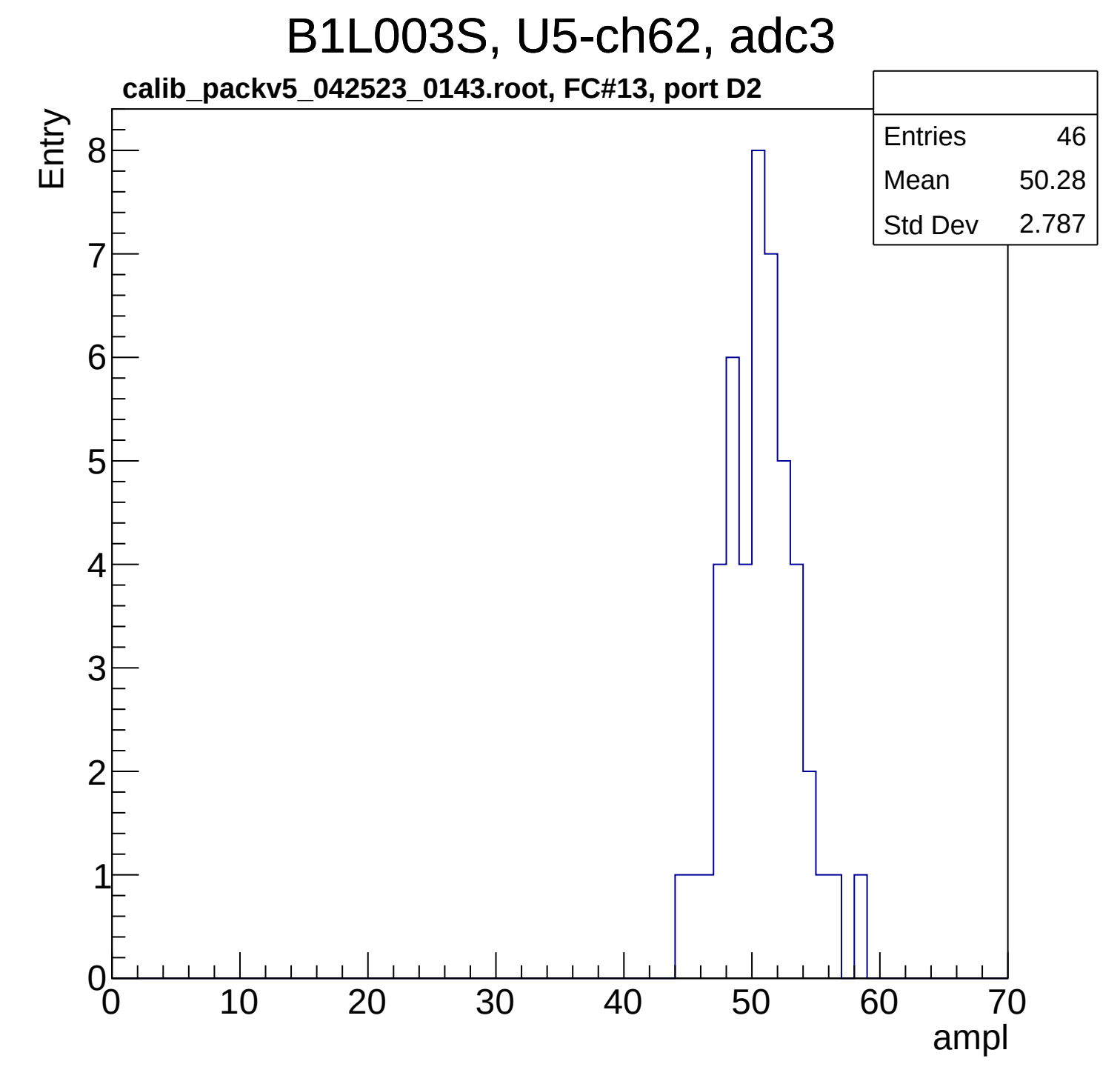
B1L003S, U5-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 50.28
Std Dev 2.787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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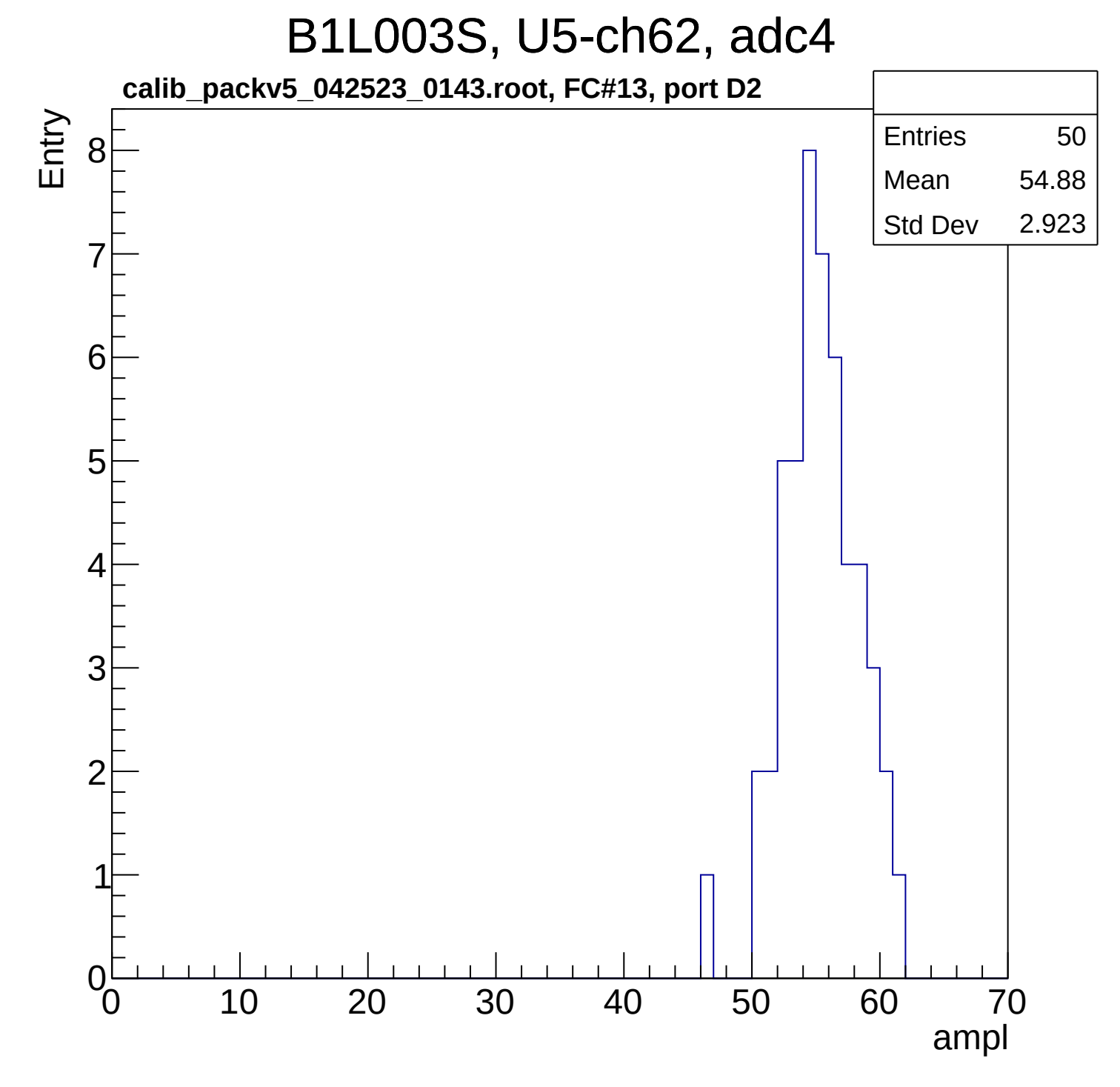
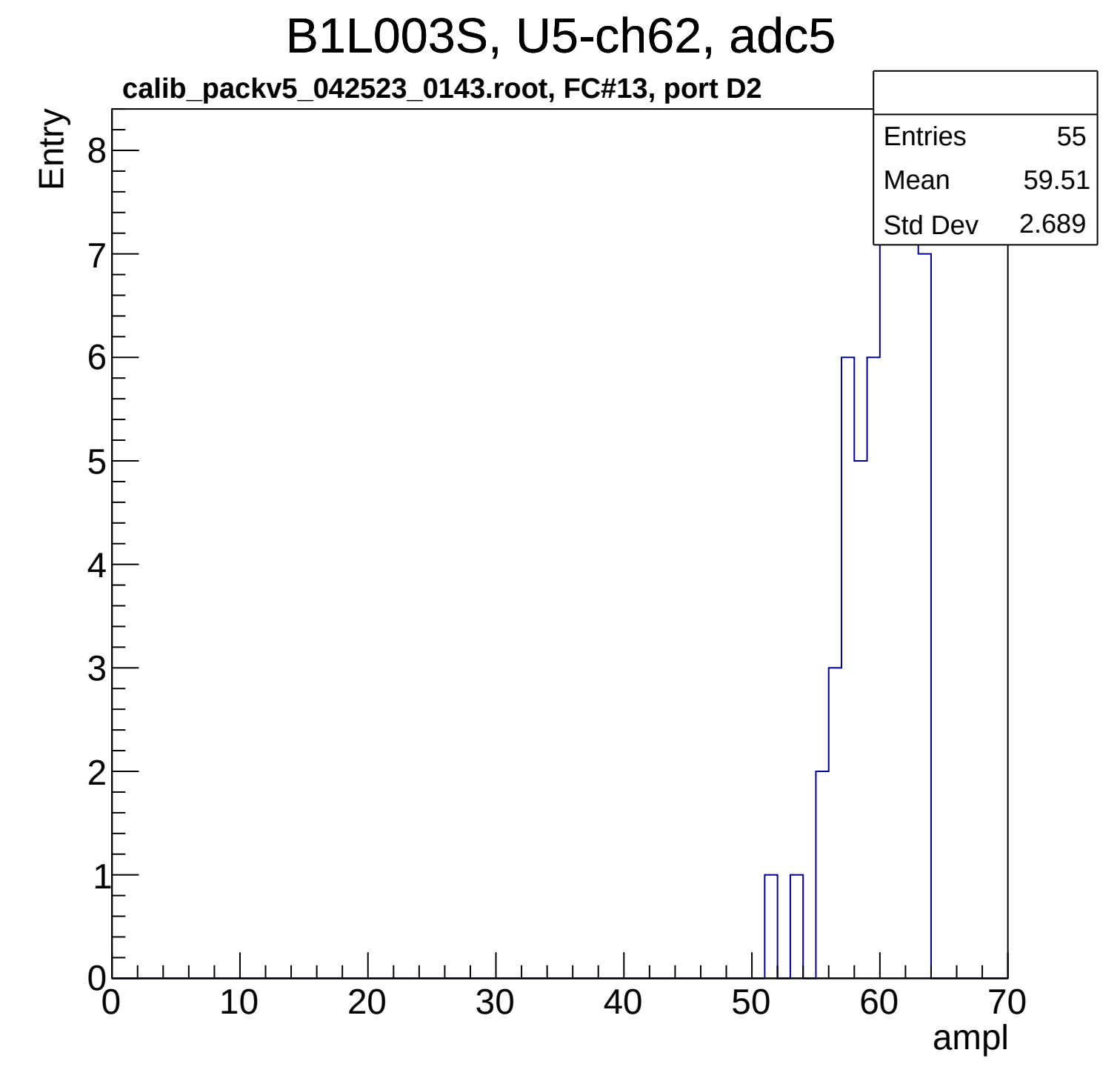

B1L003S, U5-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.88
Std Dev 2.923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.51
Std Dev 2.689
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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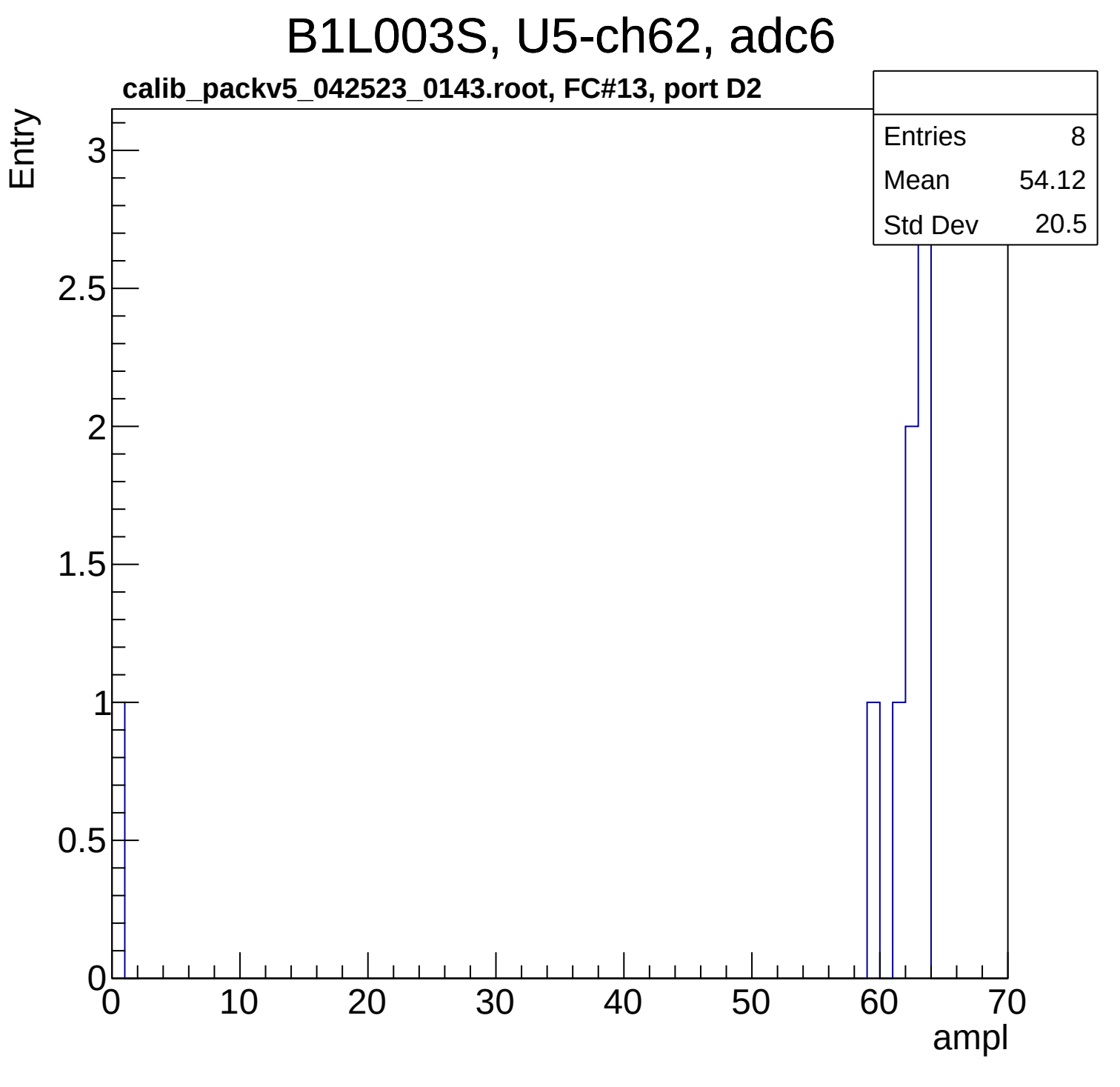
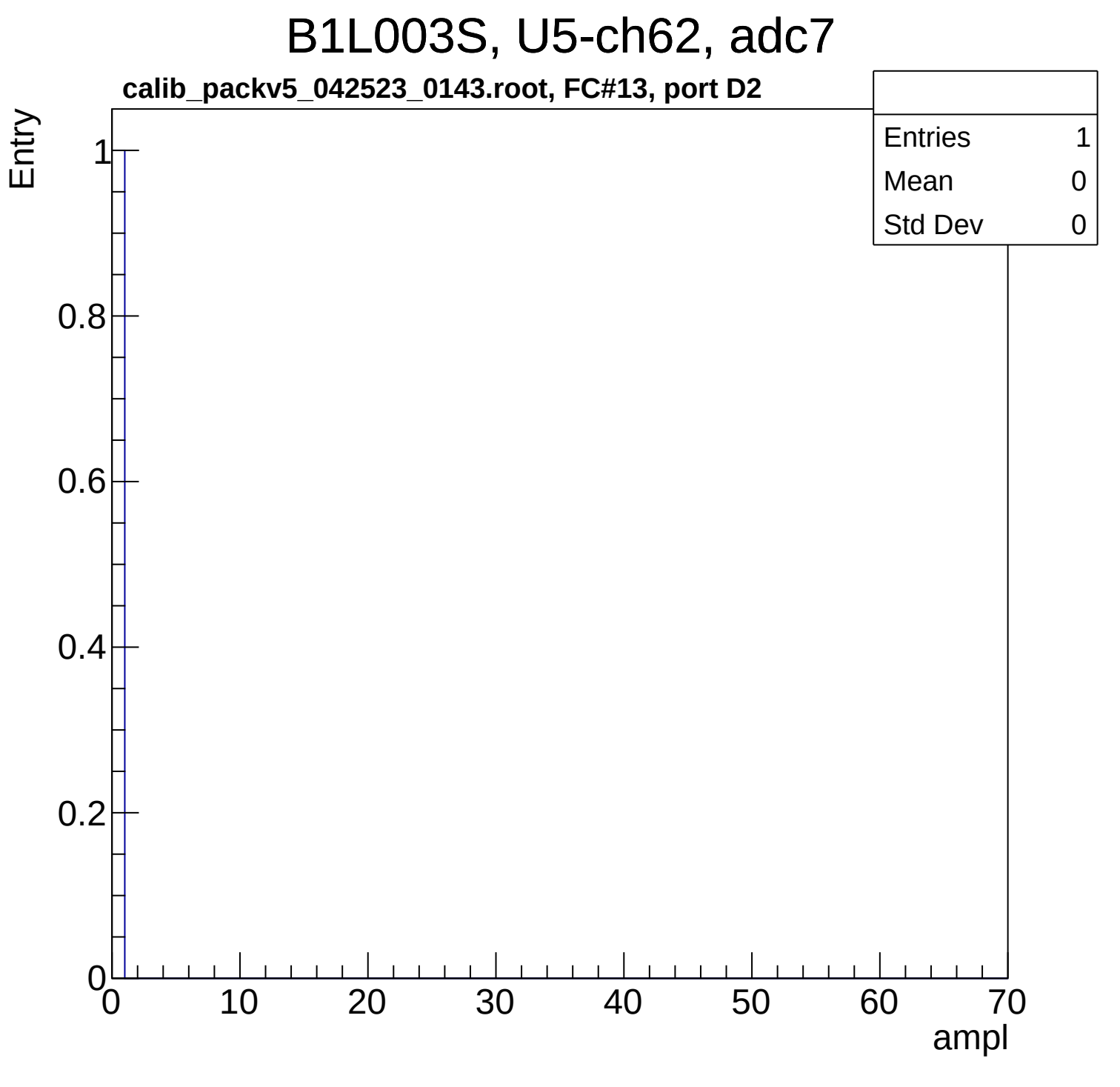
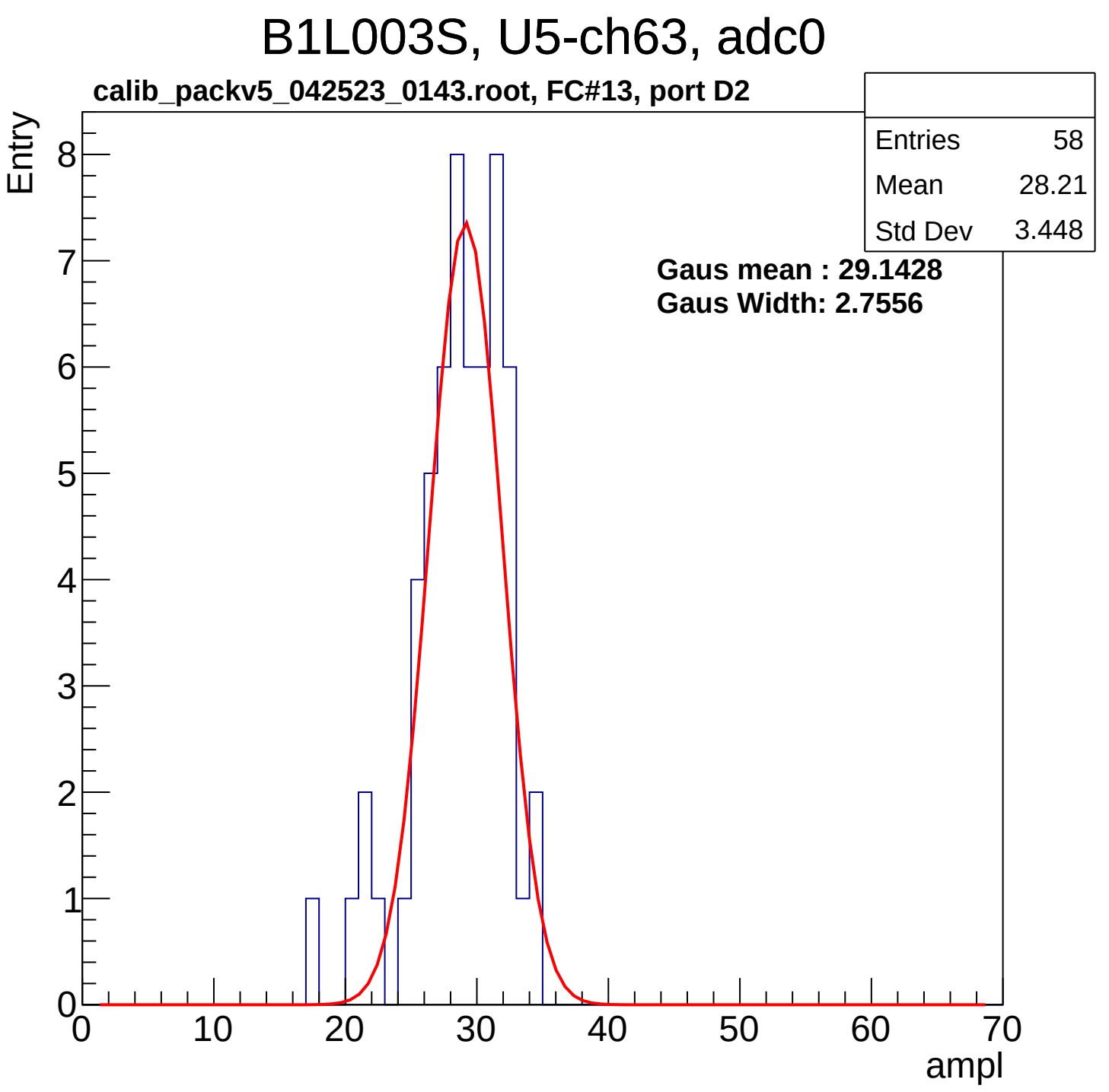

B1L003S, U5-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.12
Std Dev 20.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.21
Std Dev 3.448
Gaus mean : 29.1428
Gaus Width: 2.7556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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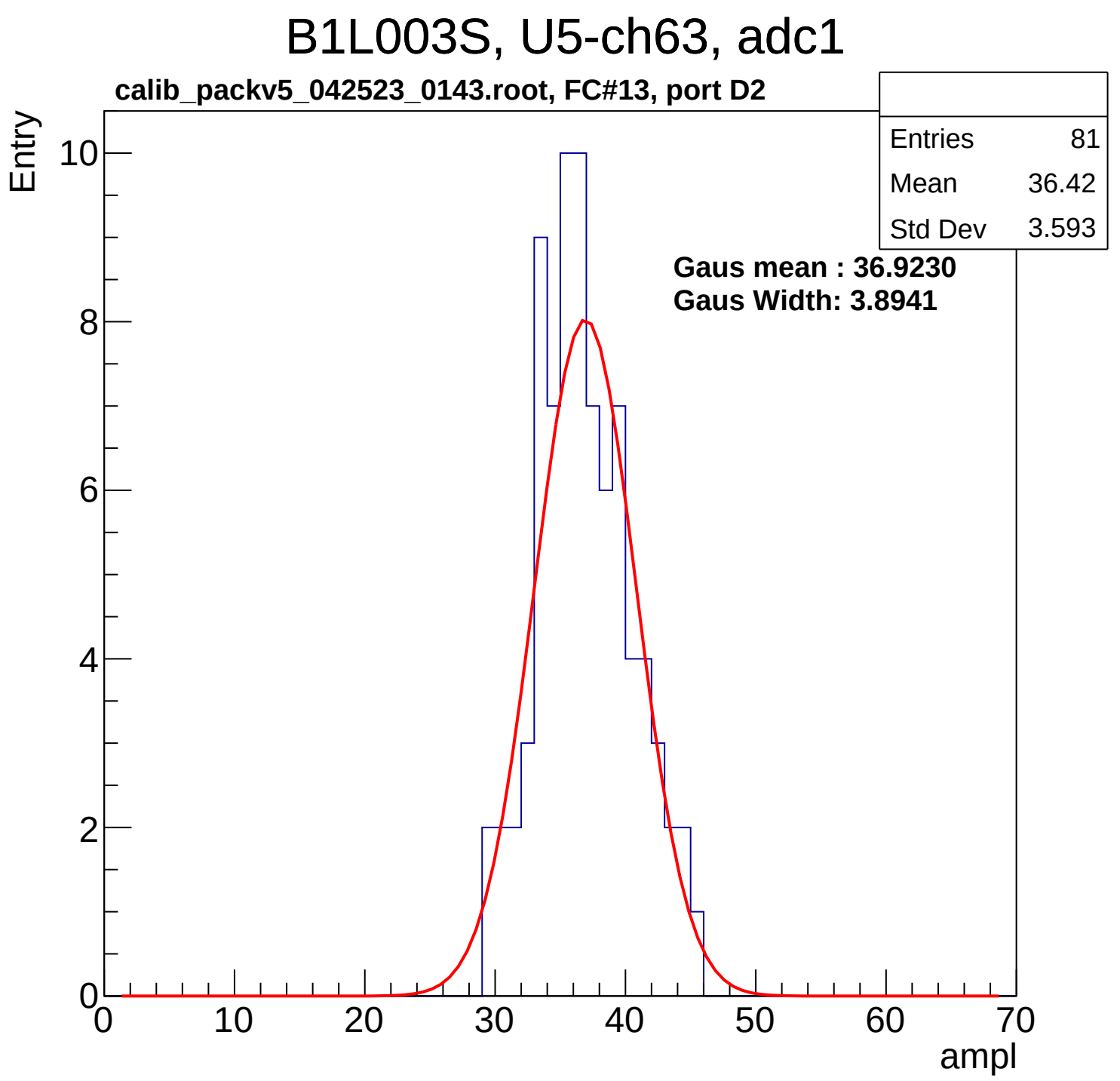
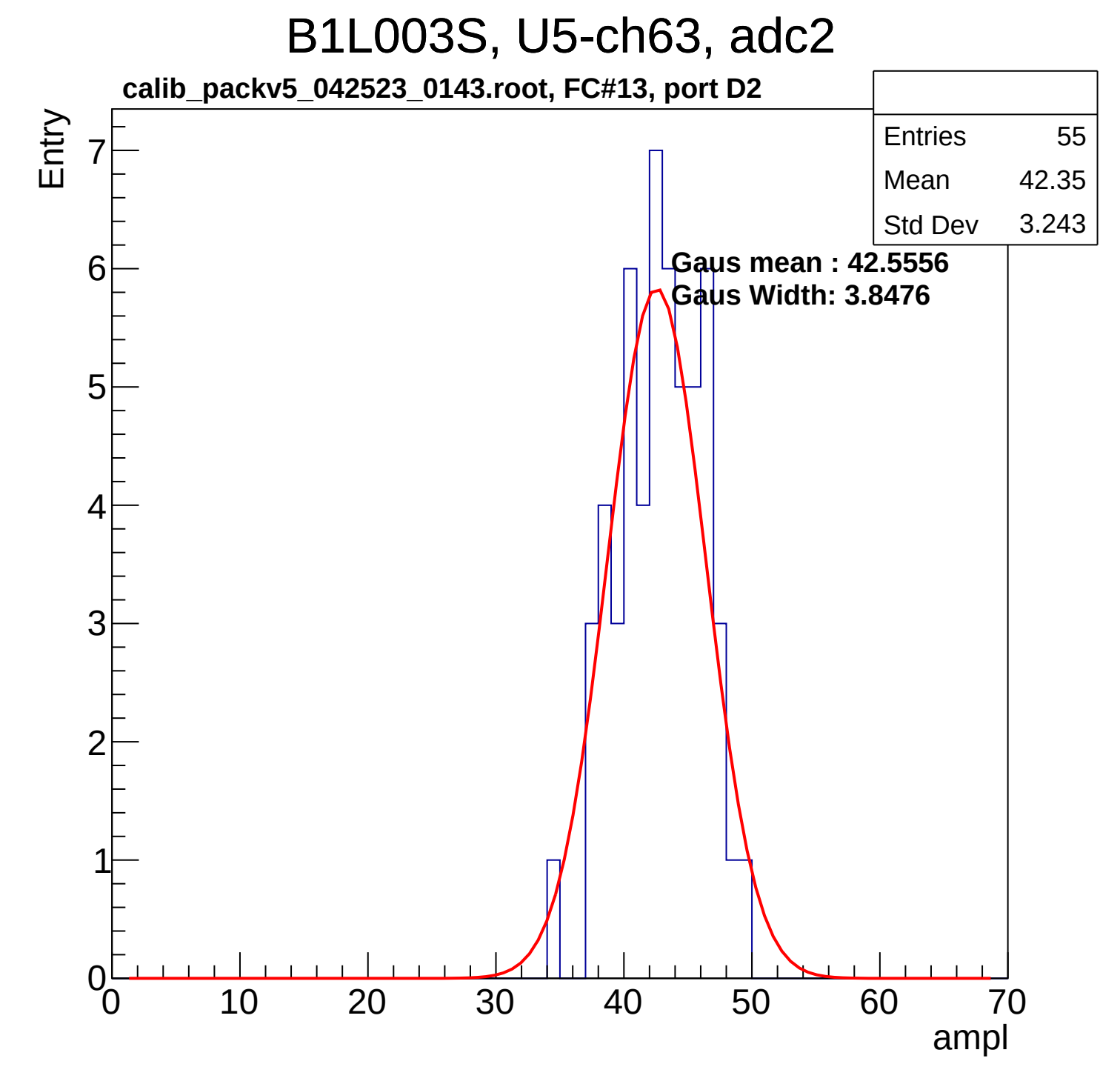
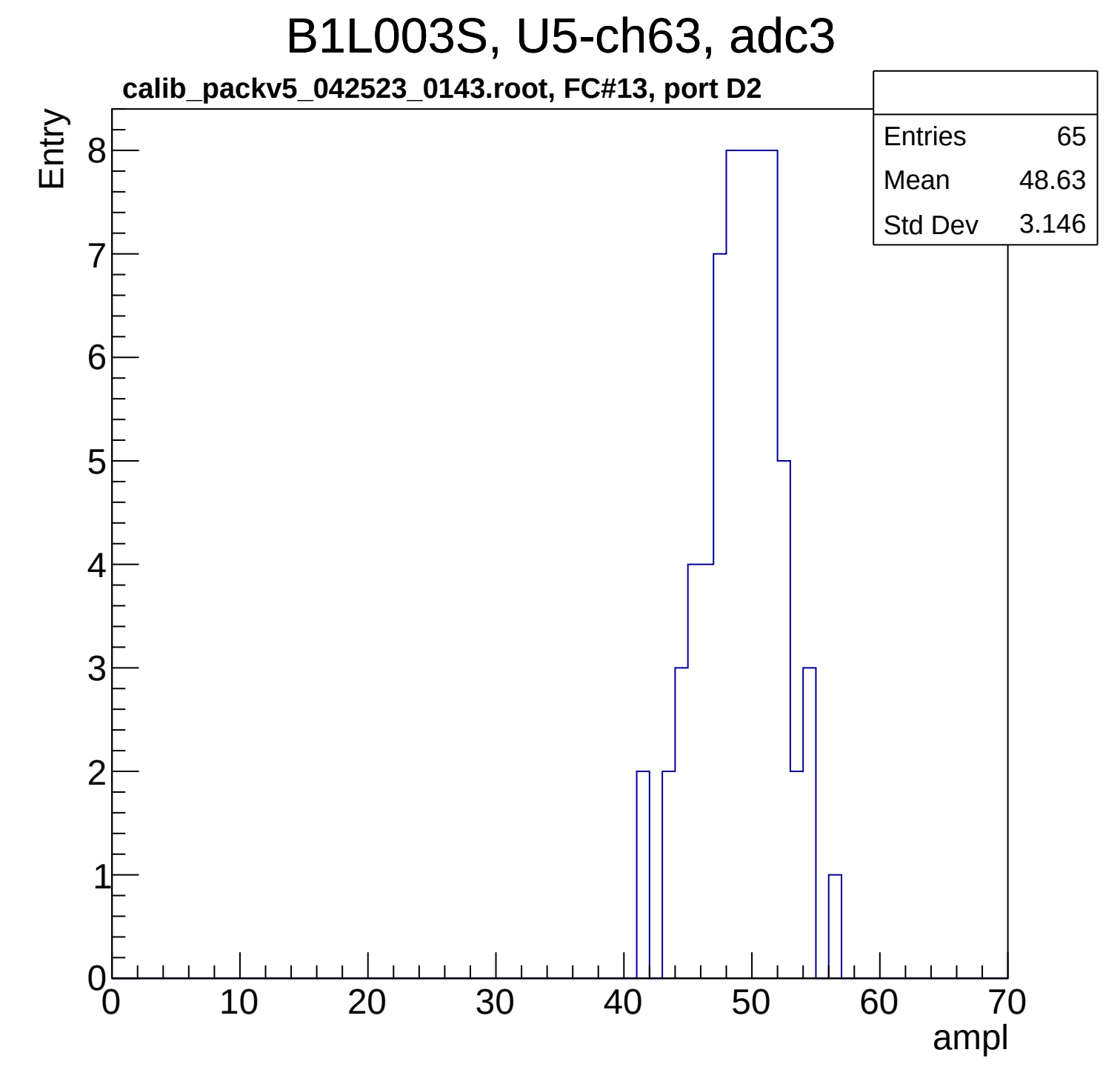

B1L003S, U5-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.42
Std Dev 3.593
Gaus mean : 36.9230
Gaus Width: 3.8941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.35
Std Dev 3.243
Gaus mean : 42.5556
Gaus Width: 3.8476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.63
Std Dev 3.146
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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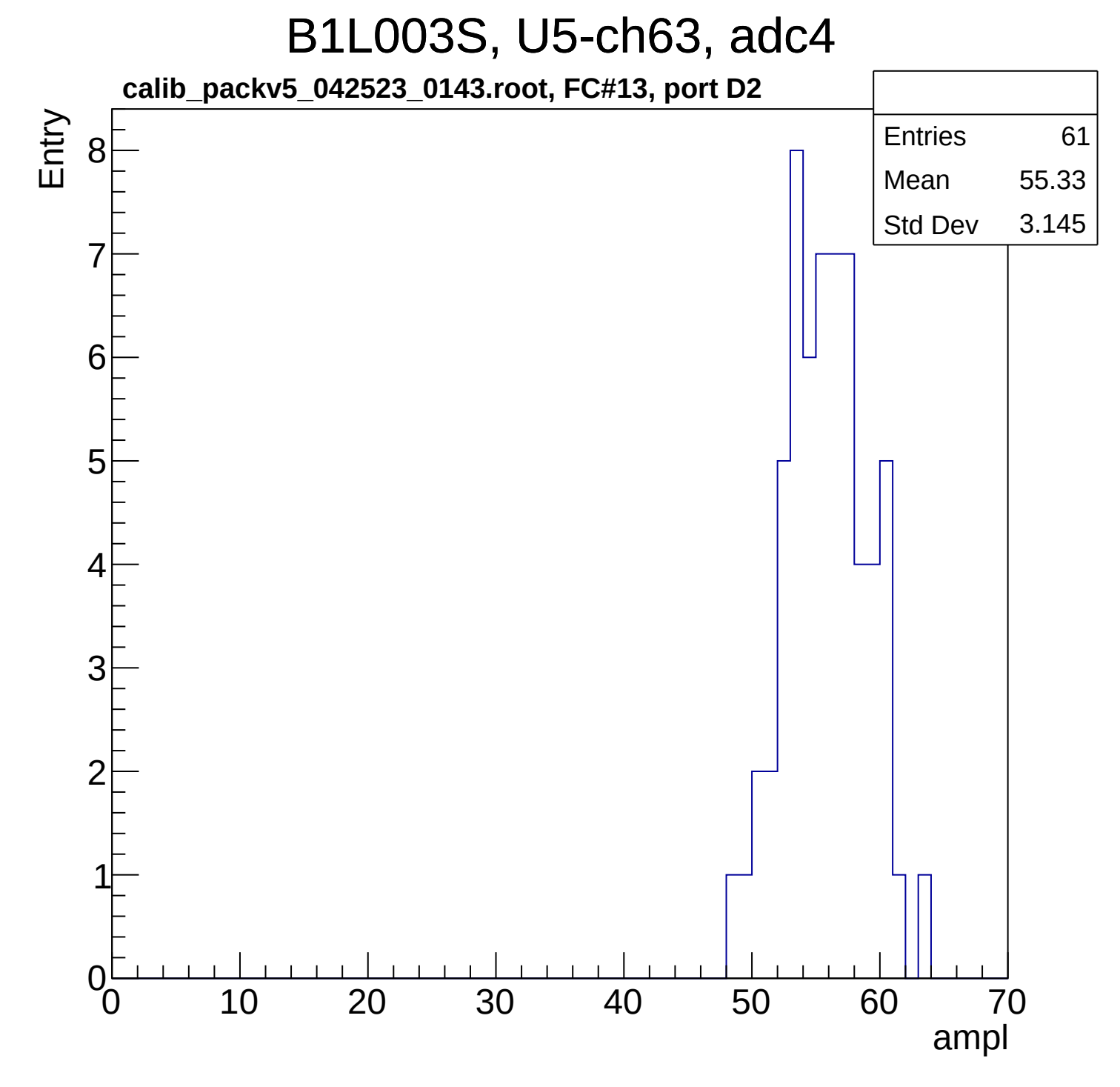
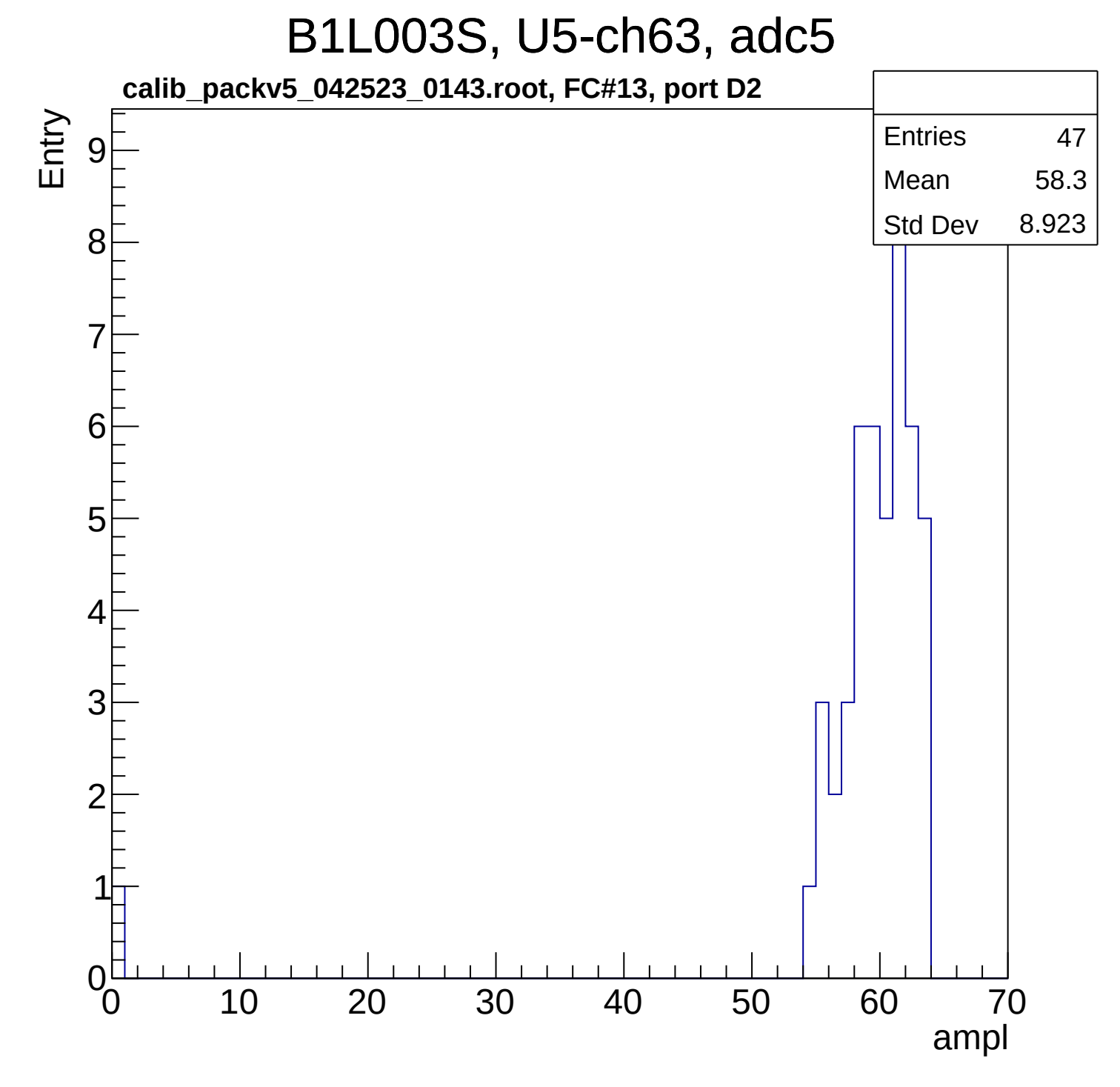
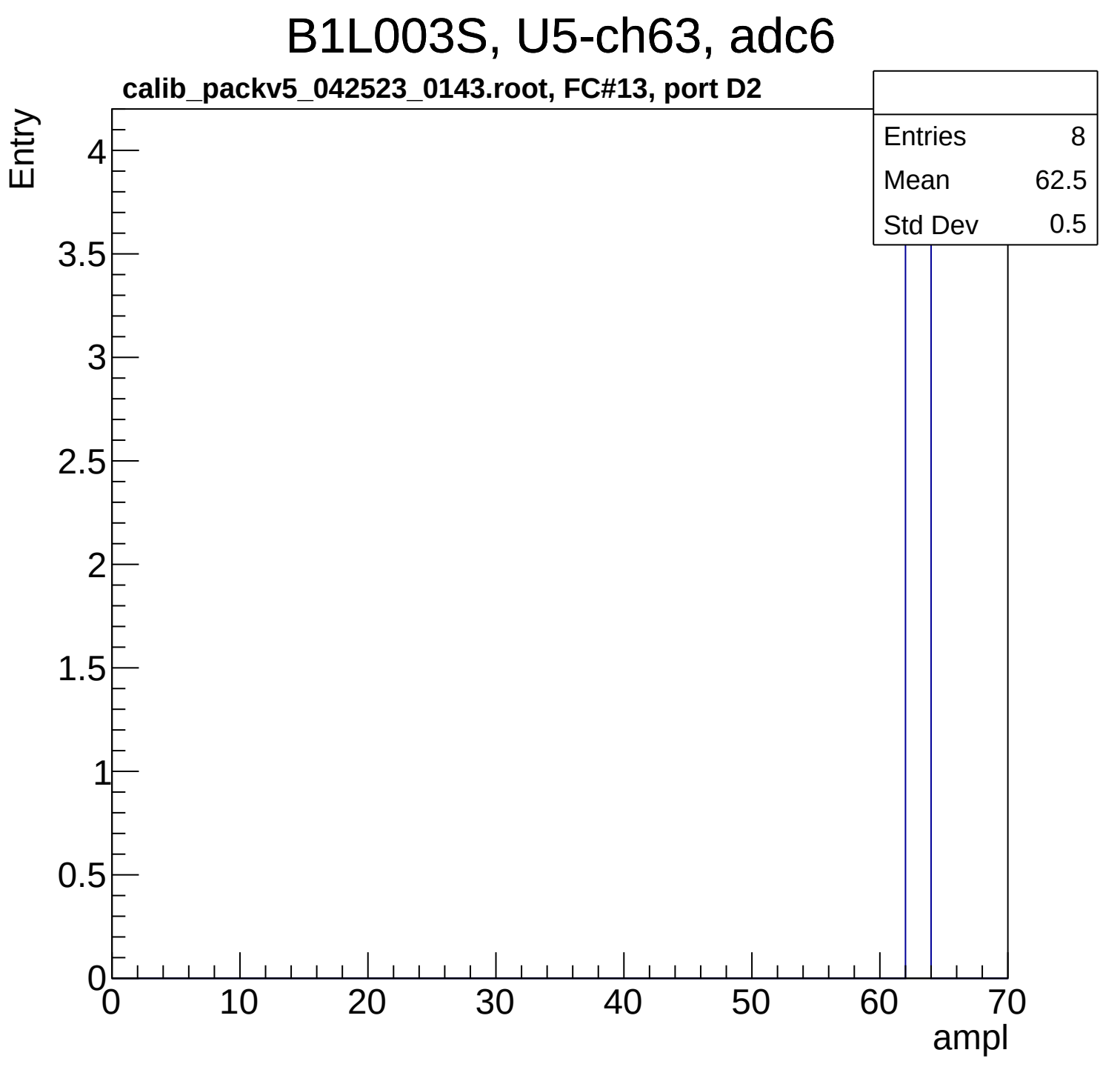
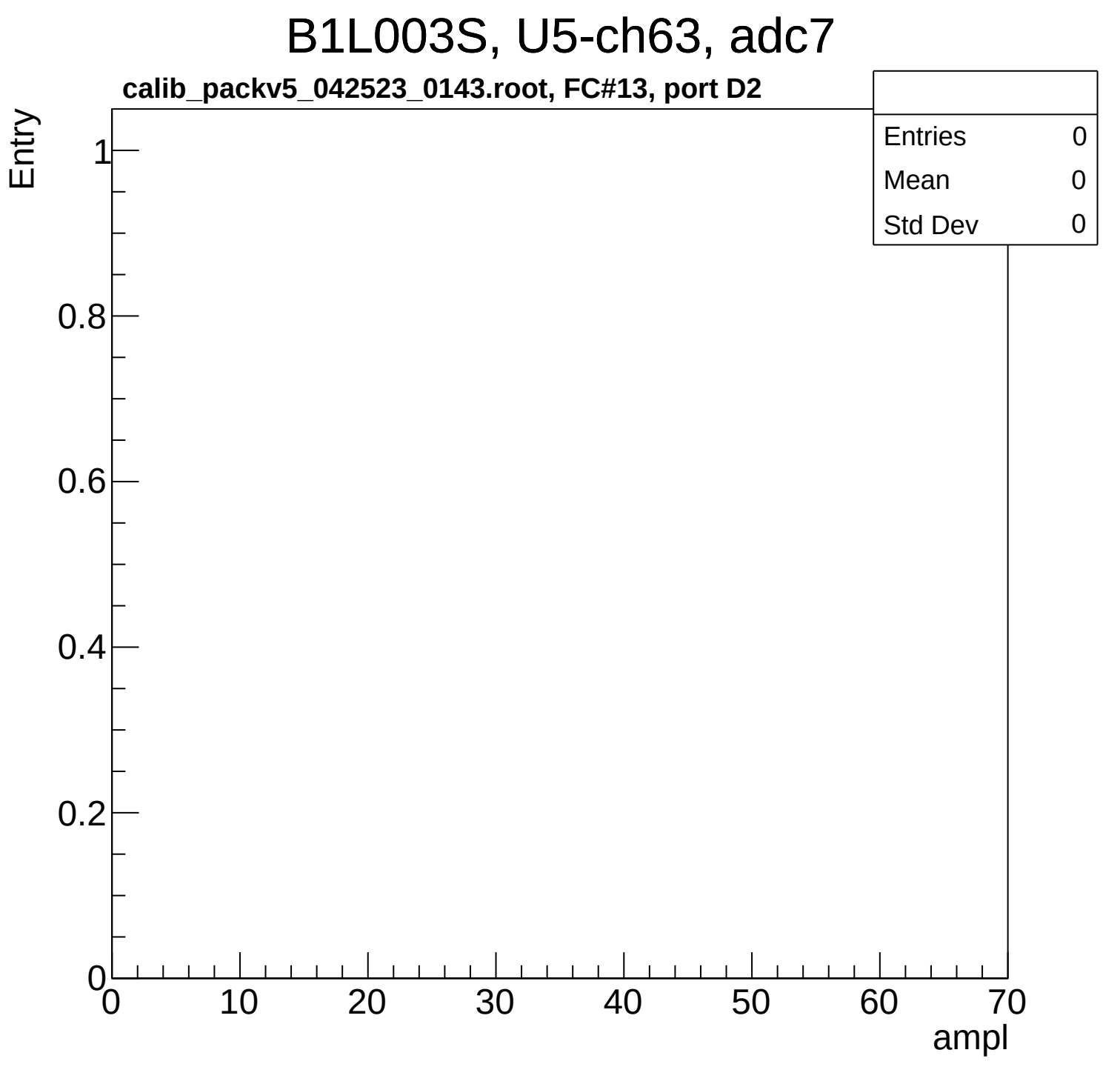
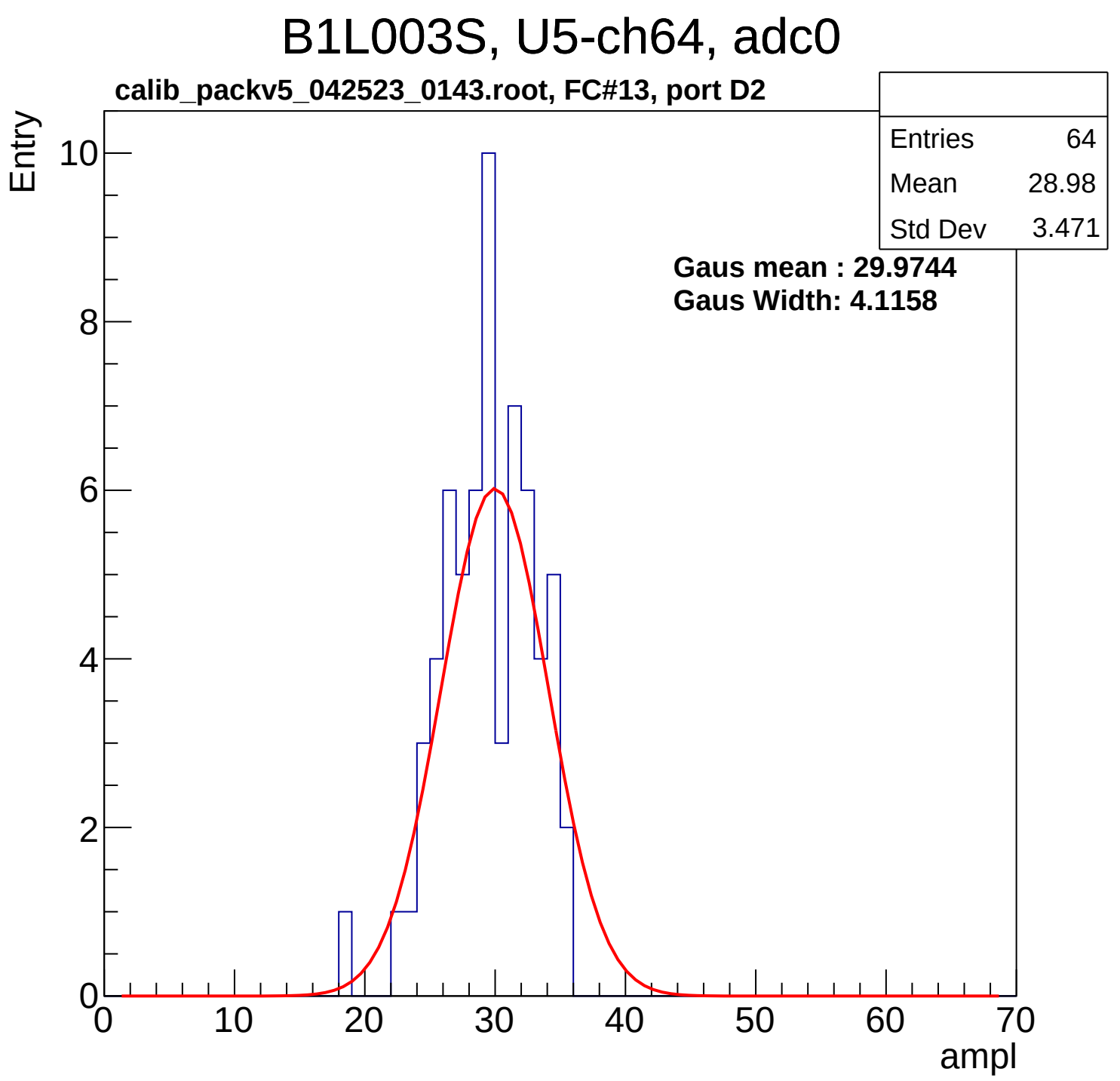
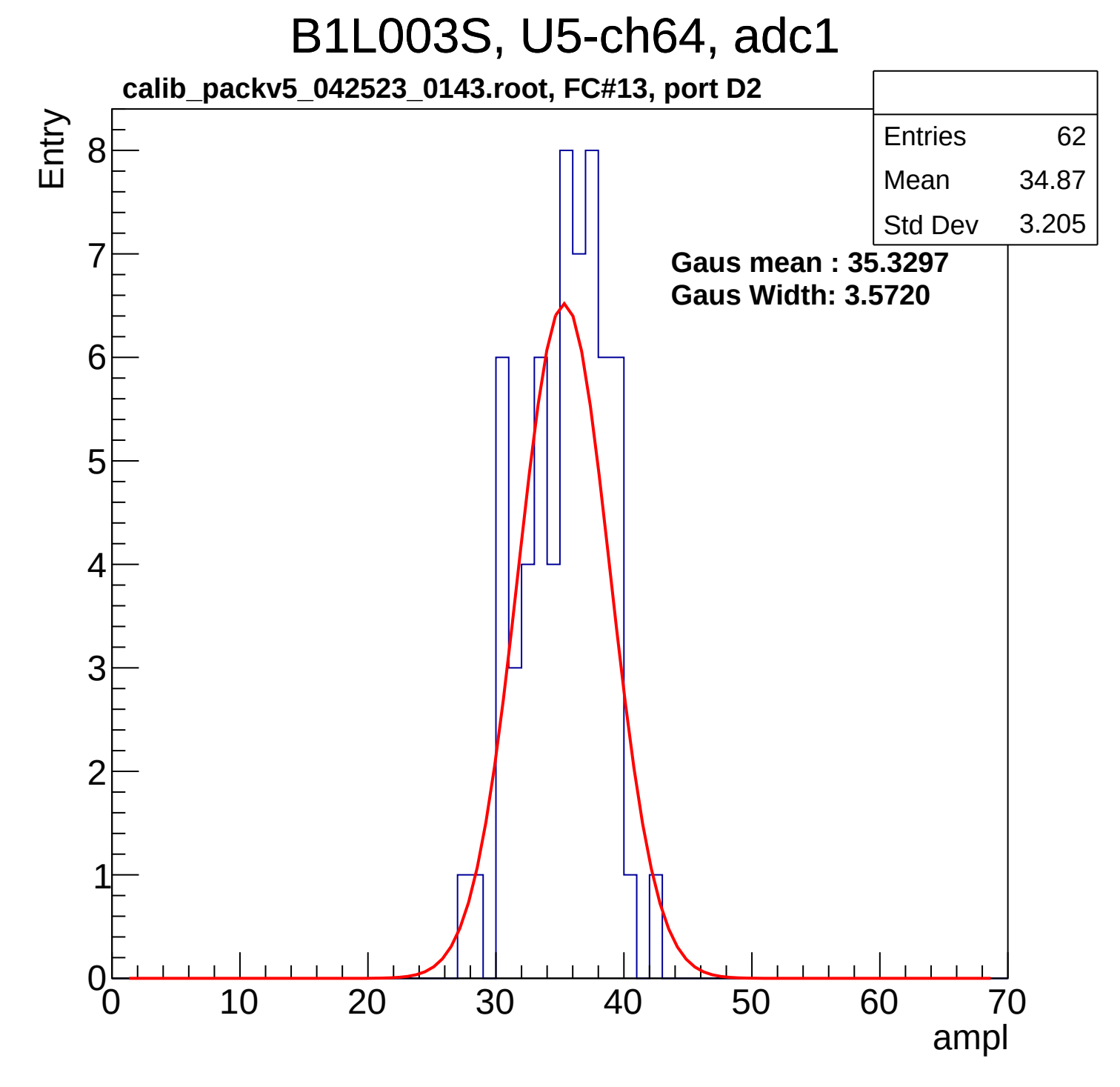

B1L003S, U5-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.33
Std Dev 3.145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.3
Std Dev 8.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.98
Std Dev 3.471
Gaus mean : 29.9744
Gaus Width: 4.1158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 34.87
Std Dev 3.205
Gaus mean : 35.3297
Gaus Width: 3.5720
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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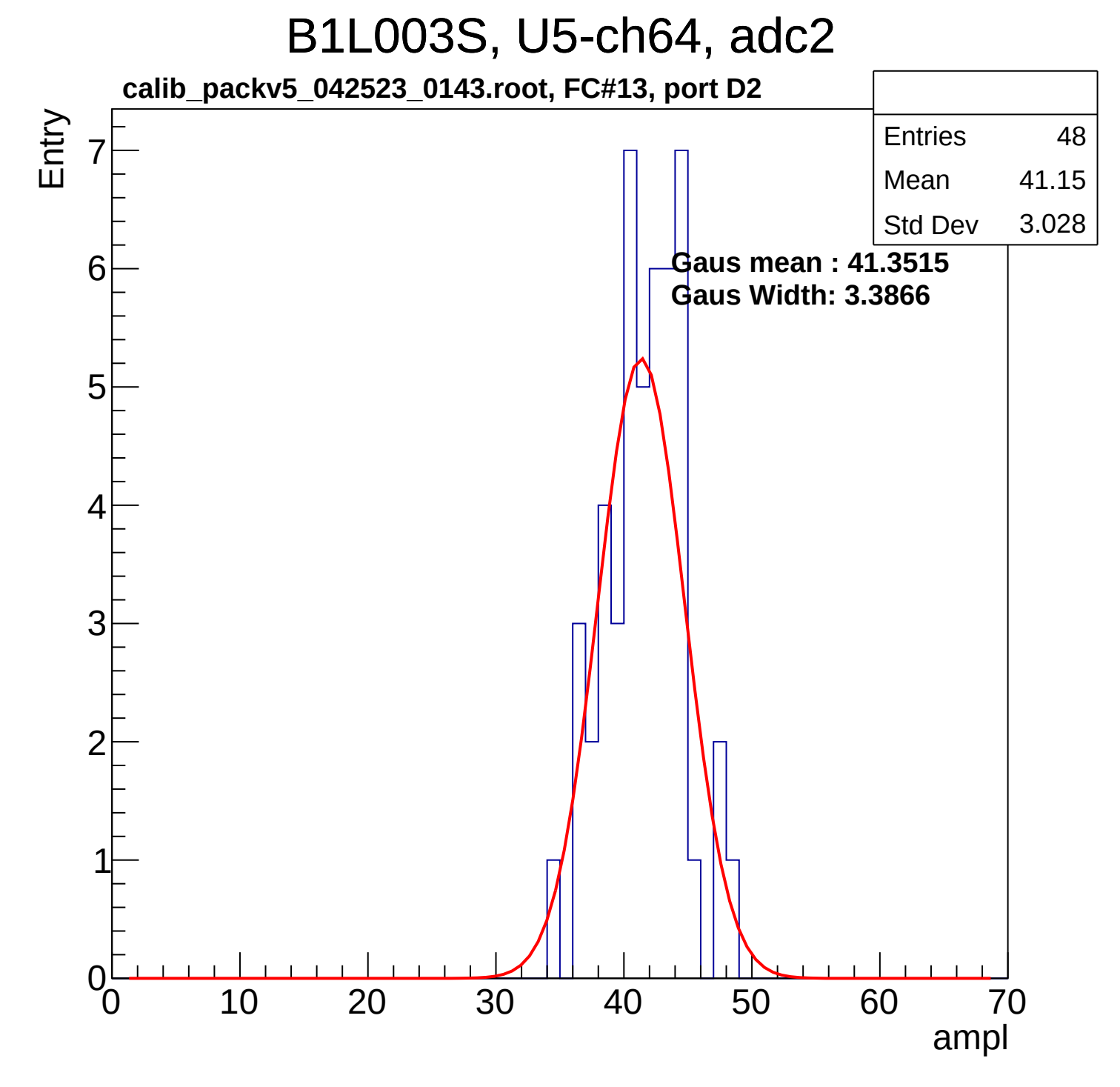
B1L003S, U5-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 41.15
Std Dev 3.028
Gaus mean : 41.3515
Gaus Width: 3.3866
Entry
ampl
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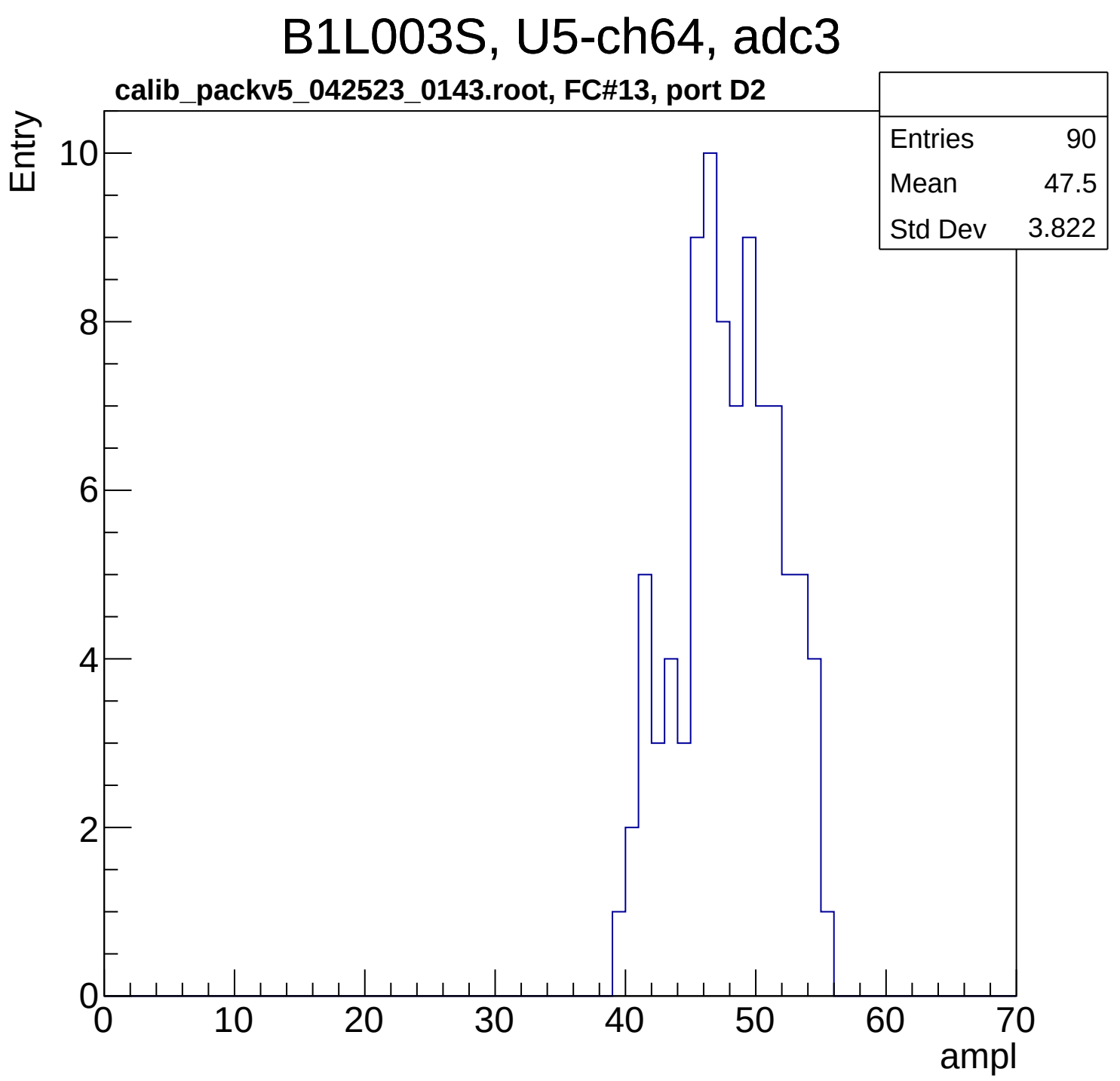
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 90
Mean 47.5
Std Dev 3.822
Entry
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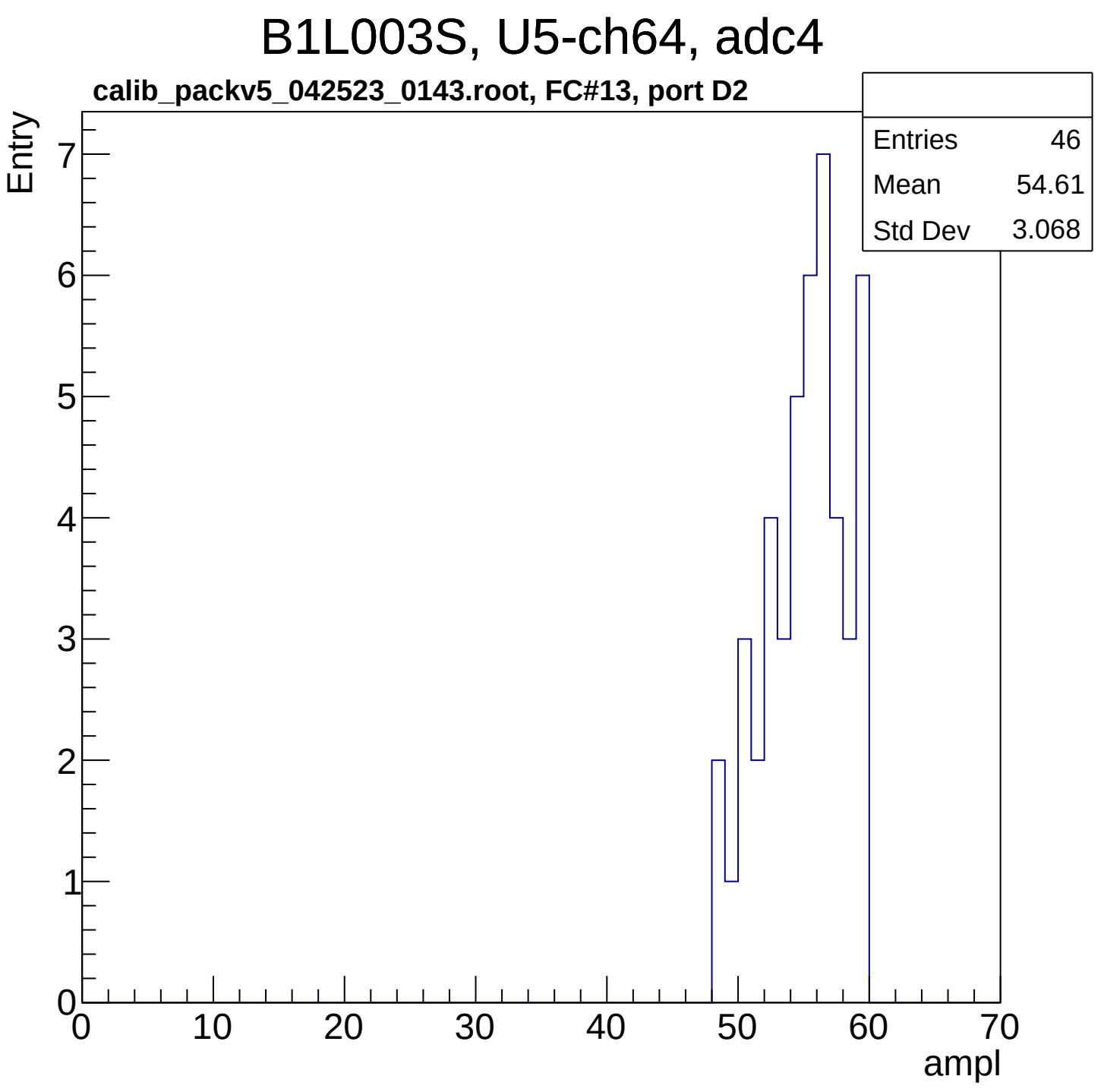
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 54.61
Std Dev 3.068
Entry
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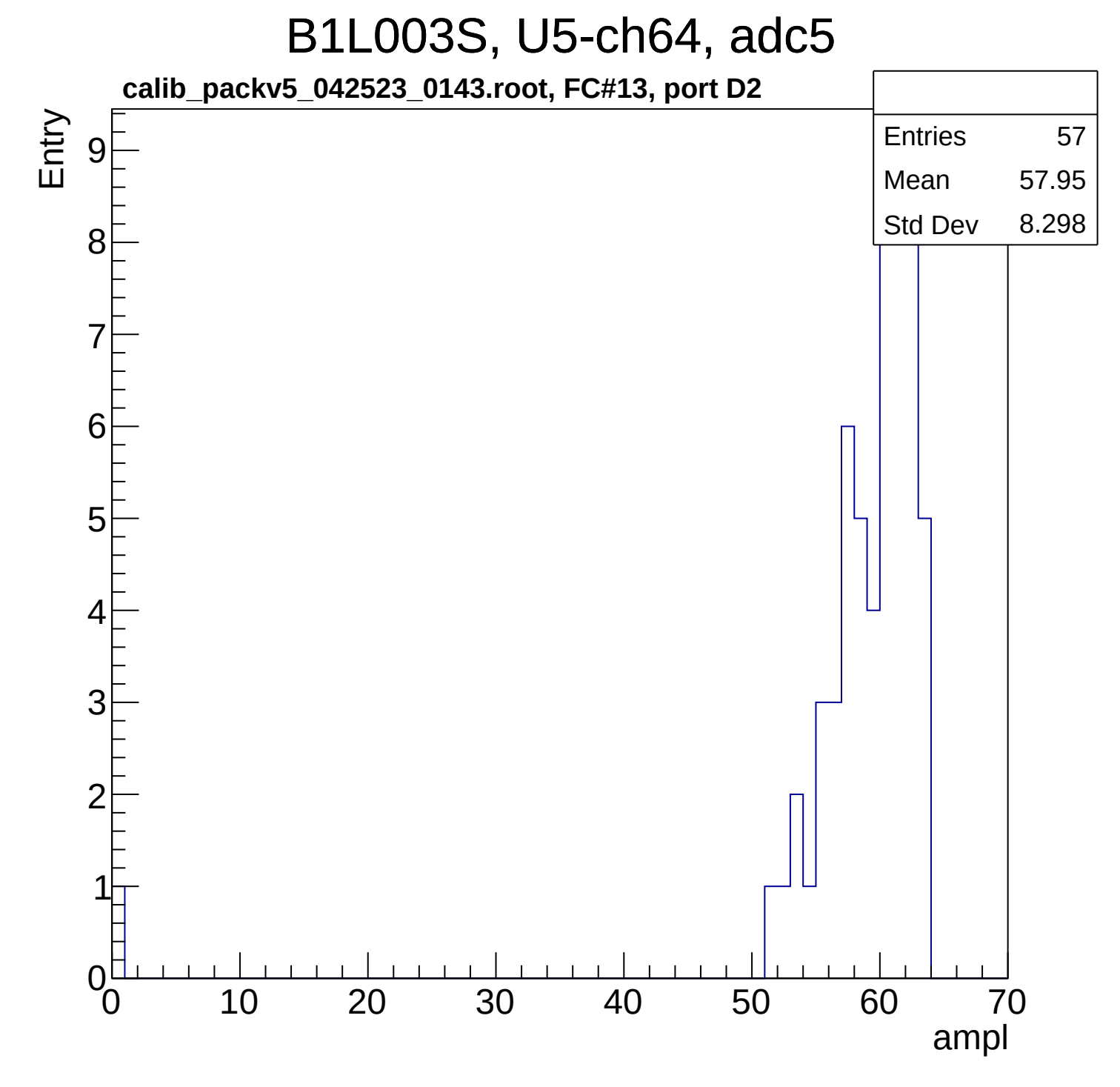
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.95
Std Dev 8.298
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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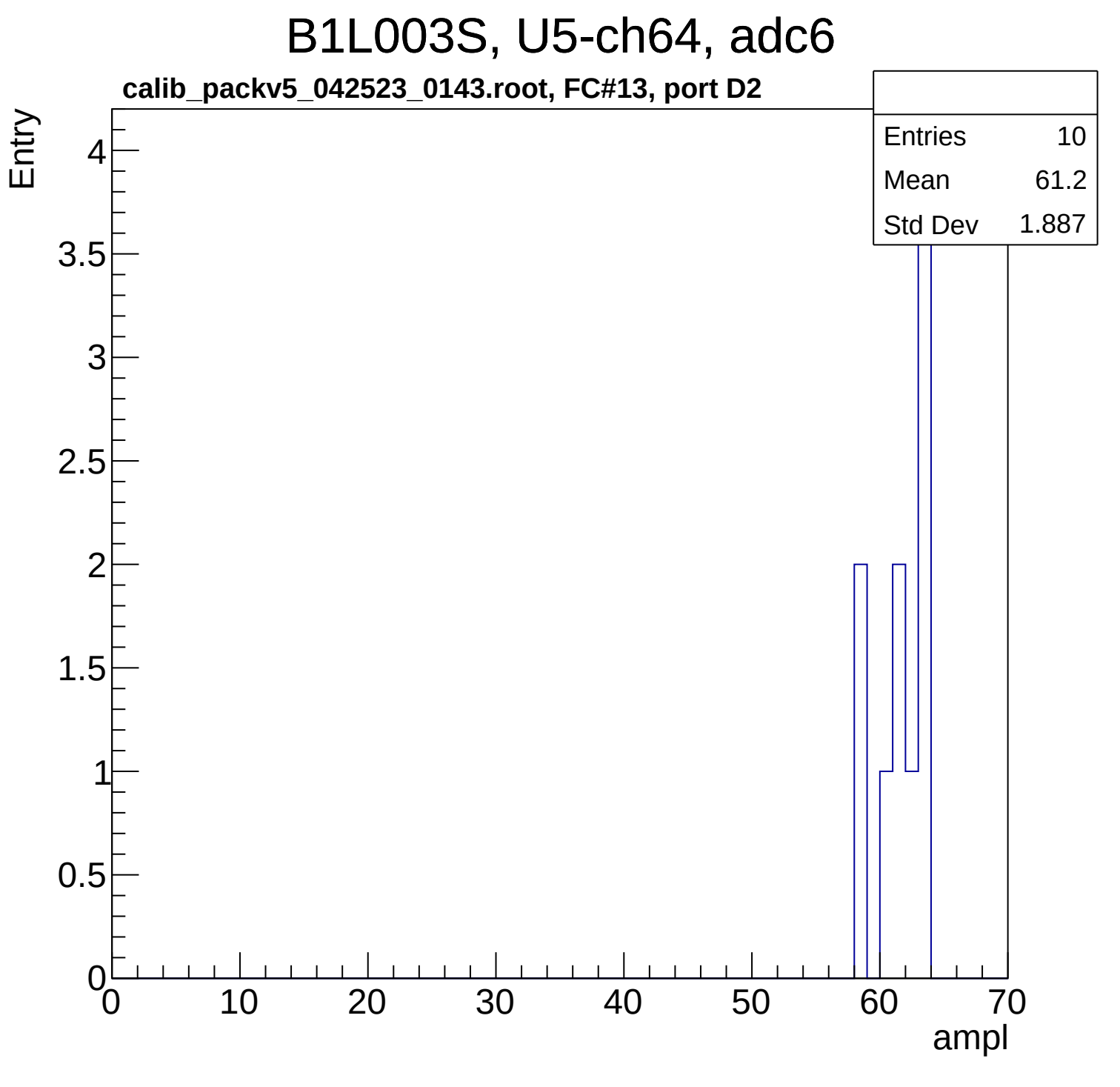
B1L003S, U5-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.2
Std Dev 1.887
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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60
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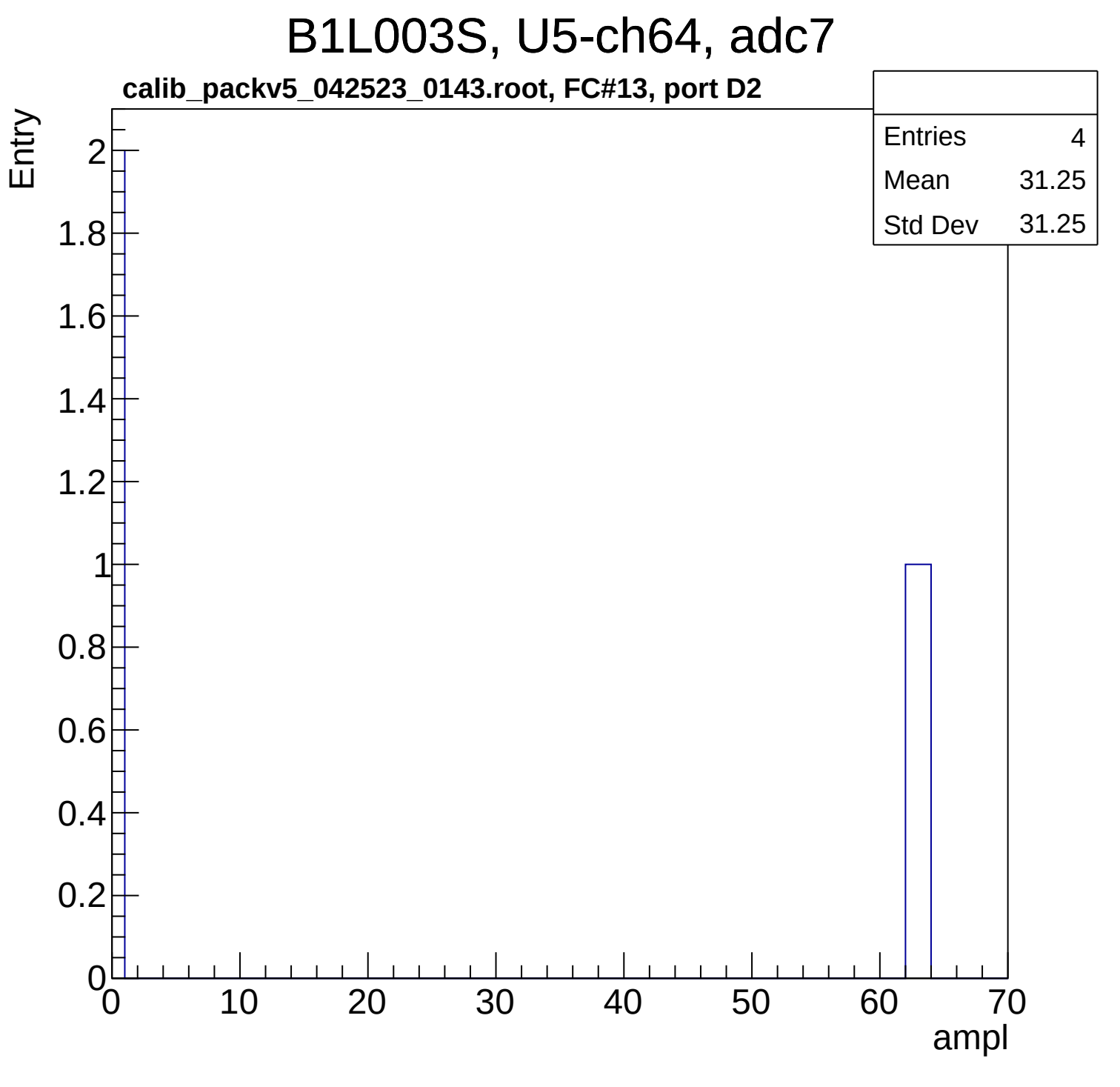
B1L003S, U5-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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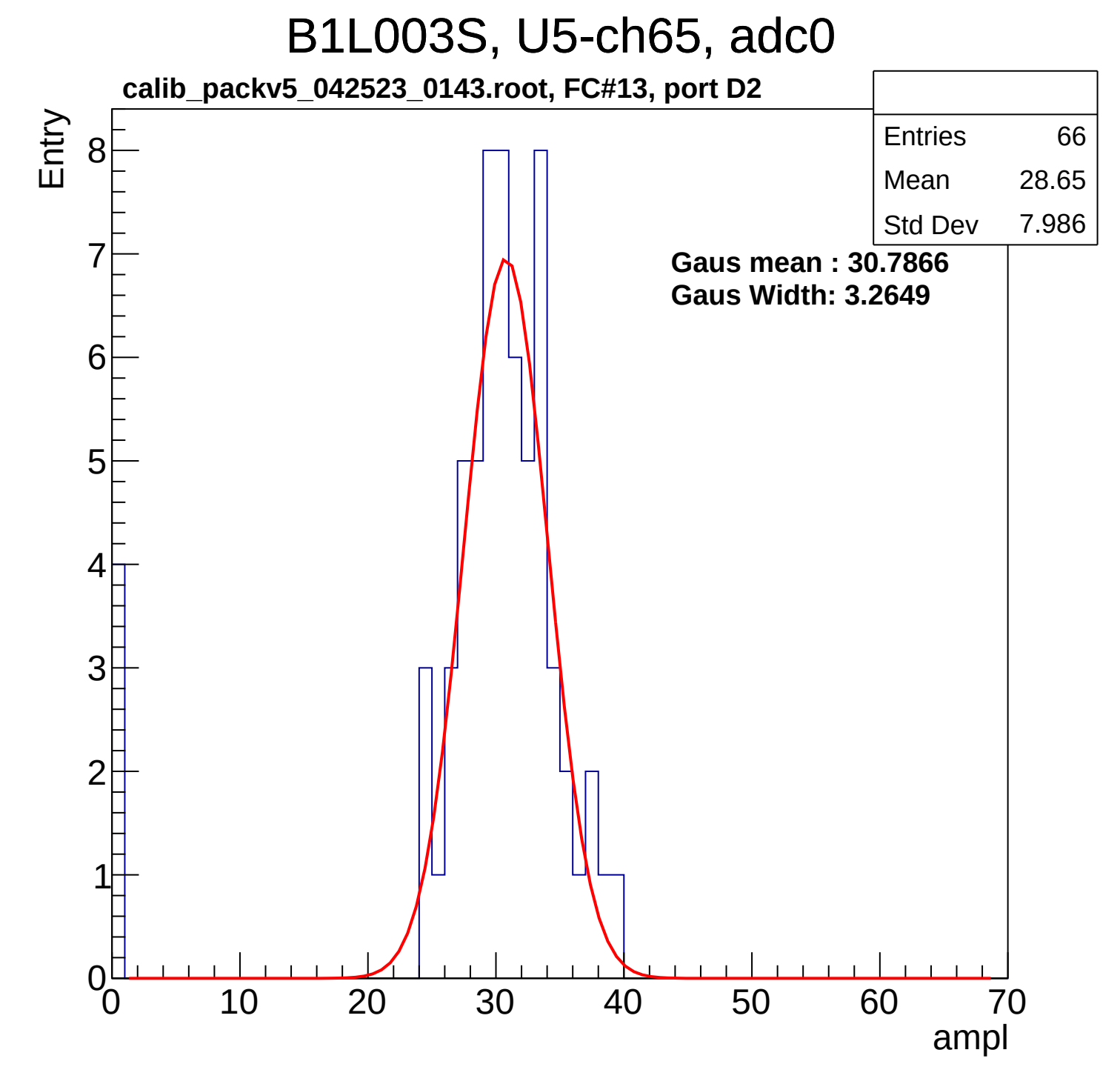
B1L003S, U5-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.65
Std Dev 7.986
Gaus mean : 30.7866
Gaus Width: 3.2649
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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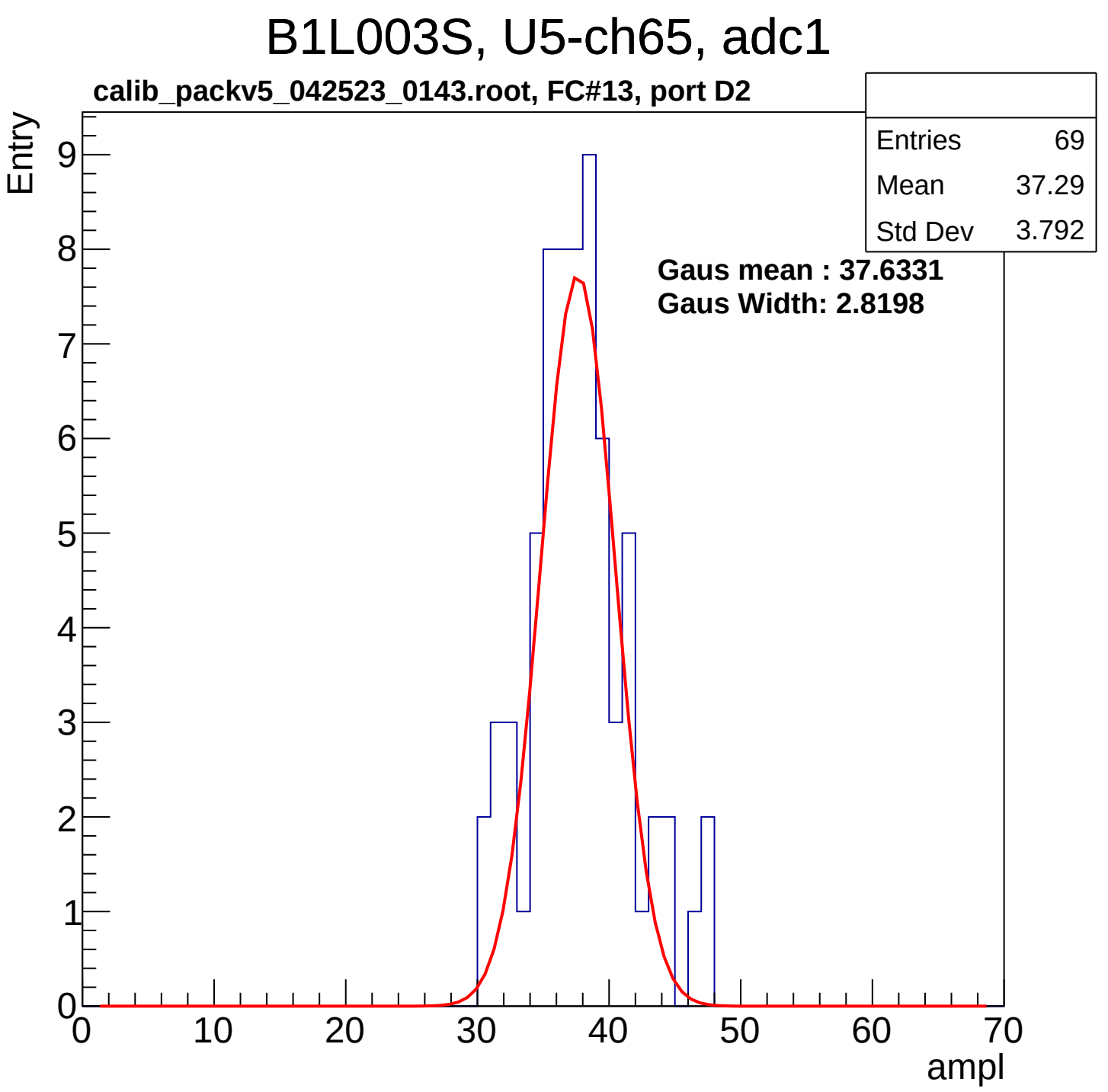
B1L003S, U5-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 37.29
Std Dev 3.792
Gaus mean : 37.6331
Gaus Width: 2.8198
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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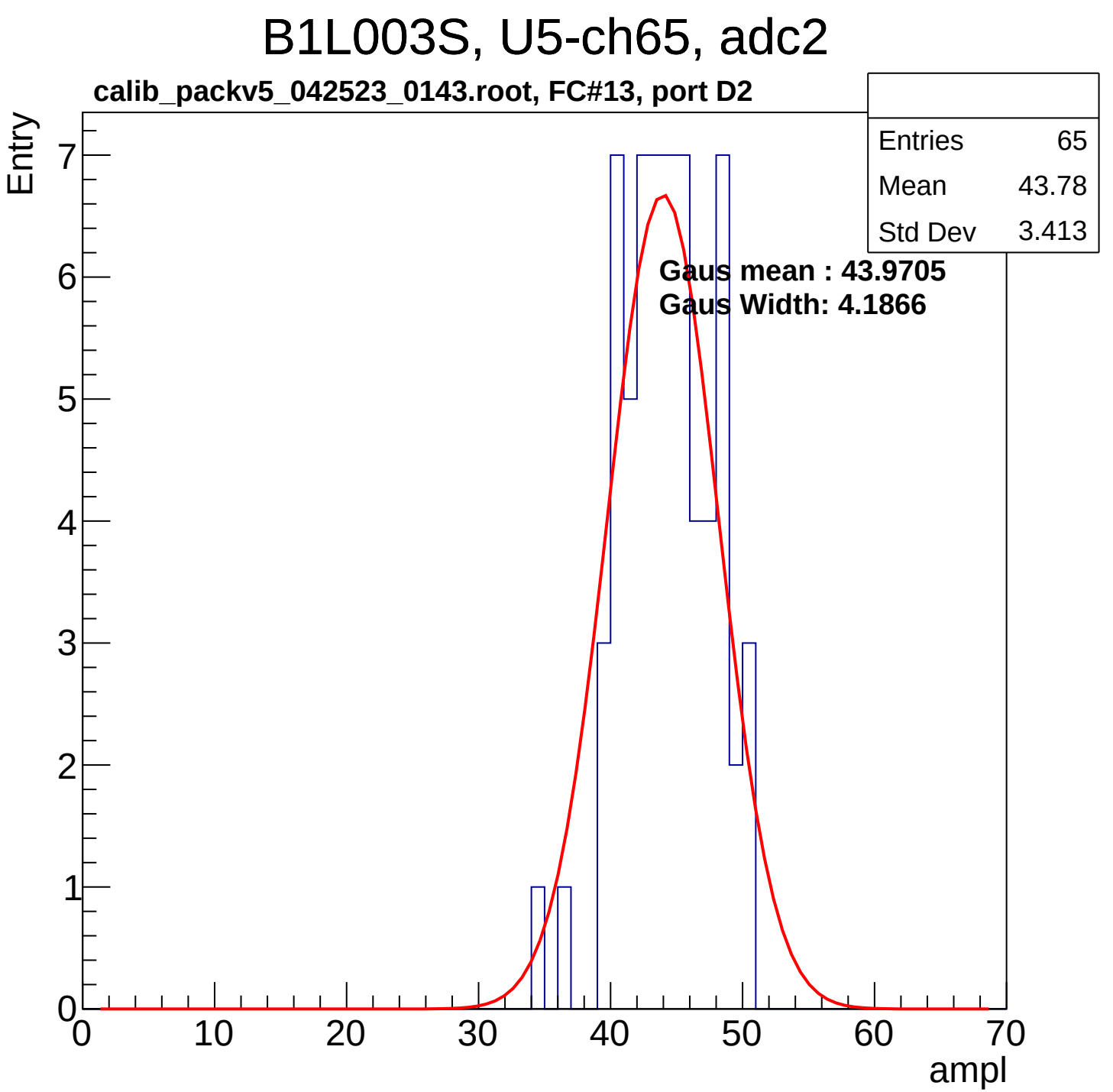
B1L003S, U5-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.78
Std Dev 3.413
Gaus mean : 43.9705
Gaus Width: 4.1866
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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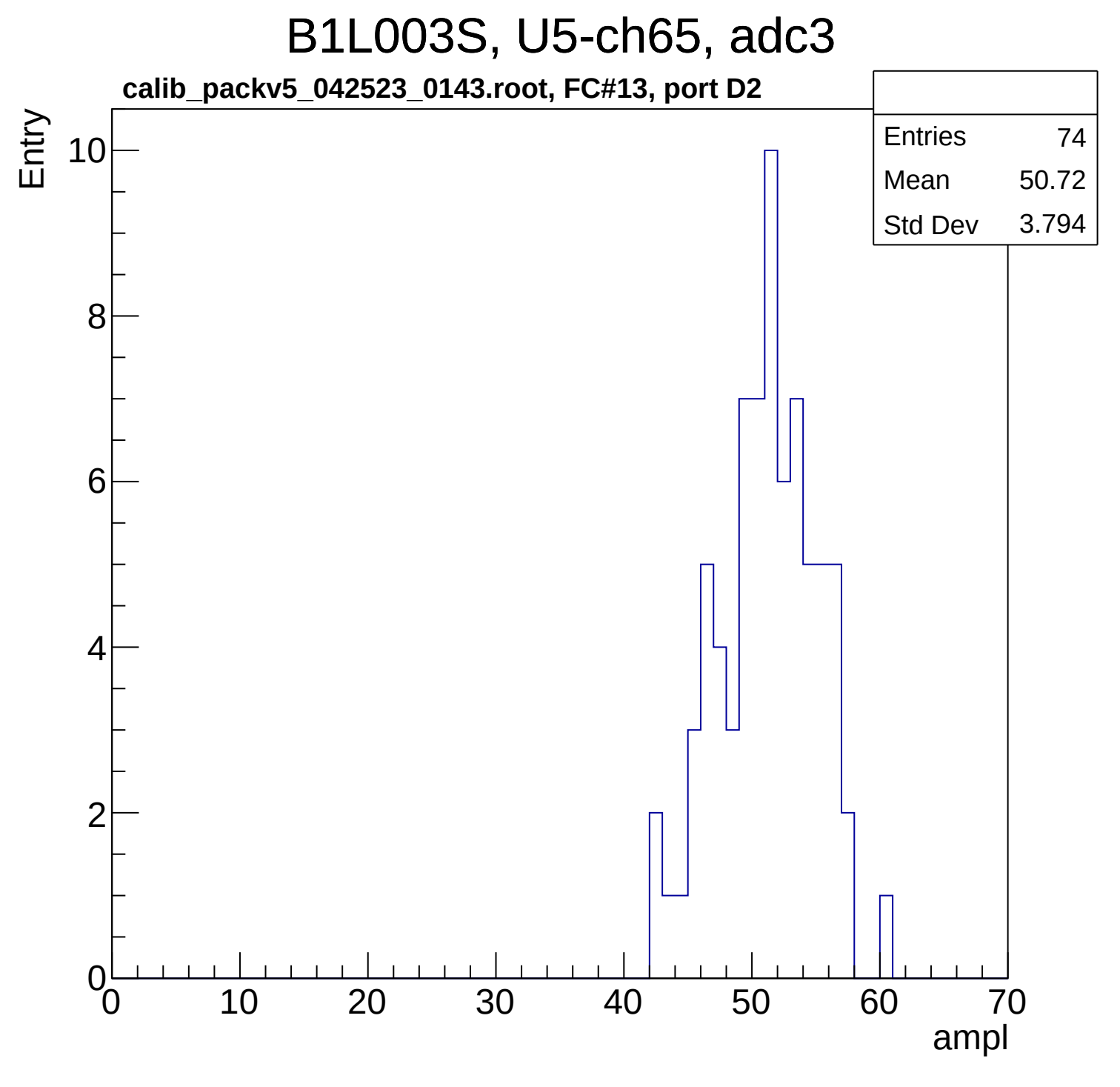
B1L003S, U5-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 50.72
Std Dev 3.794
Entry
ampl
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2
4
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10
0
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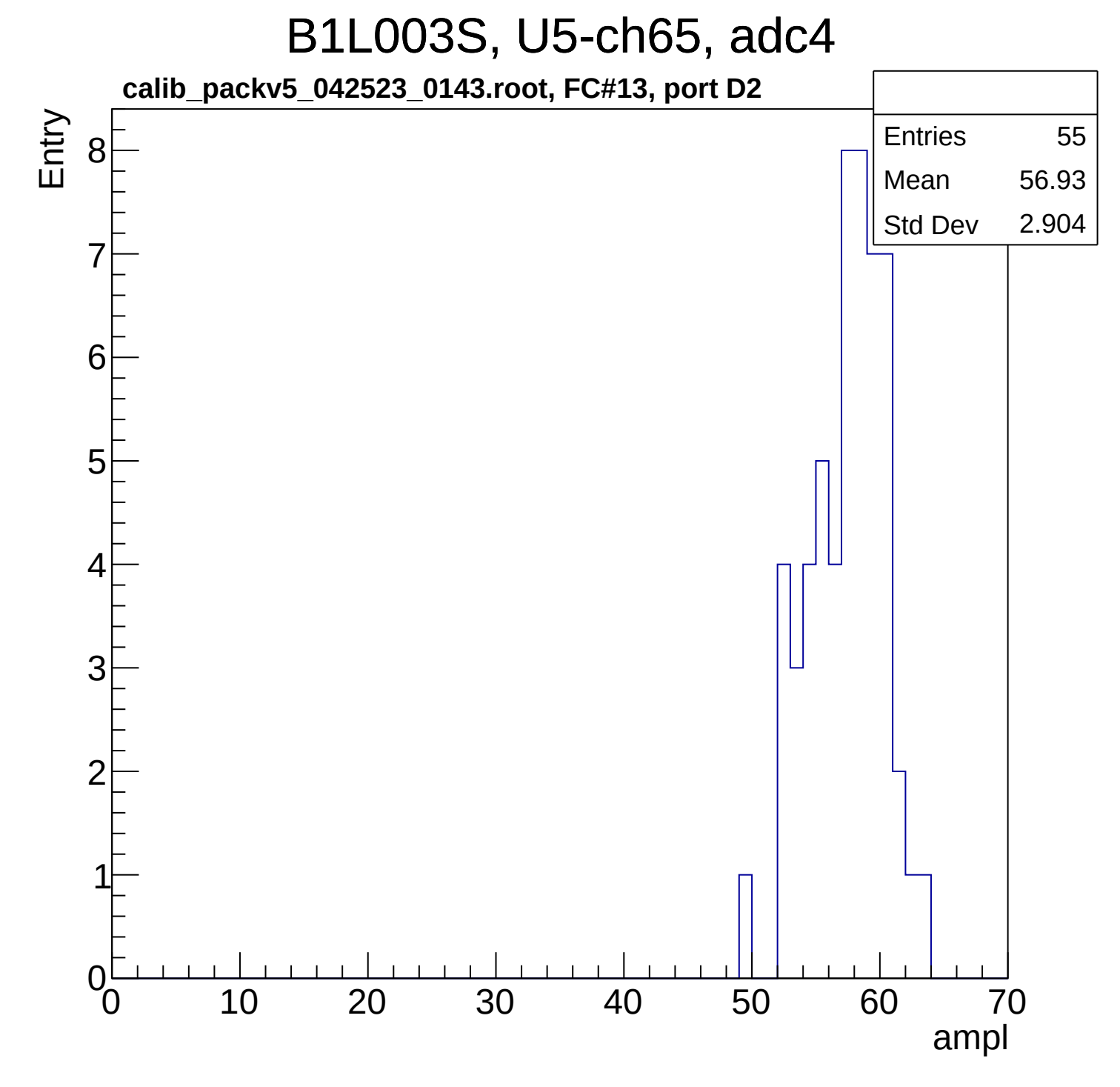
40
50
60
70


B1L003S, U5-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56.93
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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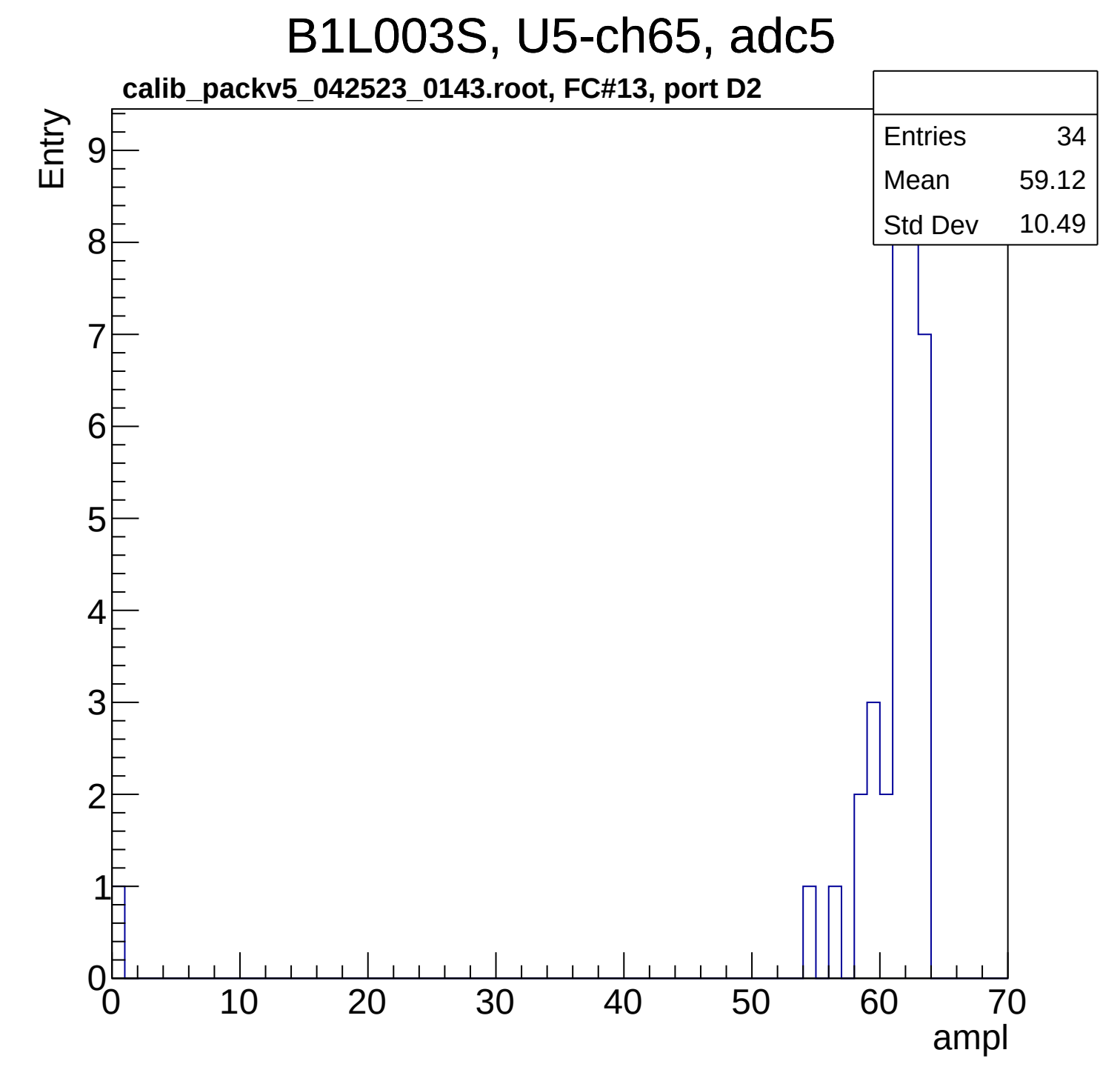
B1L003S, U5-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 59.12
Std Dev 10.49
Entry
ampl
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2
3
4
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6
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0
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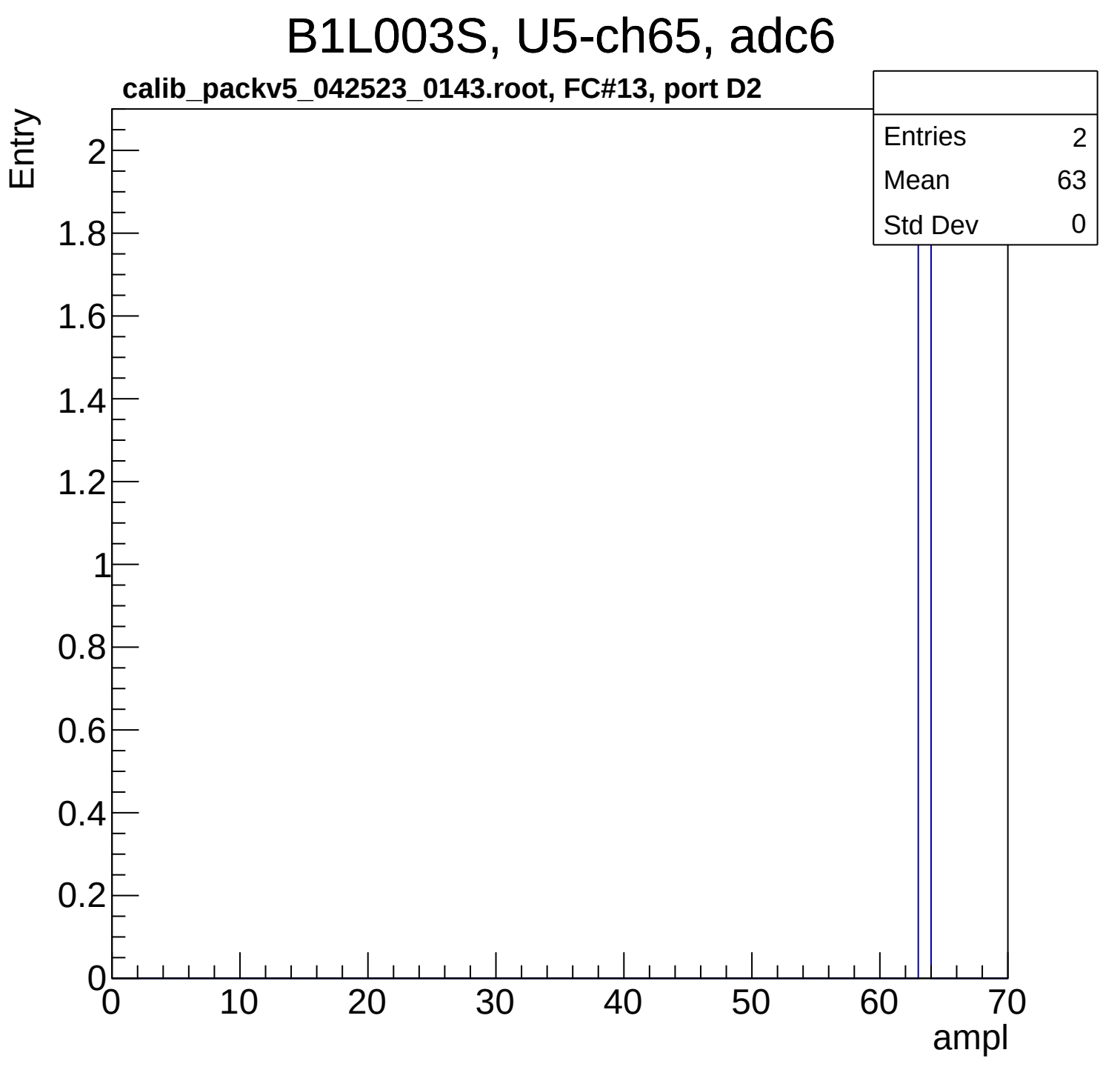
B1L003S, U5-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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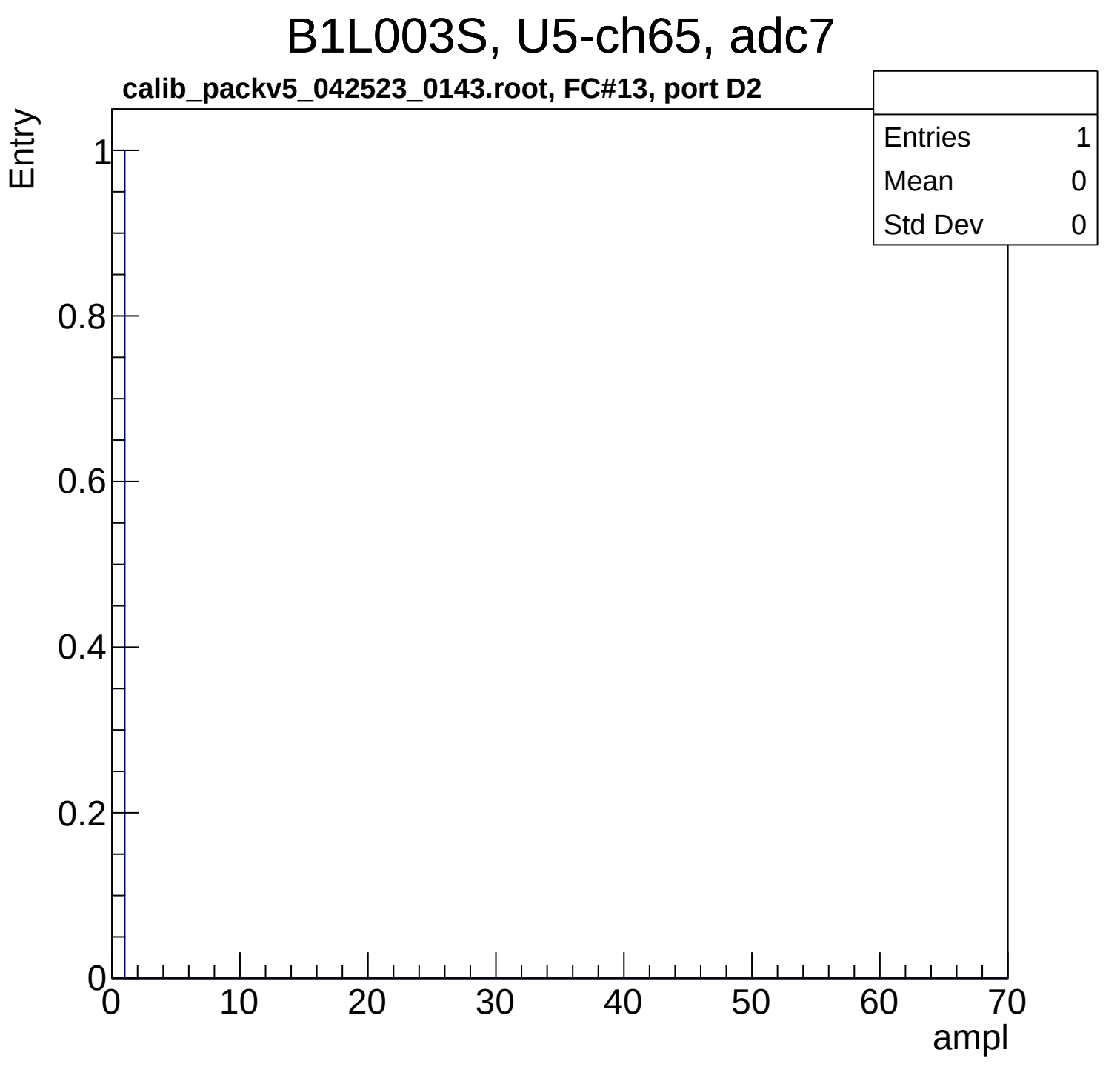
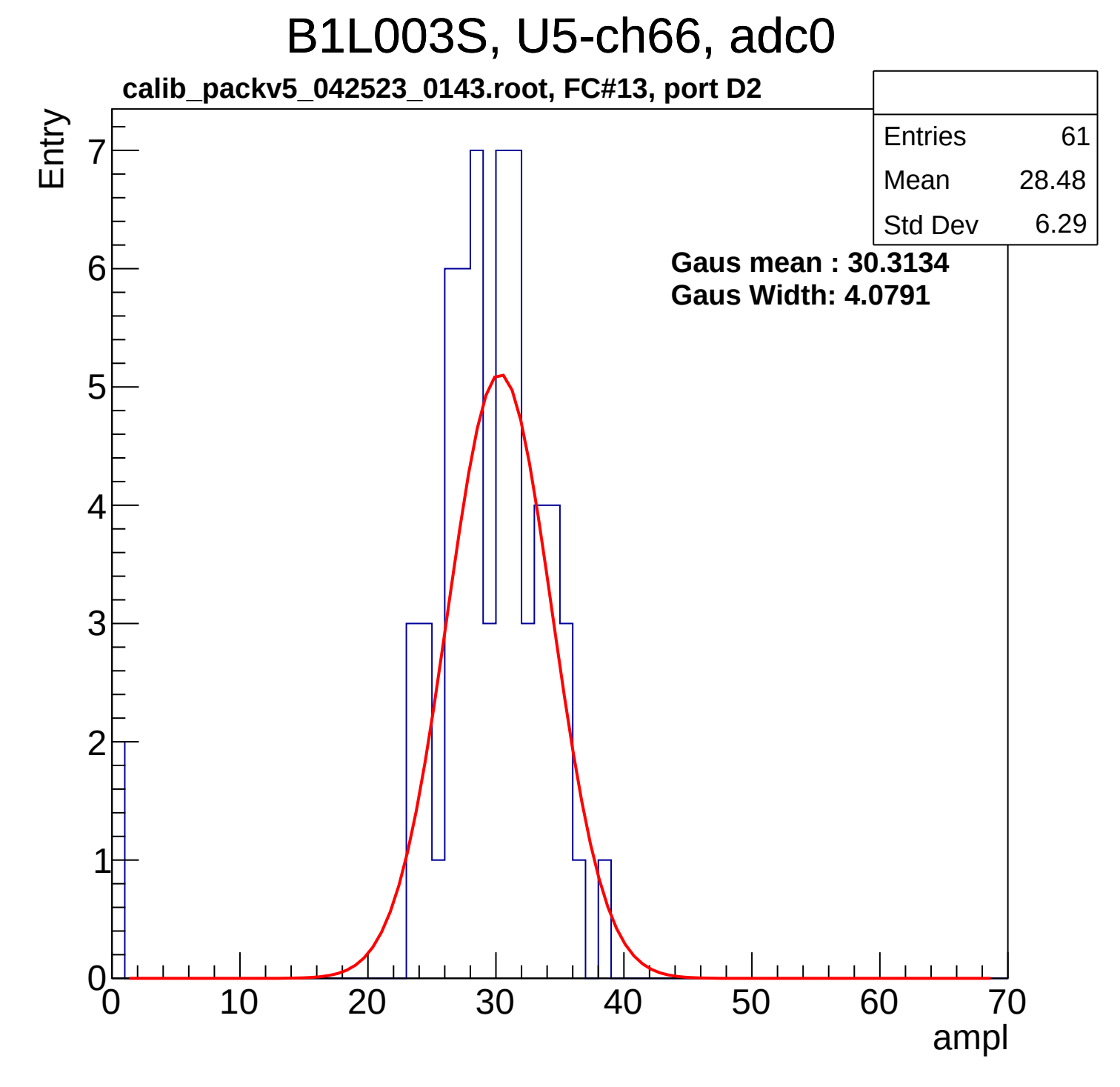
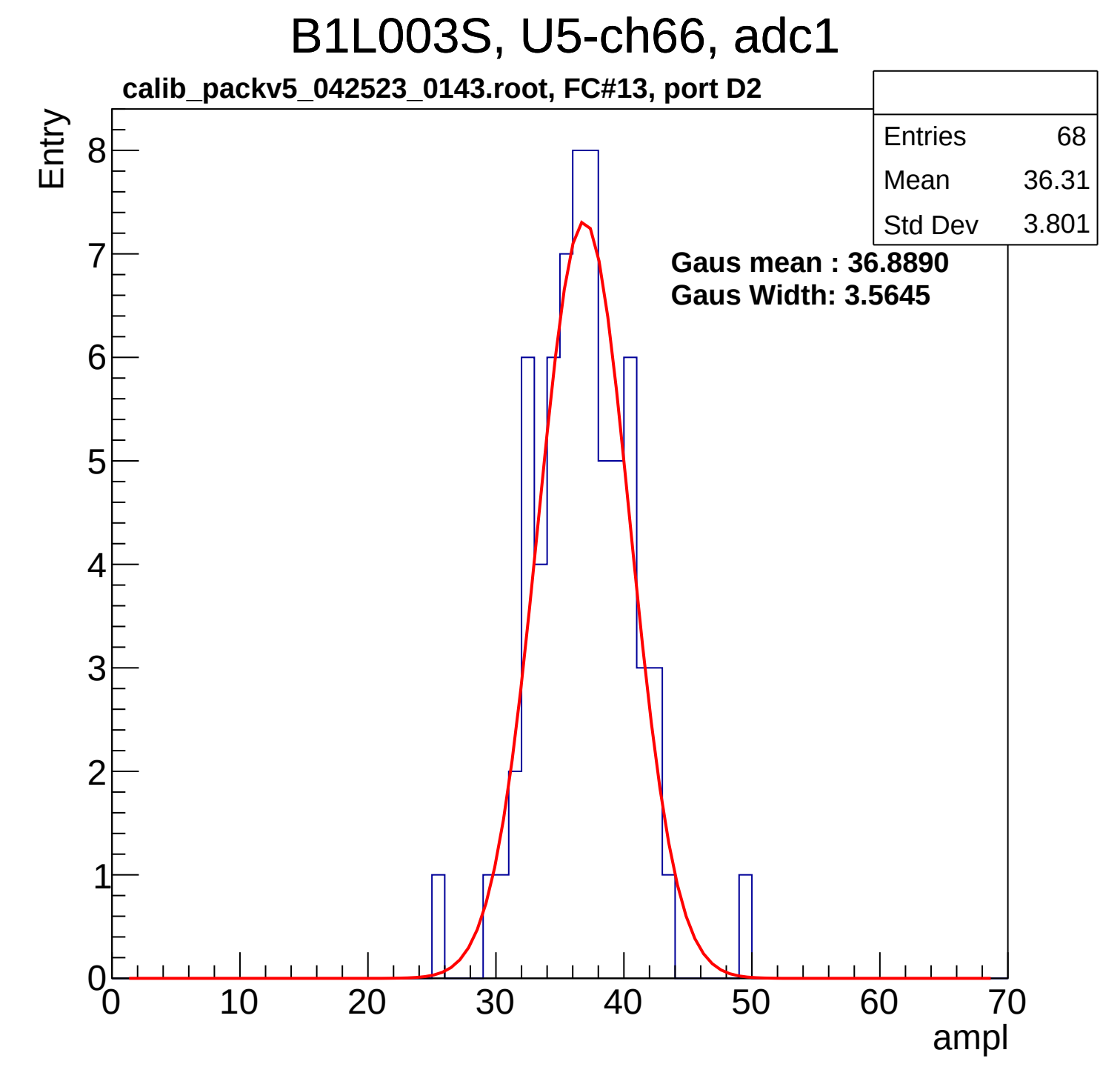

B1L003S, U5-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 28.48
Std Dev 6.29
Gaus mean : 30.3134
Gaus Width: 4.0791
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.31
Std Dev 3.801
Gaus mean : 36.8890
Gaus Width: 3.5645
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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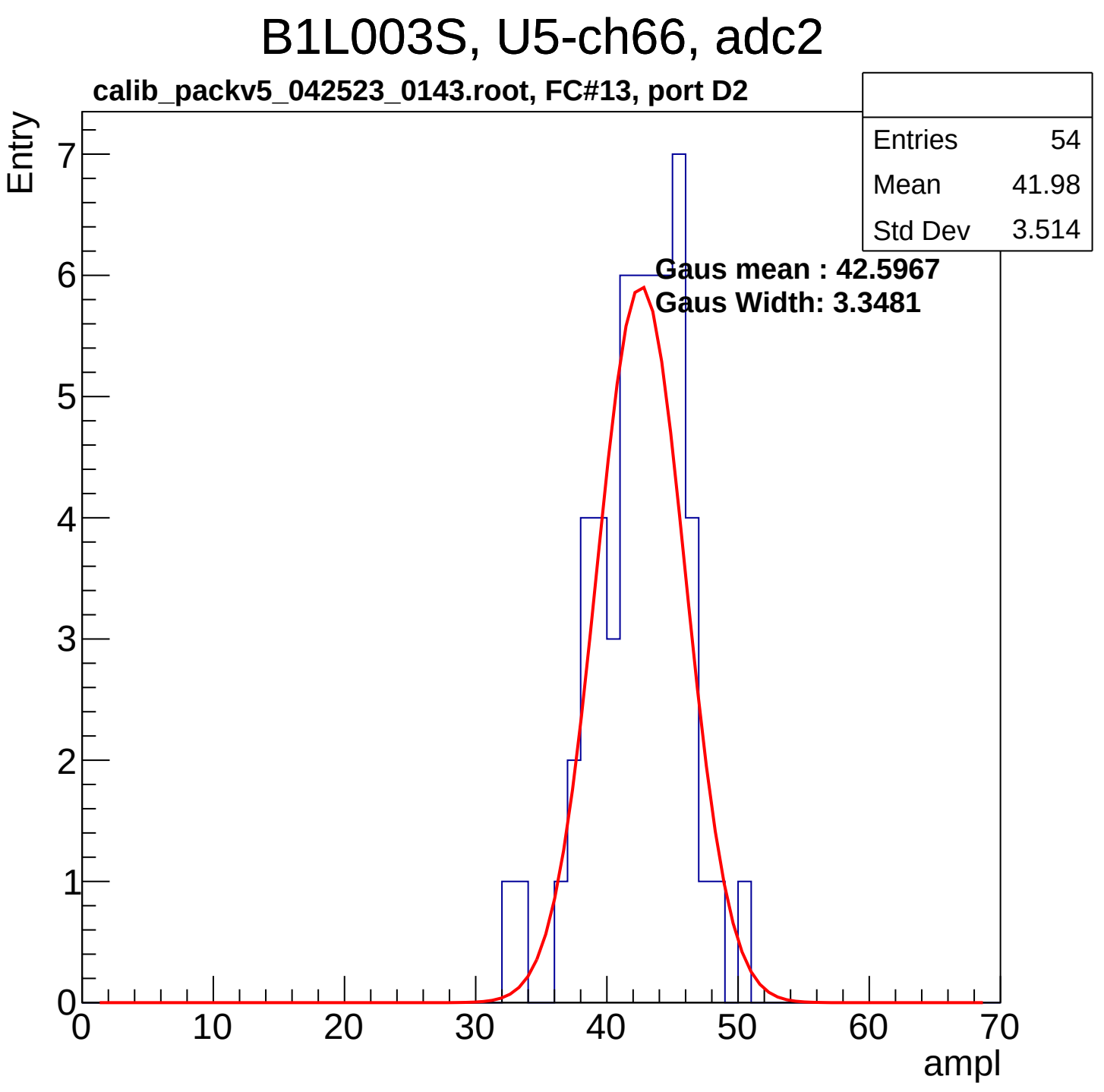
B1L003S, U5-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.98
Std Dev 3.514
Gaus mean : 42.5967
Gaus Width: 3.3481
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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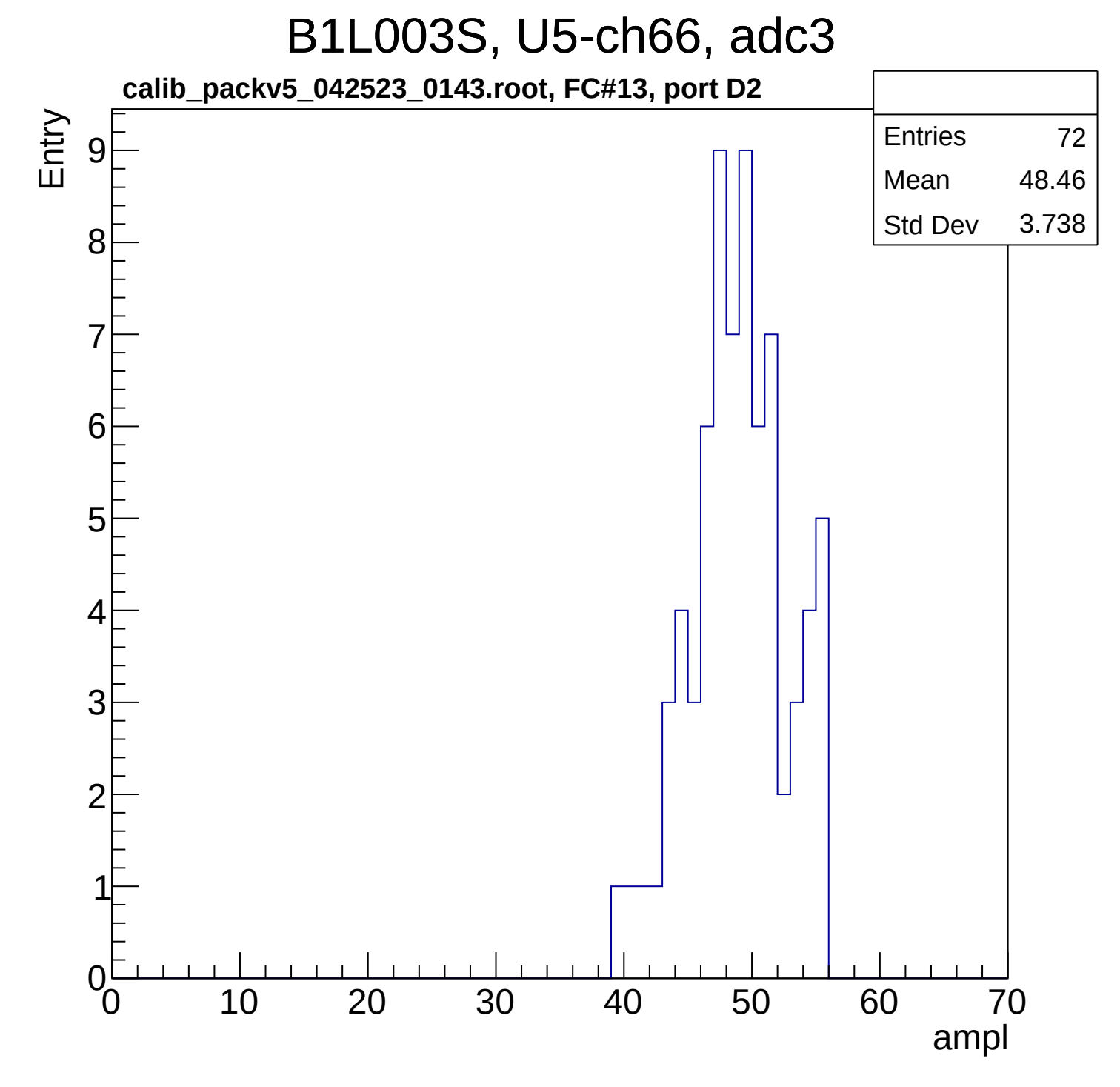
B1L003S, U5-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.46
Std Dev 3.738
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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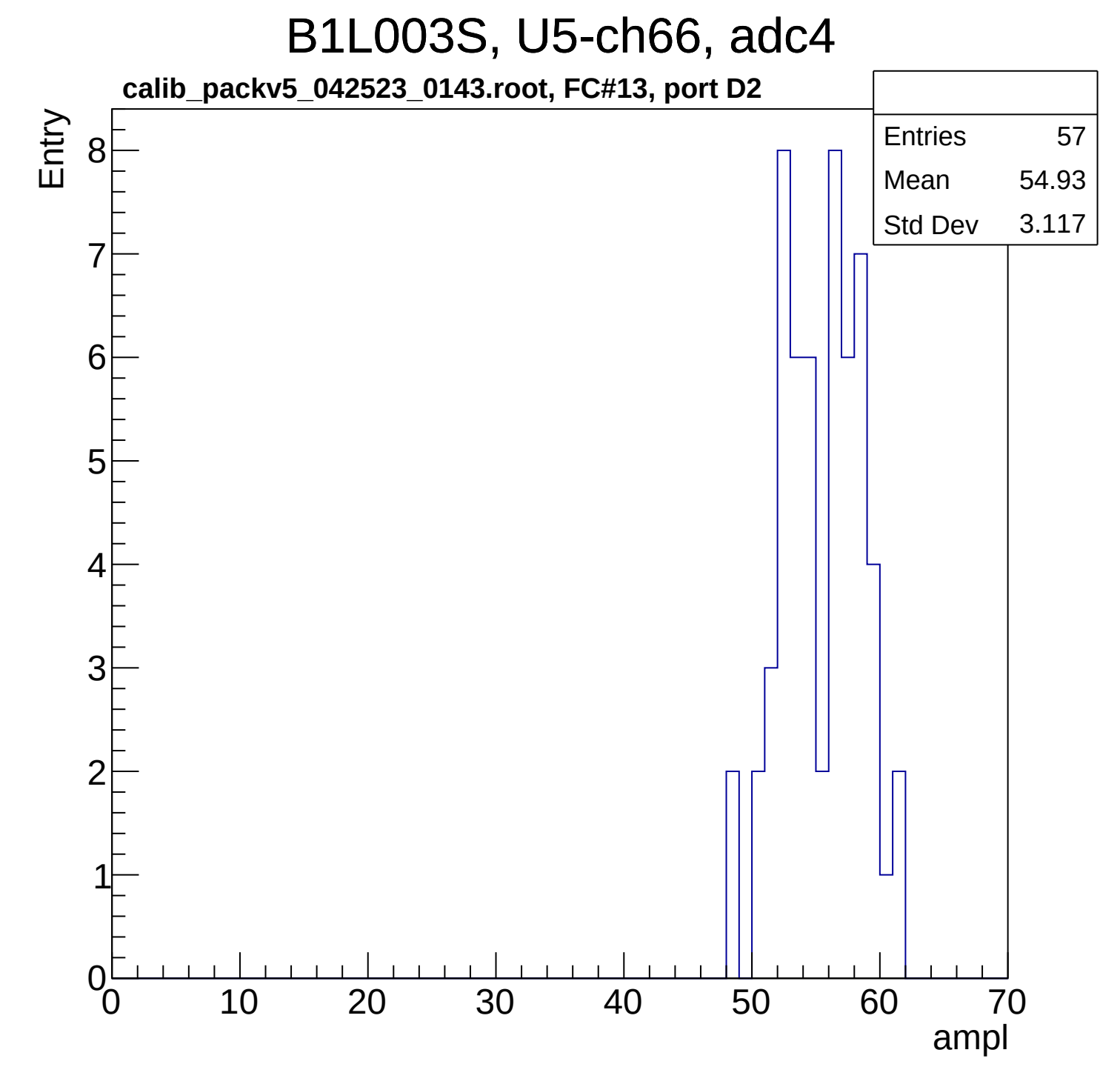
B1L003S, U5-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.93
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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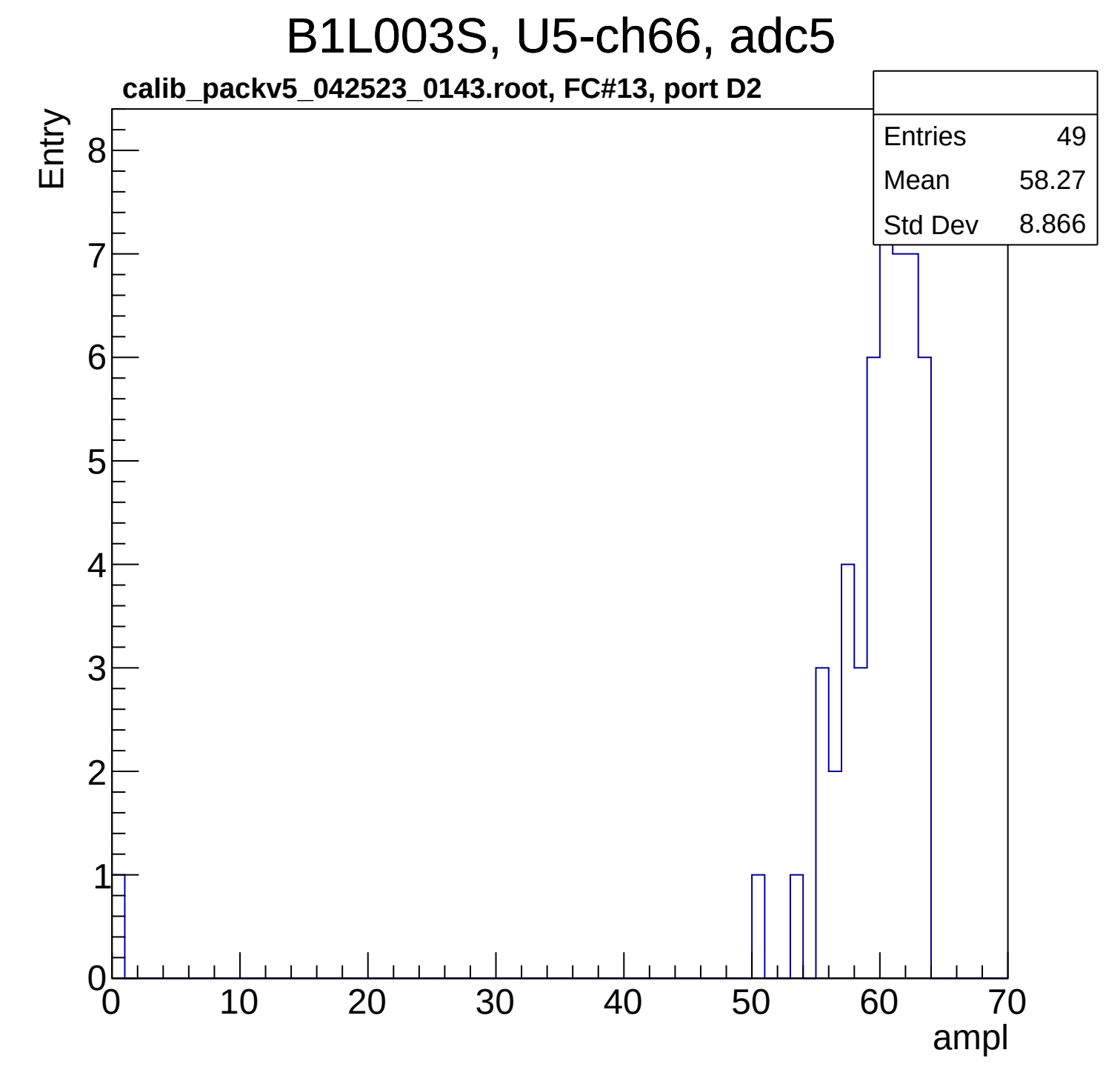
B1L003S, U5-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.27
Std Dev 8.866
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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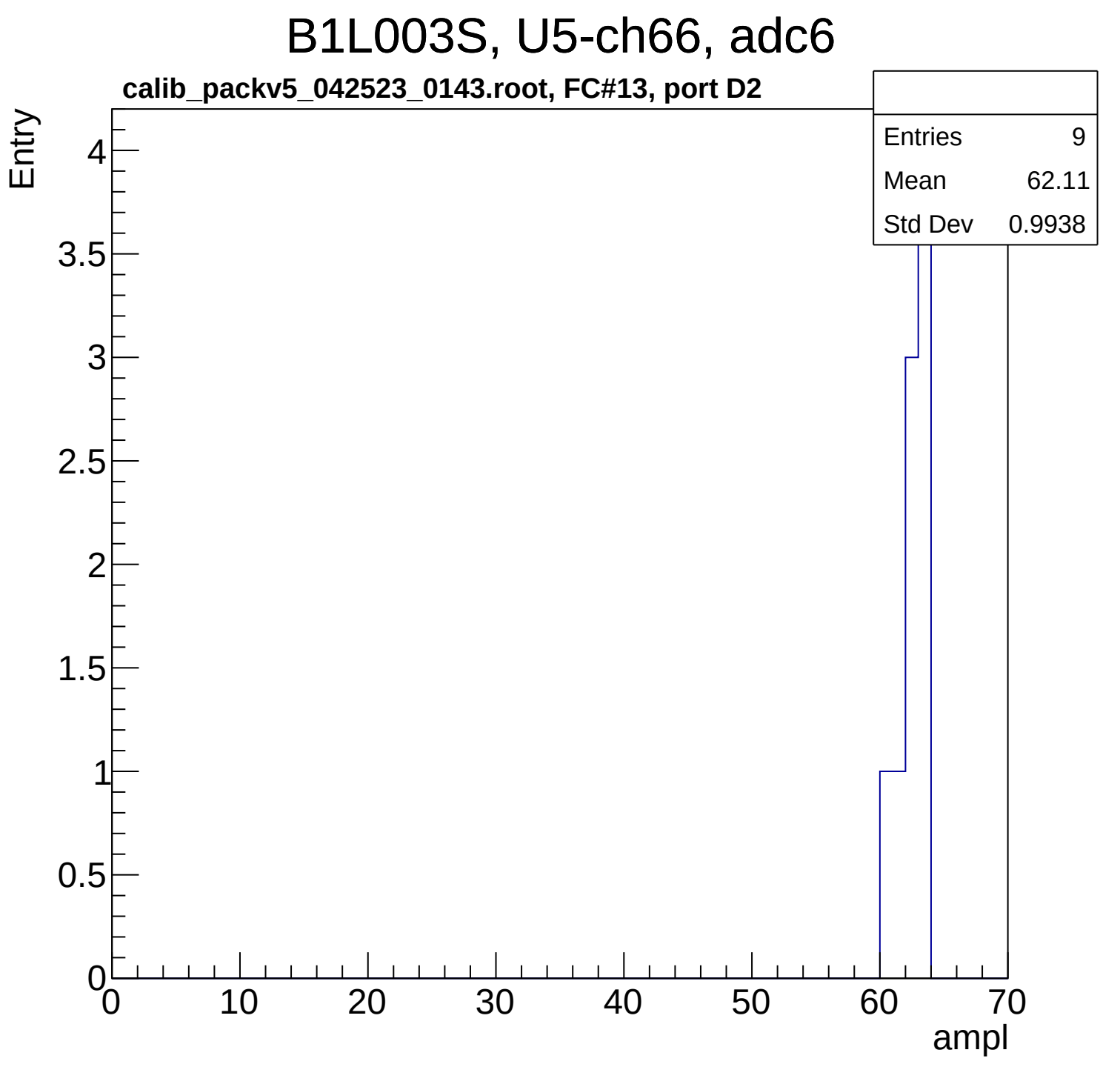
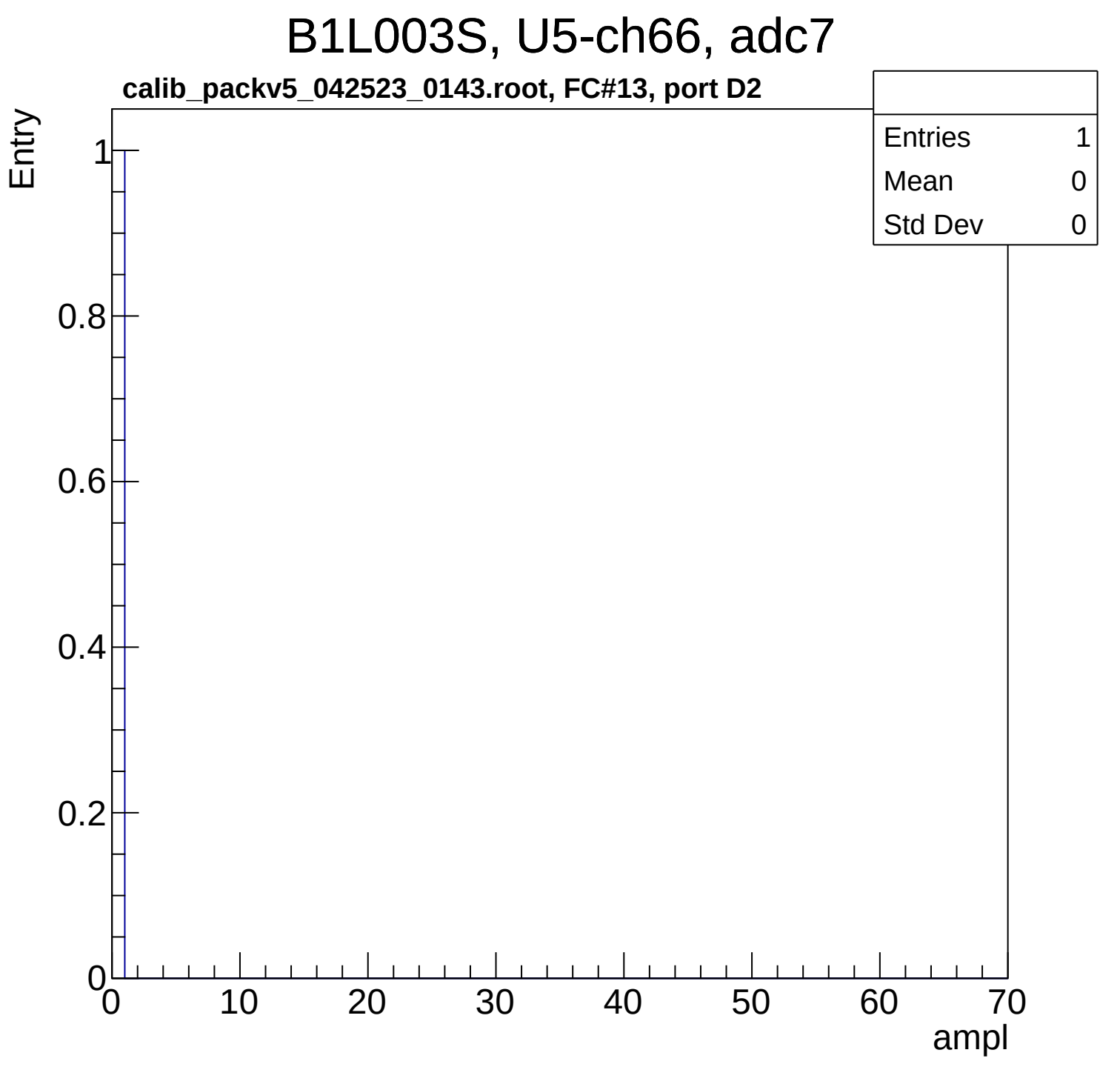
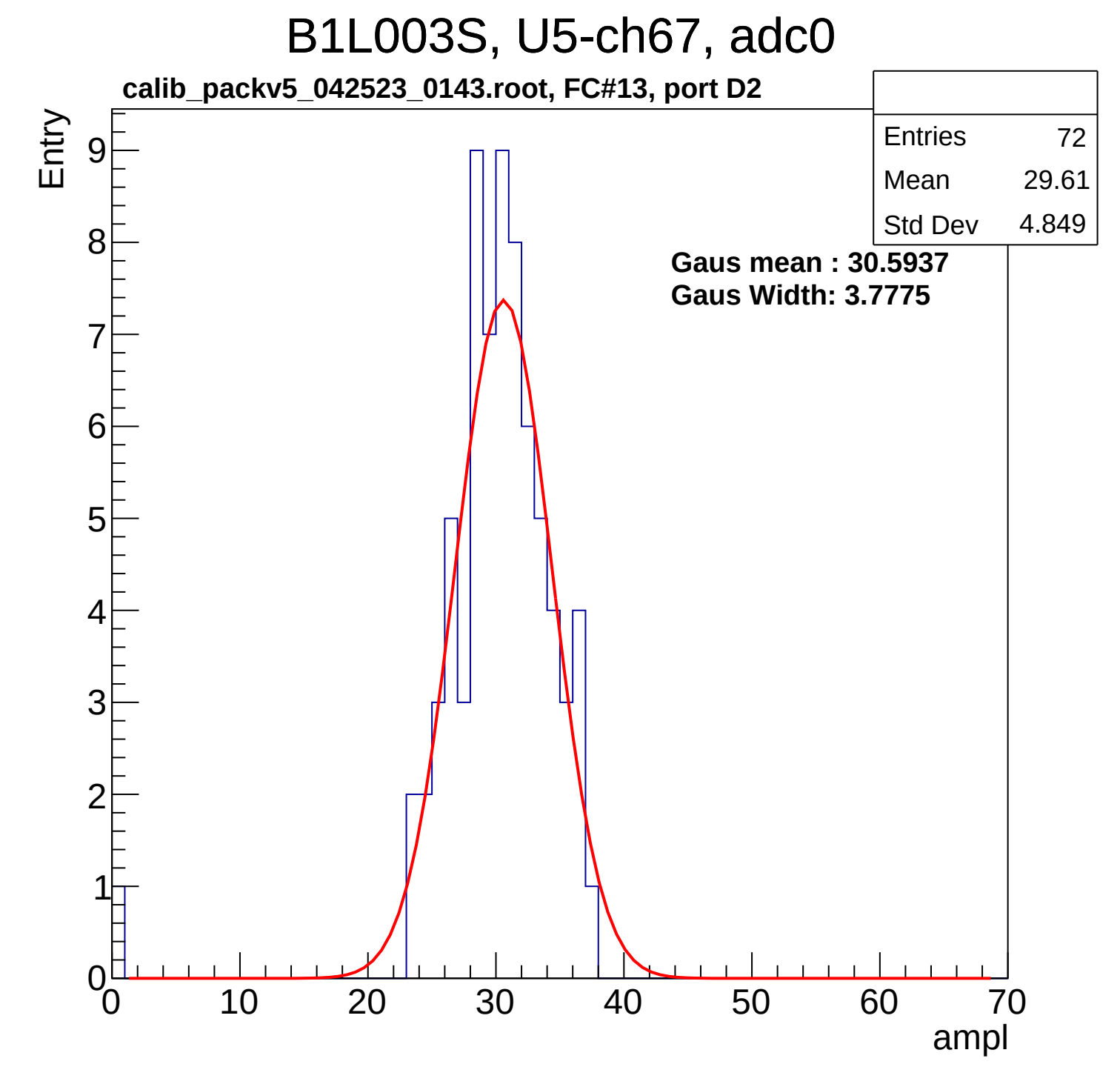

B1L003S, U5-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.61
Std Dev 4.849
Gaus mean : 30.5937
Gaus Width: 3.7775
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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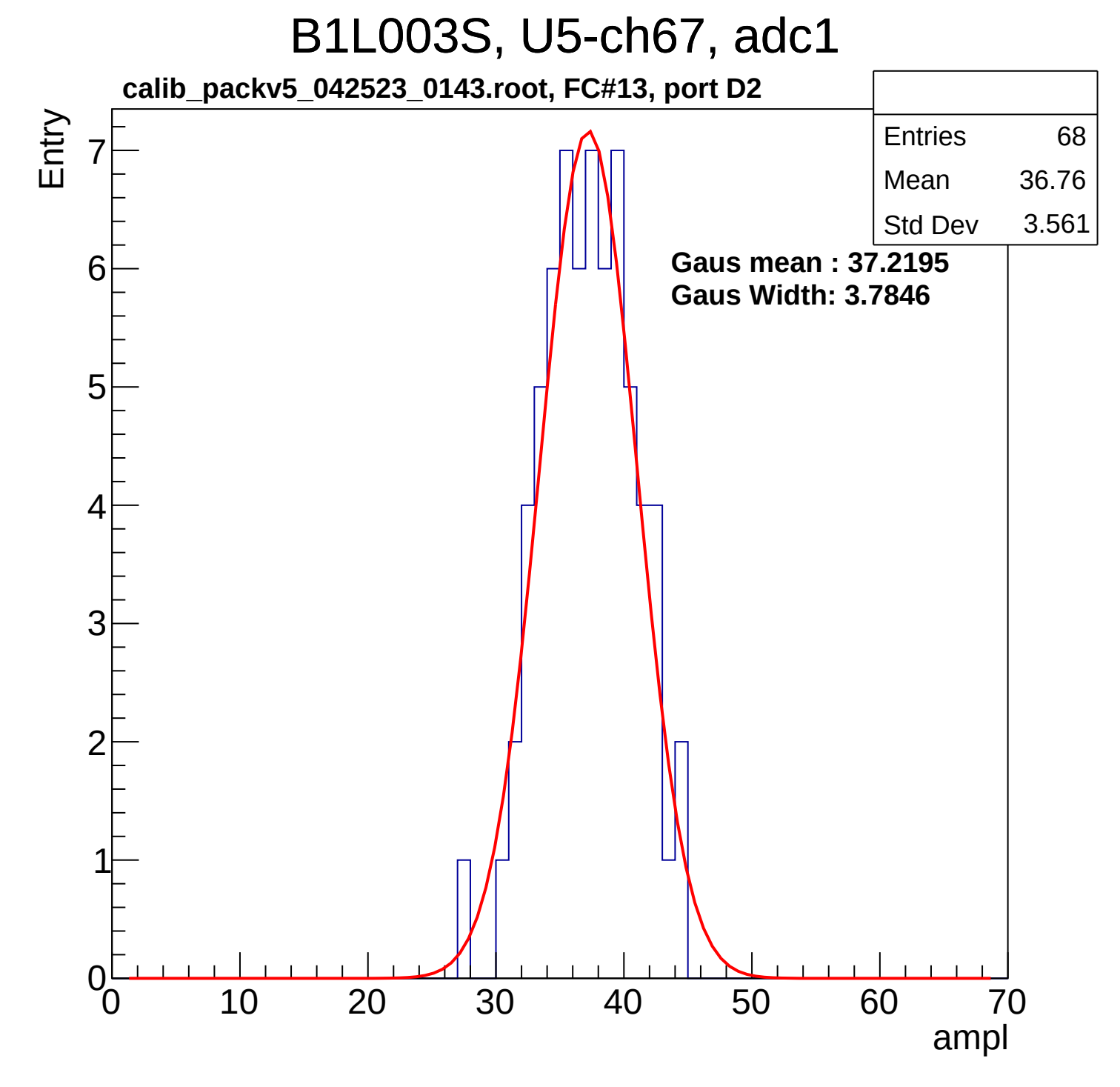
B1L003S, U5-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.76
Std Dev 3.561
Gaus mean : 37.2195
Gaus Width: 3.7846
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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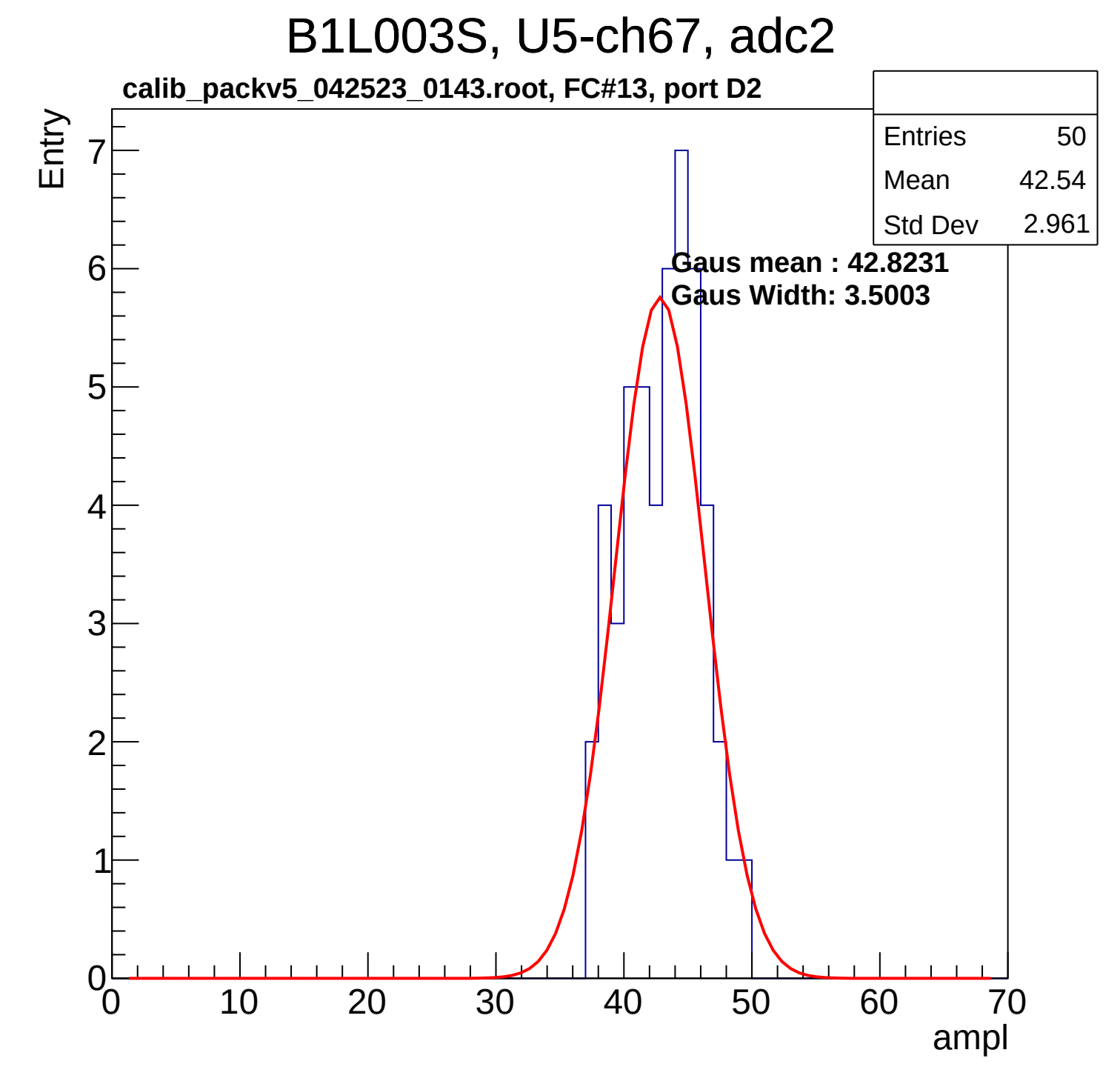
B1L003S, U5-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 42.54
Std Dev 2.961
Gaus mean : 42.8231
Gaus Width: 3.5003
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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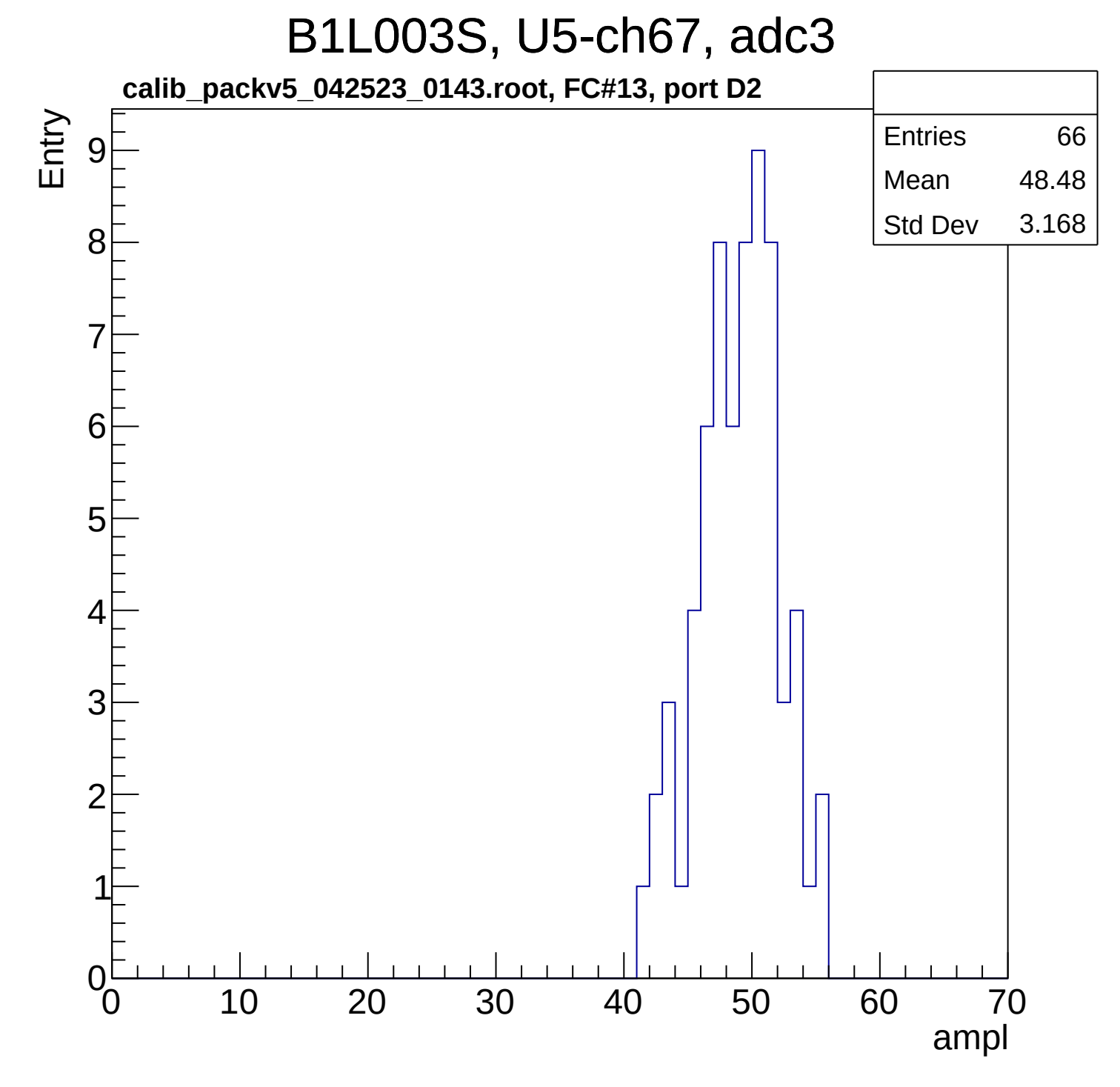
B1L003S, U5-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.48
Std Dev 3.168
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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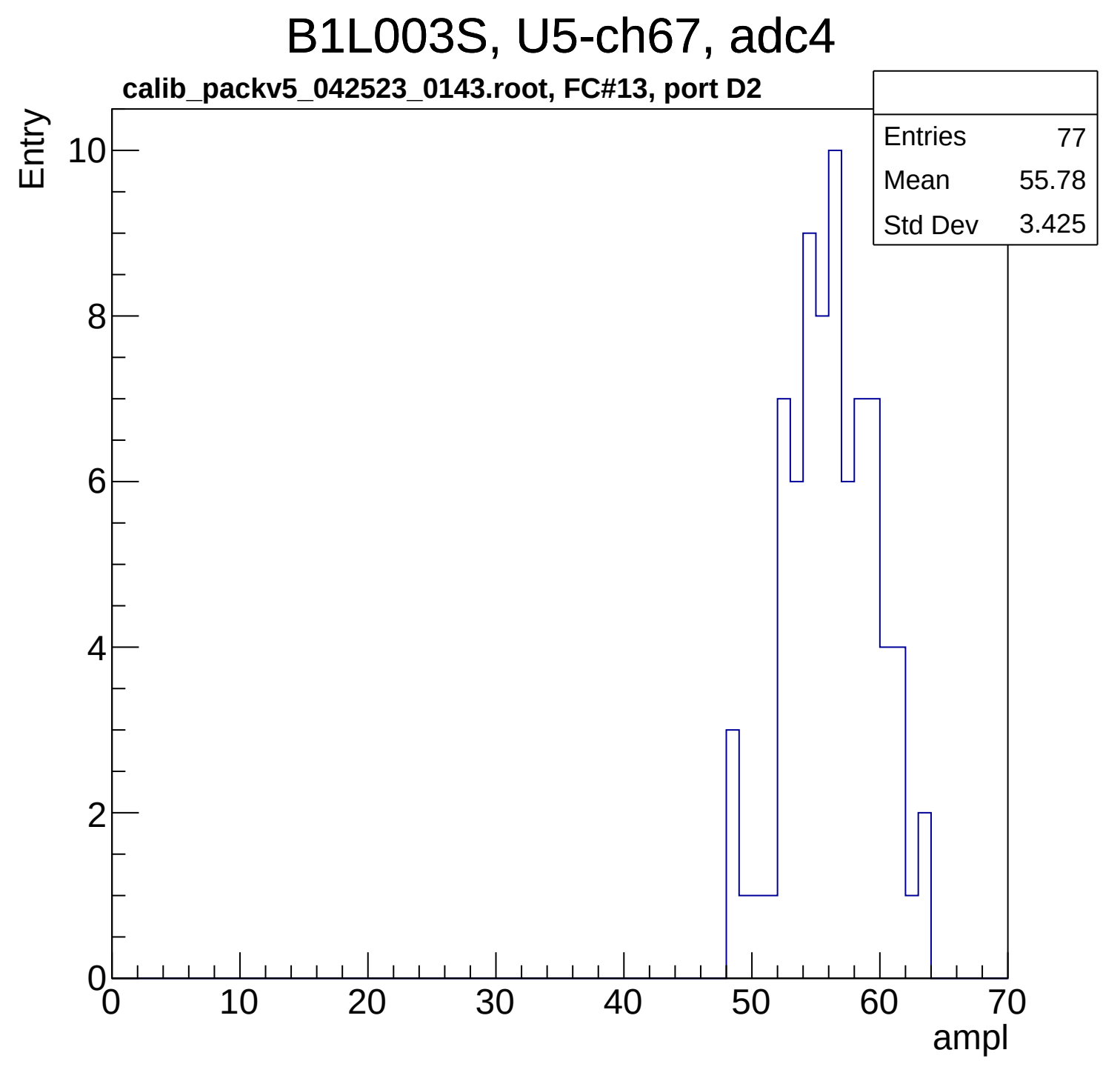
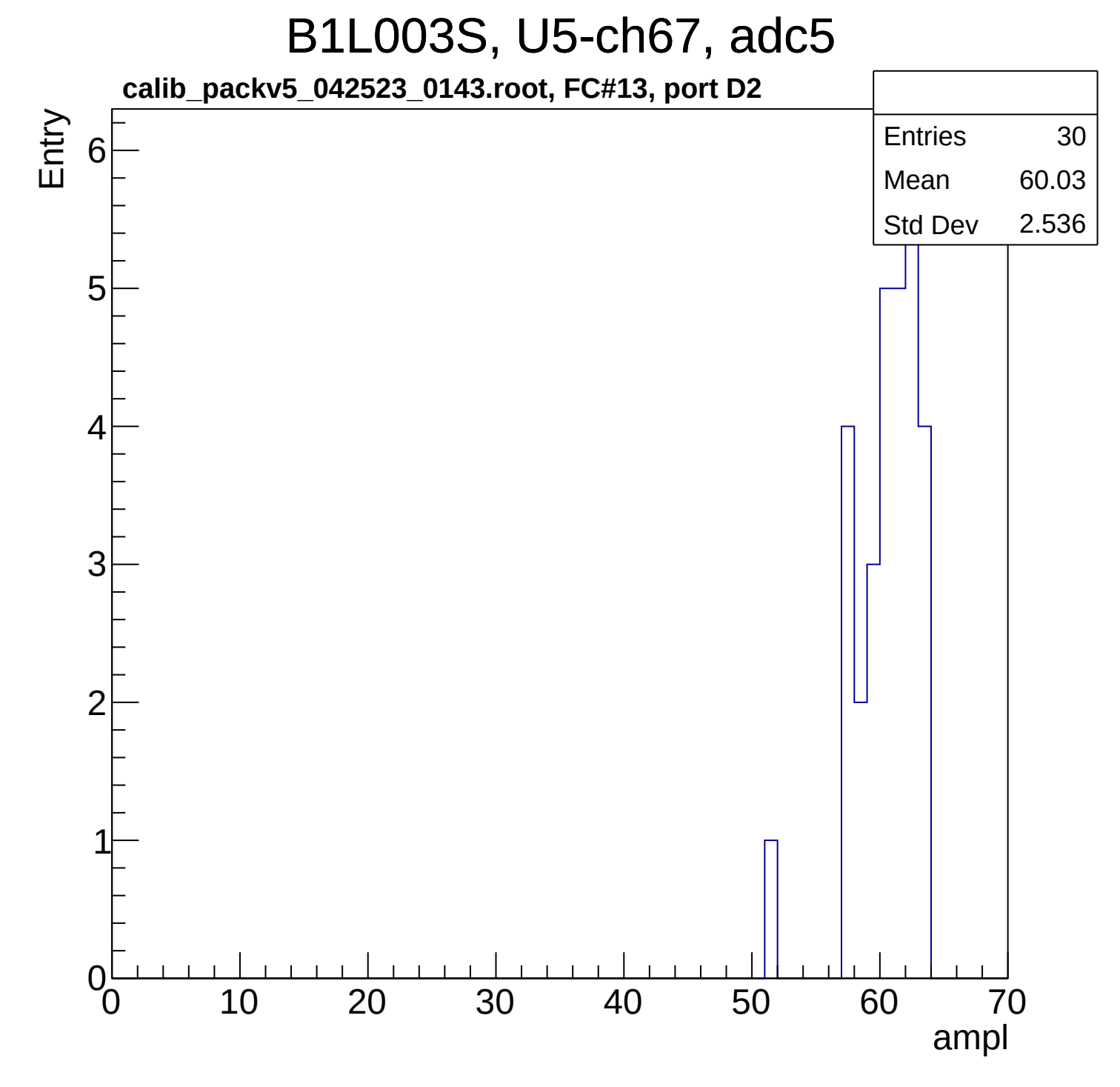

B1L003S, U5-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 55.78
Std Dev 3.425
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 60.03
Std Dev 2.536
Entry
ampl
0
1
2
3
4
5
6
0
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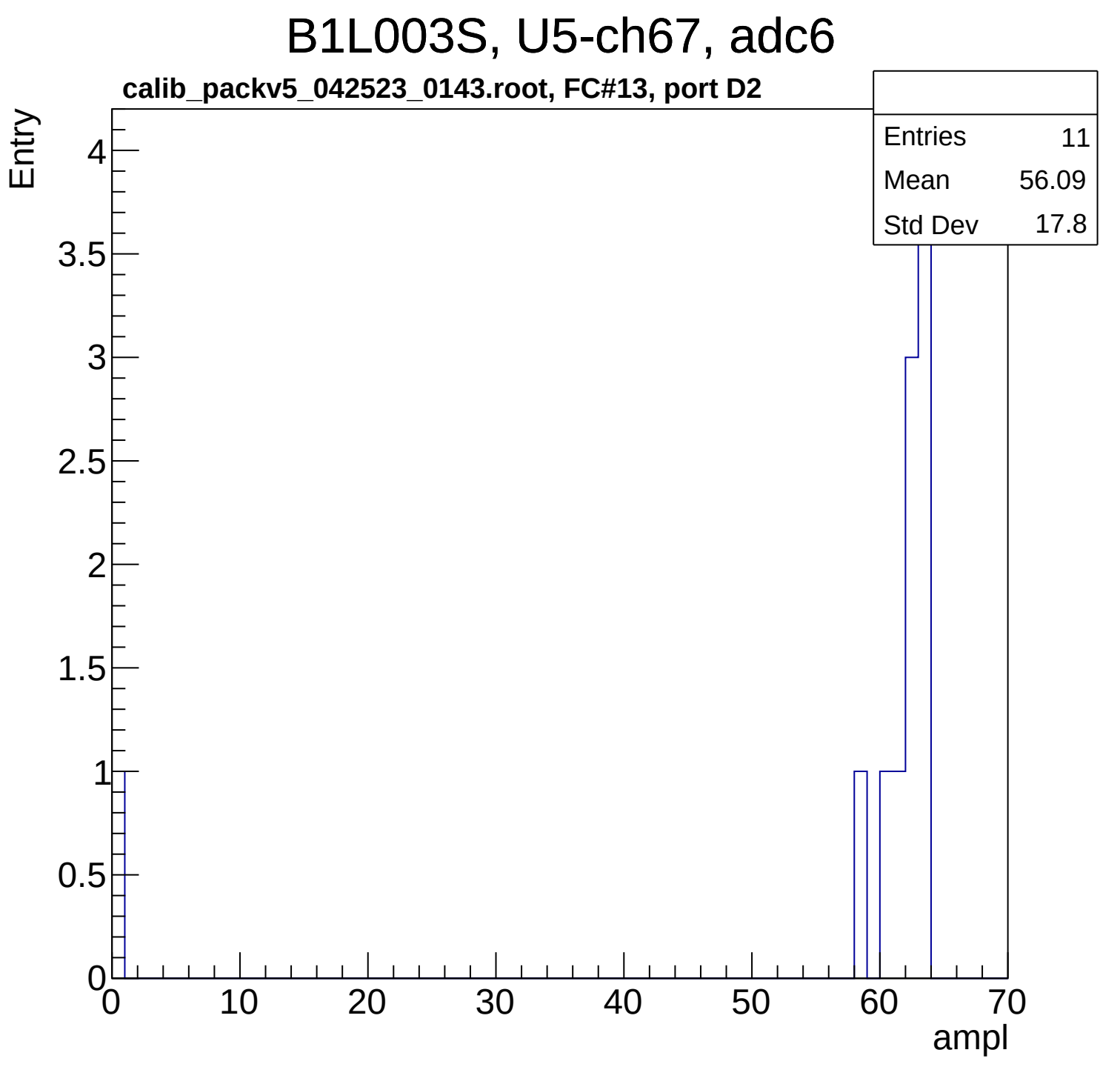
40
50
60
70


B1L003S, U5-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.09
Std Dev 17.8
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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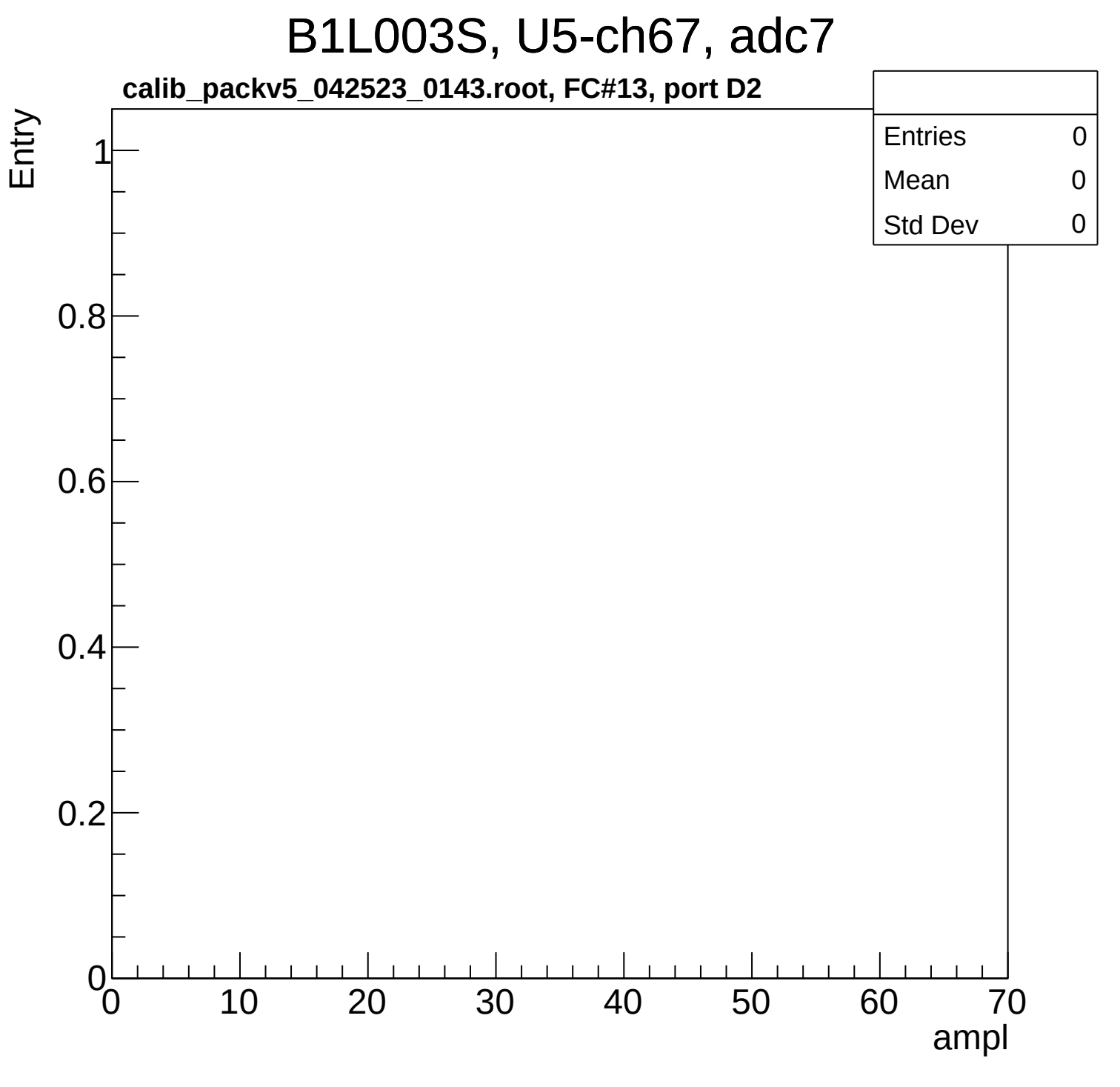
B1L003S, U5-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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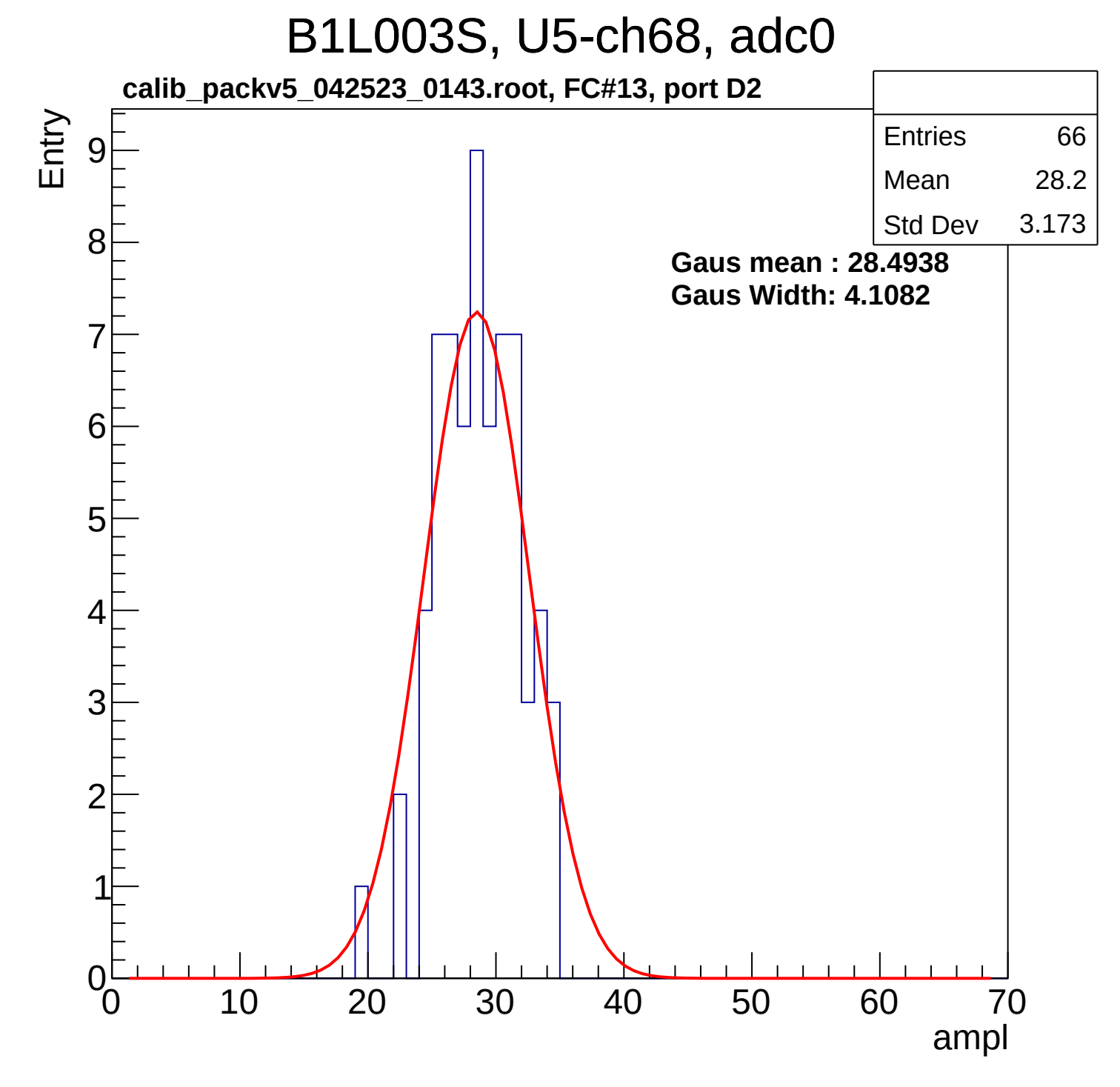
50
60
70
ampl


B1L003S, U5-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.2
Std Dev 3.173
Gaus mean : 28.4938
Gaus Width: 4.1082
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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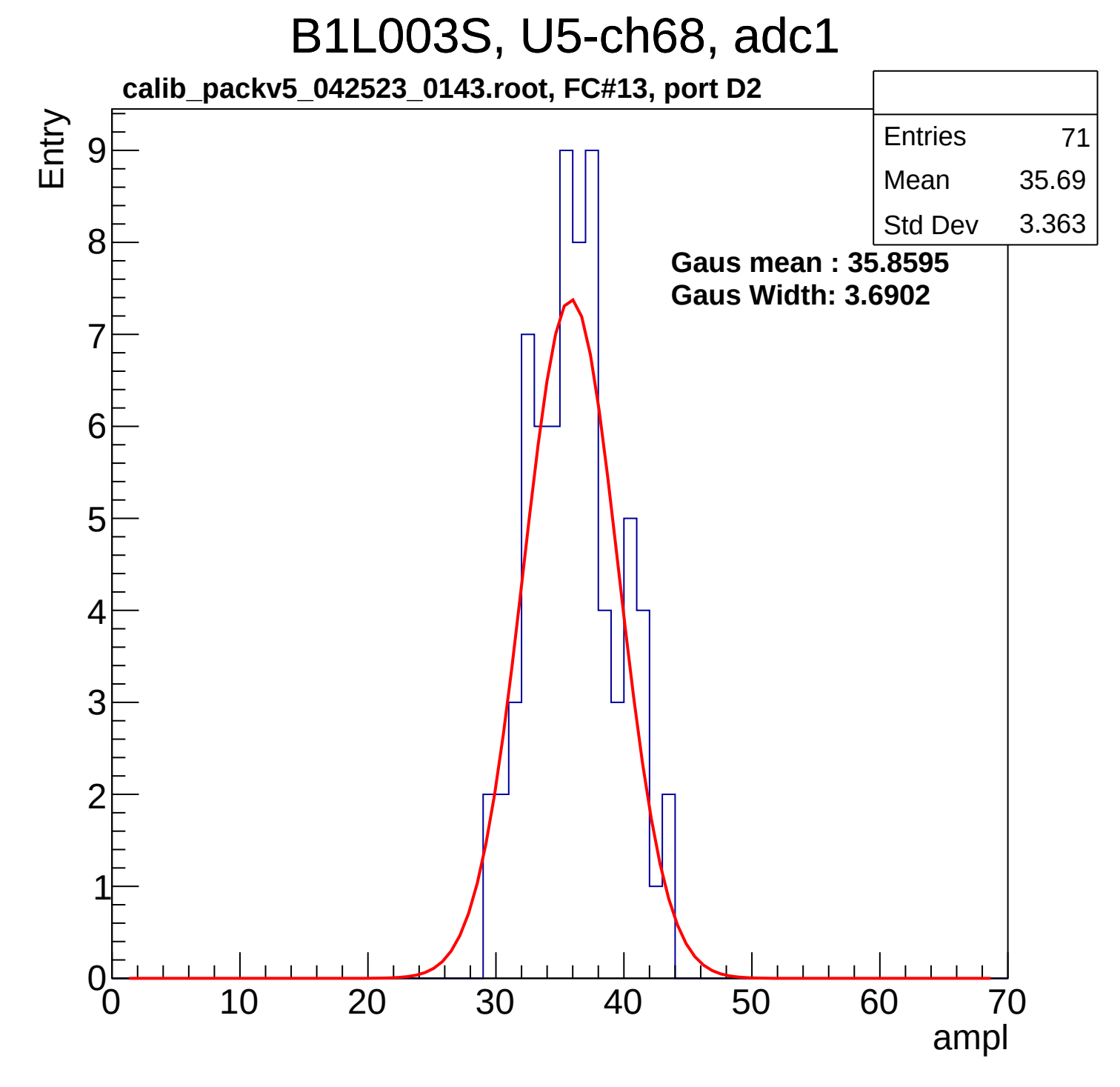
B1L003S, U5-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.69
Std Dev 3.363
Gaus mean : 35.8595
Gaus Width: 3.6902
Entry
ampl
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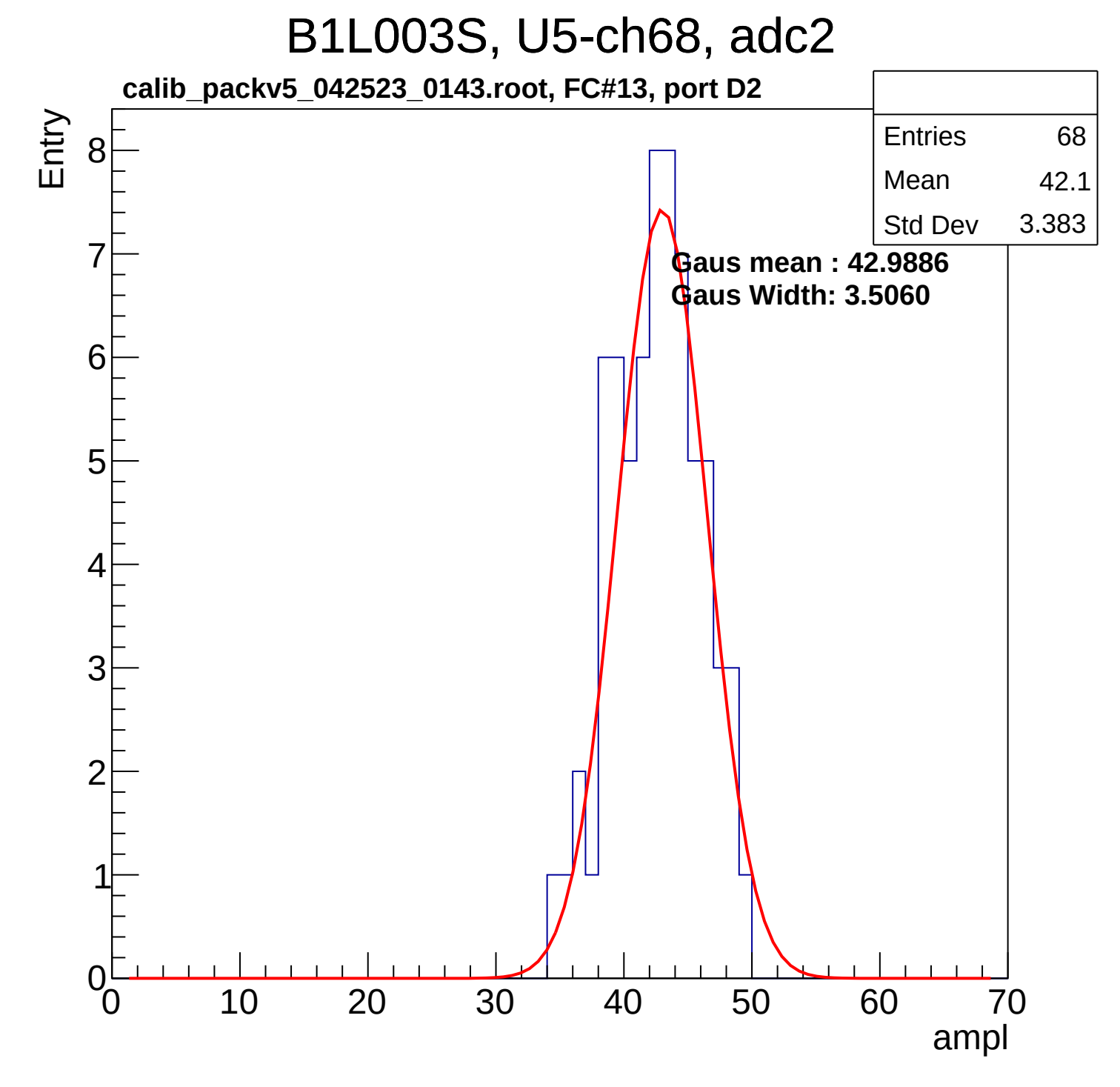
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.1
Std Dev 3.383
Gaus mean : 42.9886
Gaus Width: 3.5060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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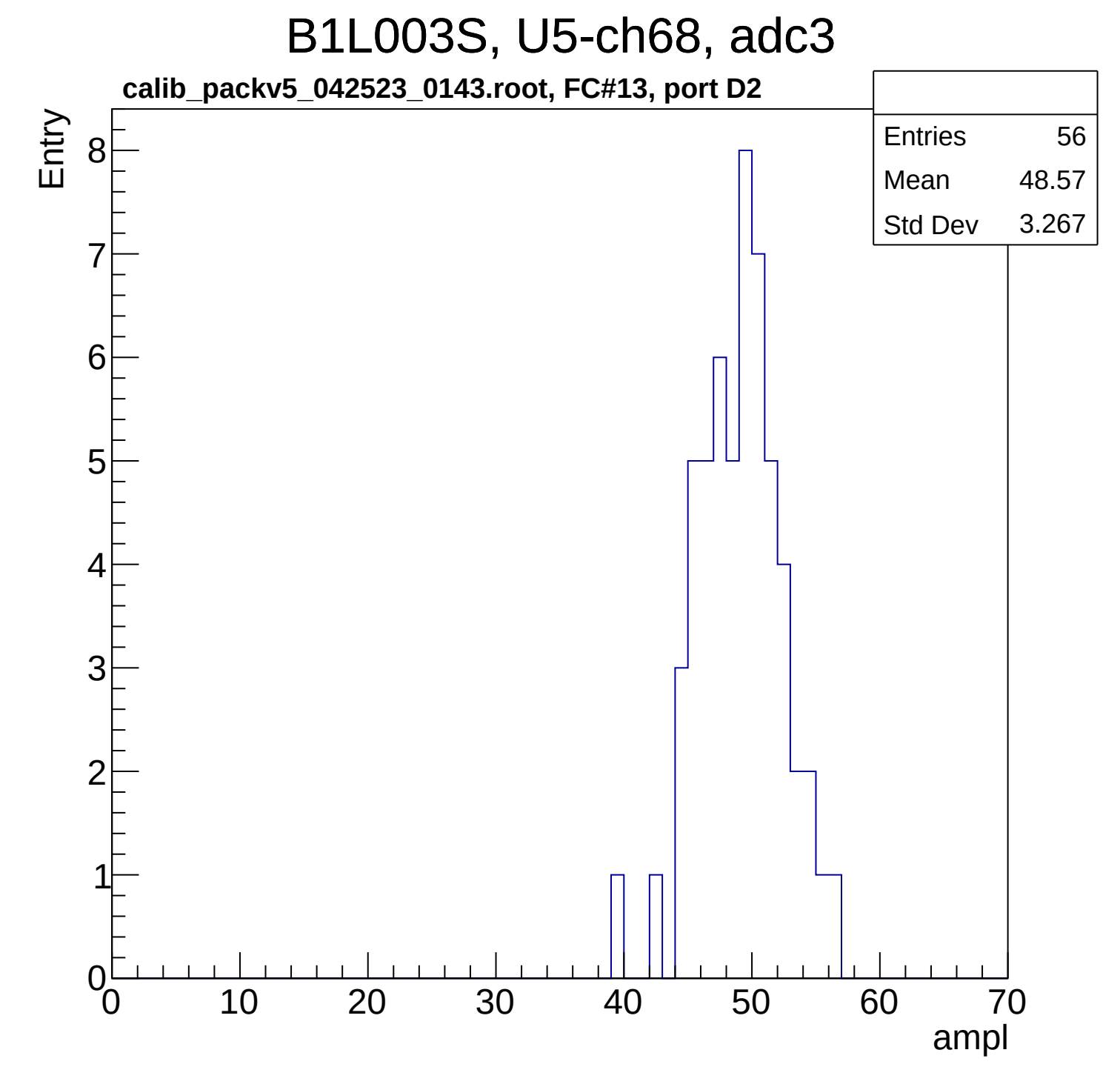
B1L003S, U5-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.57
Std Dev 3.267
Entry
ampl
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5
6
7
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0
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60
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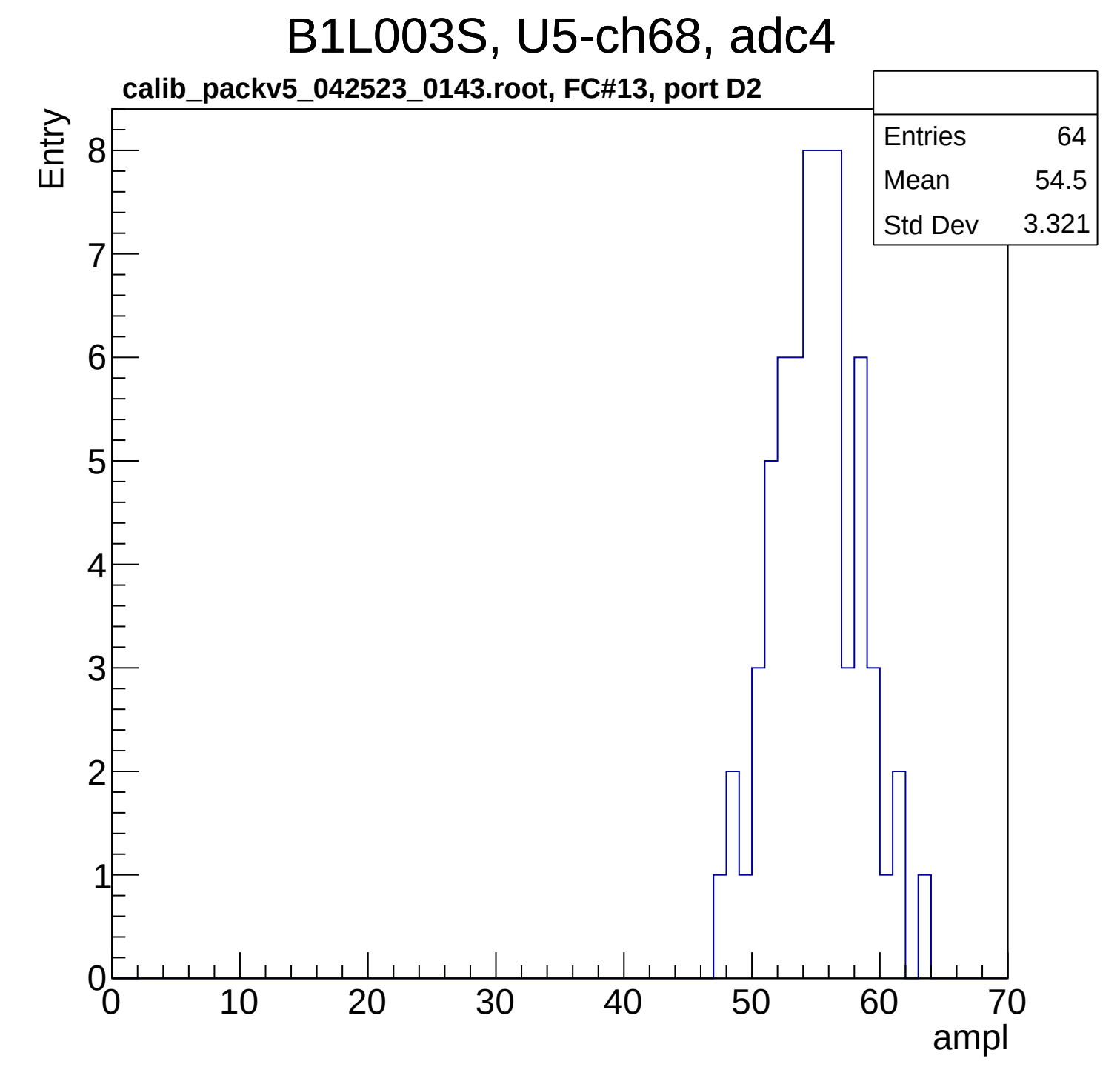
B1L003S, U5-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.5
Std Dev 3.321
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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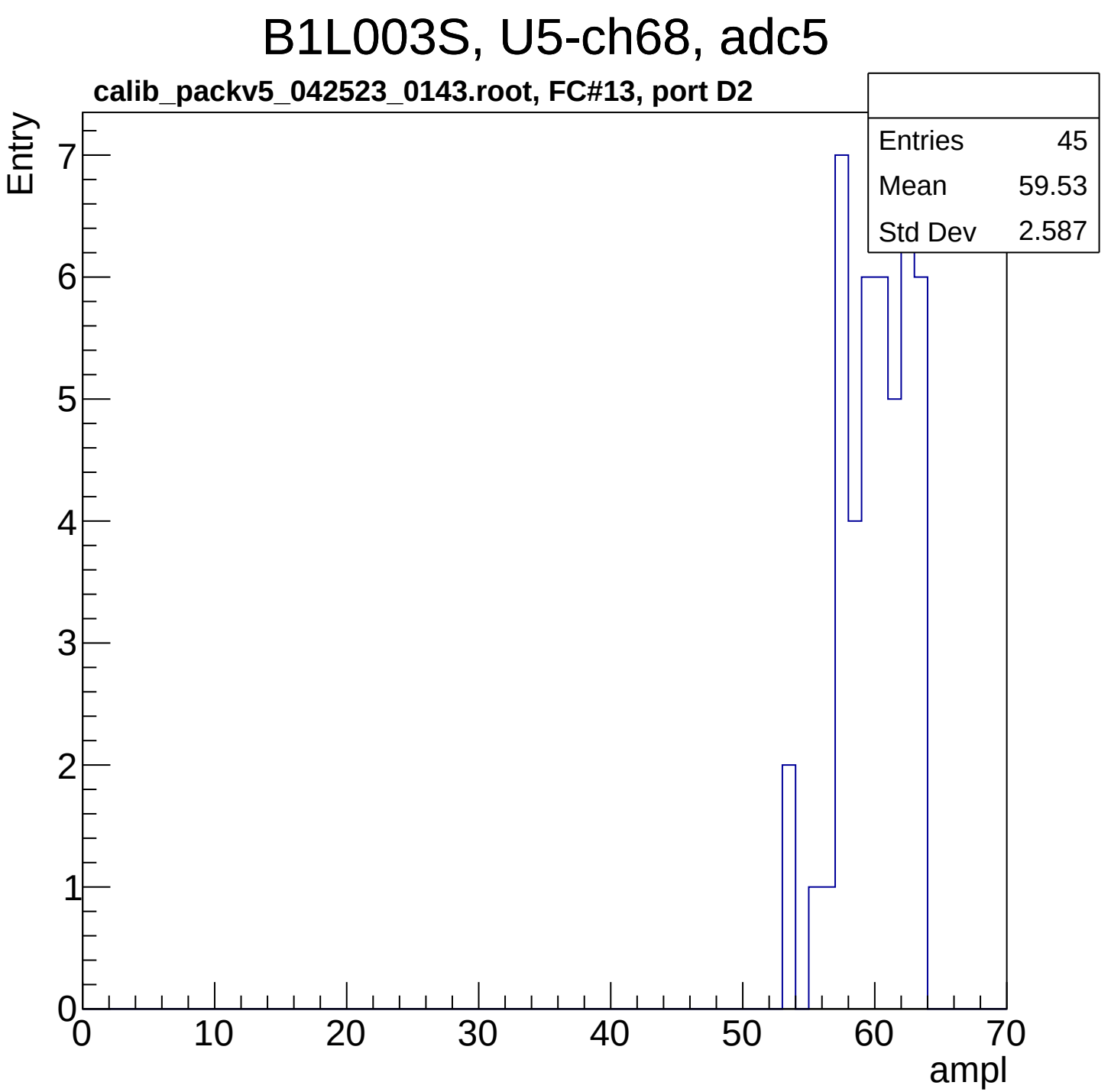
B1L003S, U5-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.53
Std Dev 2.587
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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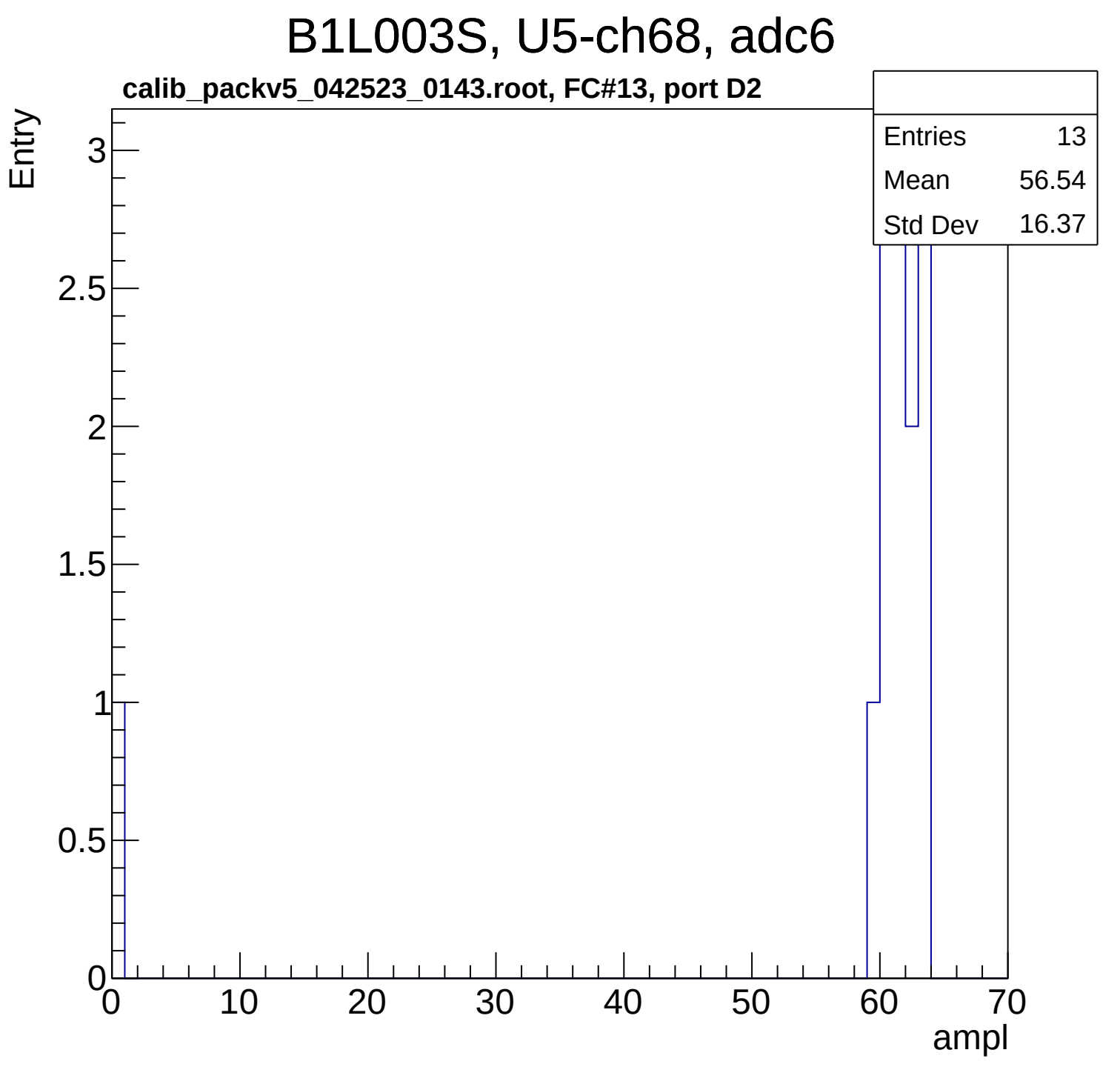
B1L003S, U5-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.54
Std Dev 16.37
Entry
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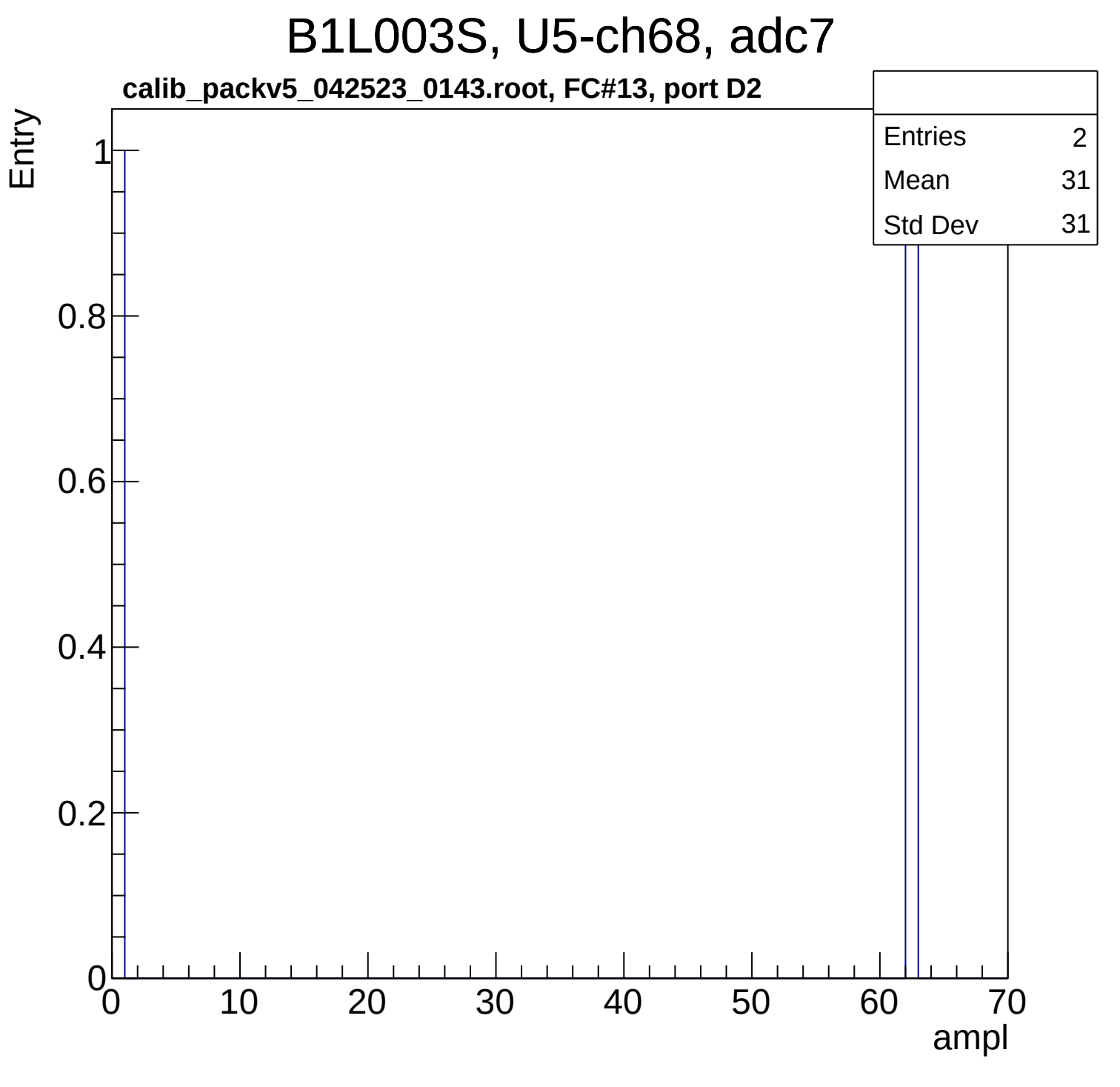
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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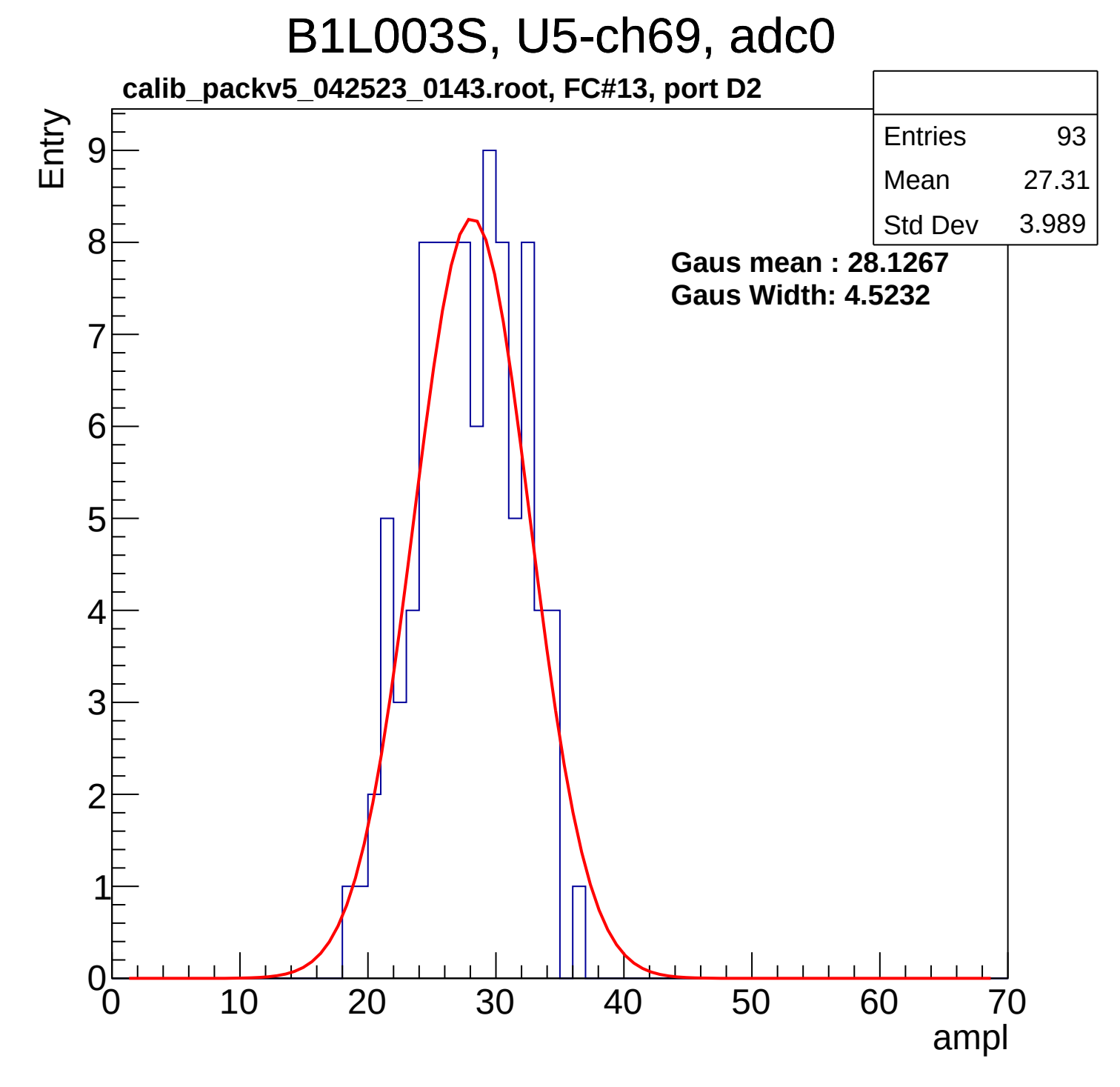
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 93
Mean 27.31
Std Dev 3.989
Gaus mean : 28.1267
Gaus Width: 4.5232
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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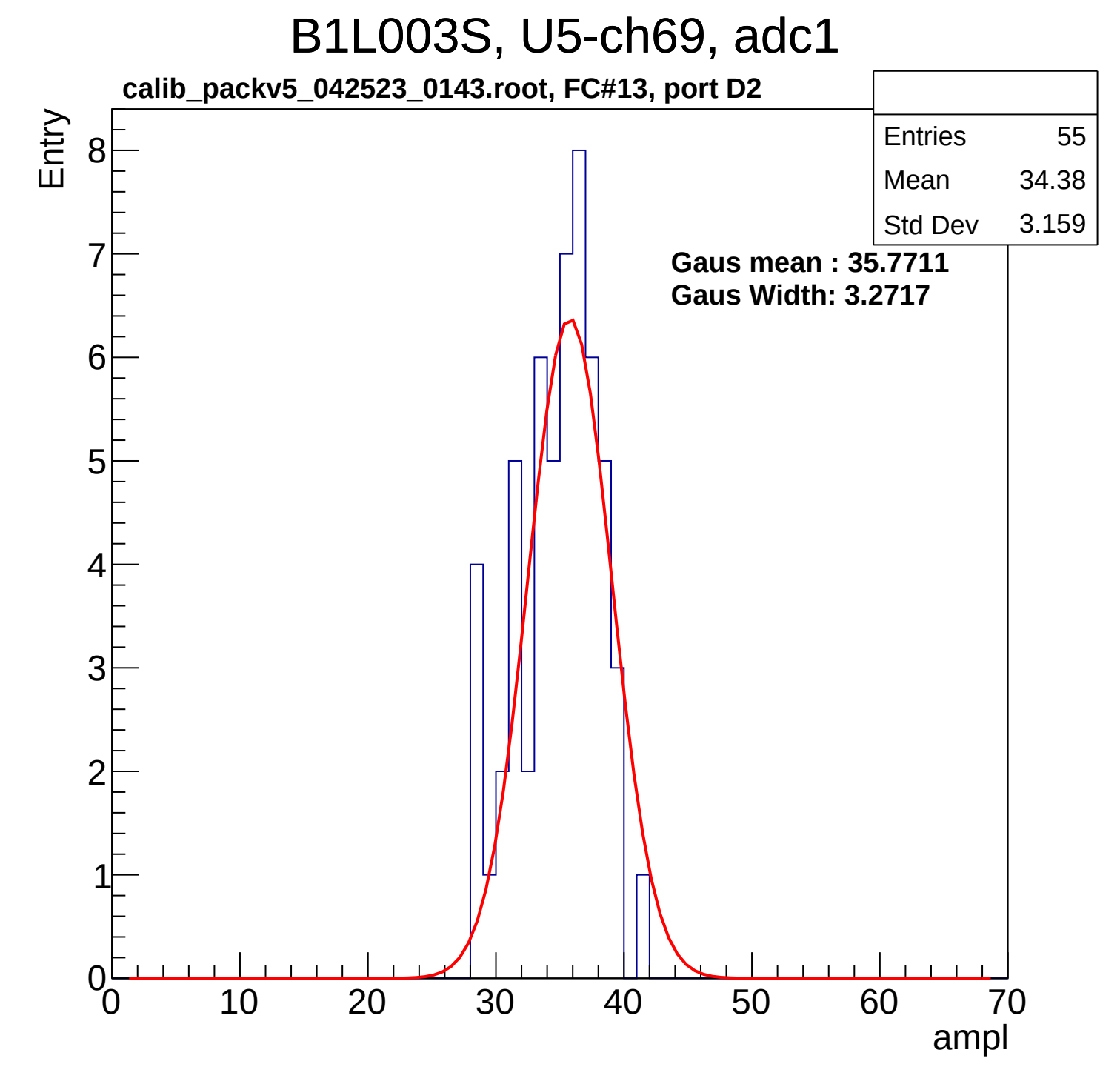
B1L003S, U5-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.38
Std Dev 3.159
Gaus mean : 35.7711
Gaus Width: 3.2717
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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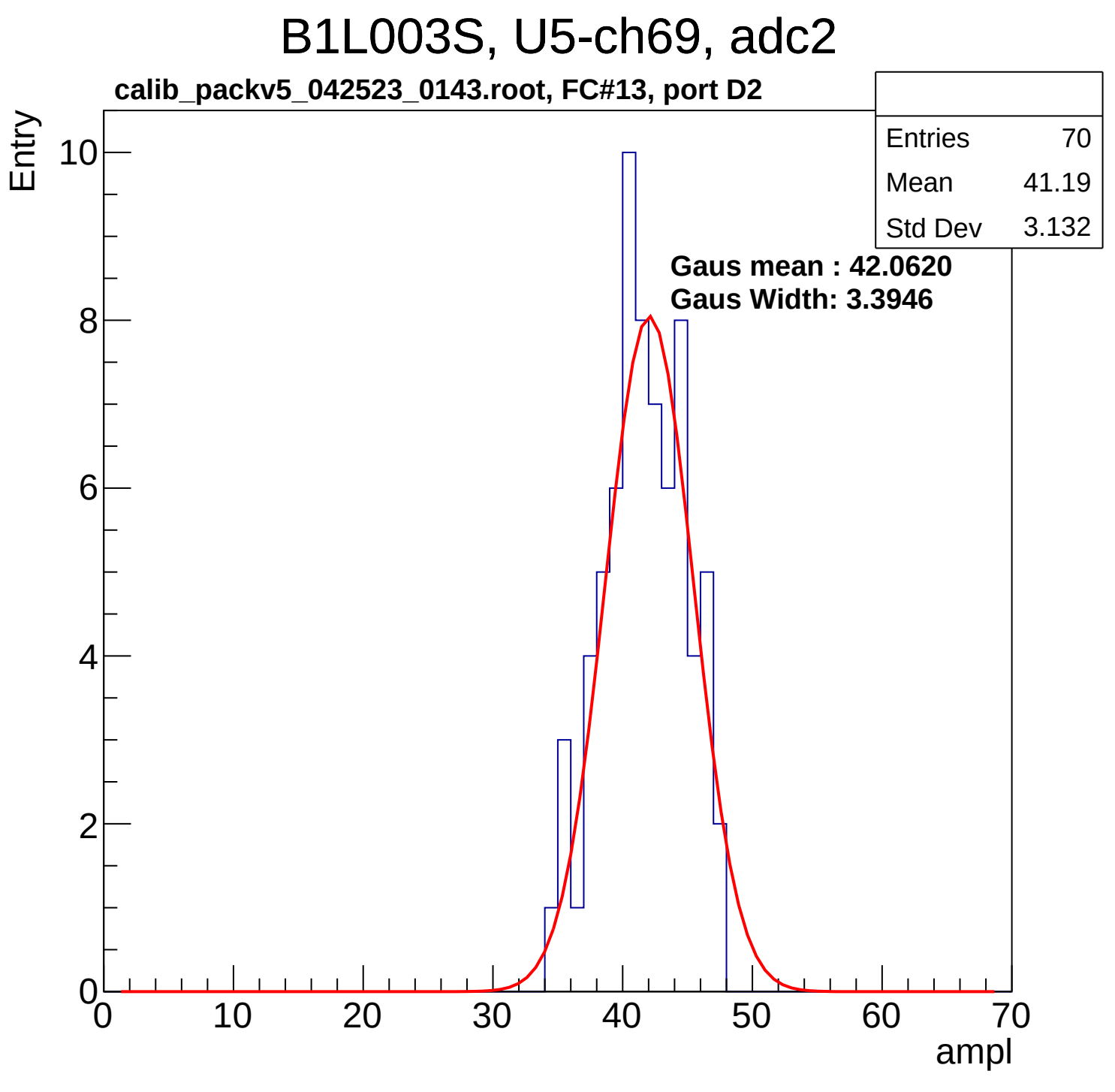
B1L003S, U5-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.19
Std Dev 3.132
Gaus mean : 42.0620
Gaus Width: 3.3946
Entry
ampl
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2
4
6
8
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0
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60
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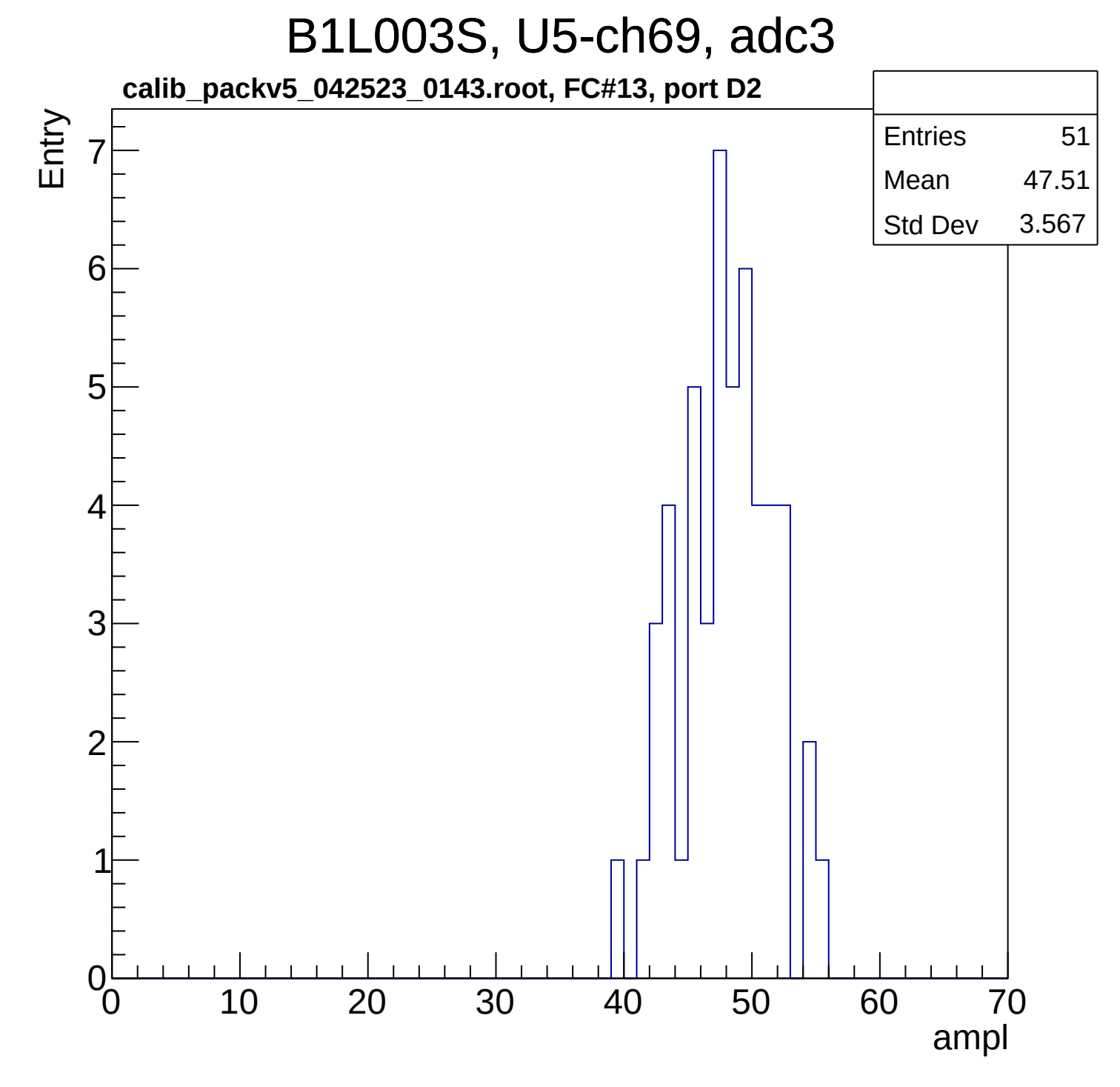
B1L003S, U5-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 47.51
Std Dev 3.567
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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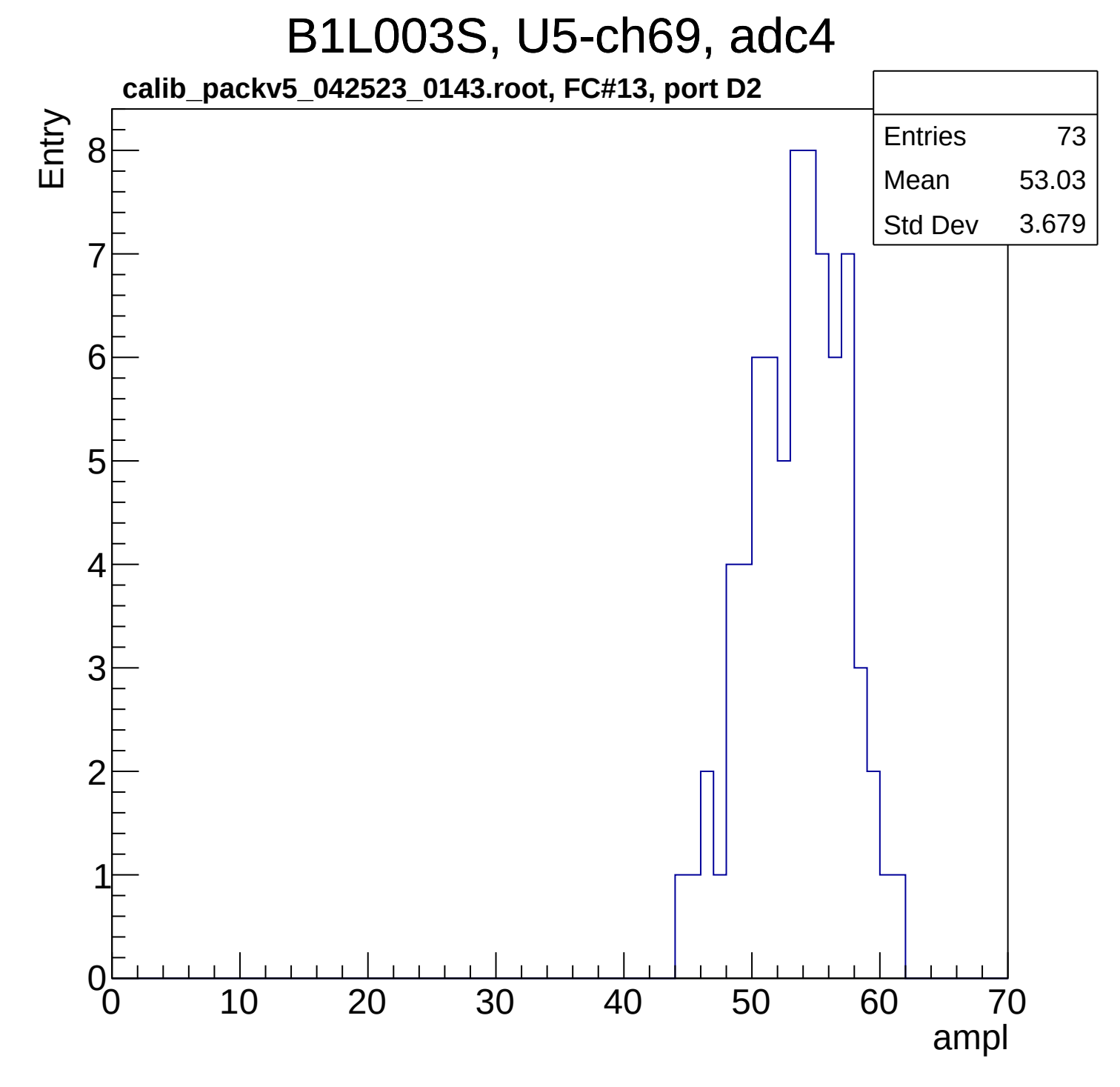
40
50
60
70


B1L003S, U5-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 53.03
Std Dev 3.679
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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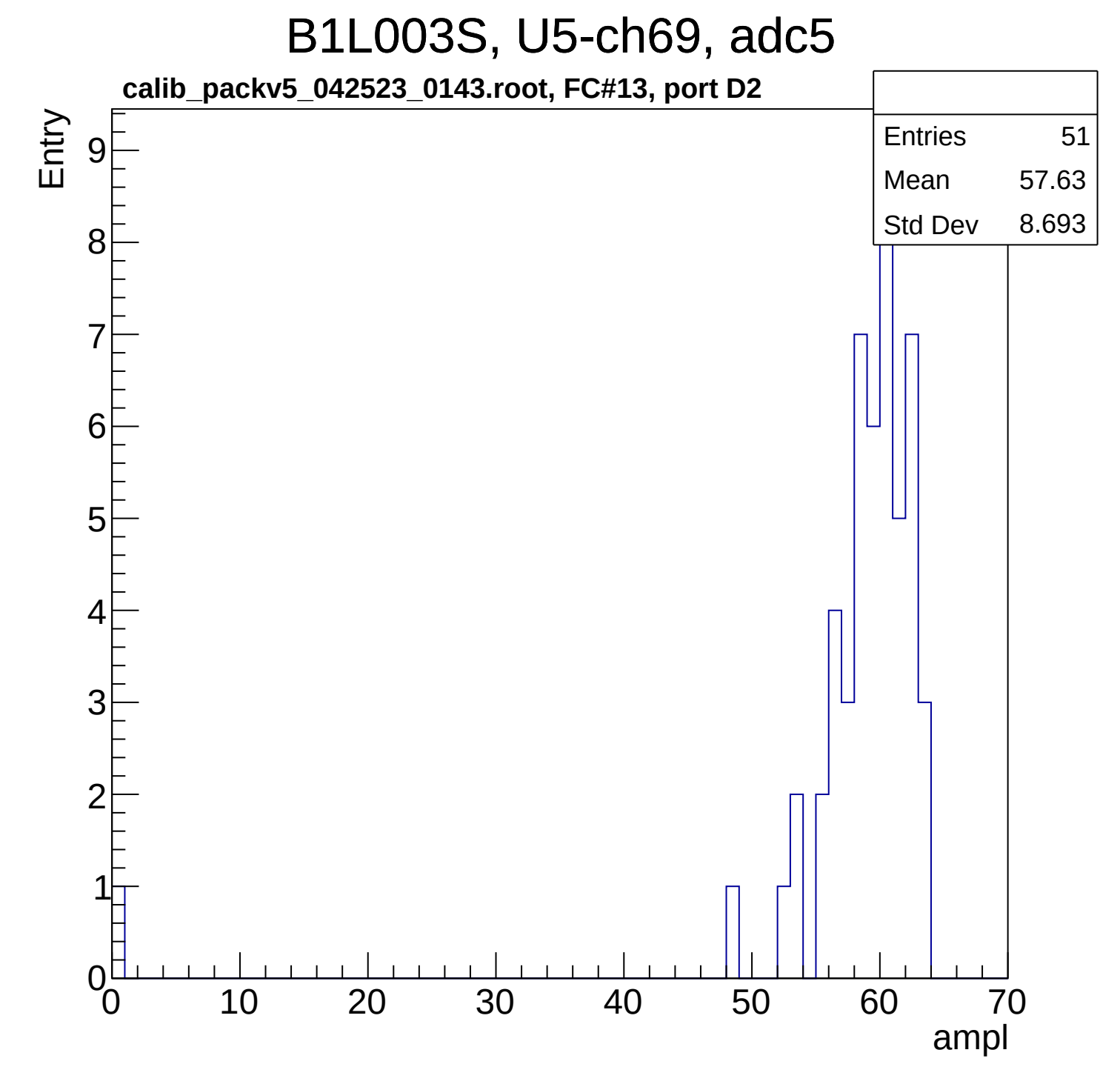
B1L003S, U5-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 57.63
Std Dev 8.693
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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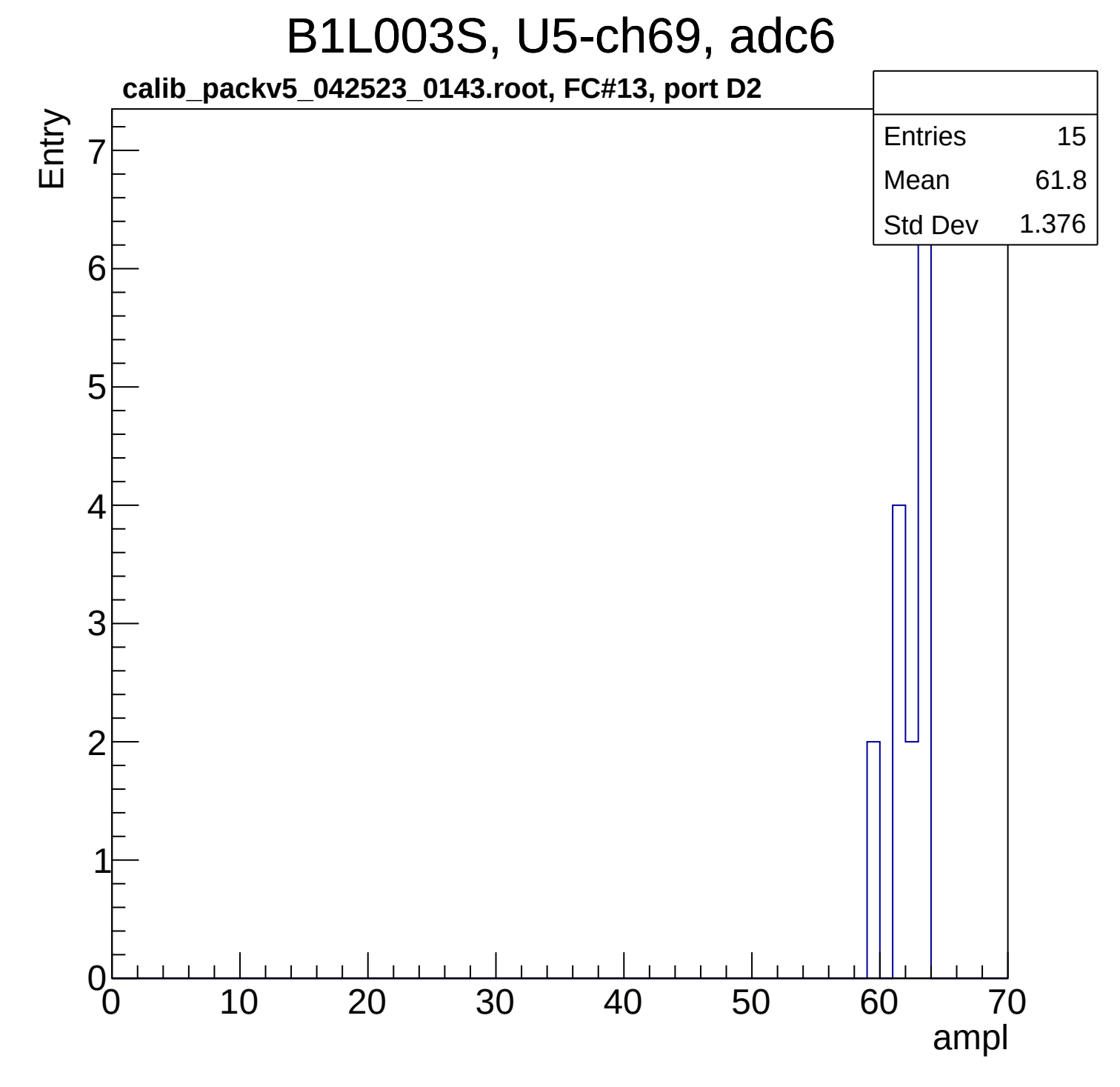
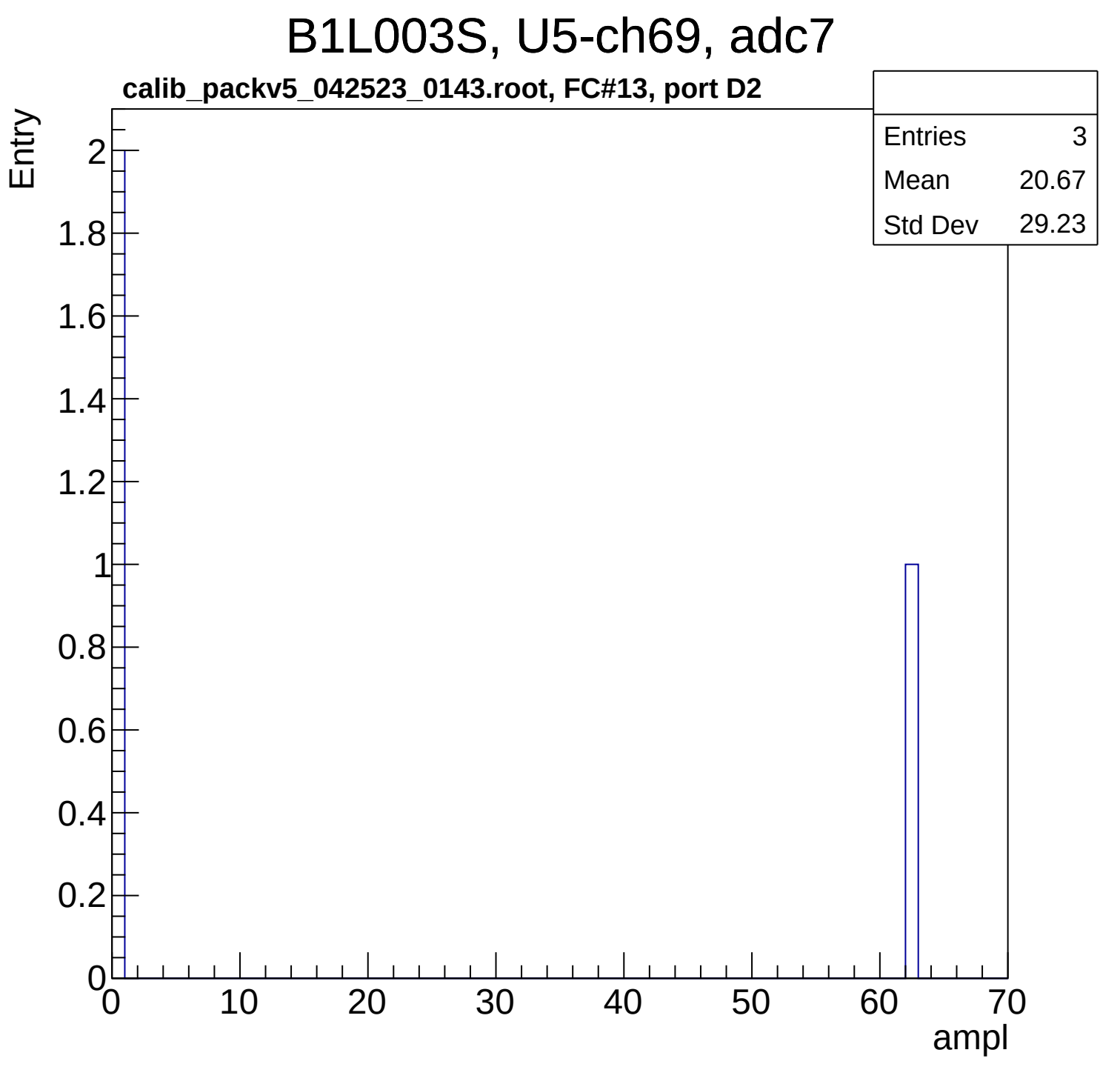
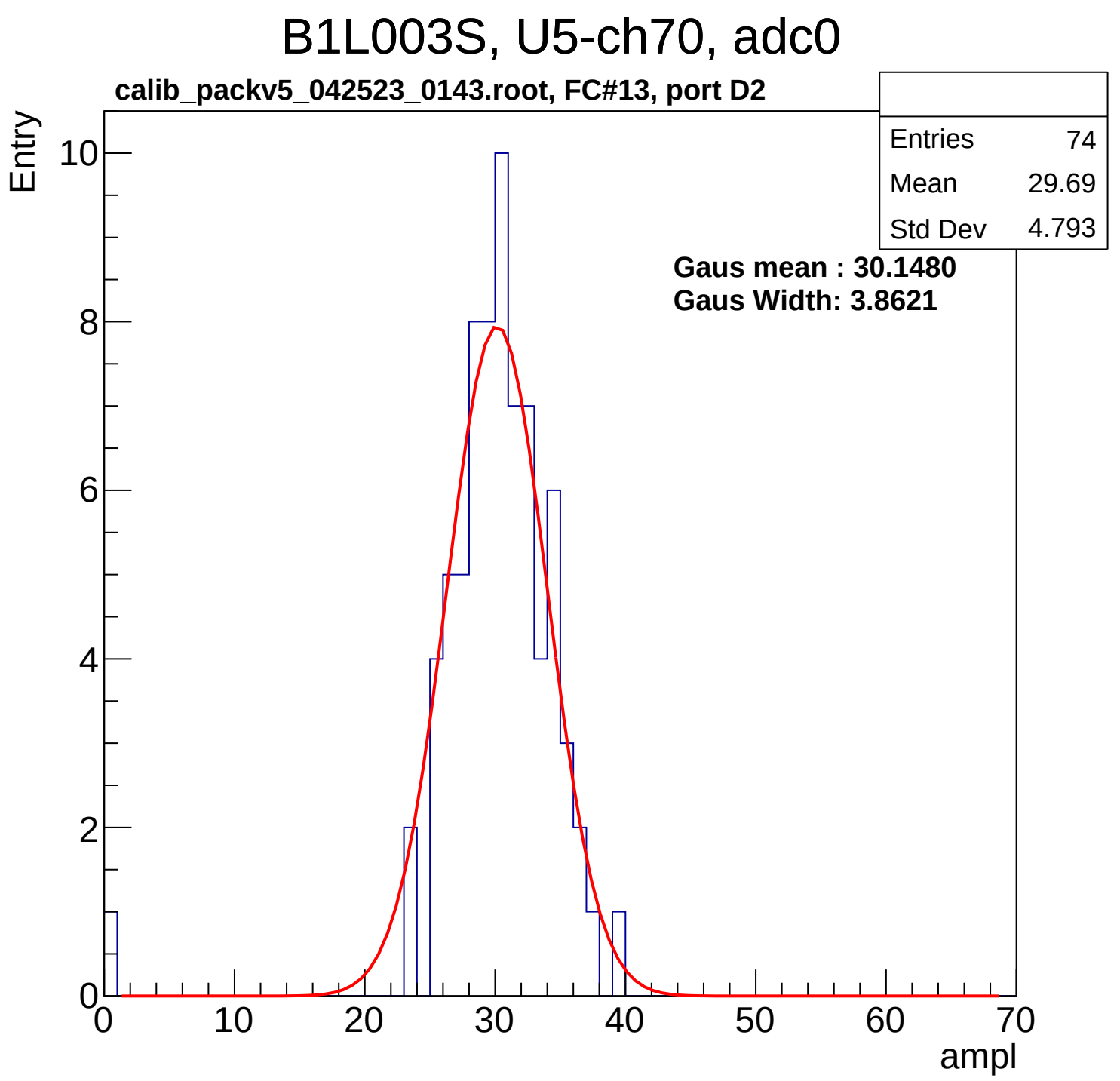
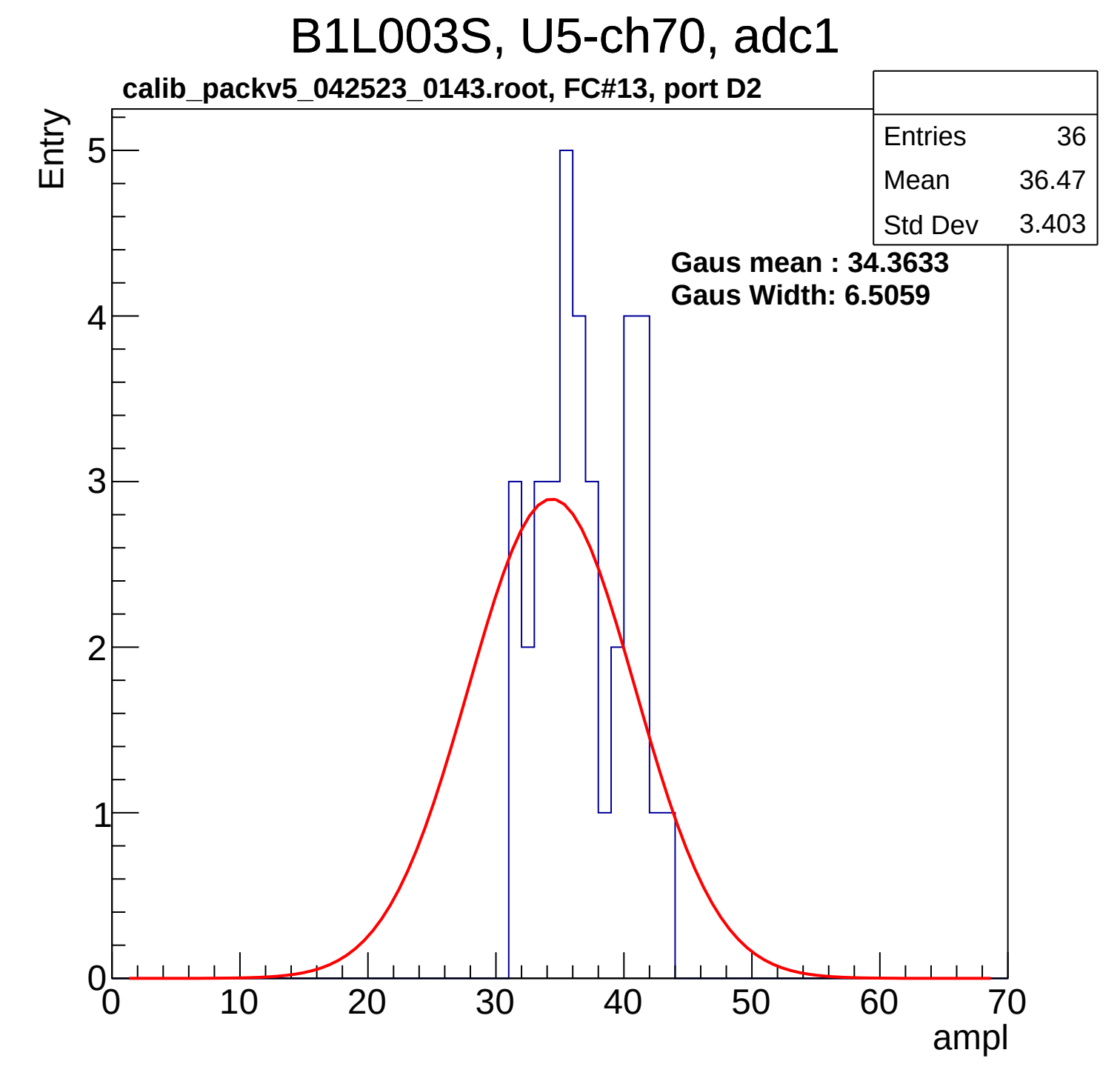
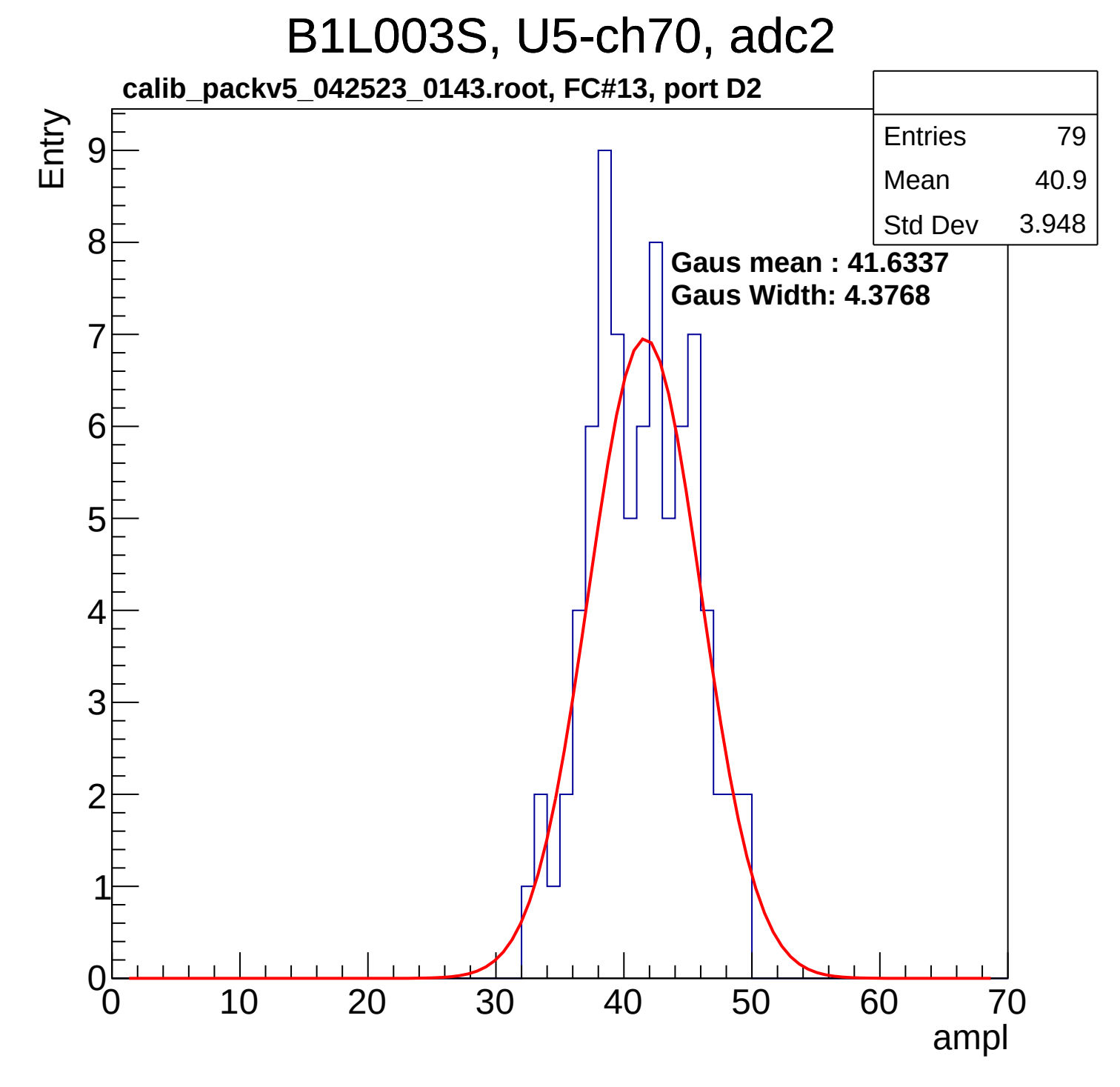

B1L003S, U5-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.8
Std Dev 1.376
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.69
Std Dev 4.793
Gaus mean : 30.1480
Gaus Width: 3.8621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 36.47
Std Dev 3.403
Gaus mean : 34.3633
Gaus Width: 6.5059
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 40.9
Std Dev 3.948
Gaus mean : 41.6337
Gaus Width: 4.3768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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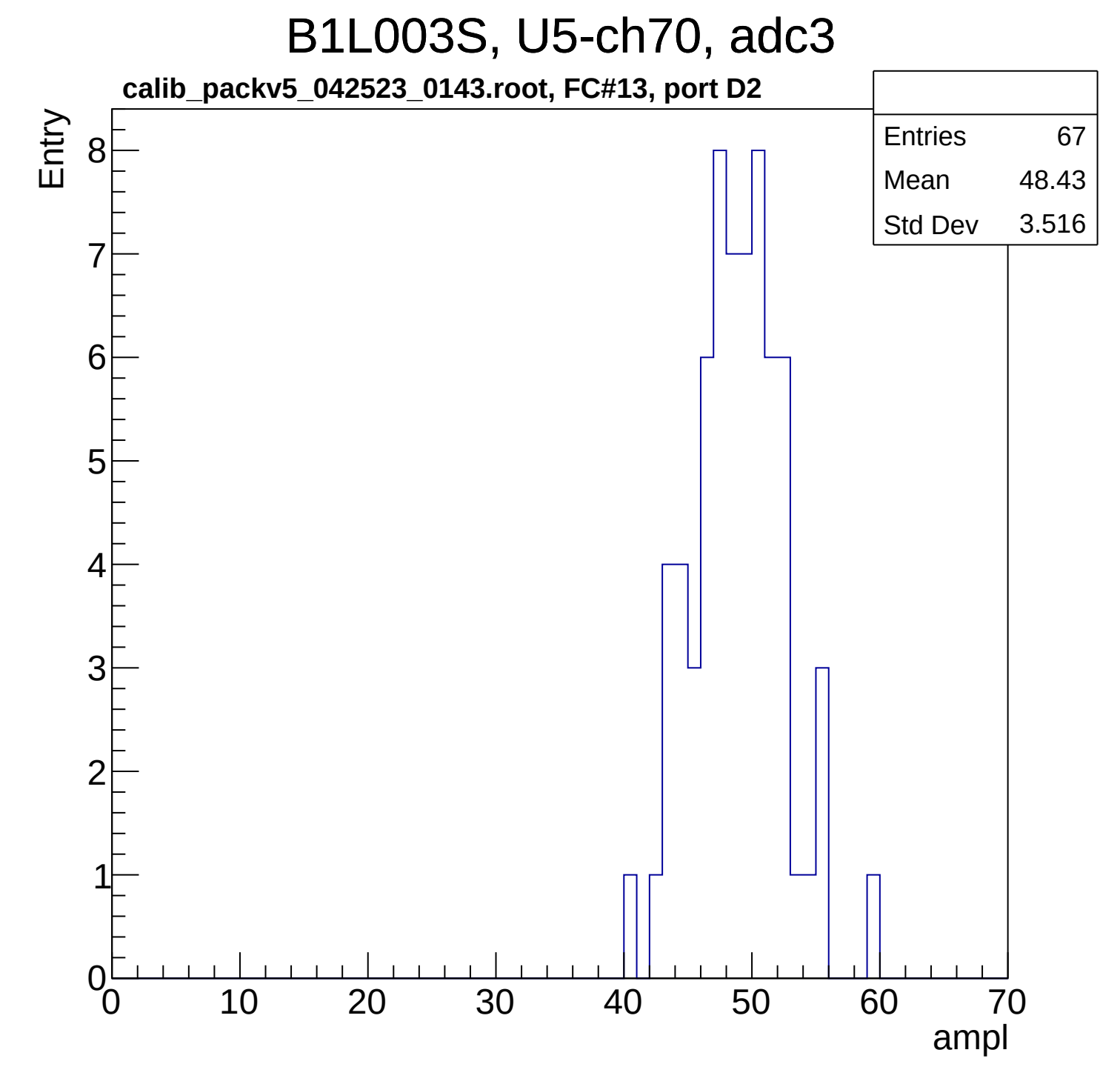
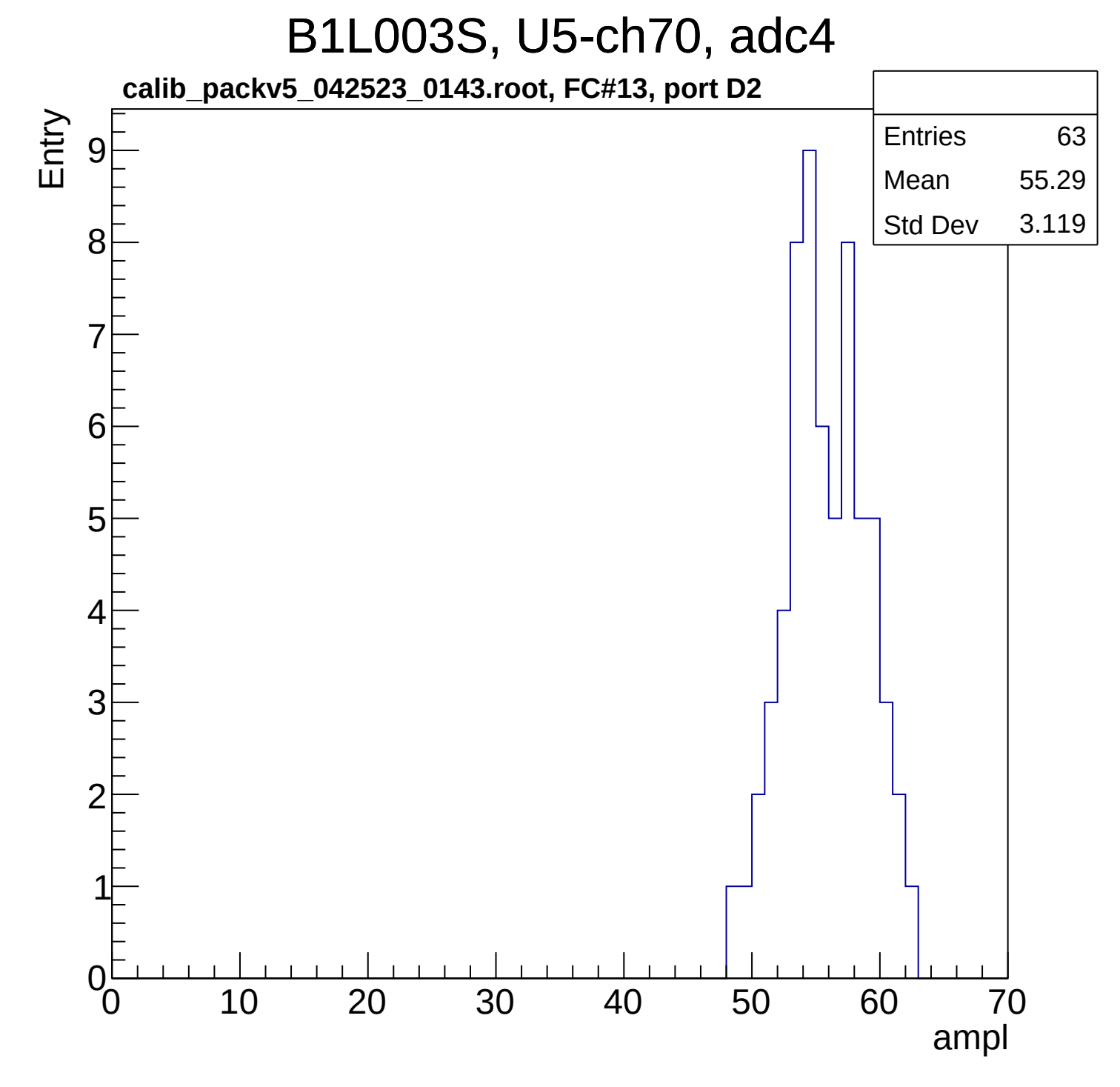
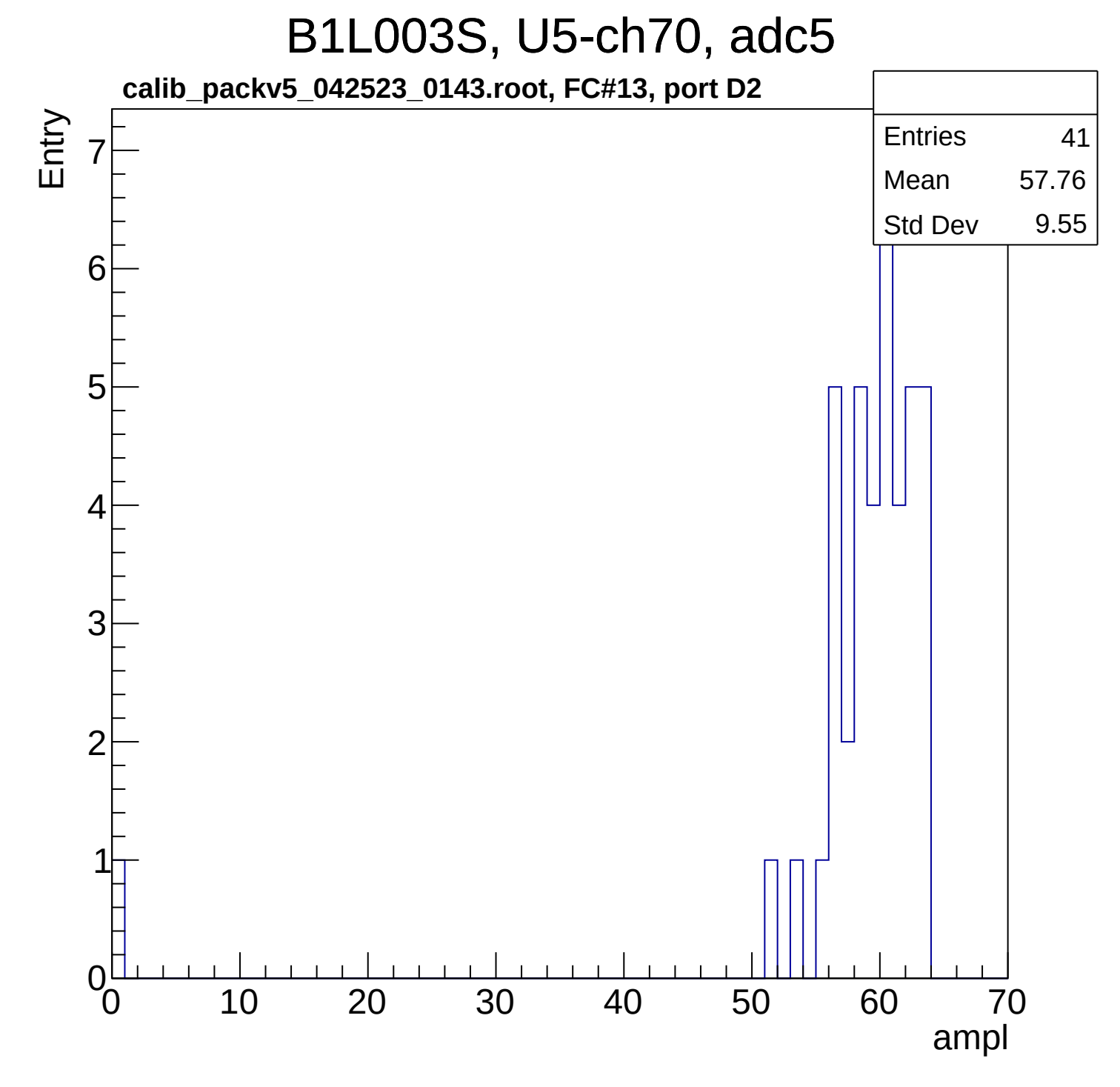

B1L003S, U5-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.43
Std Dev 3.516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.29
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 57.76
Std Dev 9.55
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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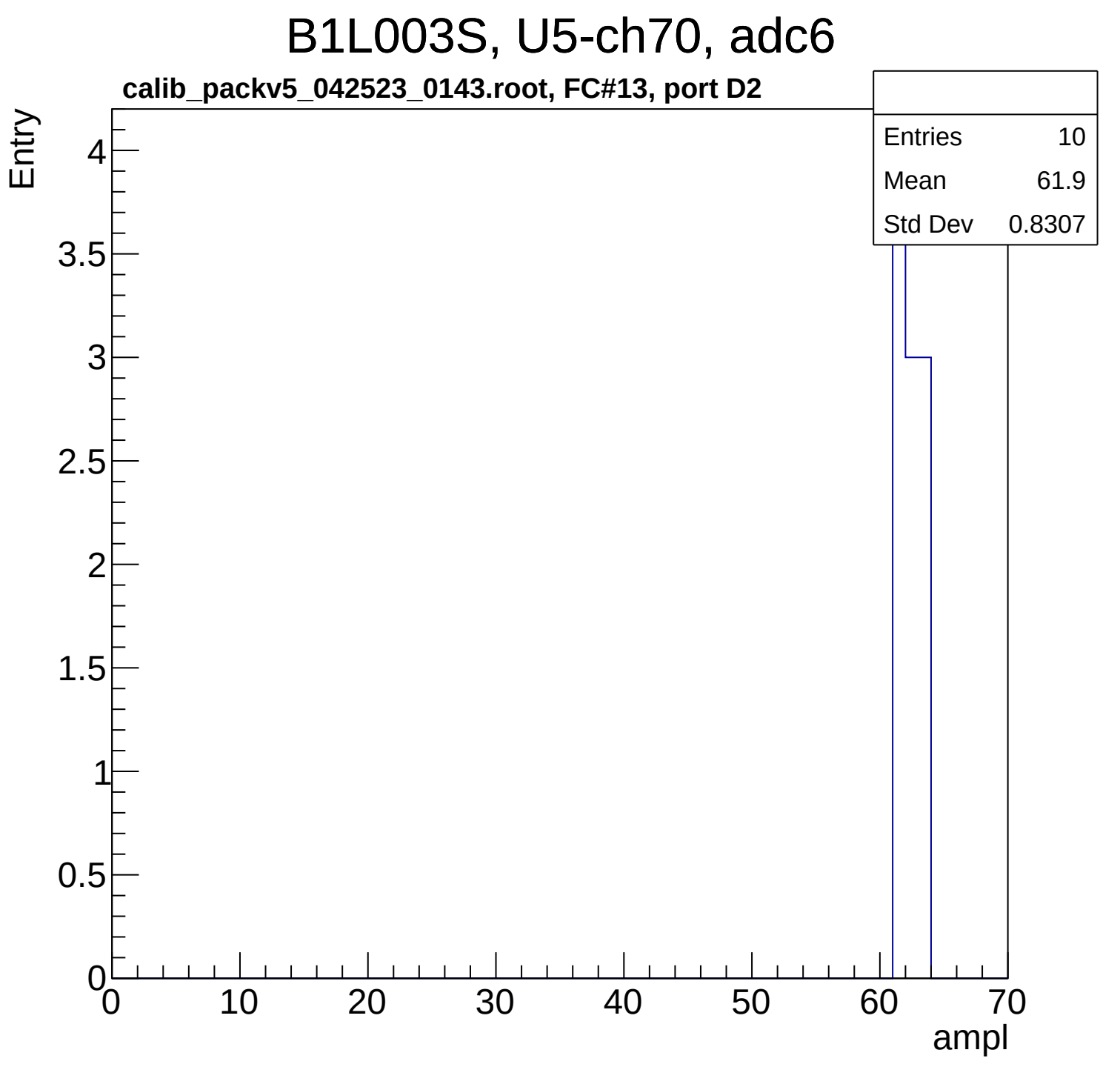
B1L003S, U5-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.9
Std Dev 0.8307
Entry
ampl
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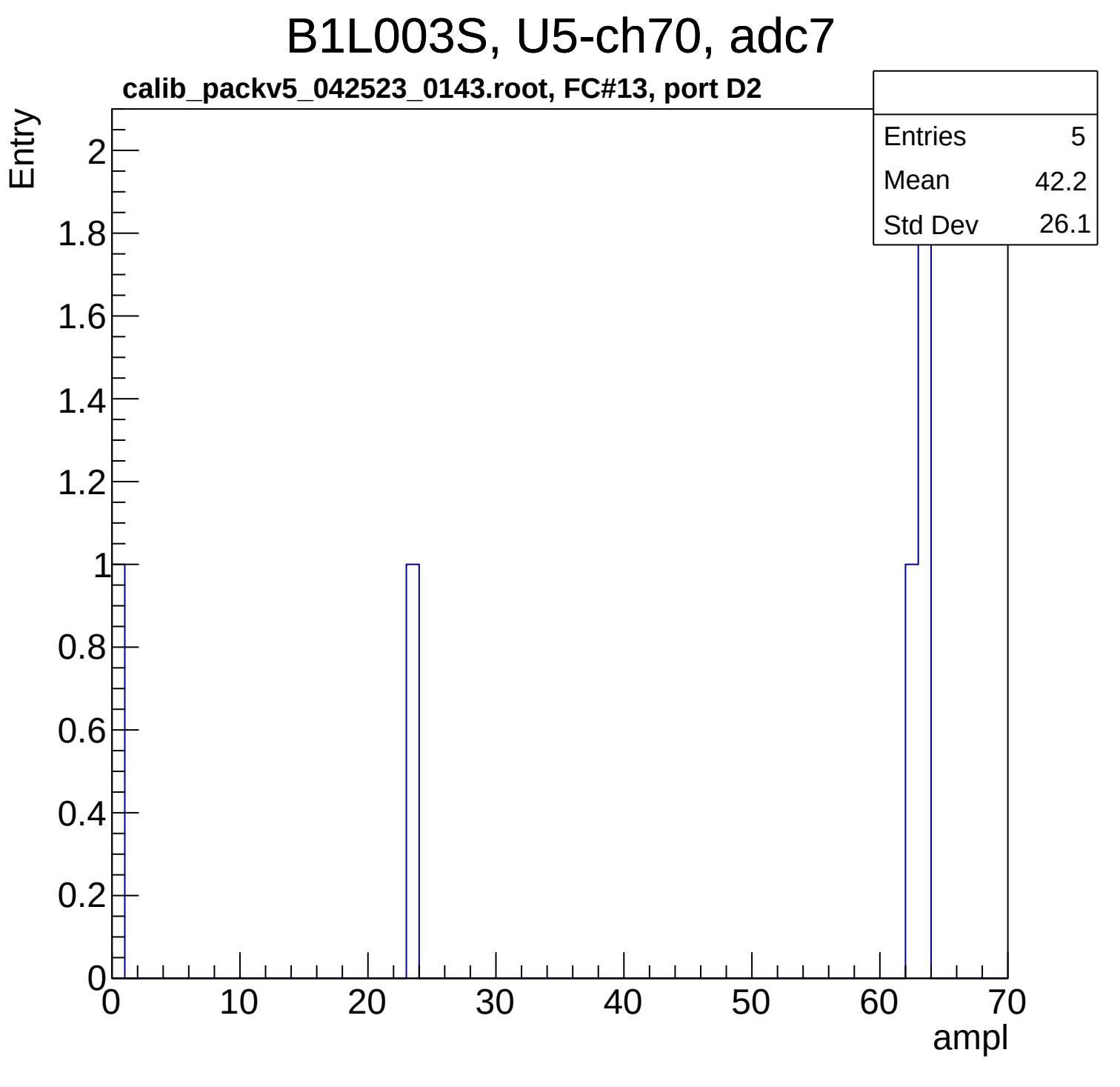
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 42.2
Std Dev 26.1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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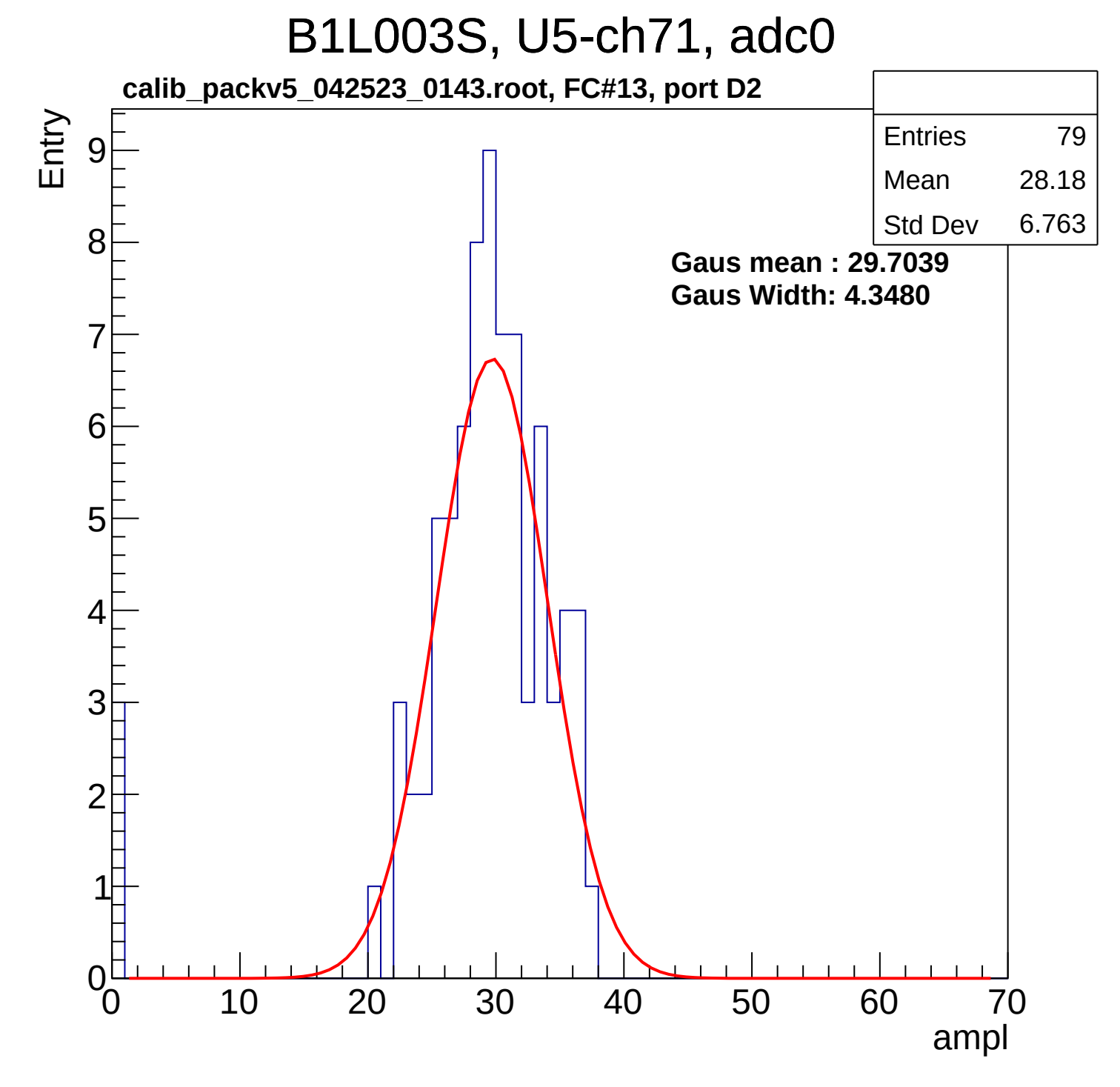
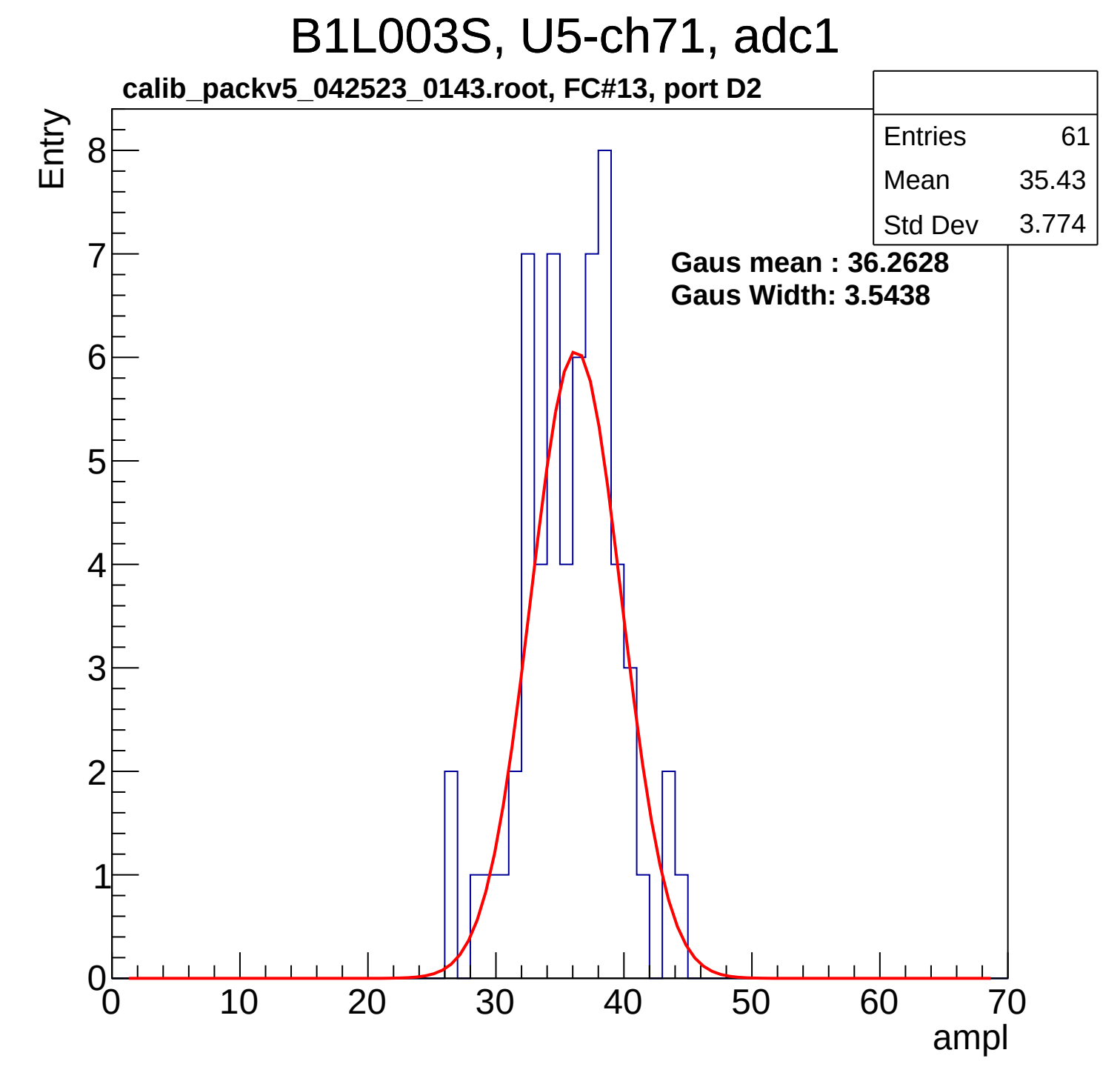

B1L003S, U5-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.18
Std Dev 6.763
Gaus mean : 29.7039
Gaus Width: 4.3480
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.43
Std Dev 3.774
Gaus mean : 36.2628
Gaus Width: 3.5438
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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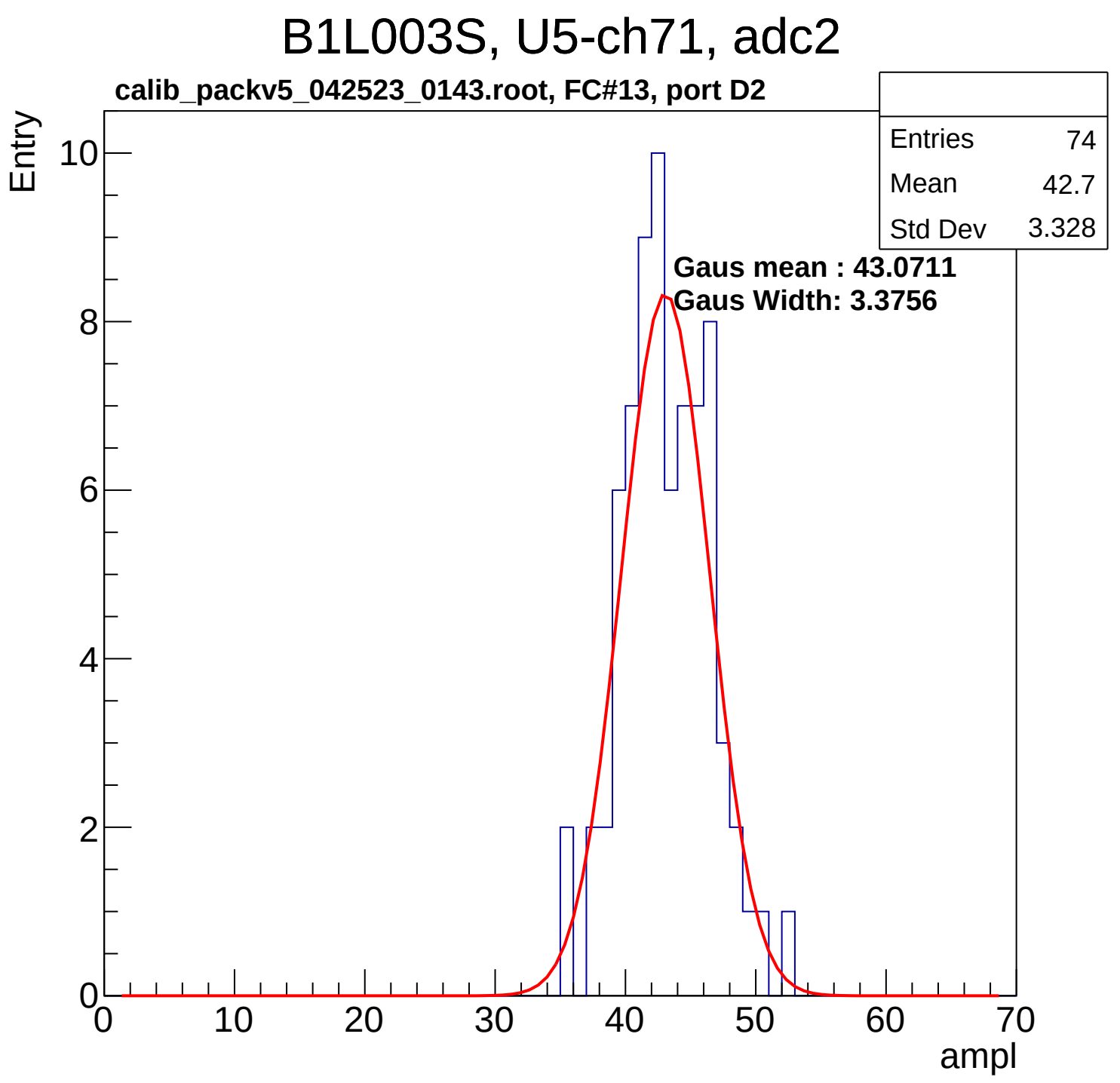
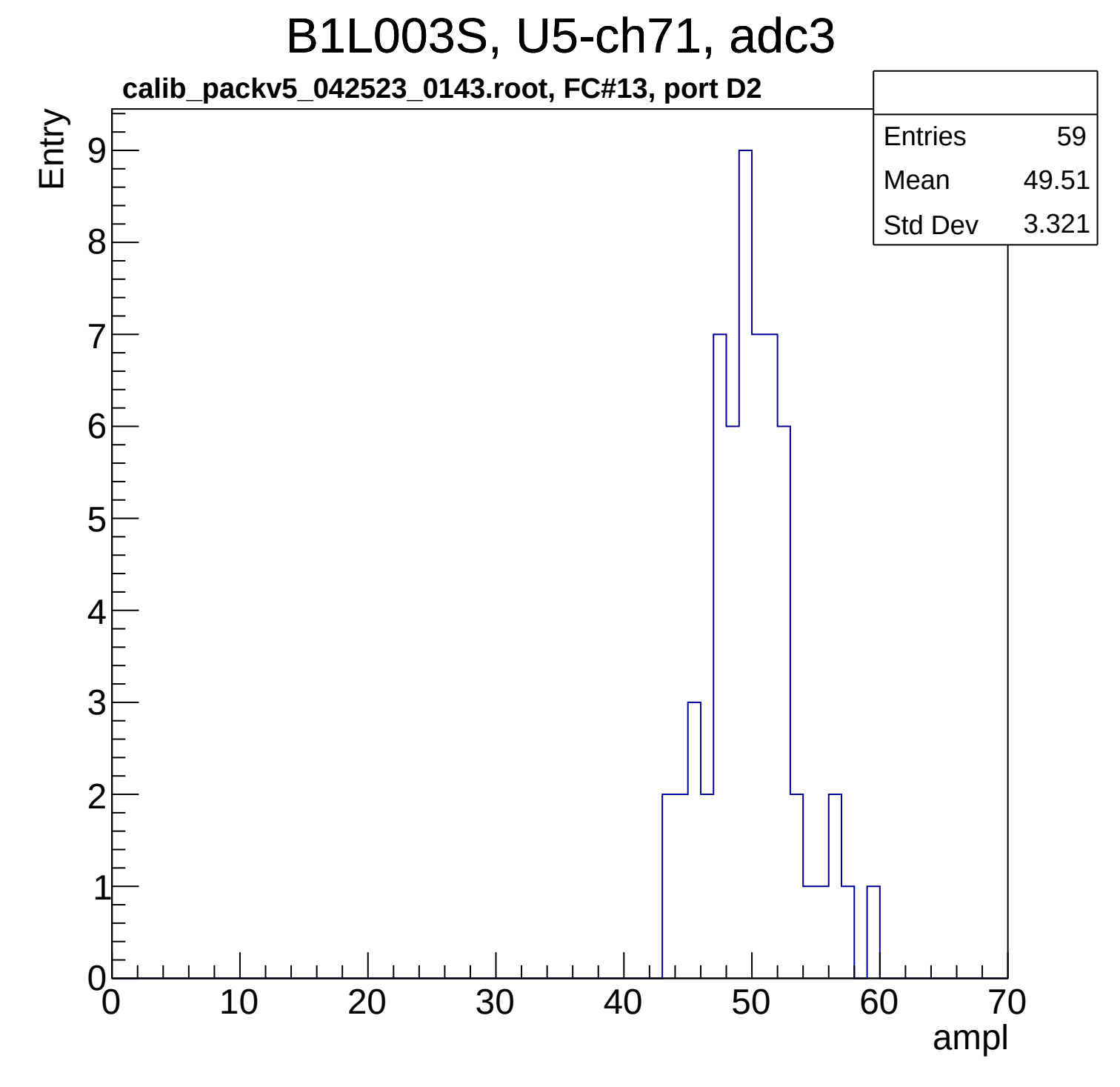

B1L003S, U5-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.7
Std Dev 3.328
Gaus mean : 43.0711
Gaus Width: 3.3756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.51
Std Dev 3.321
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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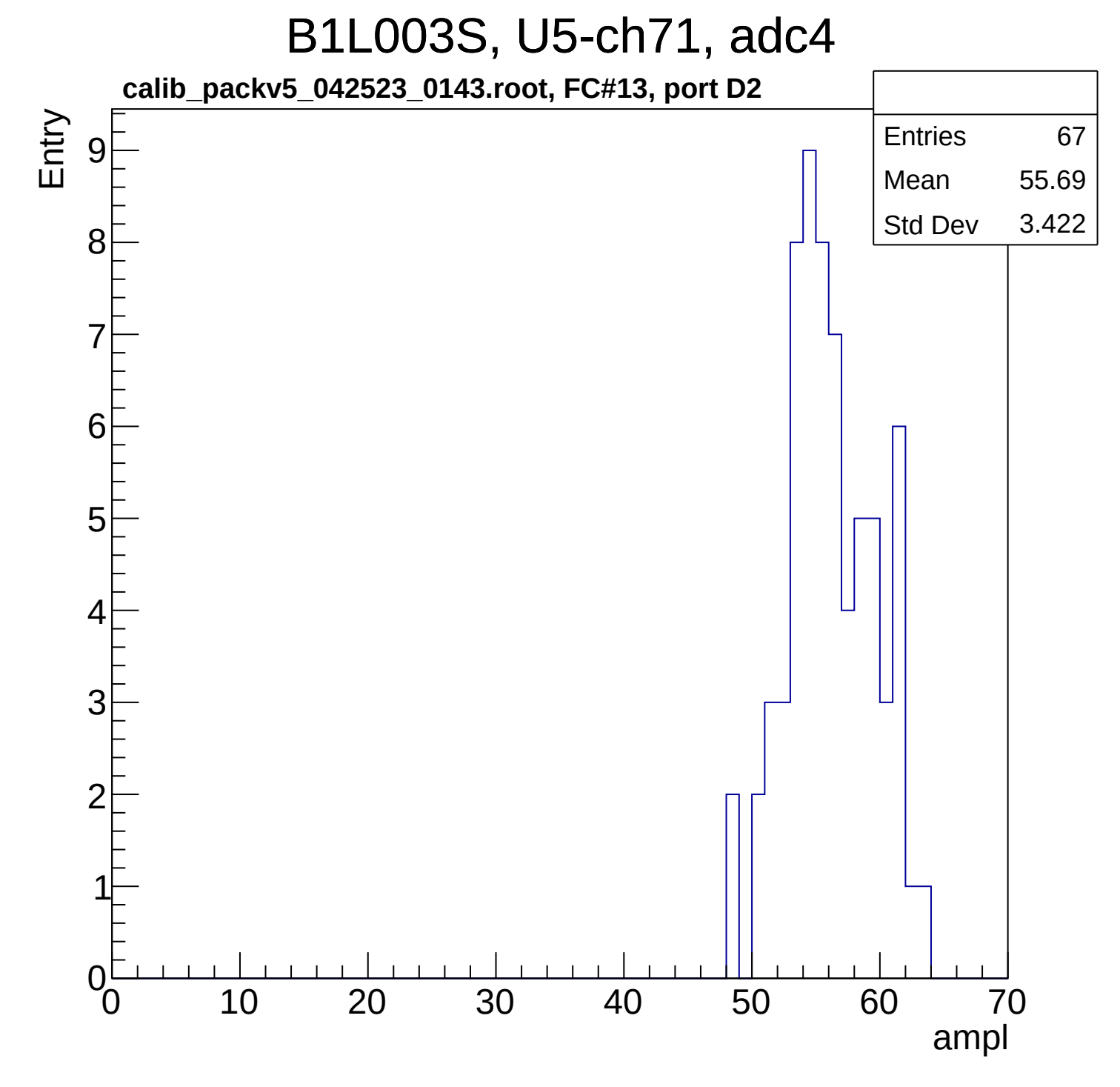
B1L003S, U5-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.69
Std Dev 3.422
Entry
ampl
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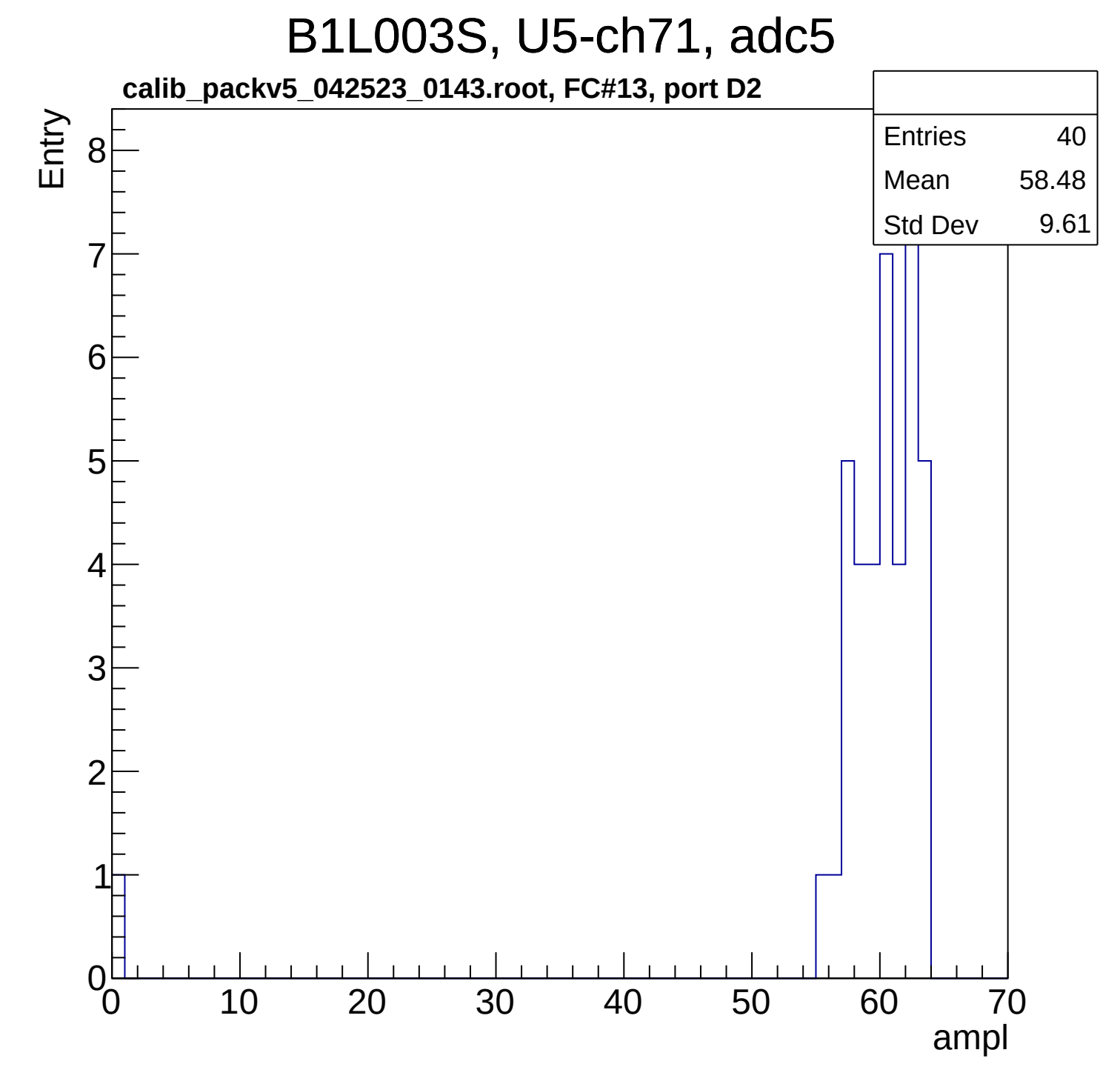
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.48
Std Dev 9.61
Entry
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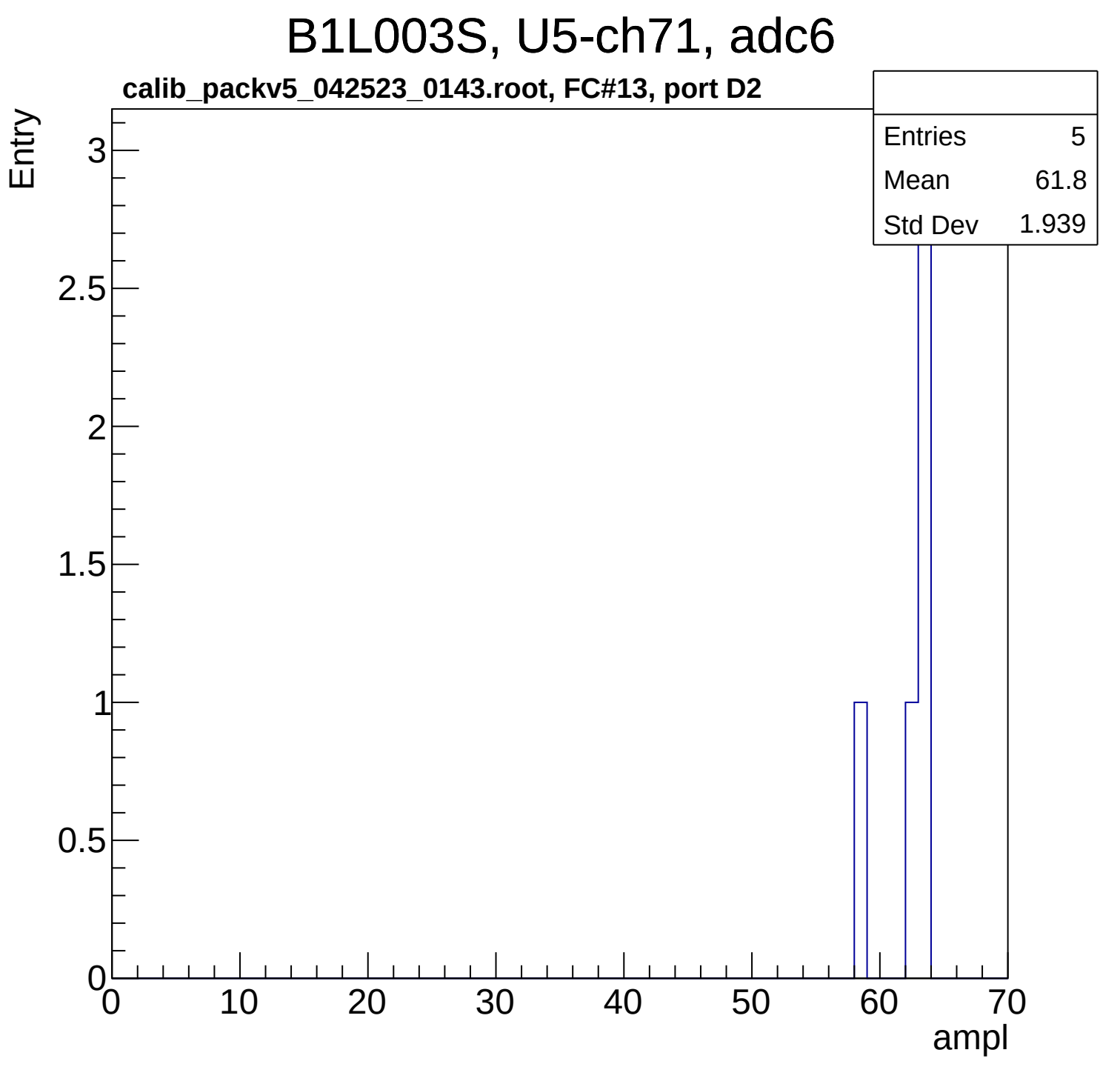
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.939
Entry
ampl
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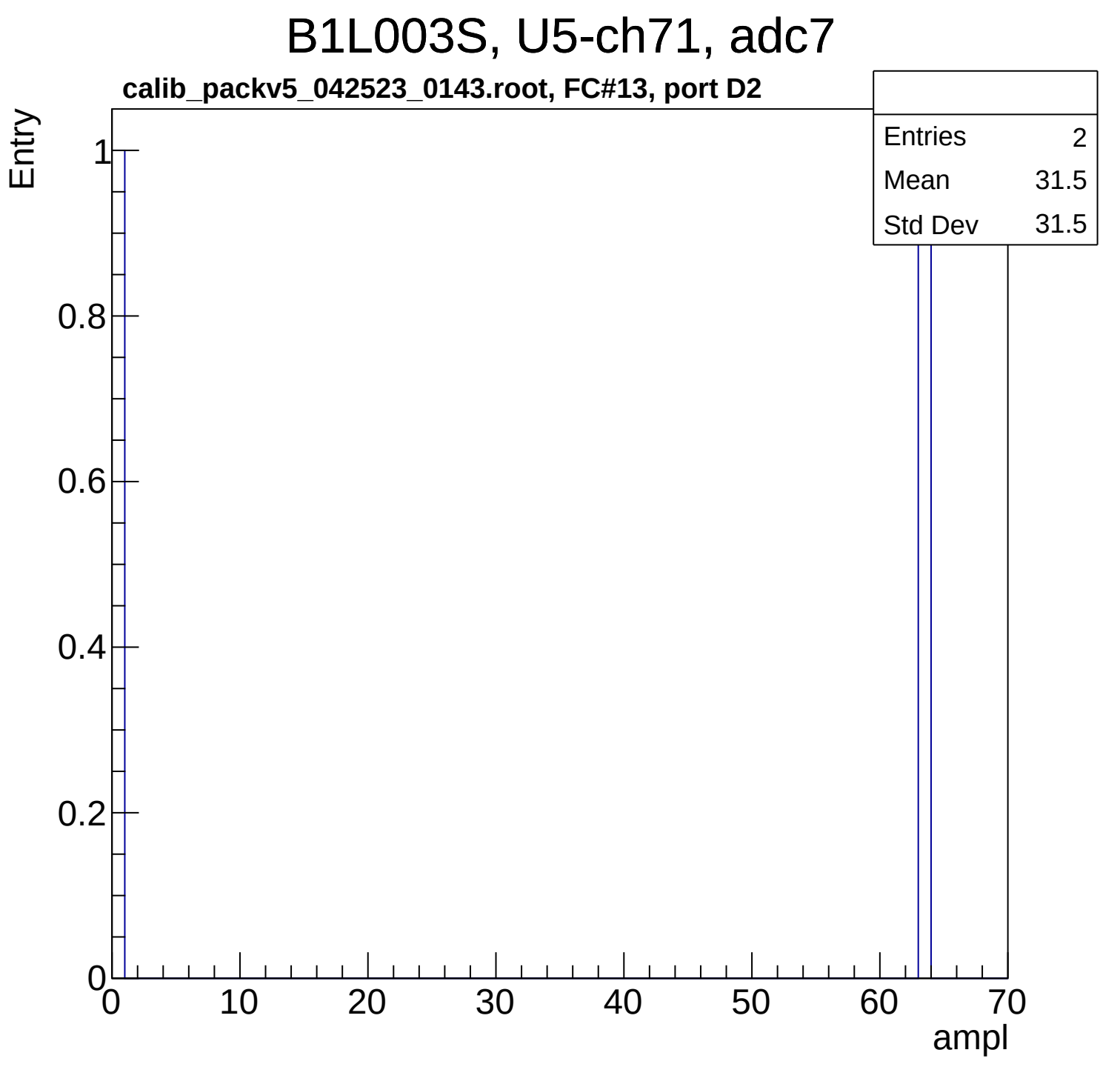
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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20
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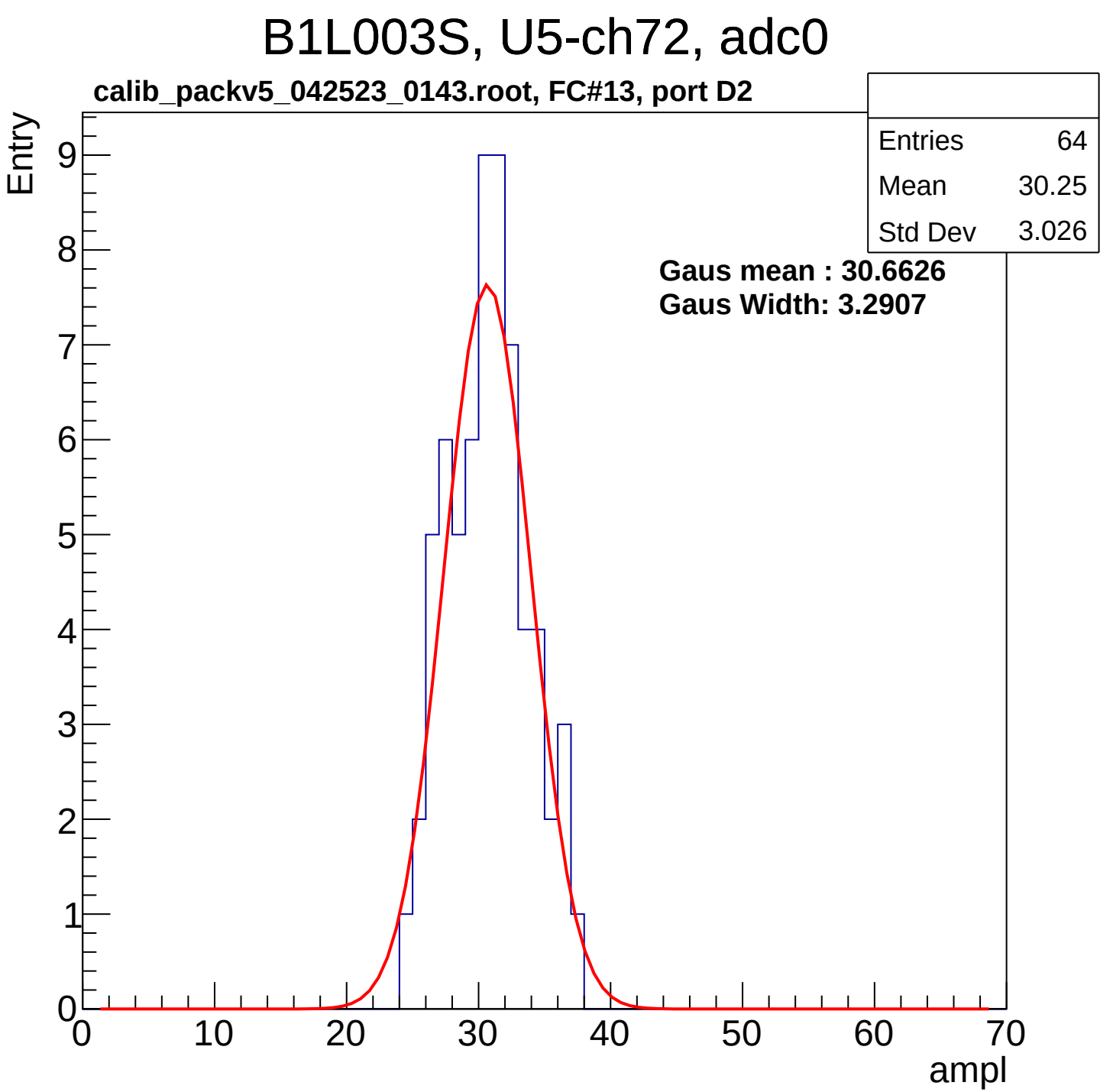
60
70
ampl


B1L003S, U5-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 30.25
Std Dev 3.026
Gaus mean : 30.6626
Gaus Width: 3.2907
Entry
ampl
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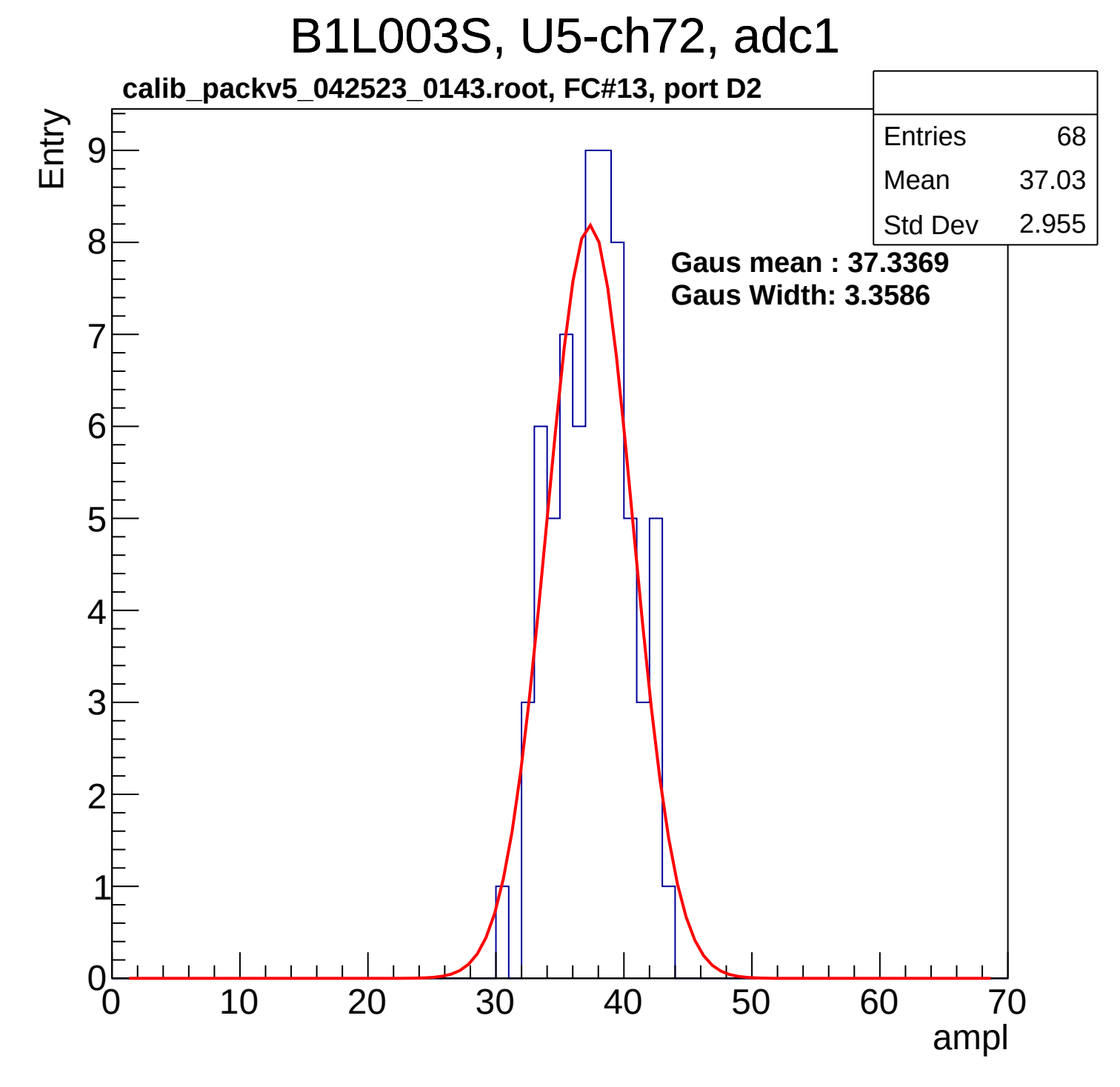
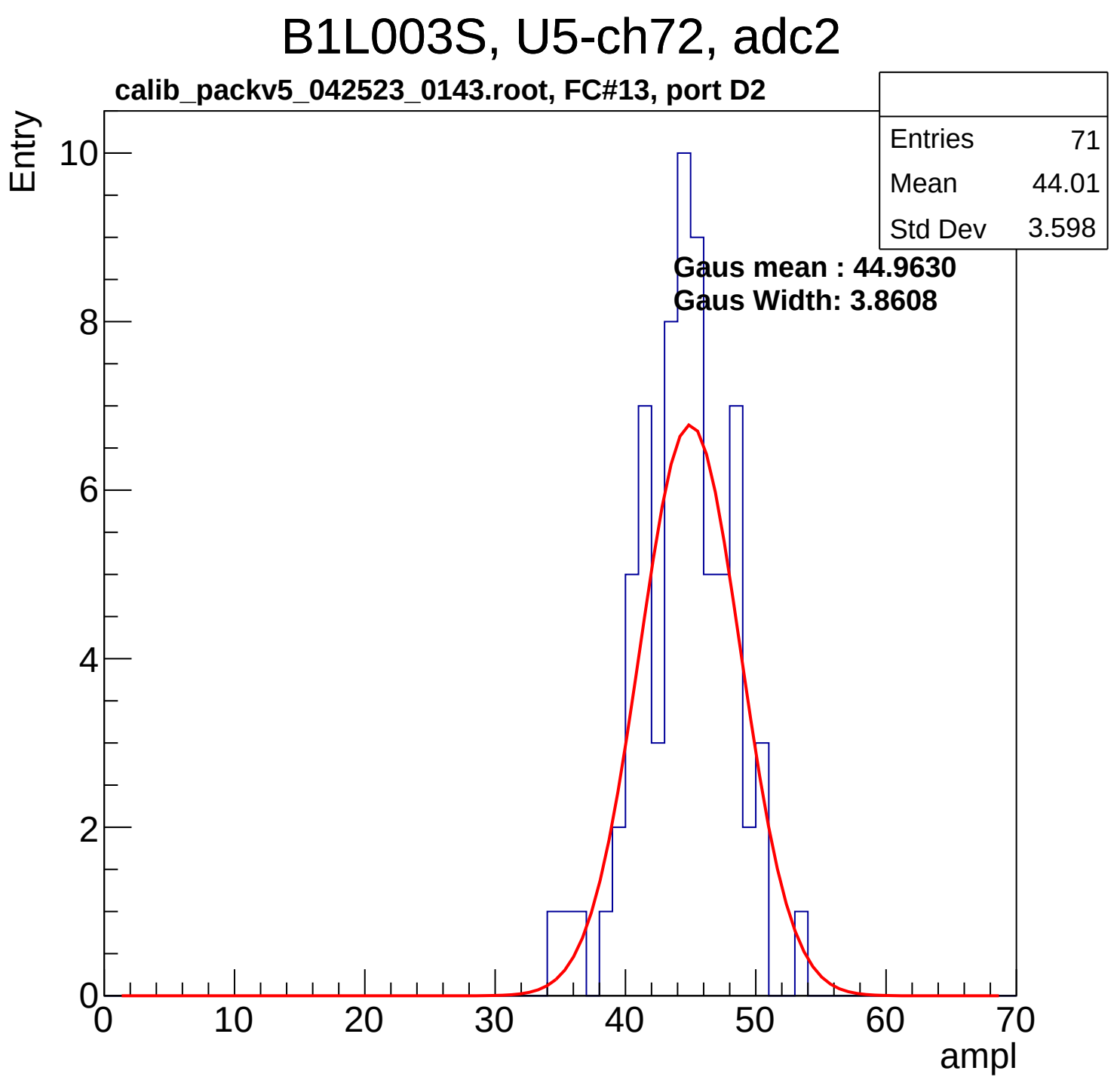
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 37.03
Std Dev 2.955
Gaus mean : 37.3369
Gaus Width: 3.3586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 44.01
Std Dev 3.598
Gaus mean : 44.9630
Gaus Width: 3.8608
Entry
ampl
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6
8
10
0
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40
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60
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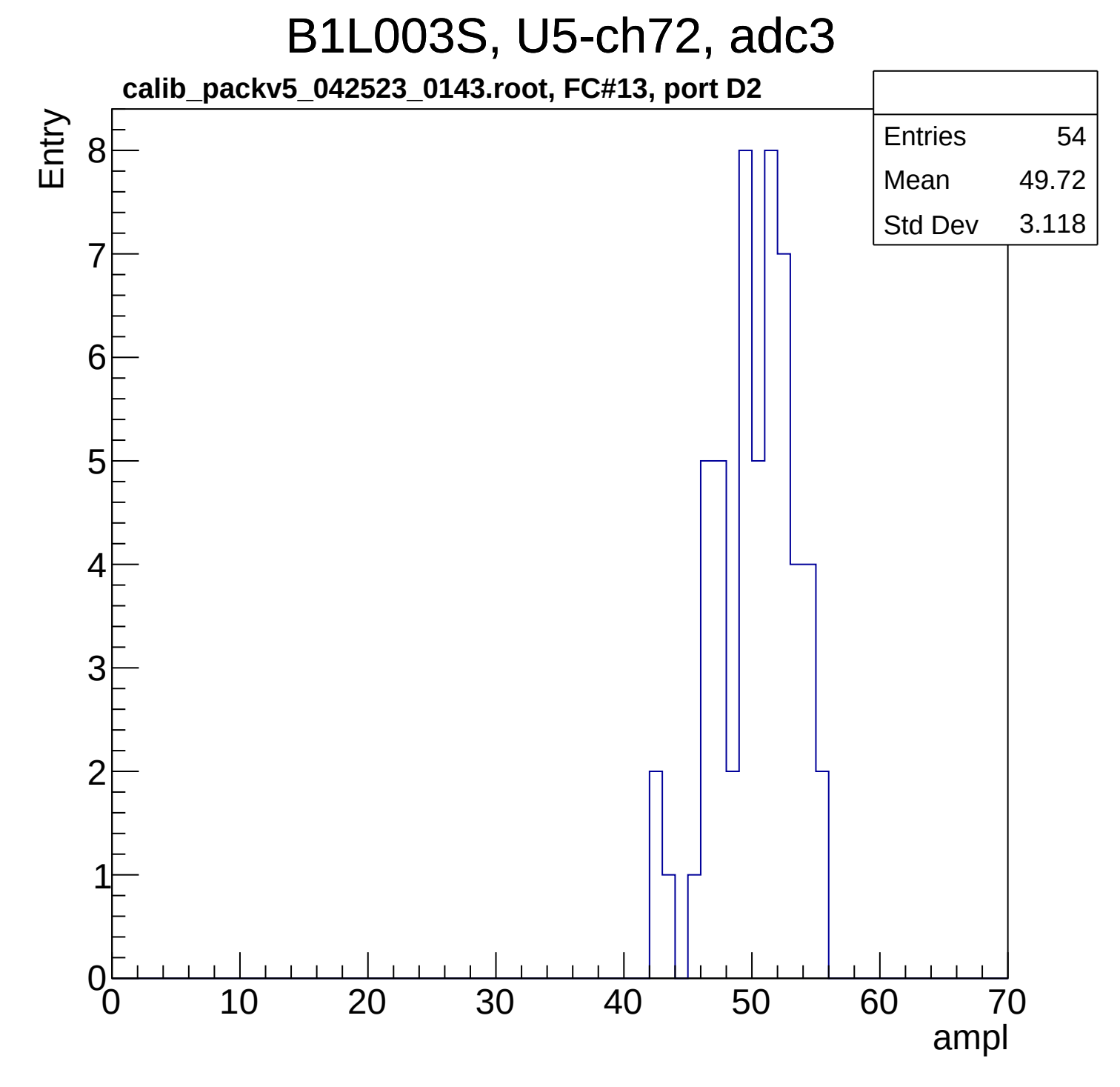
B1L003S, U5-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.72
Std Dev 3.118
Entry
ampl
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3
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7
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60
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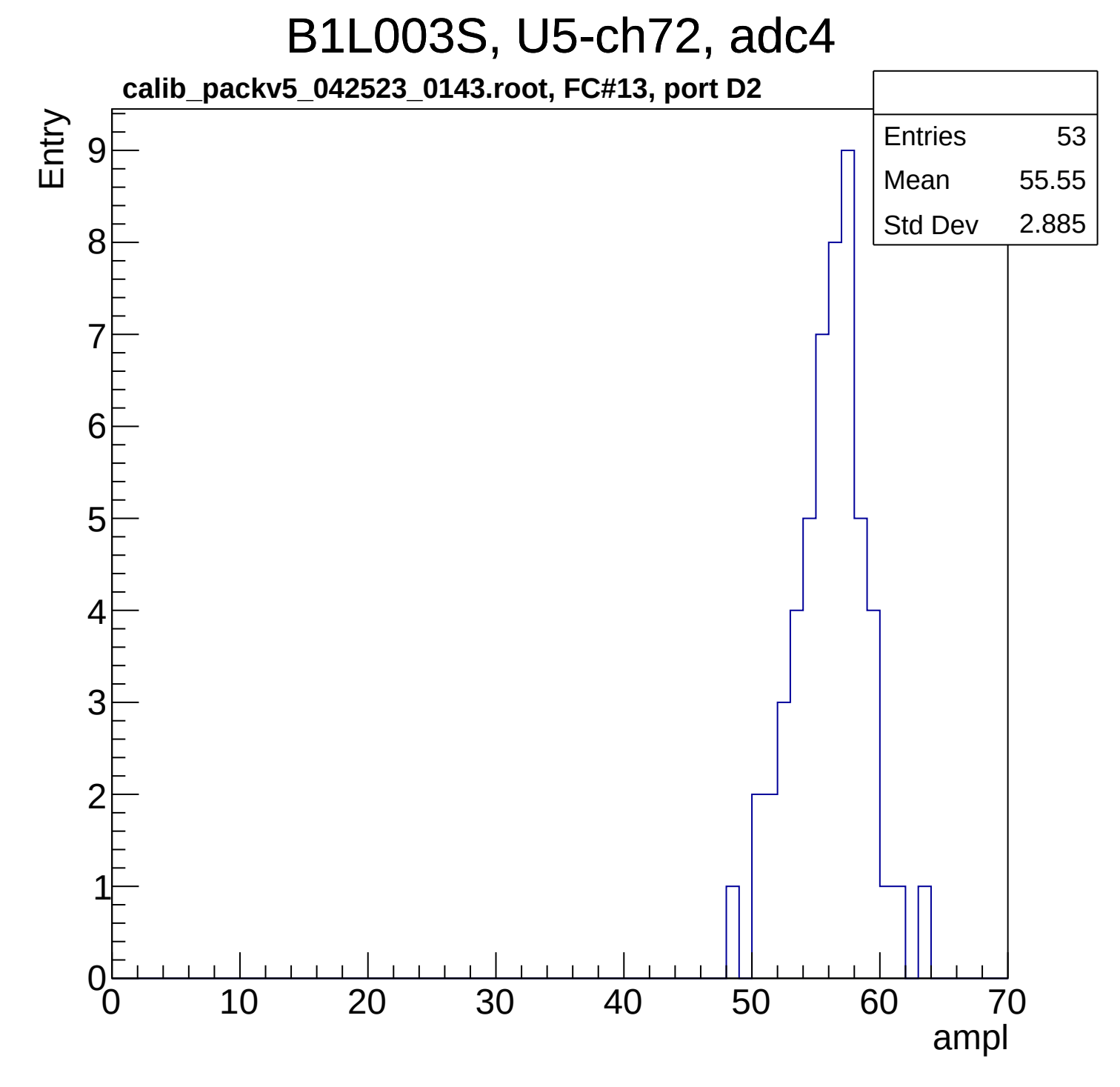
B1L003S, U5-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.55
Std Dev 2.885
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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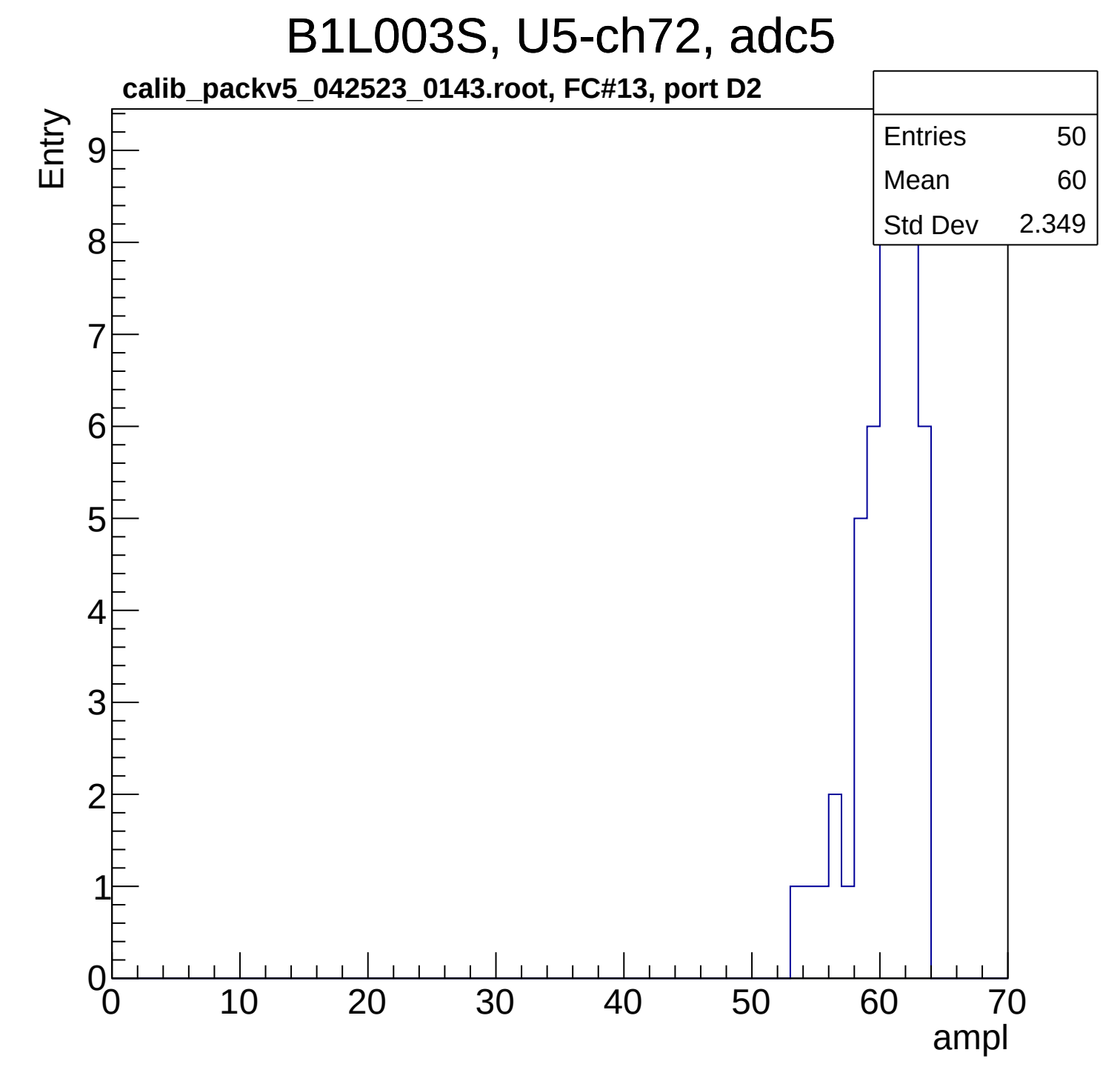
B1L003S, U5-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 60
Std Dev 2.349
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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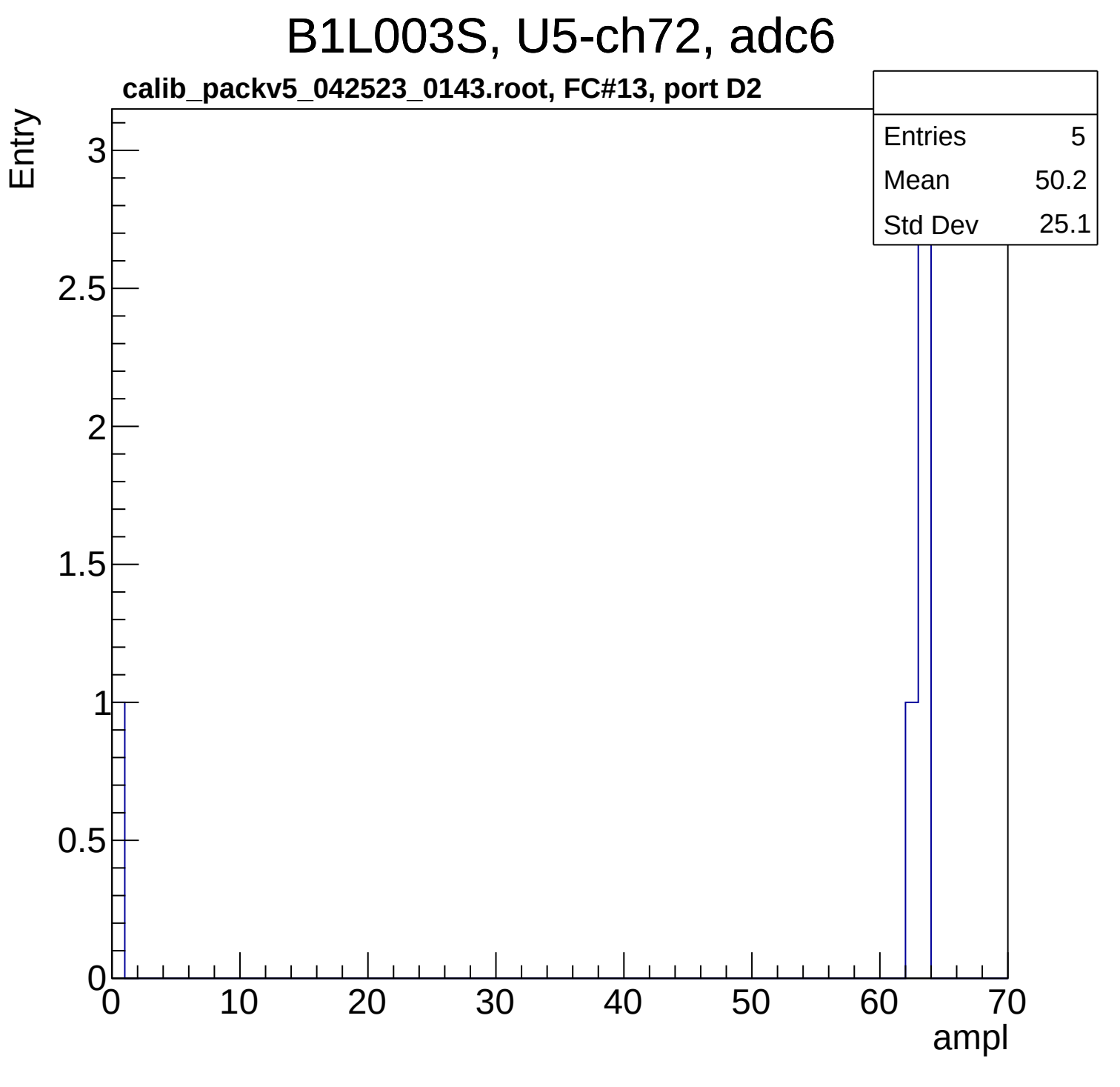
B1L003S, U5-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
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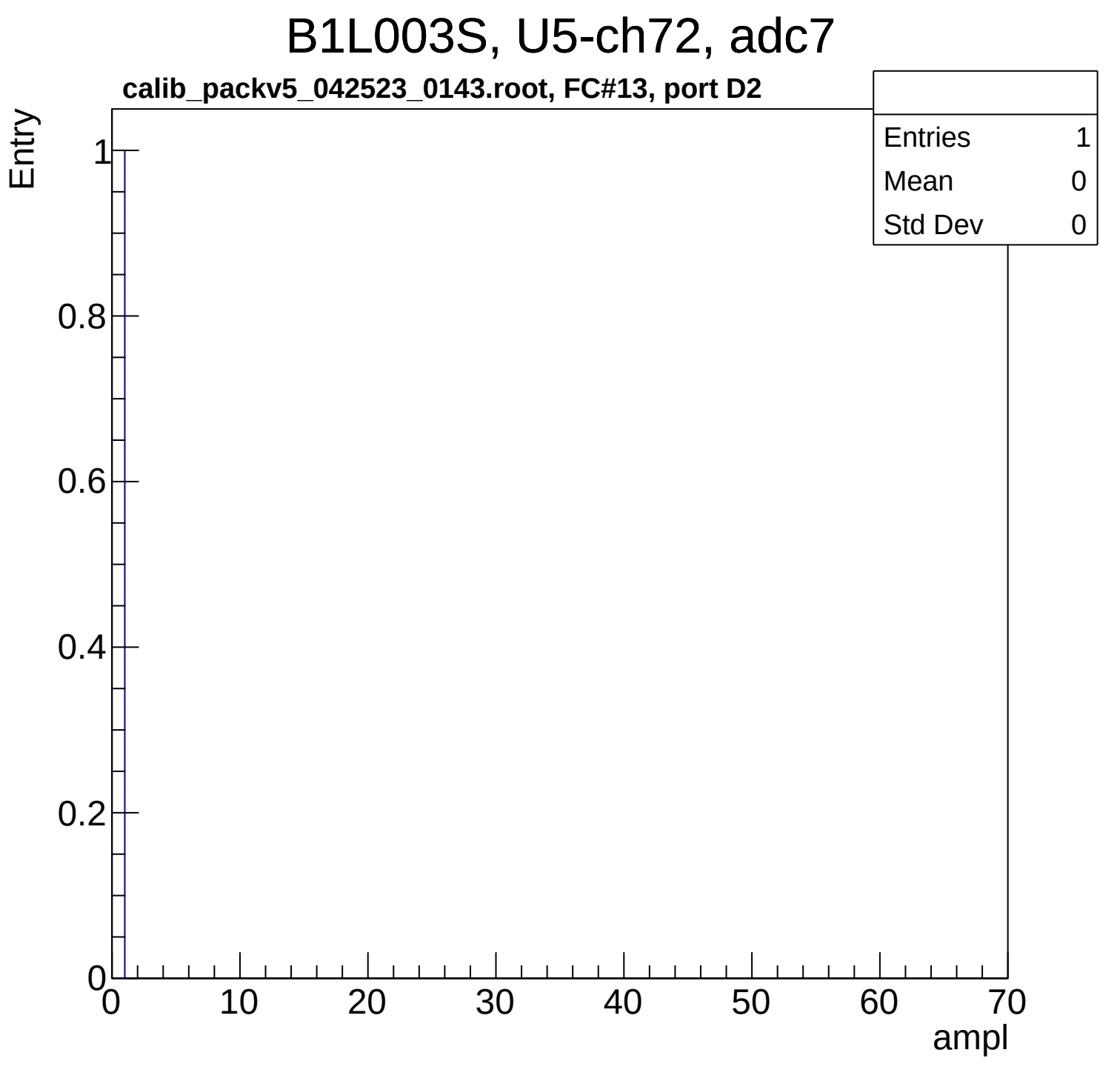
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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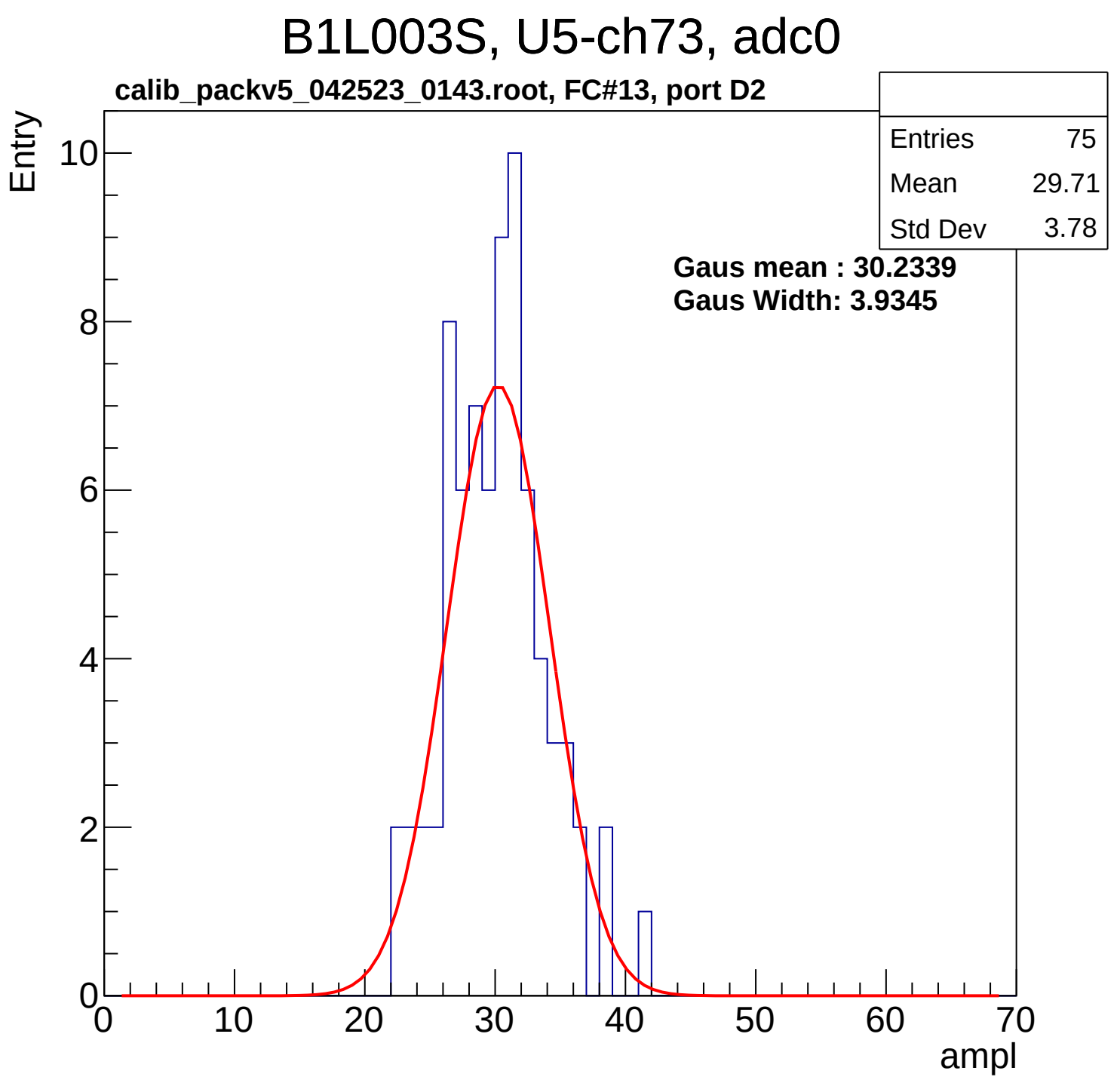
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.71
Std Dev 3.78
Gaus mean : 30.2339
Gaus Width: 3.9345
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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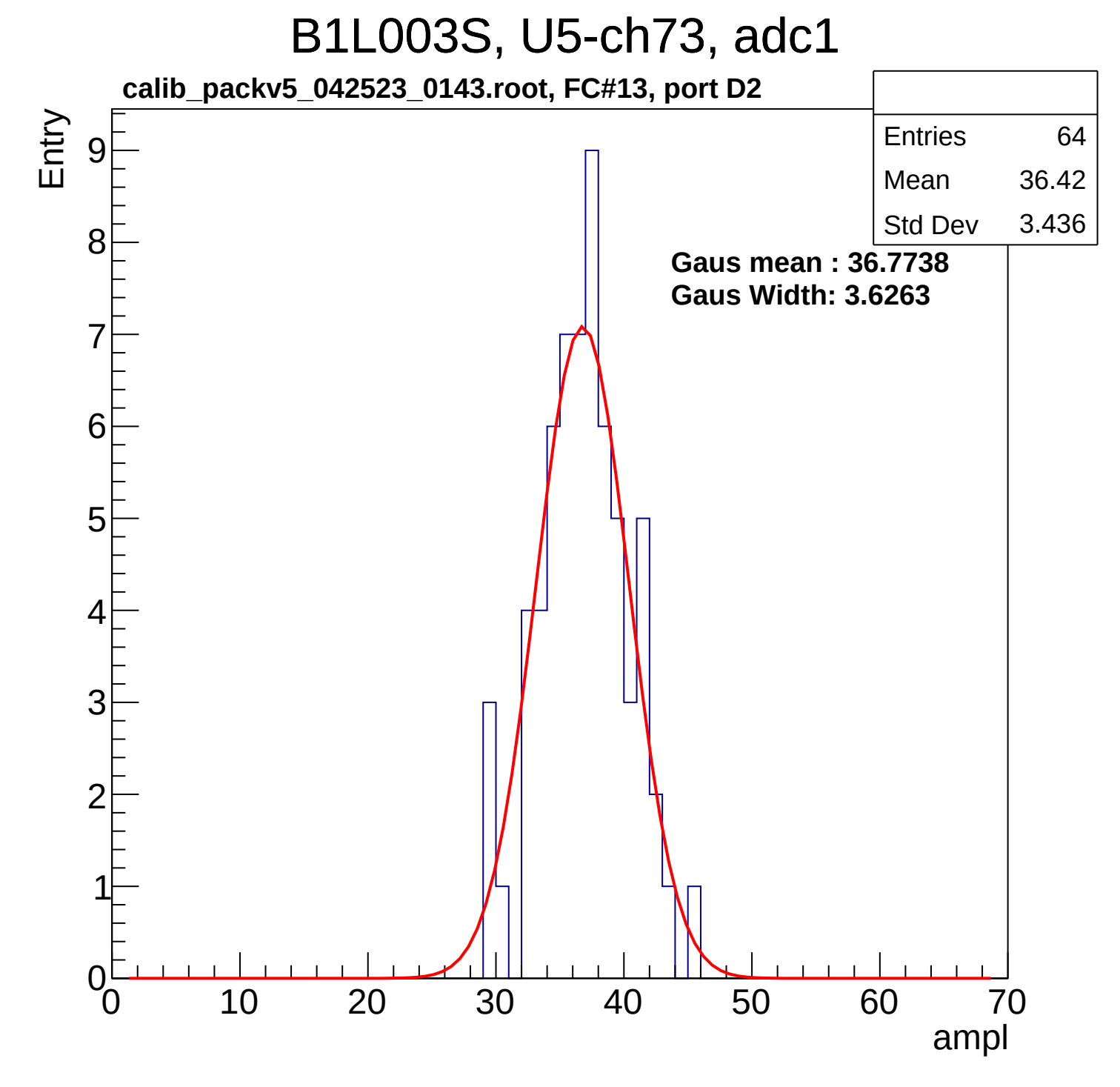
B1L003S, U5-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.42
Std Dev 3.436
Gaus mean : 36.7738
Gaus Width: 3.6263
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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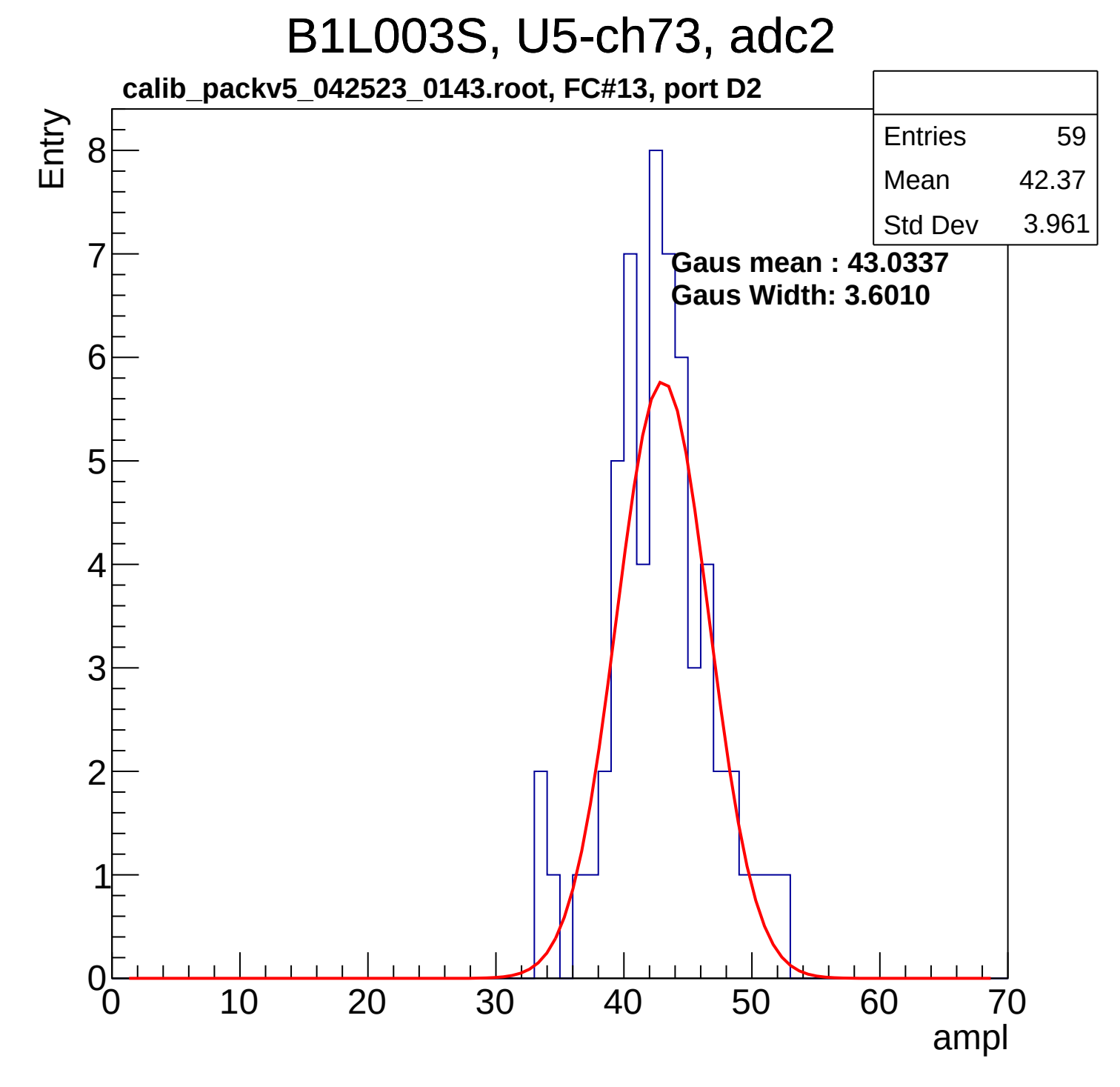
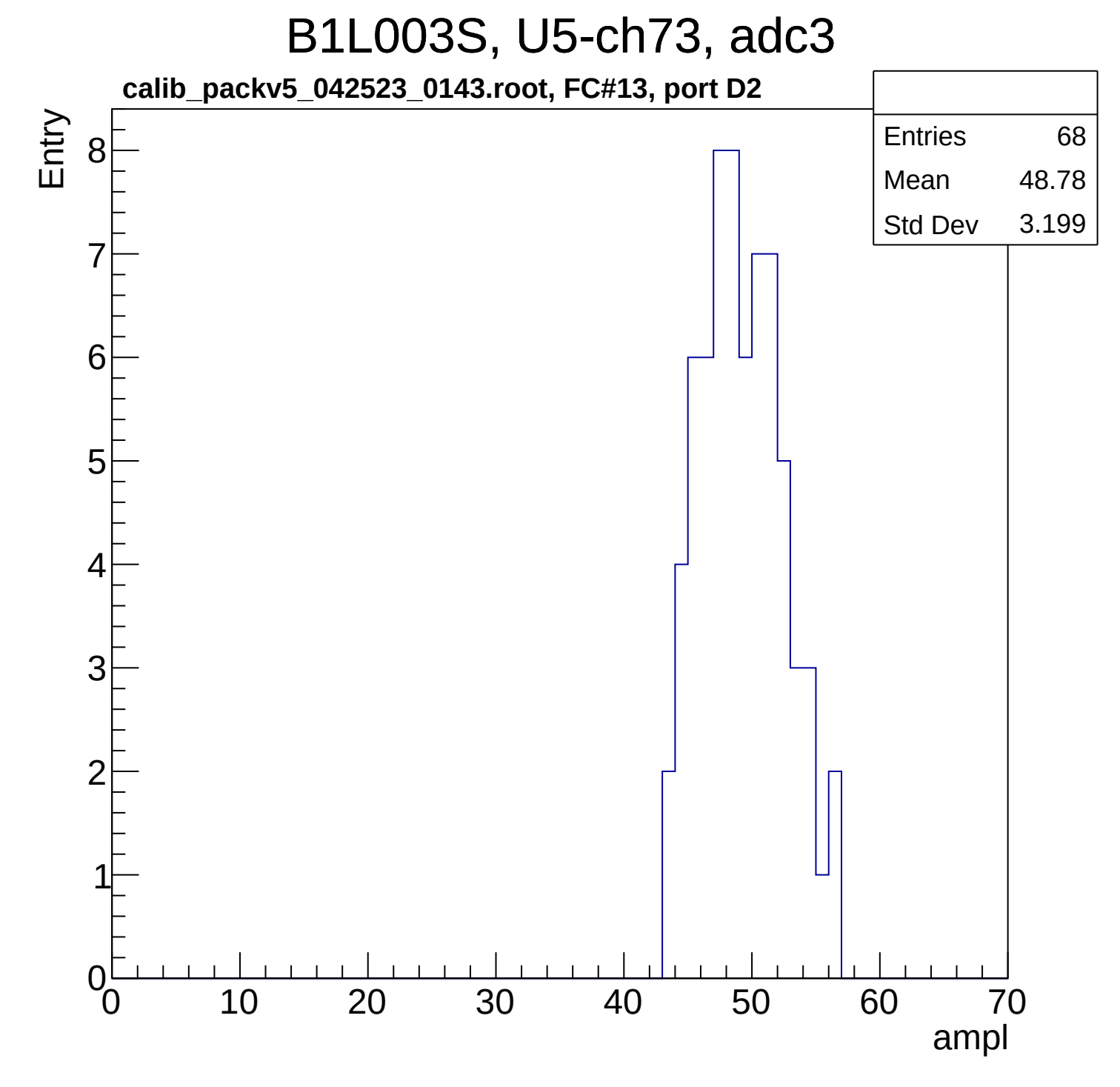

B1L003S, U5-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.37
Std Dev 3.961
Gaus mean : 43.0337
Gaus Width: 3.6010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.78
Std Dev 3.199
Entry
ampl
0
1
2
3
4
5
6
7
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60
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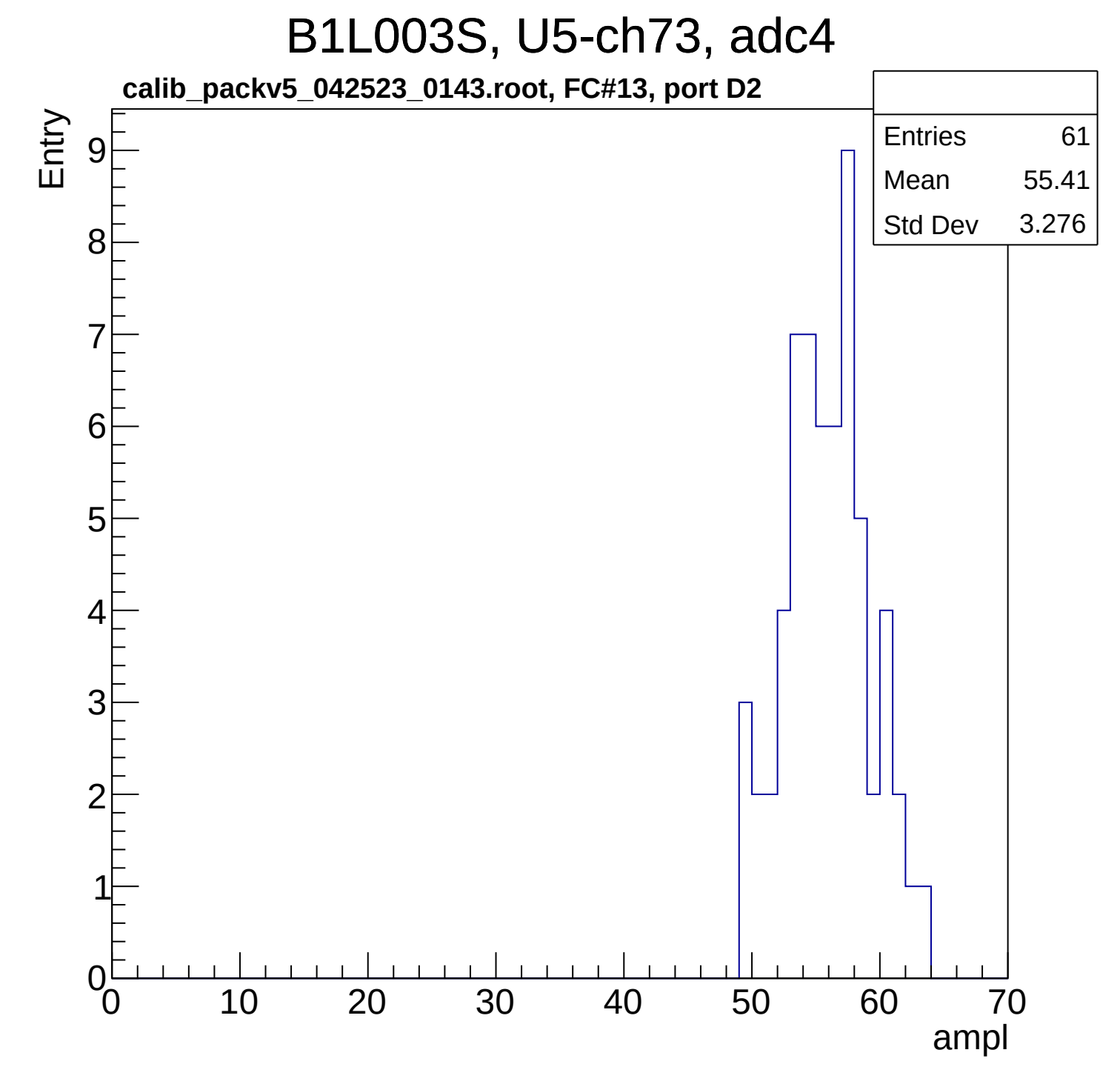
B1L003S, U5-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.41
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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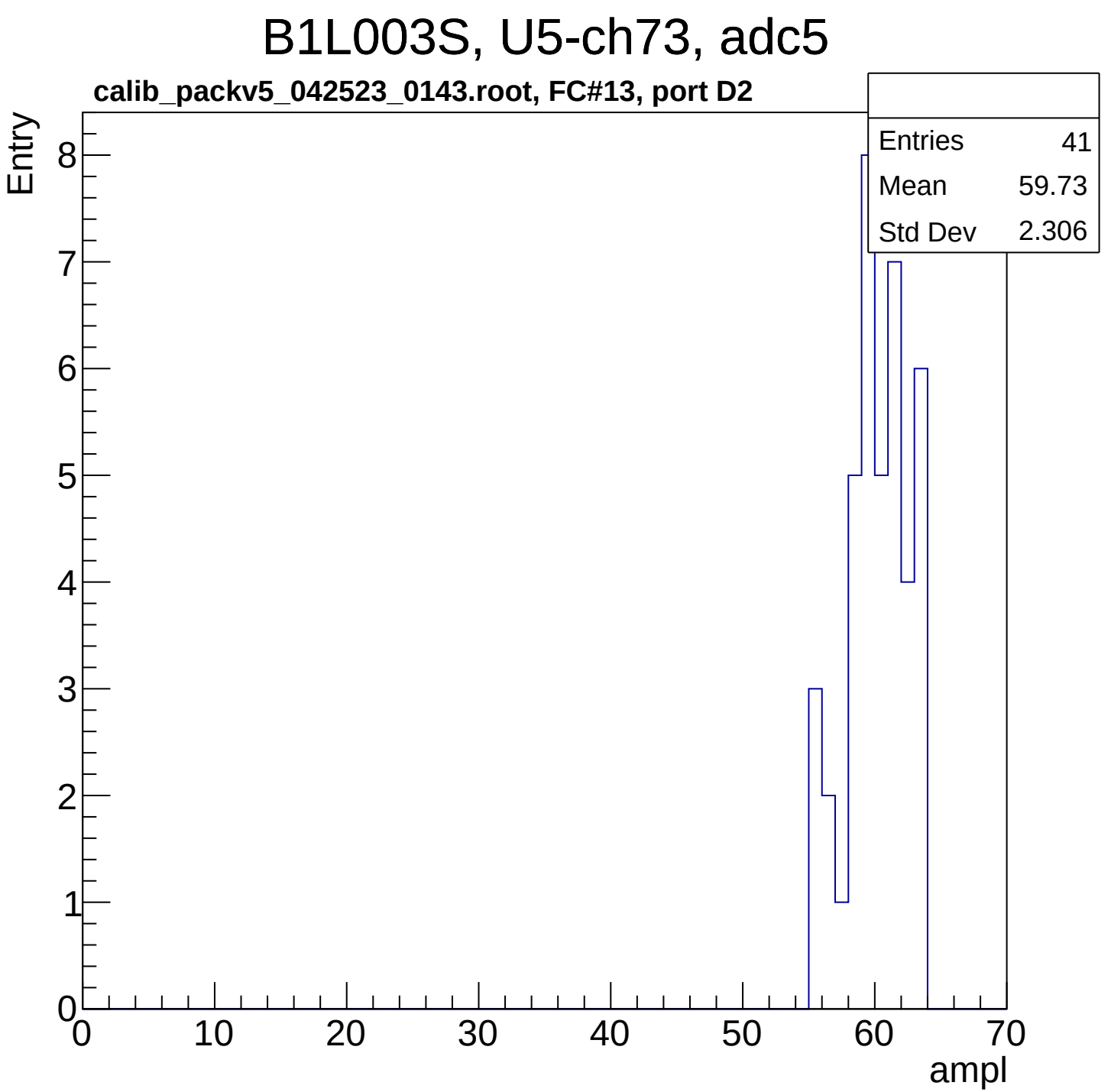
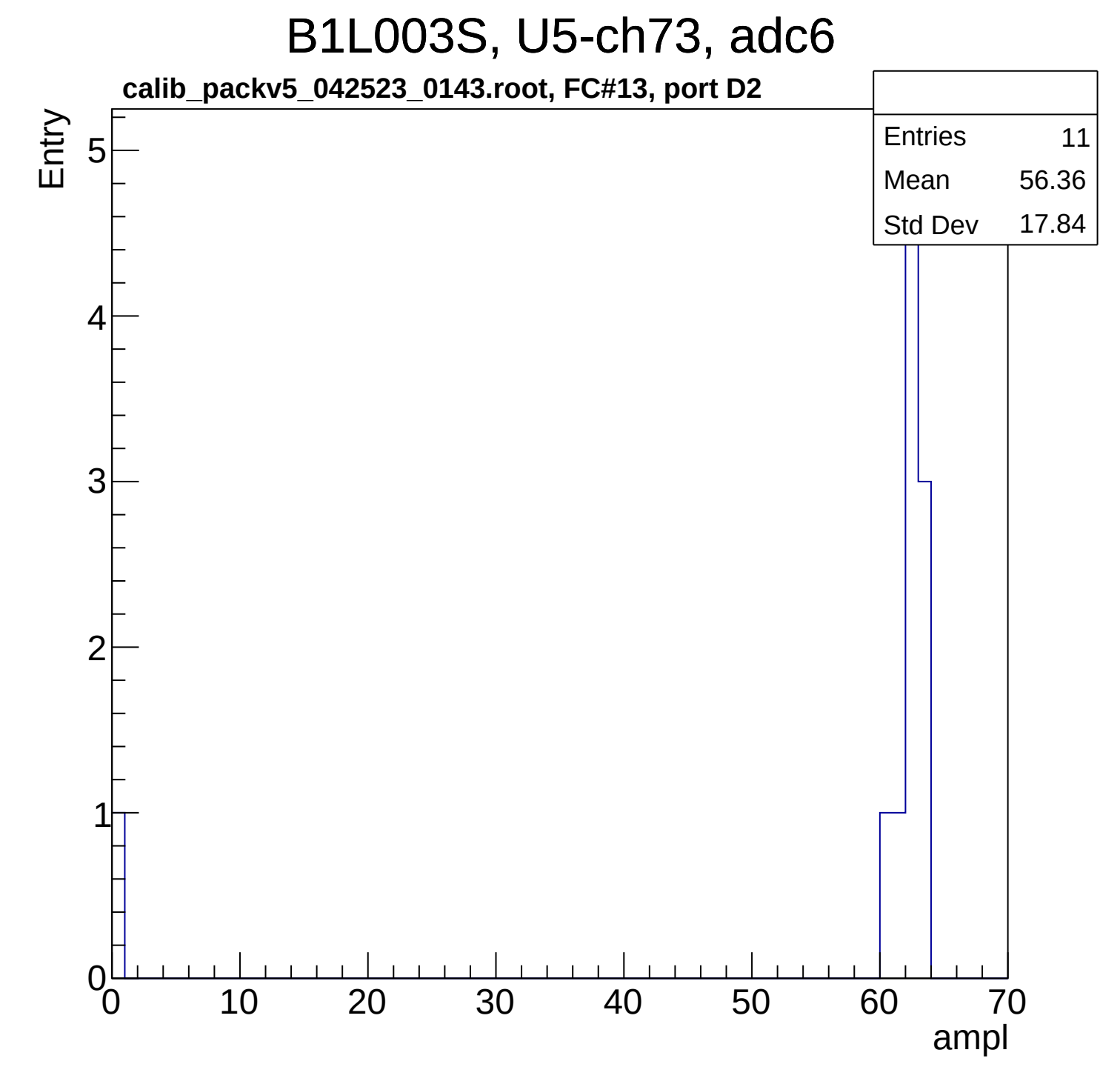
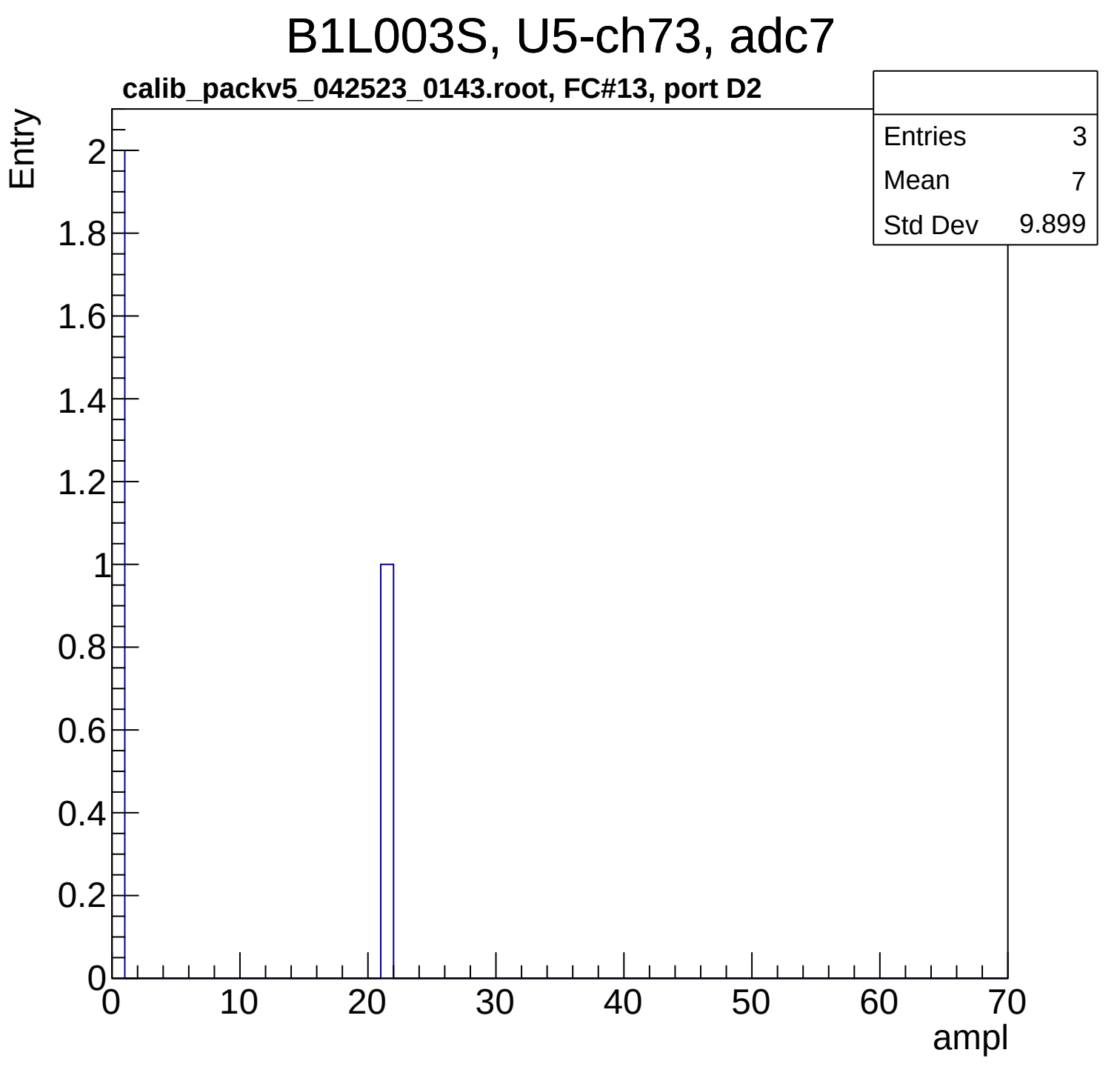

B1L003S, U5-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.73
Std Dev 2.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.36
Std Dev 17.84
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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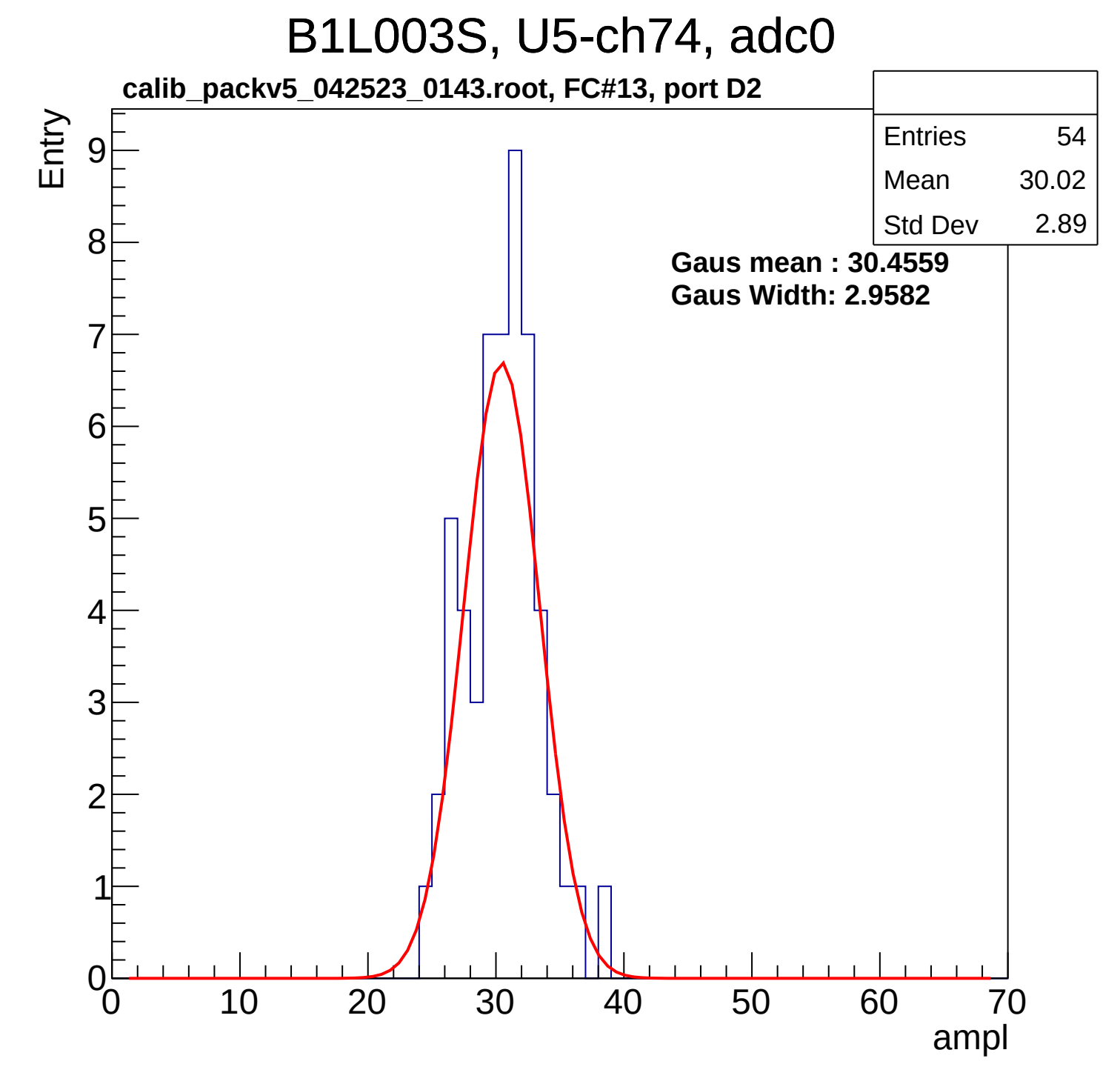
B1L003S, U5-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 30.02
Std Dev 2.89
Gaus mean : 30.4559
Gaus Width: 2.9582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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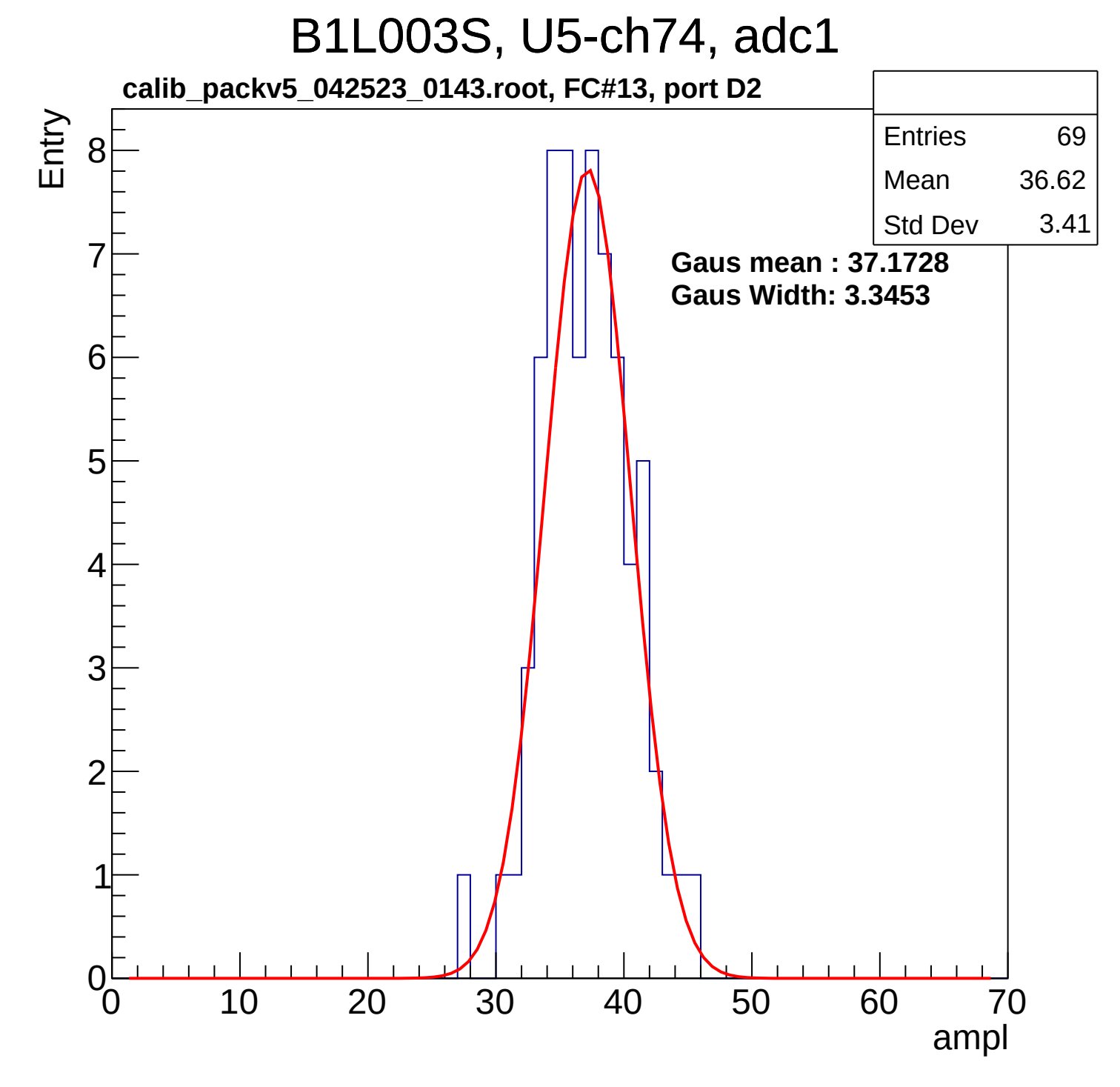
B1L003S, U5-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.62
Std Dev 3.41
Gaus mean : 37.1728
Gaus Width: 3.3453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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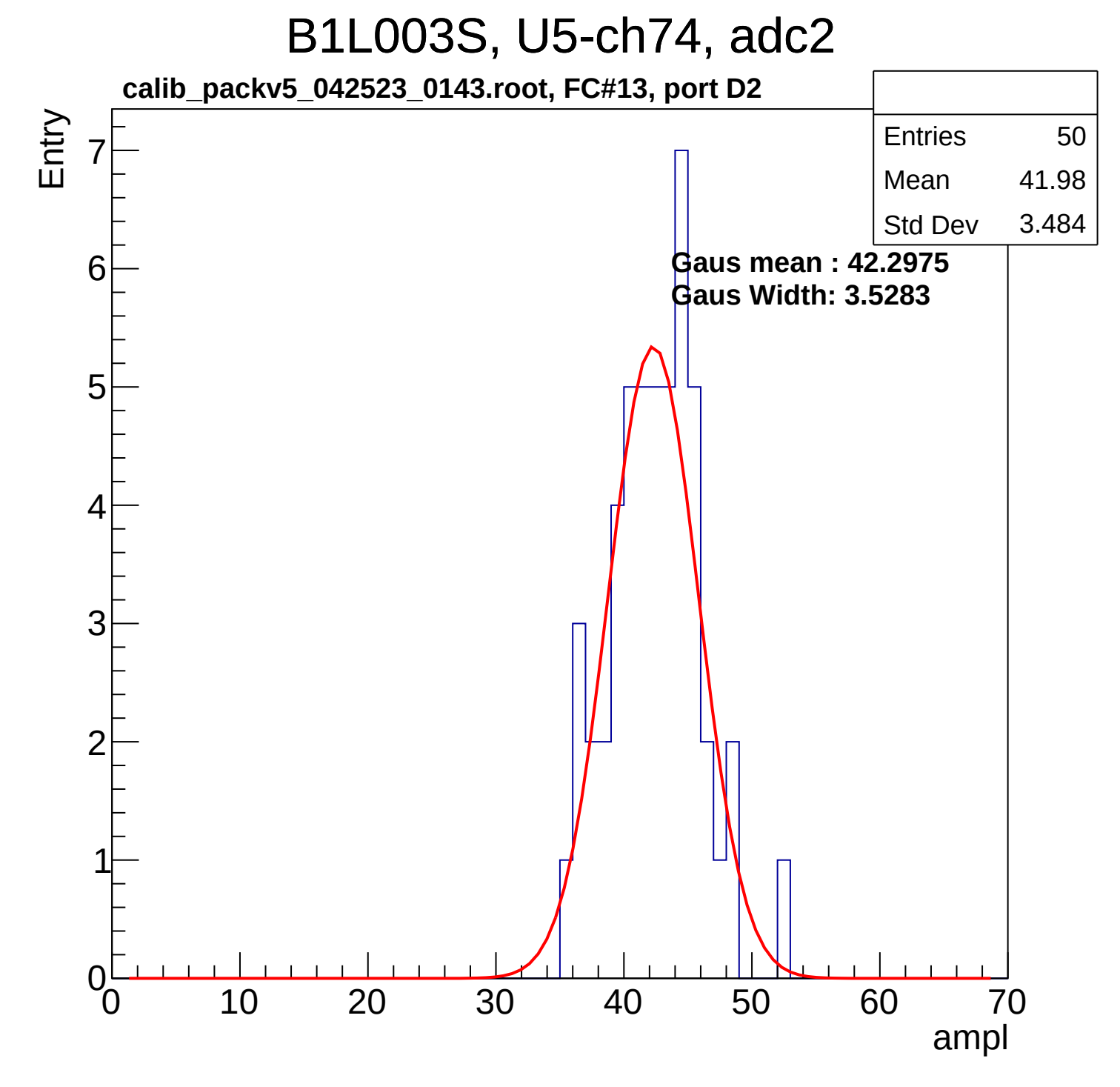
B1L003S, U5-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.98
Std Dev 3.484
Gaus mean : 42.2975
Gaus Width: 3.5283
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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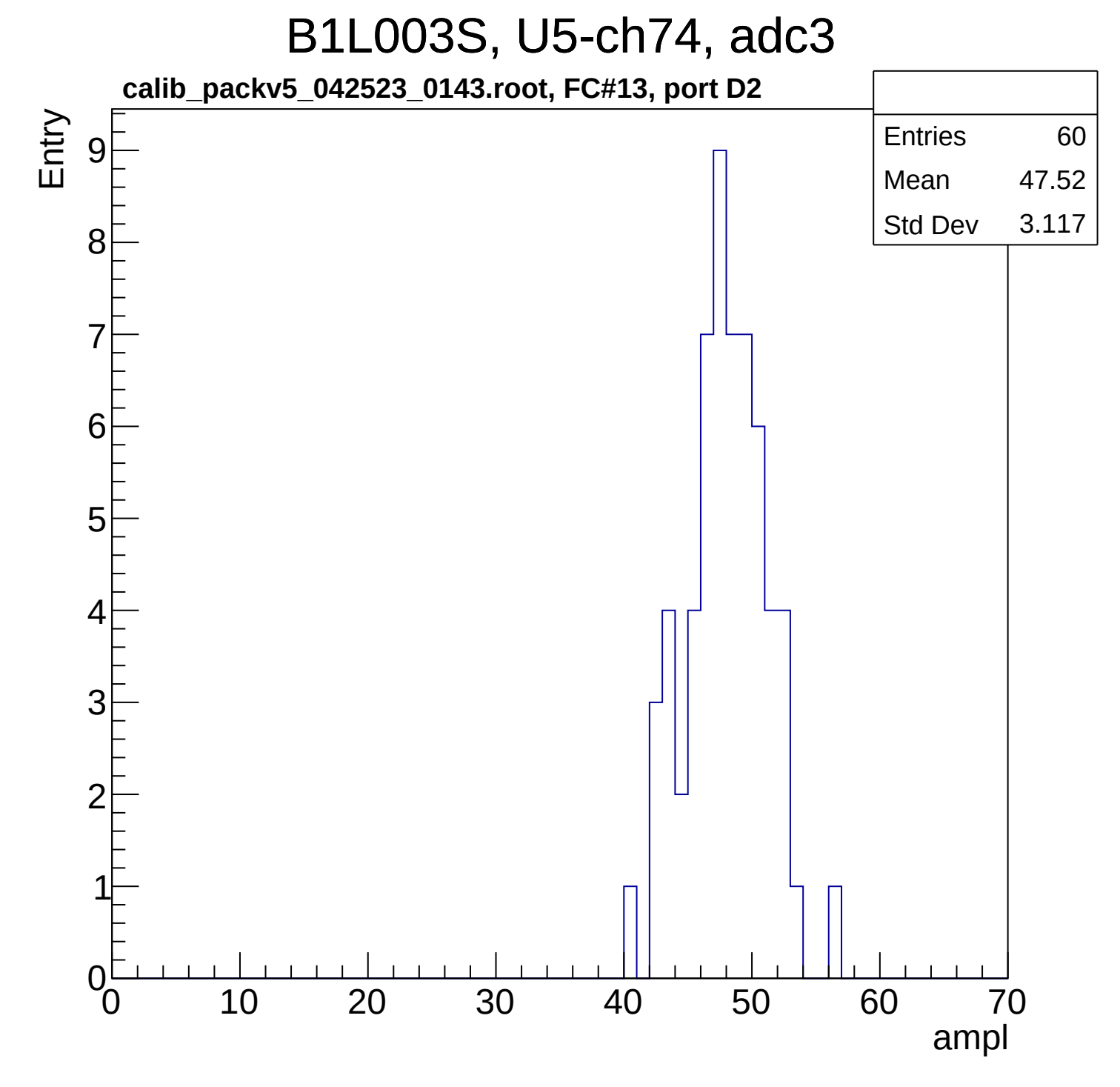
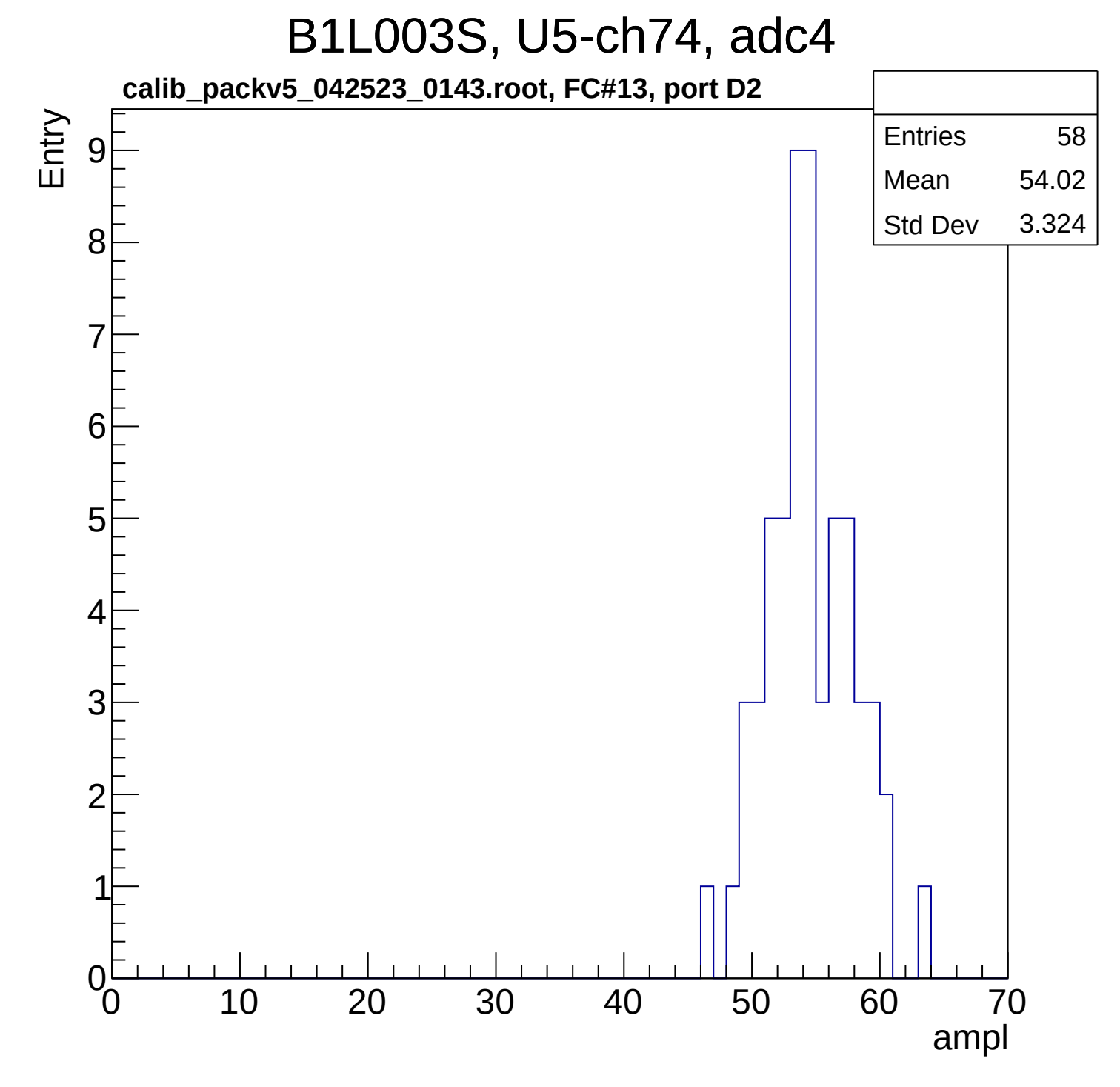
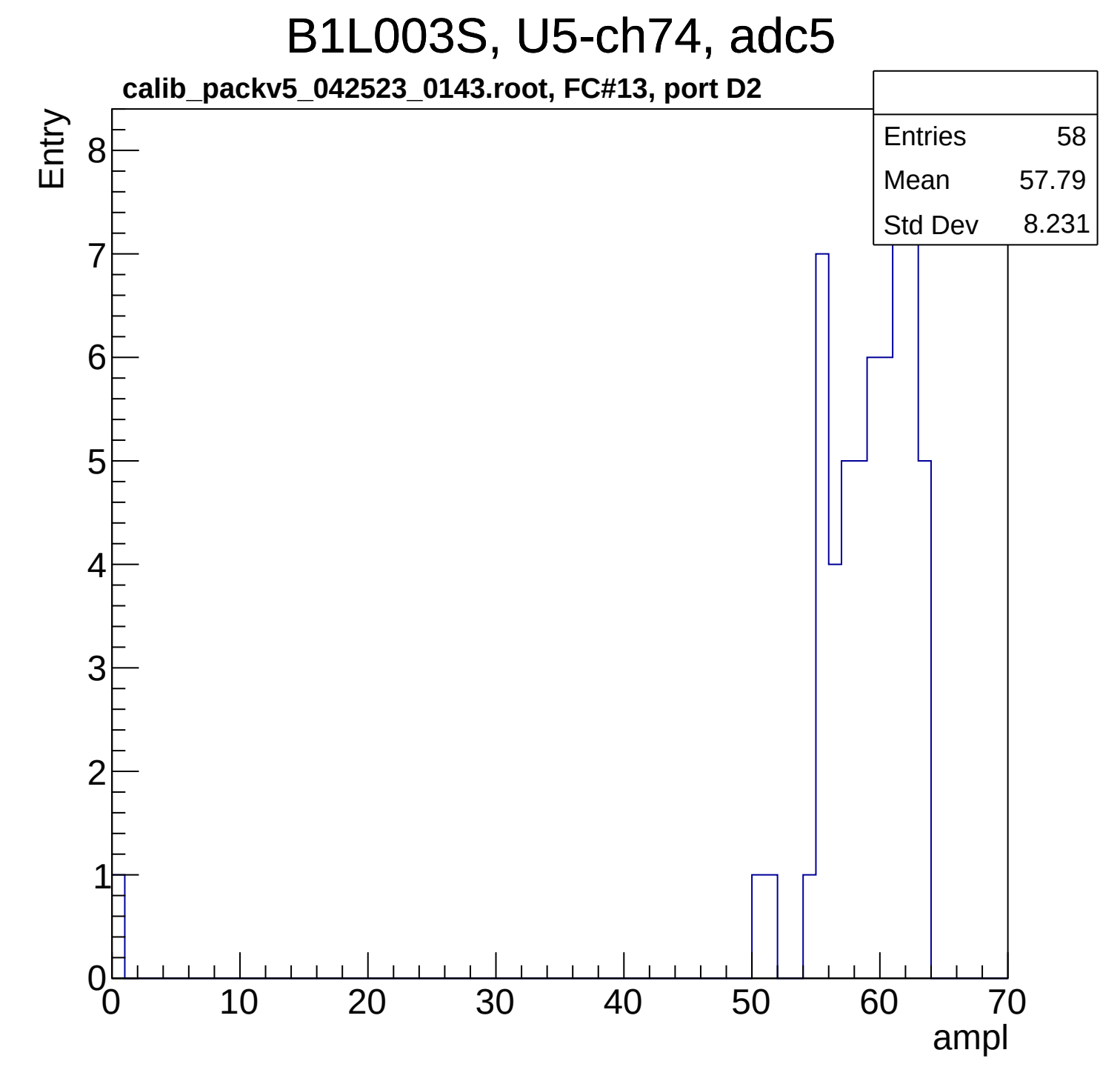

B1L003S, U5-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 47.52
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.02
Std Dev 3.324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.79
Std Dev 8.231
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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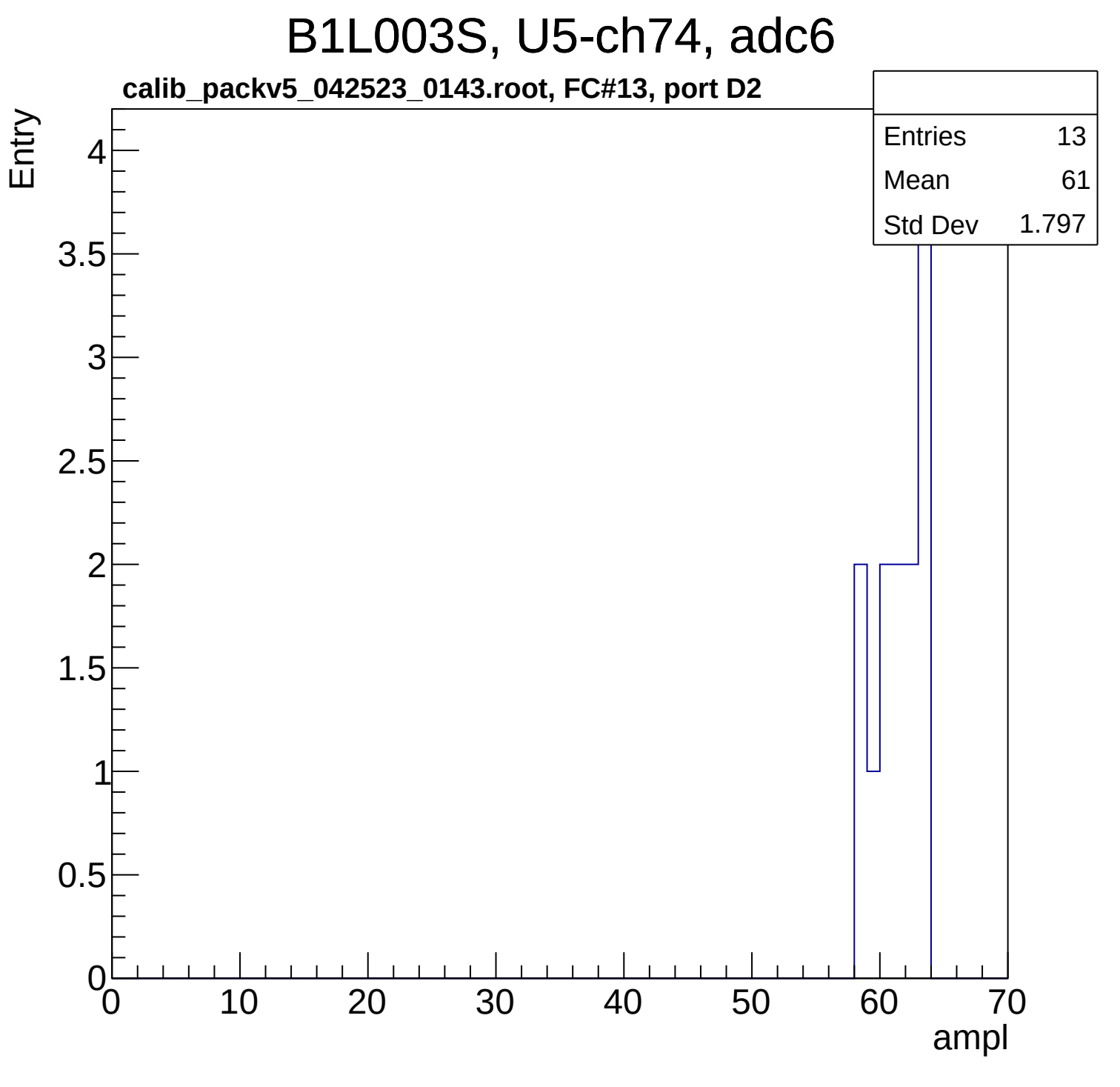
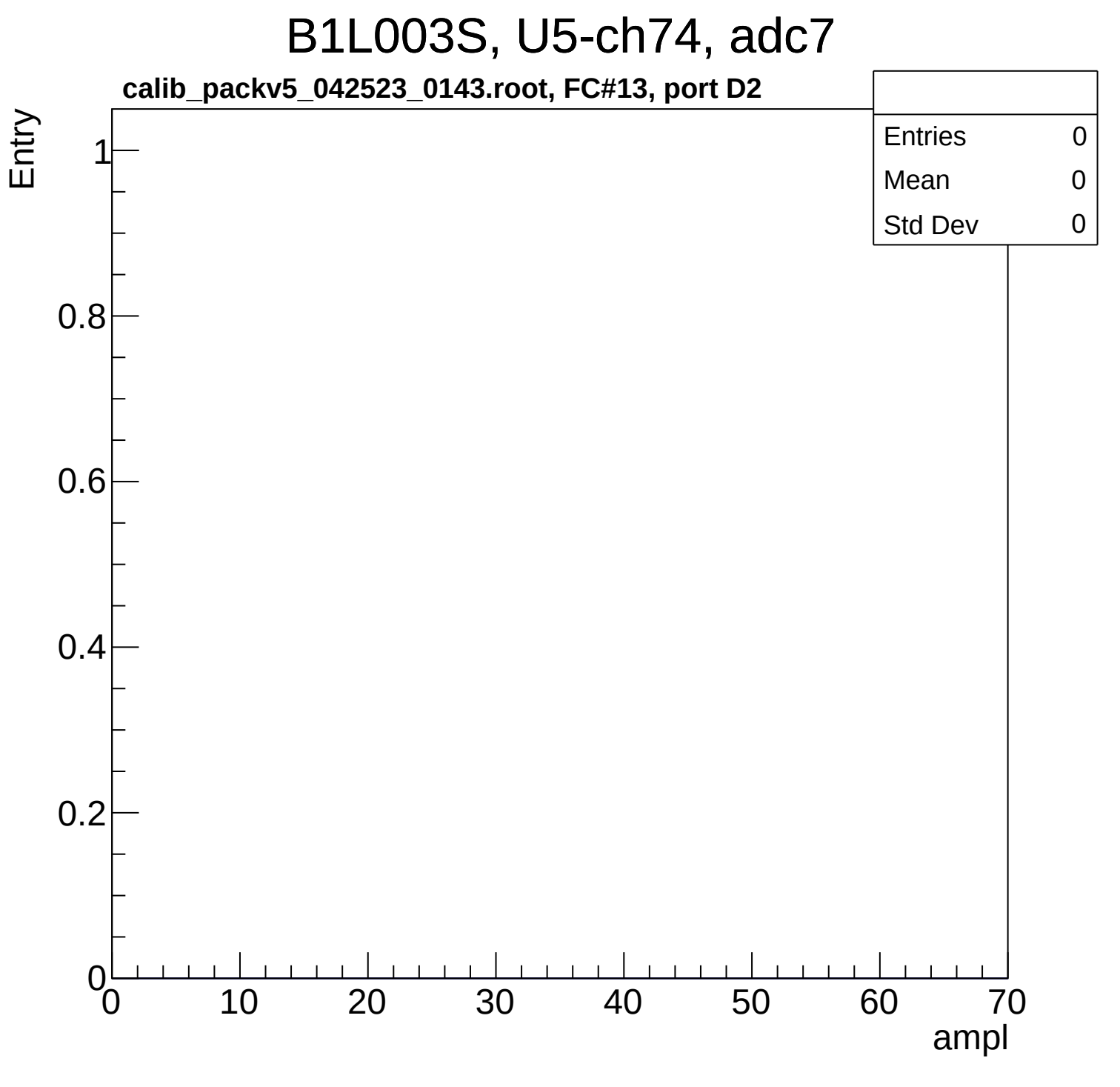
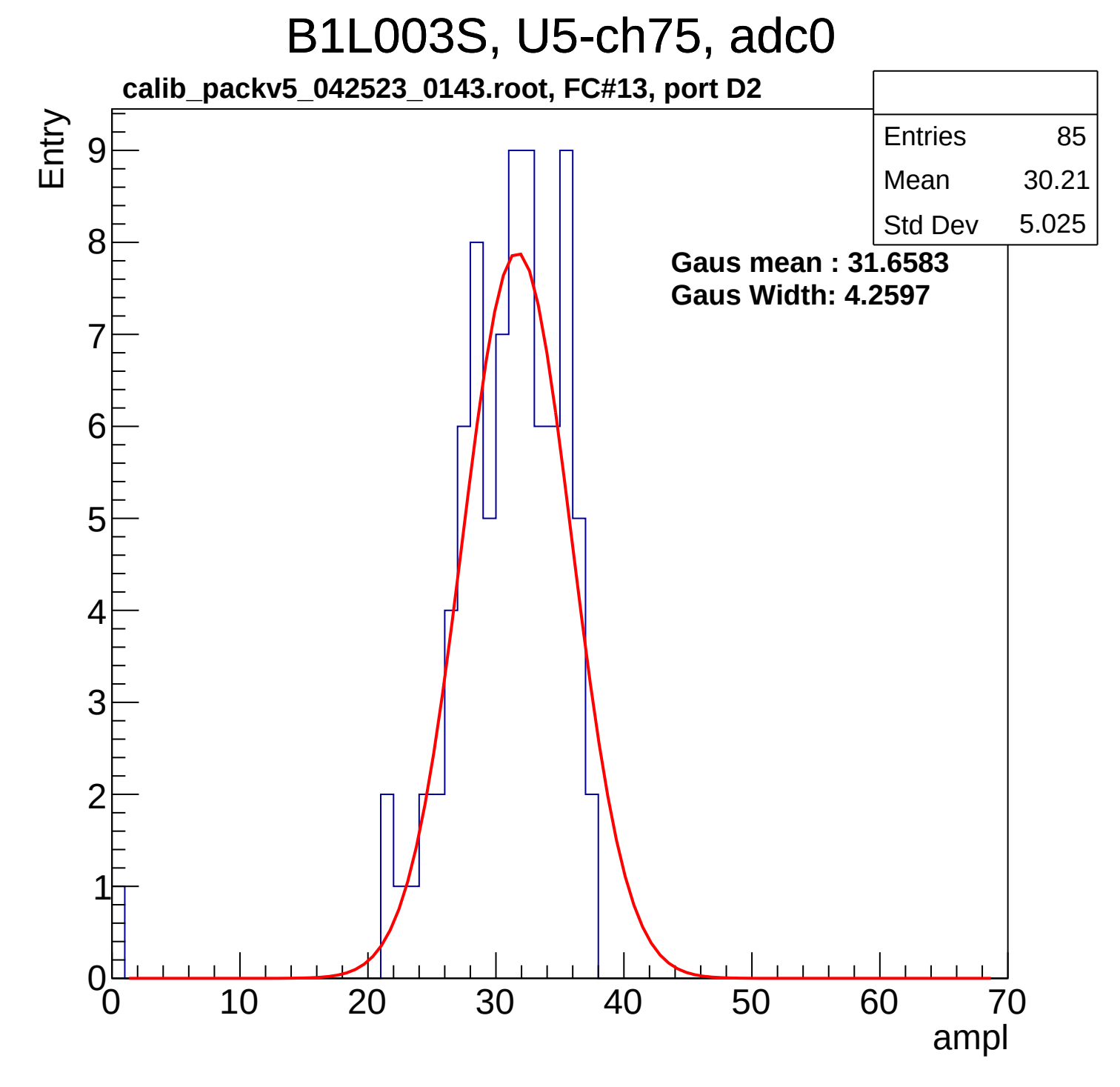

B1L003S, U5-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61
Std Dev 1.797
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 30.21
Std Dev 5.025
Gaus mean : 31.6583
Gaus Width: 4.2597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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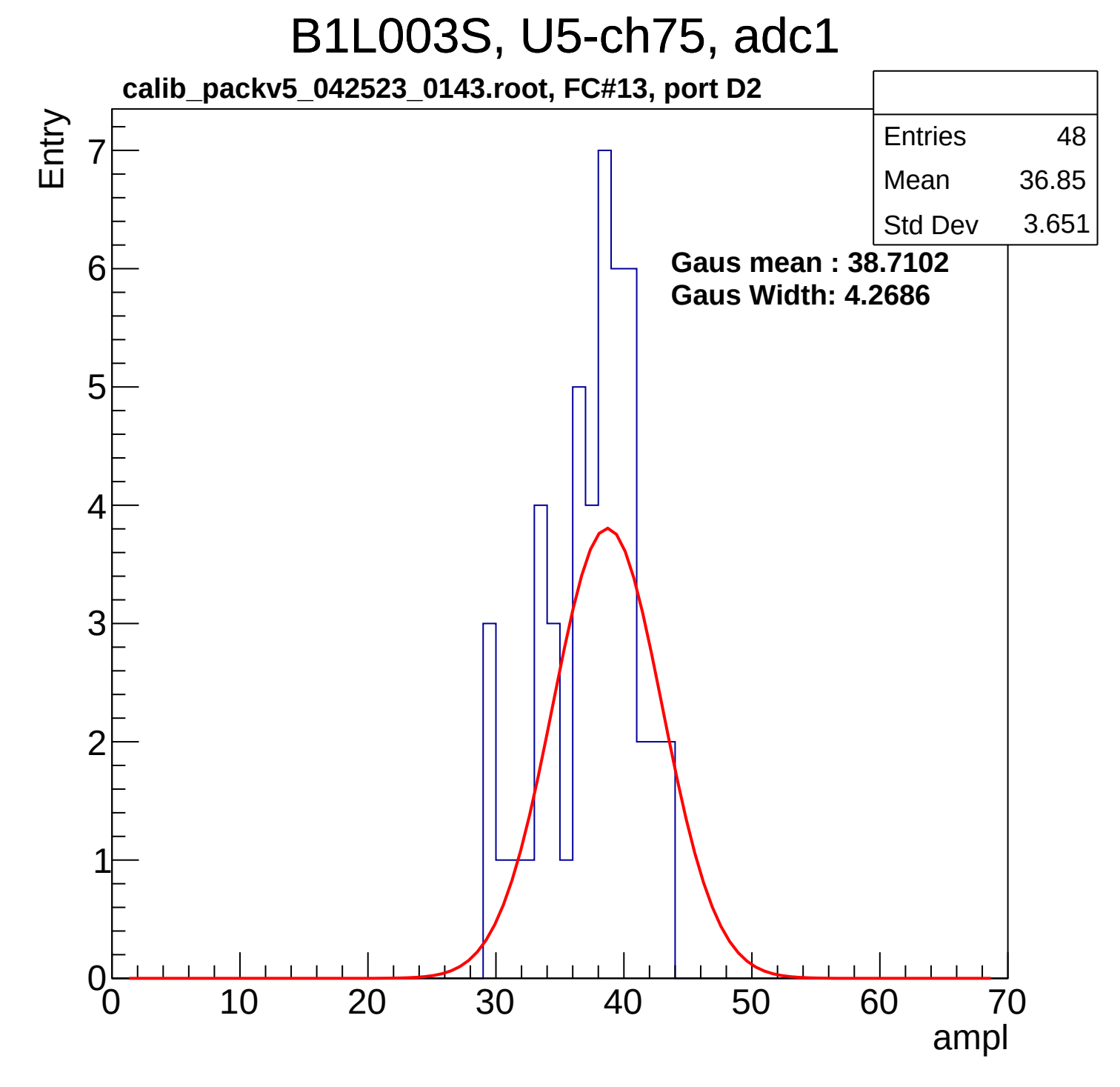
B1L003S, U5-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 36.85
Std Dev 3.651
Gaus mean : 38.7102
Gaus Width: 4.2686
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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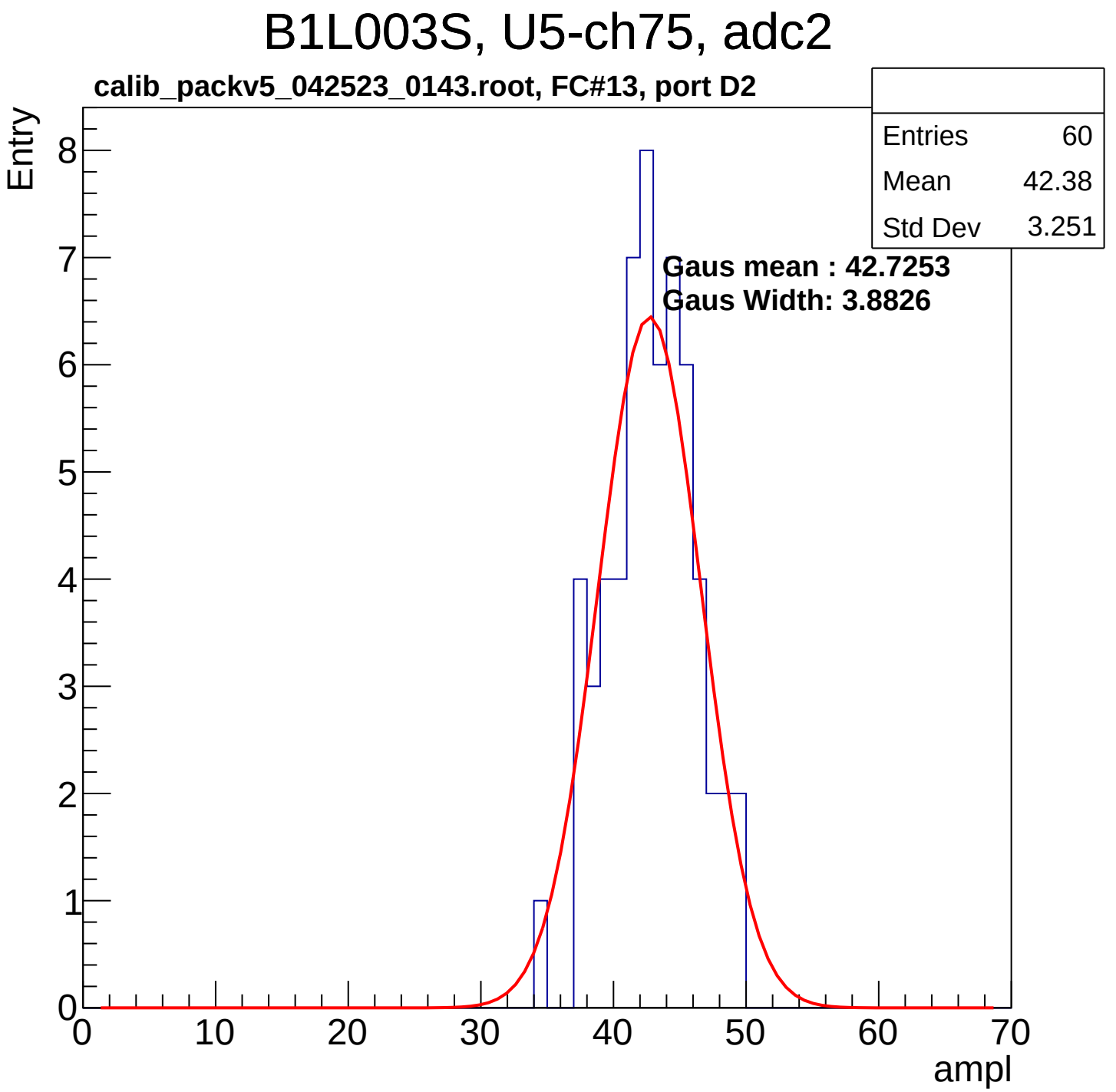
60
70


B1L003S, U5-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.38
Std Dev 3.251
Gaus mean : 42.7253
Gaus Width: 3.8826
Entry
ampl
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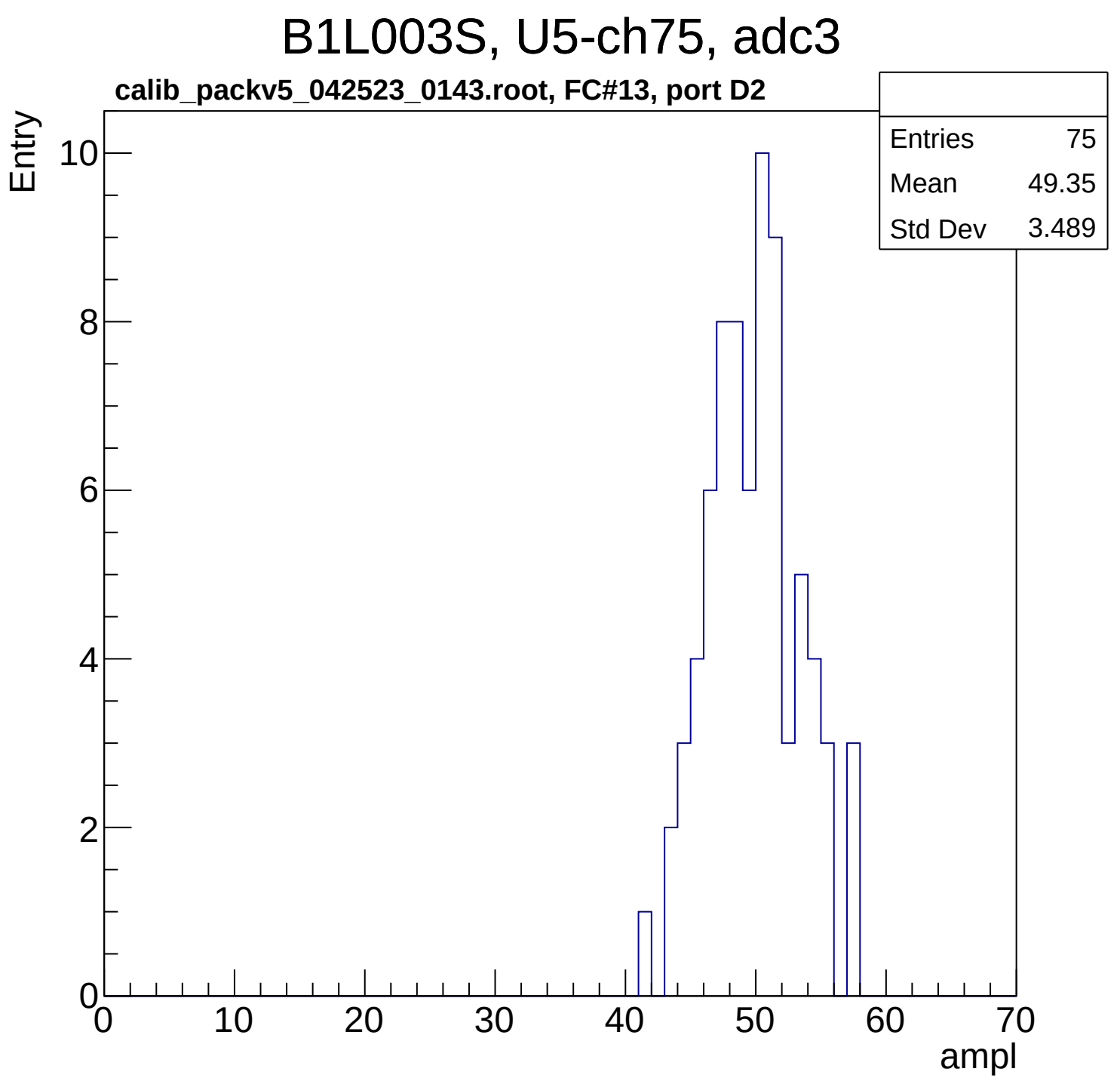
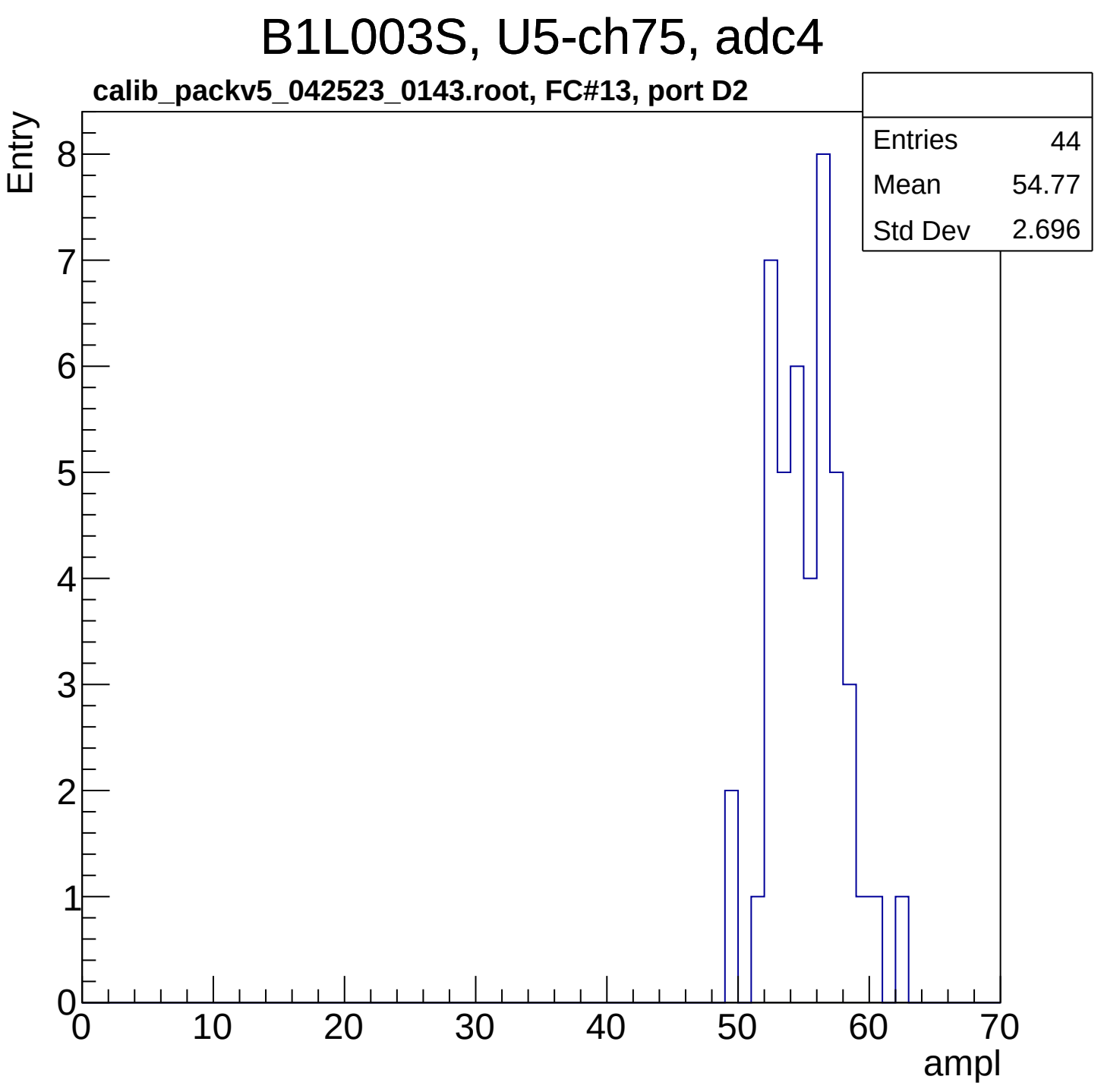
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.35
Std Dev 3.489
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 54.77
Std Dev 2.696
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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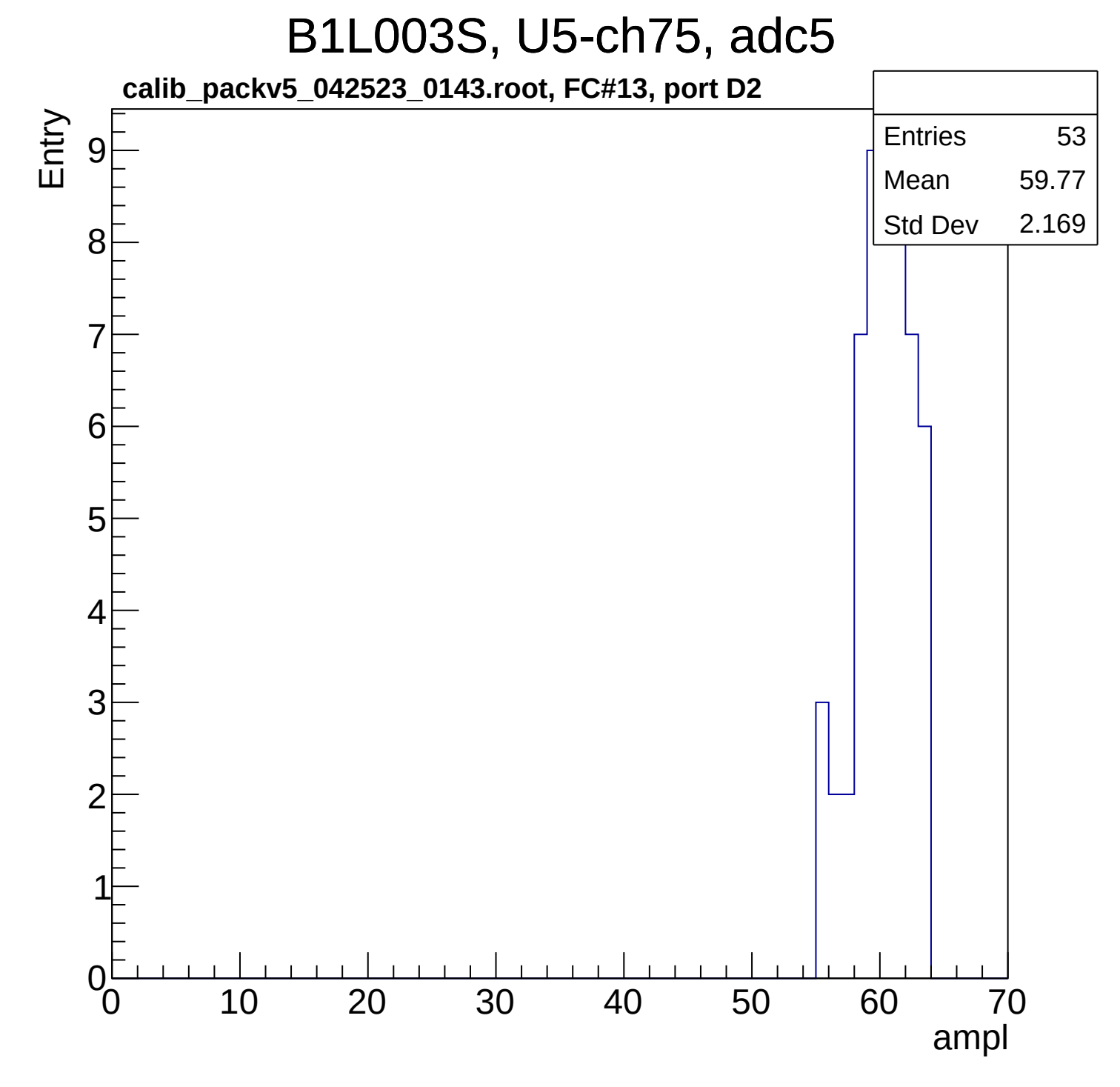
60
70


B1L003S, U5-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.77
Std Dev 2.169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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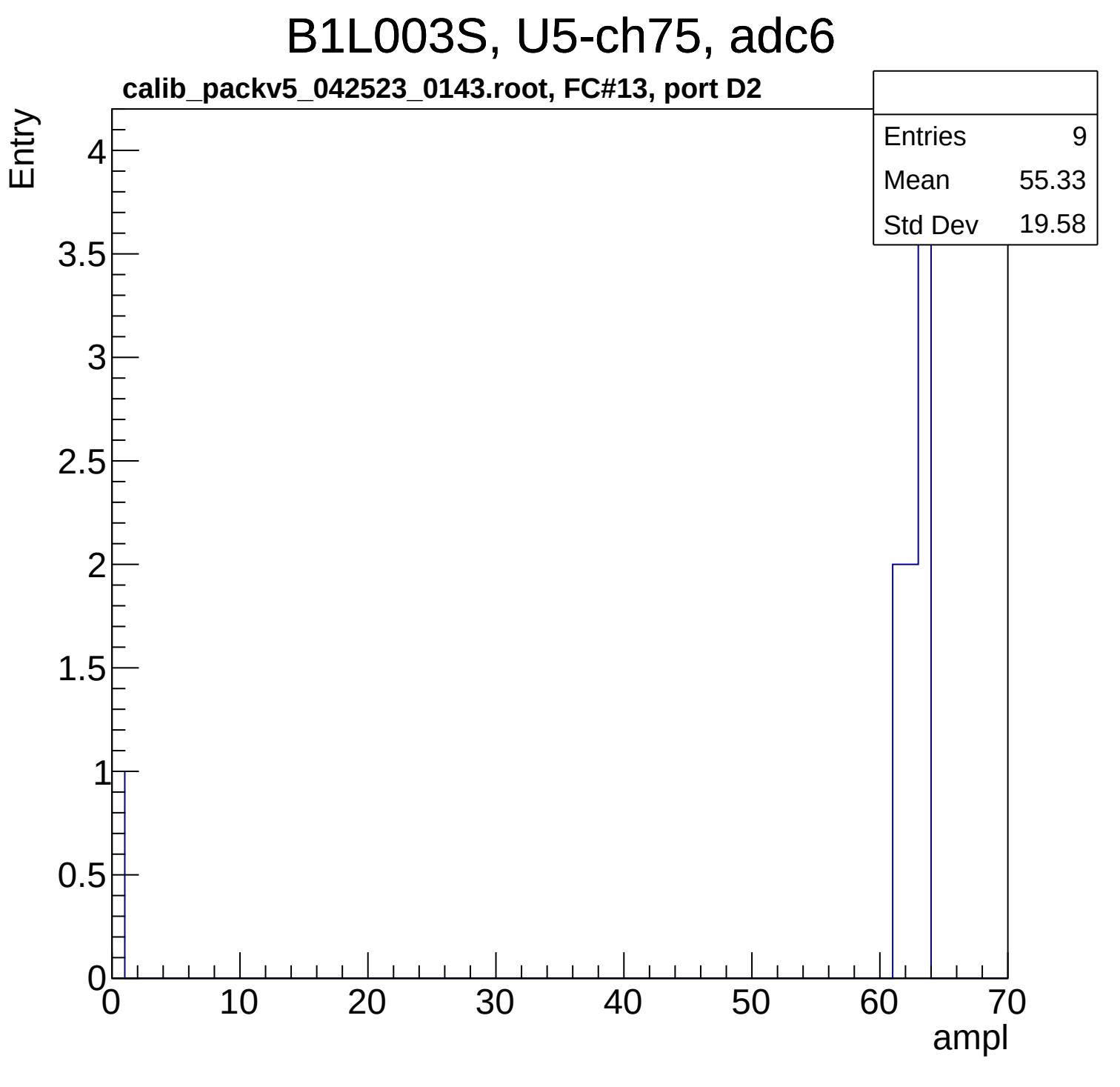
B1L003S, U5-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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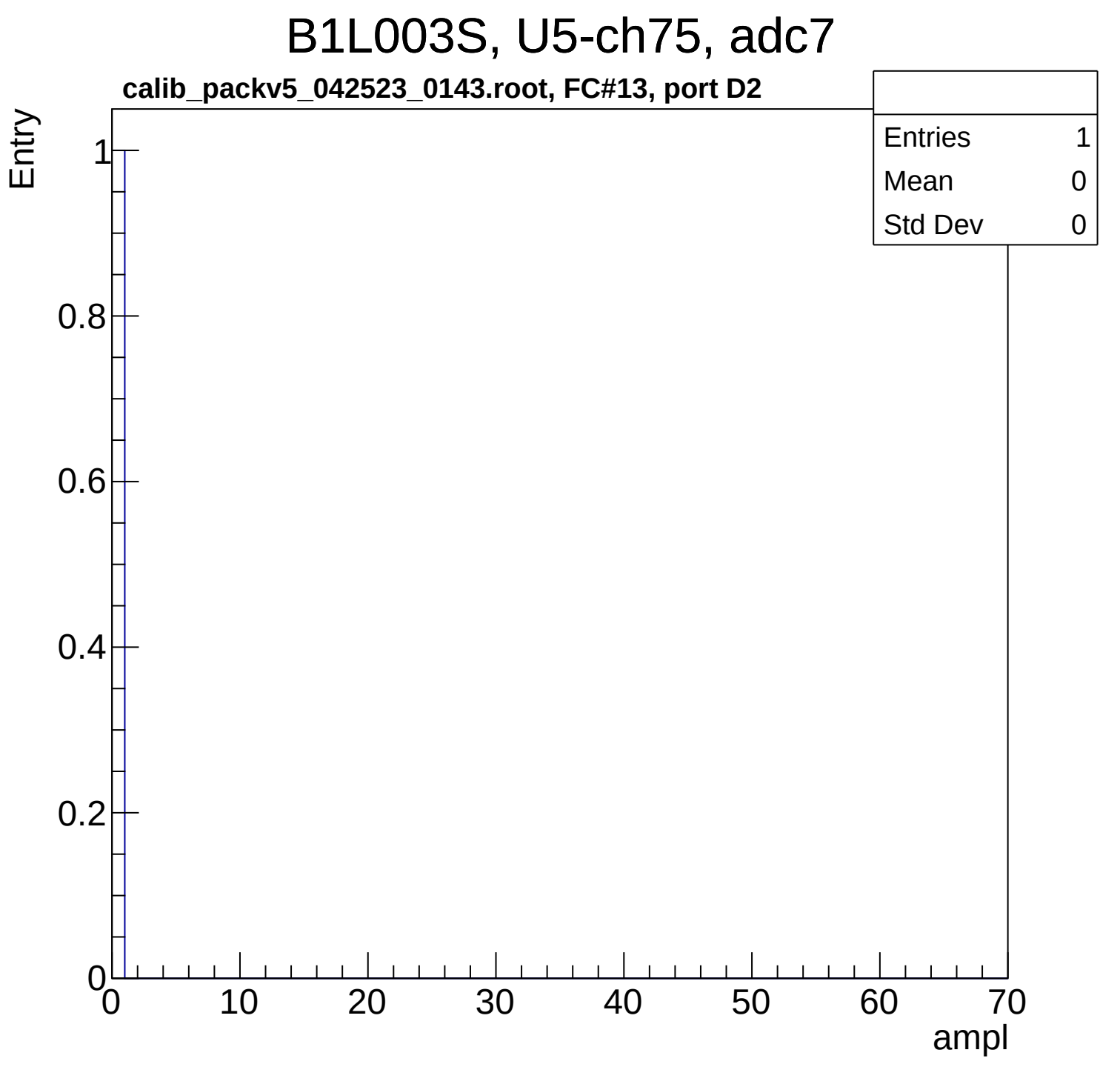
B1L003S, U5-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
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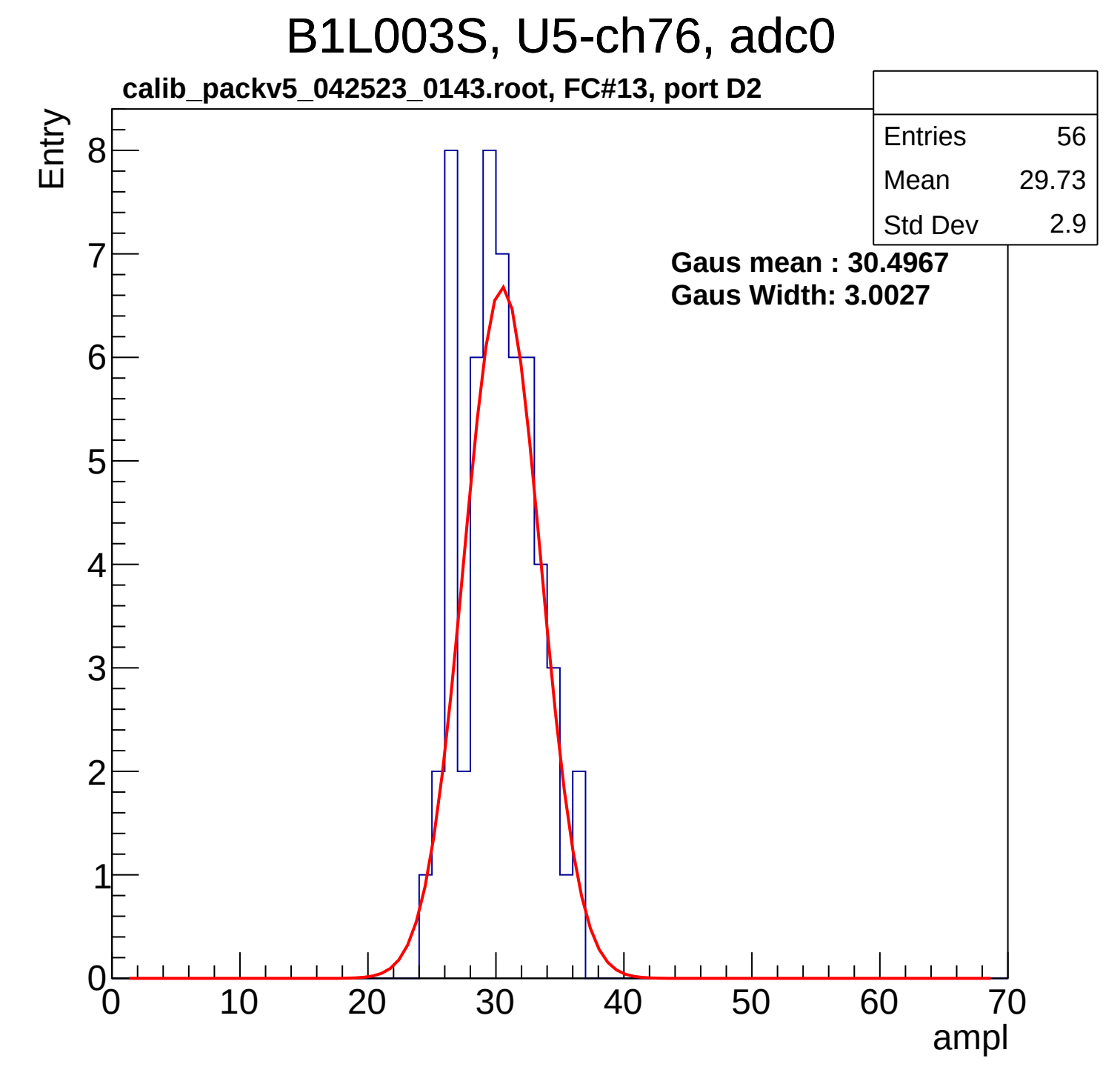
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 29.73
Std Dev 2.9
Gaus mean : 30.4967
Gaus Width: 3.0027
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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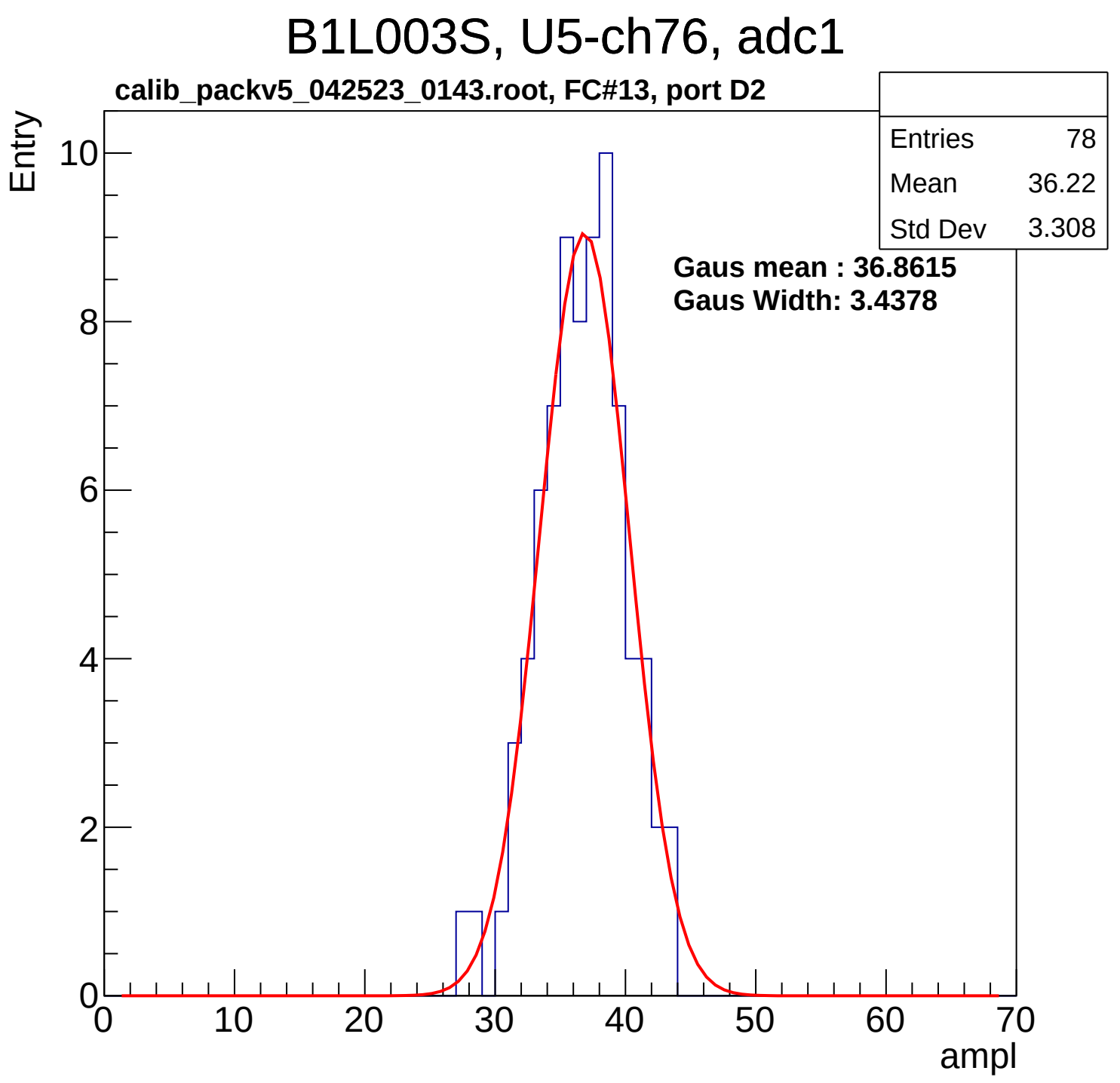
40
50
60
70


B1L003S, U5-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.22
Std Dev 3.308
Gaus mean : 36.8615
Gaus Width: 3.4378
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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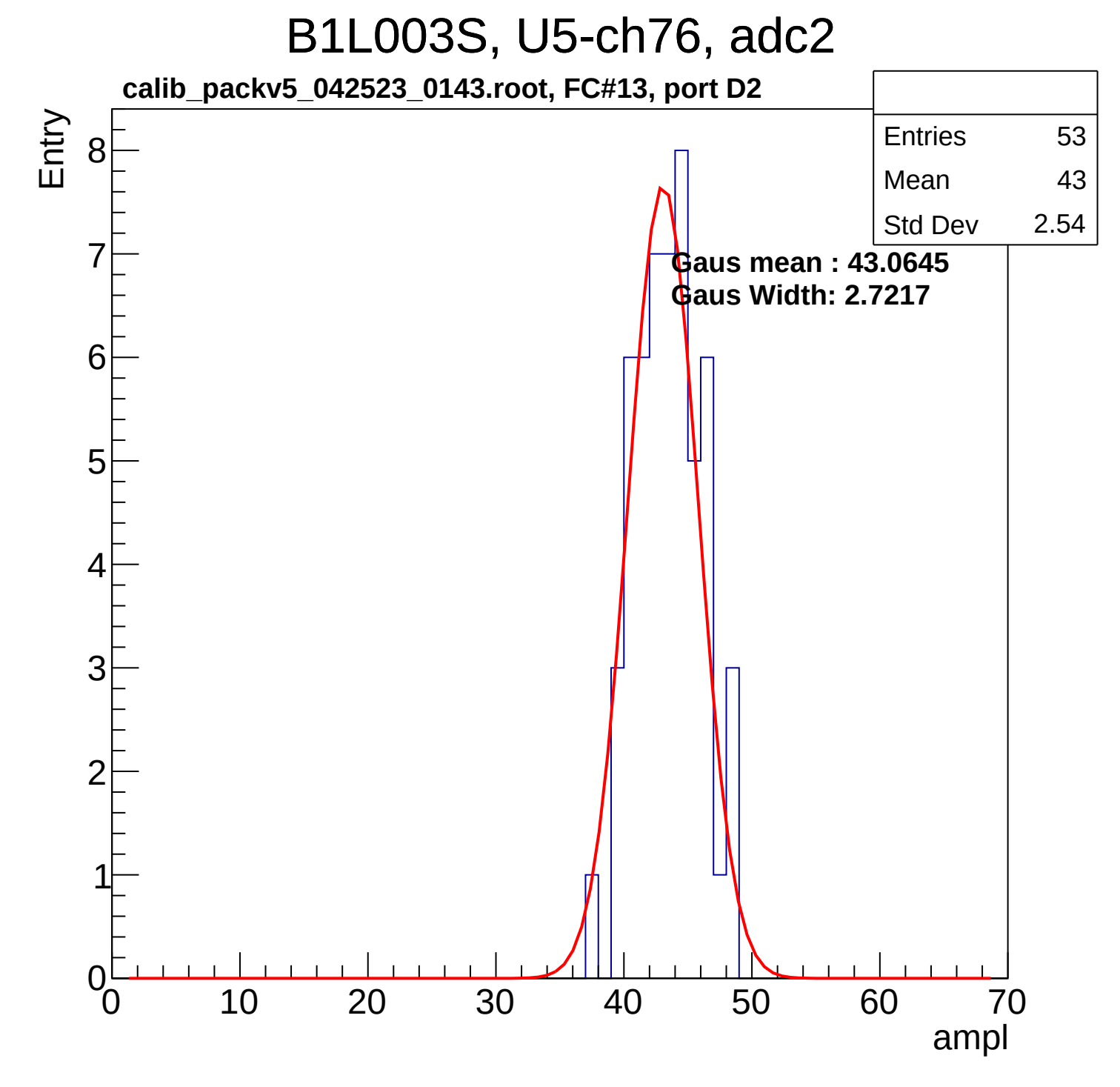
B1L003S, U5-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 43
Std Dev 2.54
Gaus mean : 43.0645
Gaus Width: 2.7217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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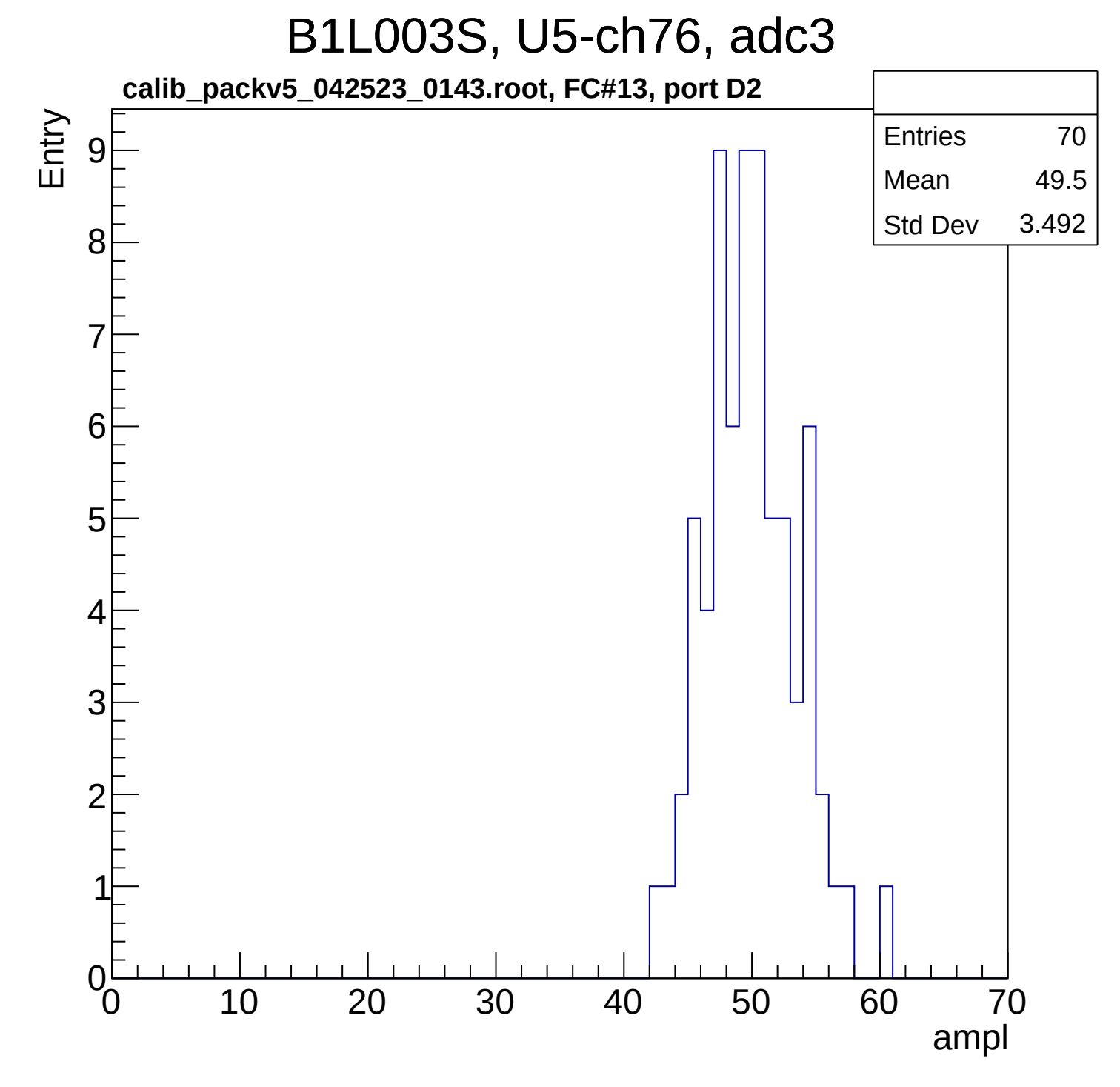
B1L003S, U5-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.5
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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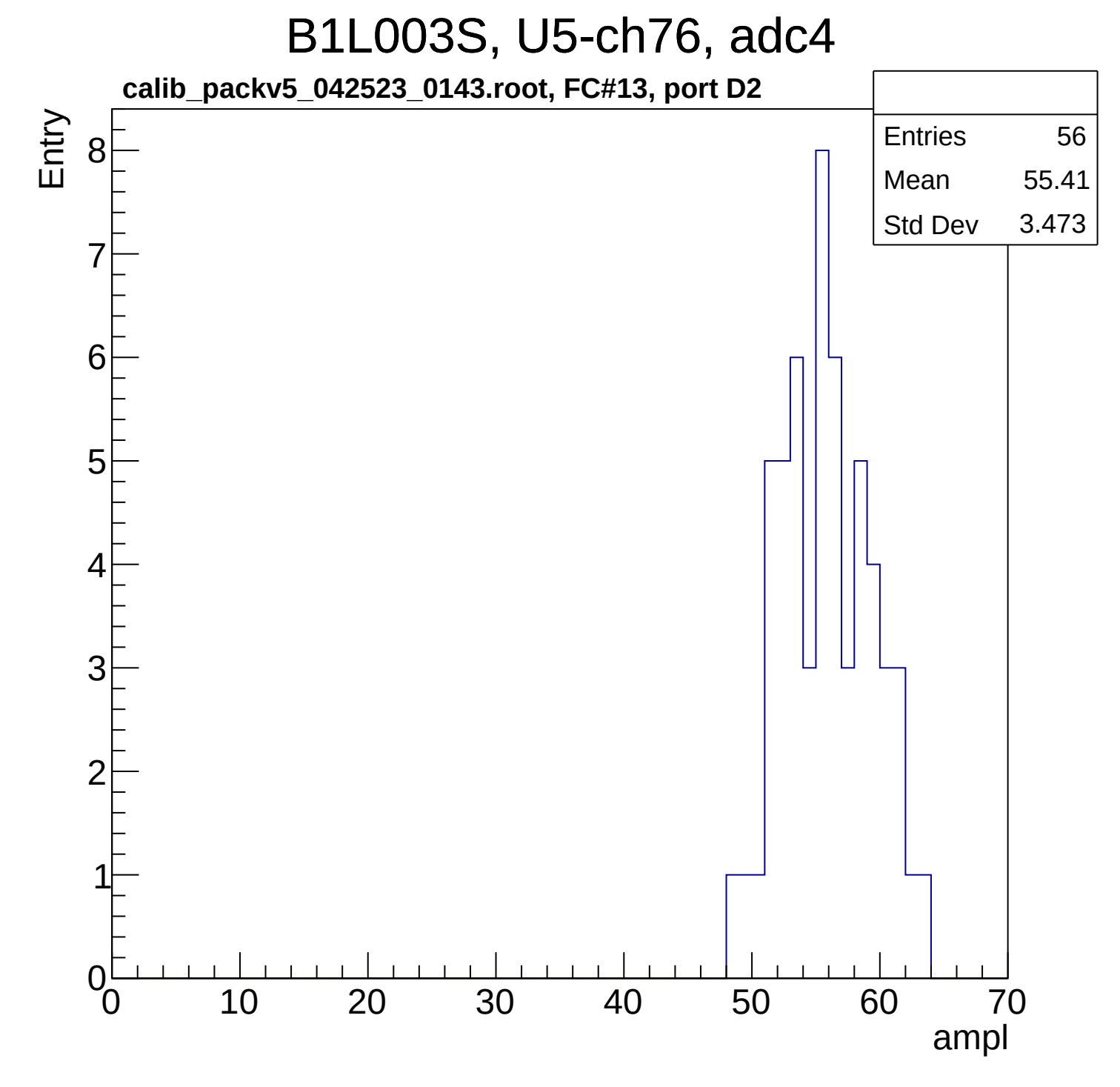
60
70


B1L003S, U5-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.41
Std Dev 3.473
Entry
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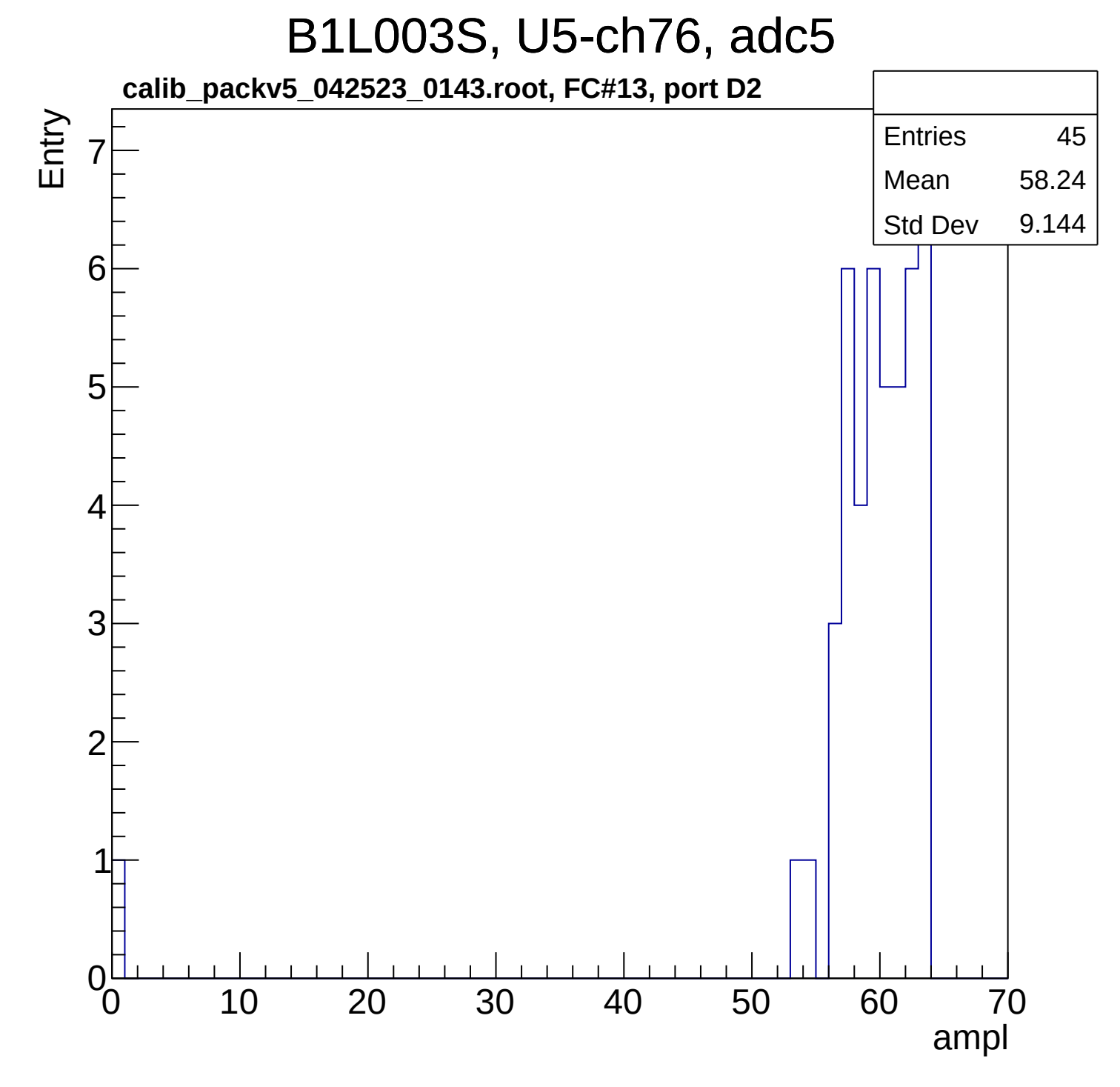
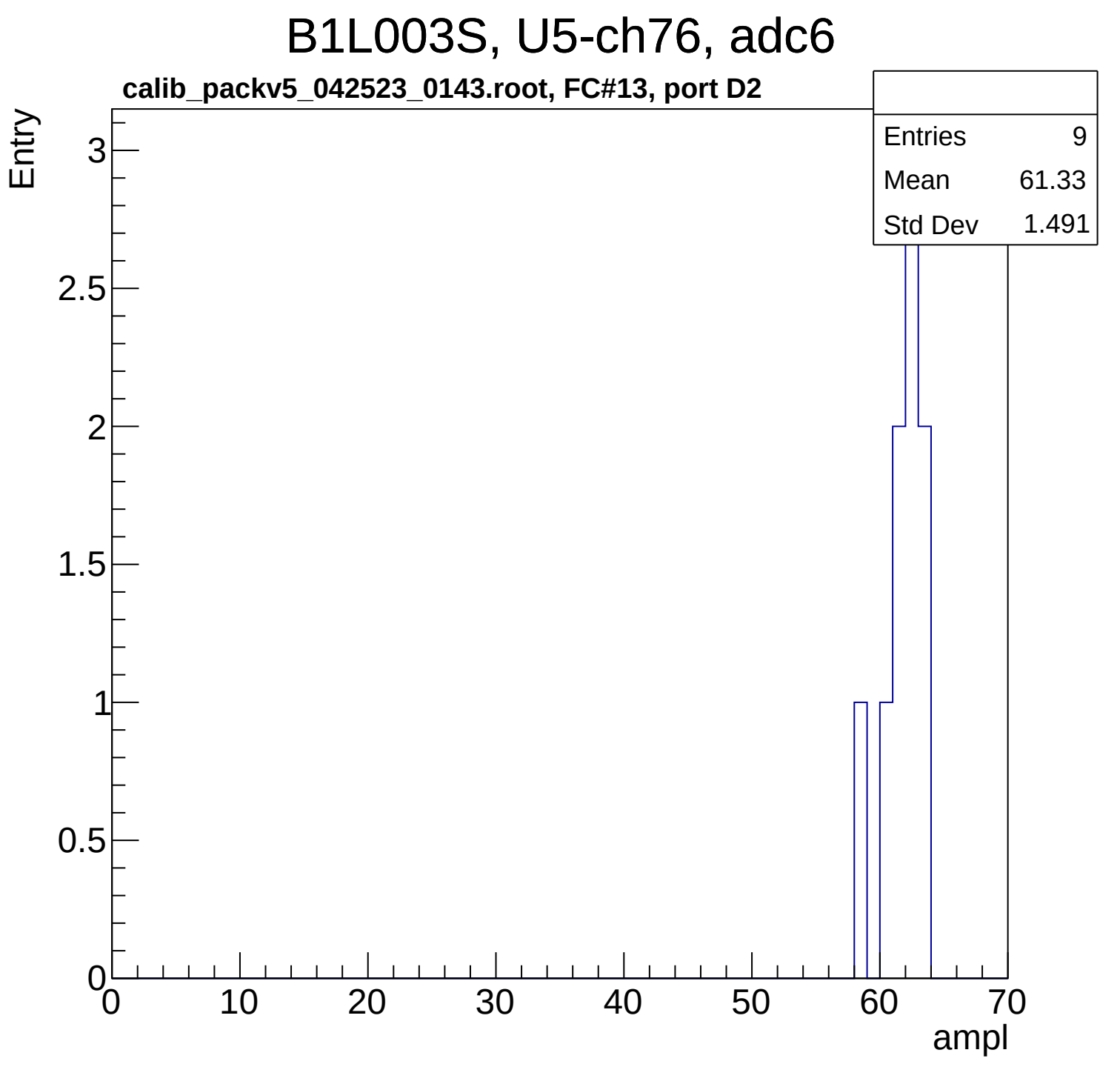
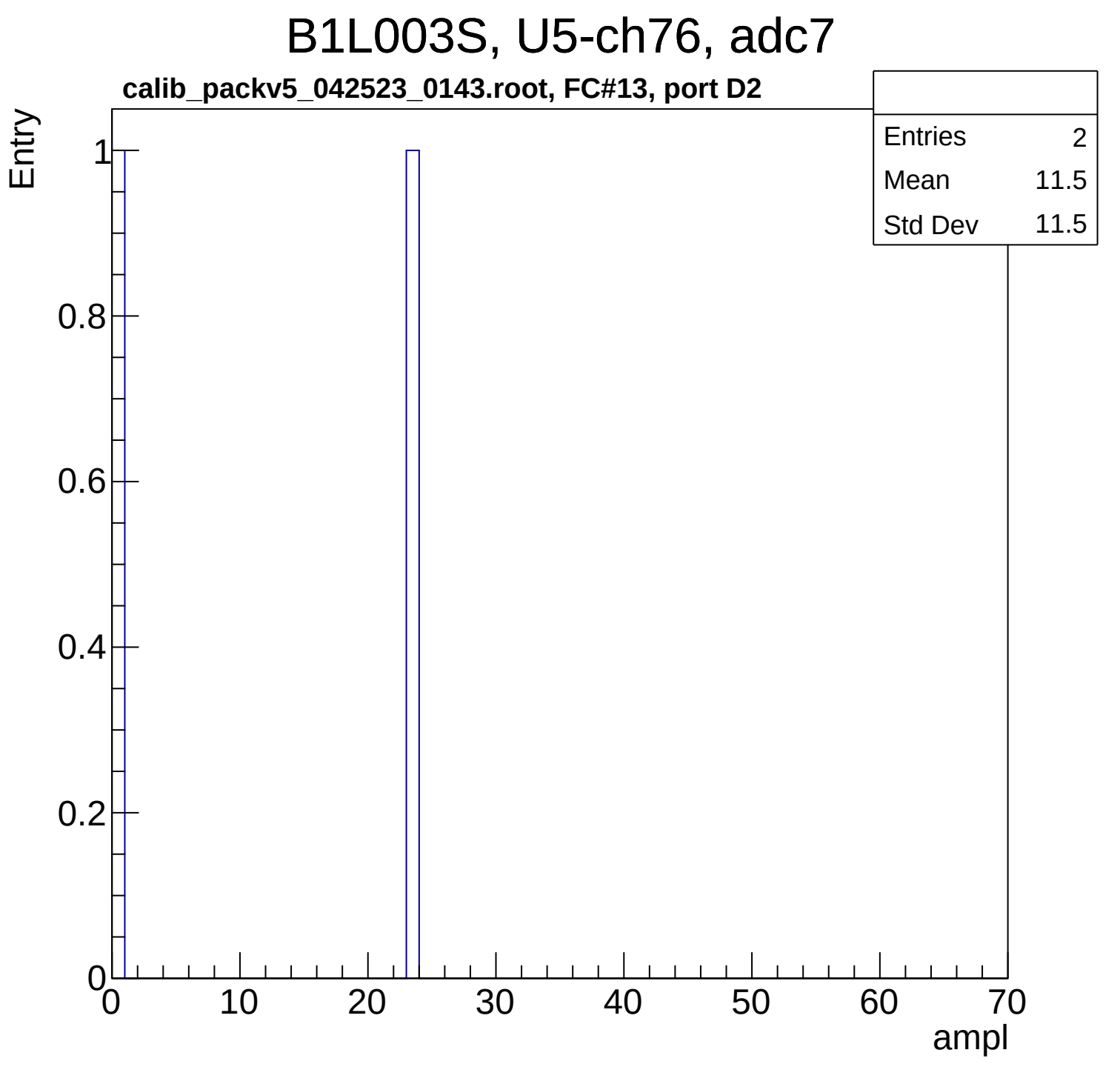
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.24
Std Dev 9.144
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.33
Std Dev 1.491
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
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0
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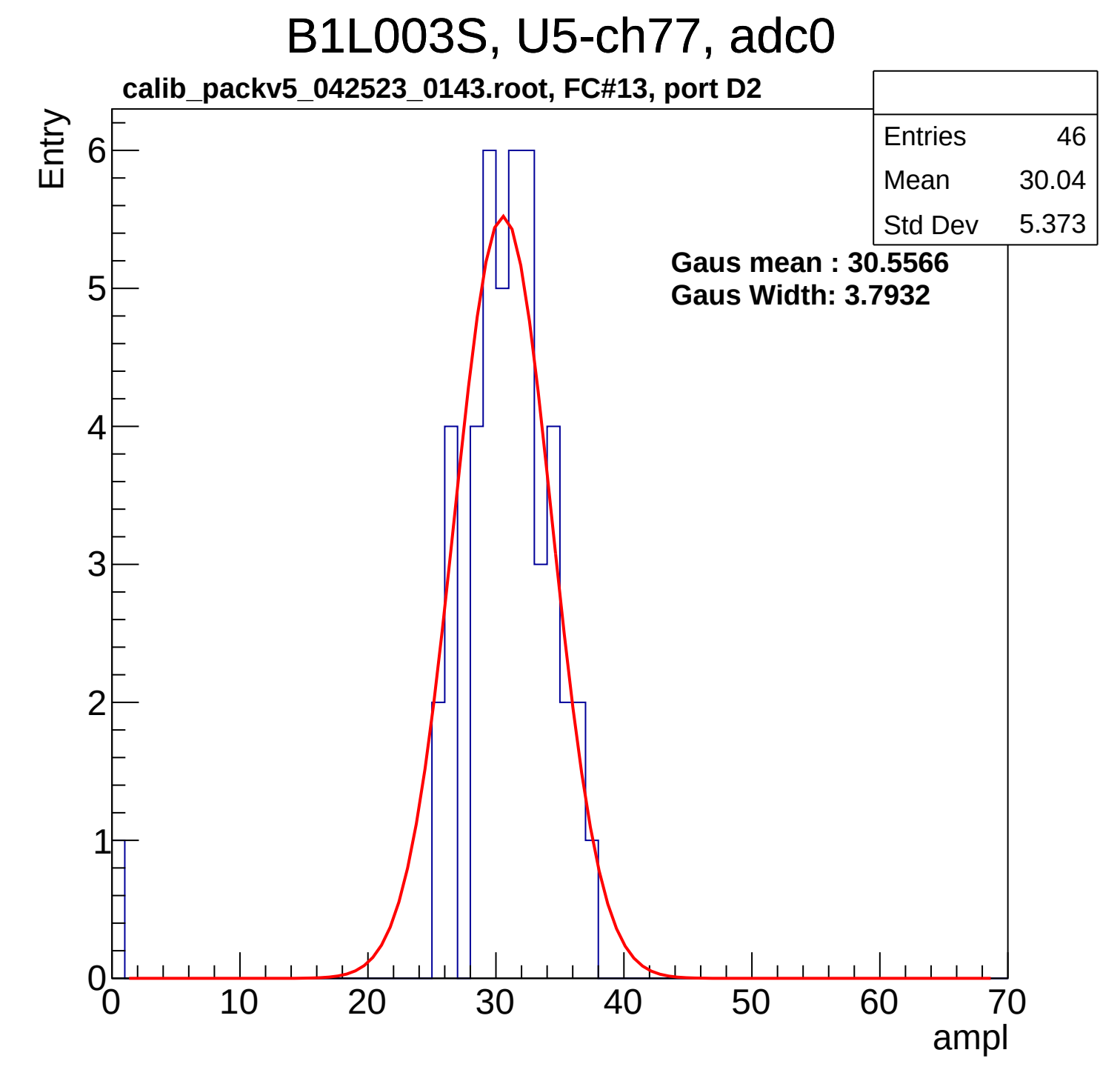
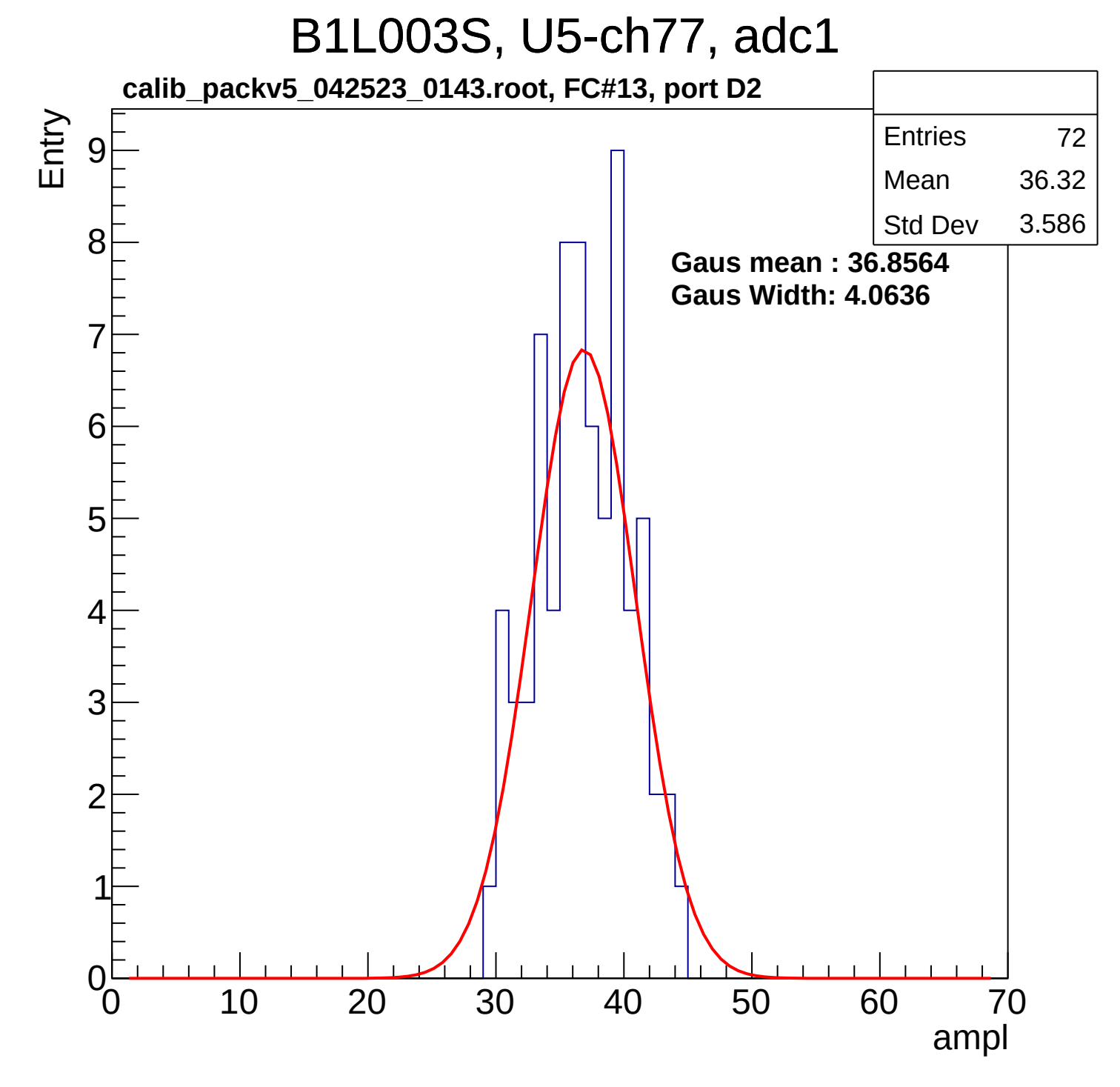
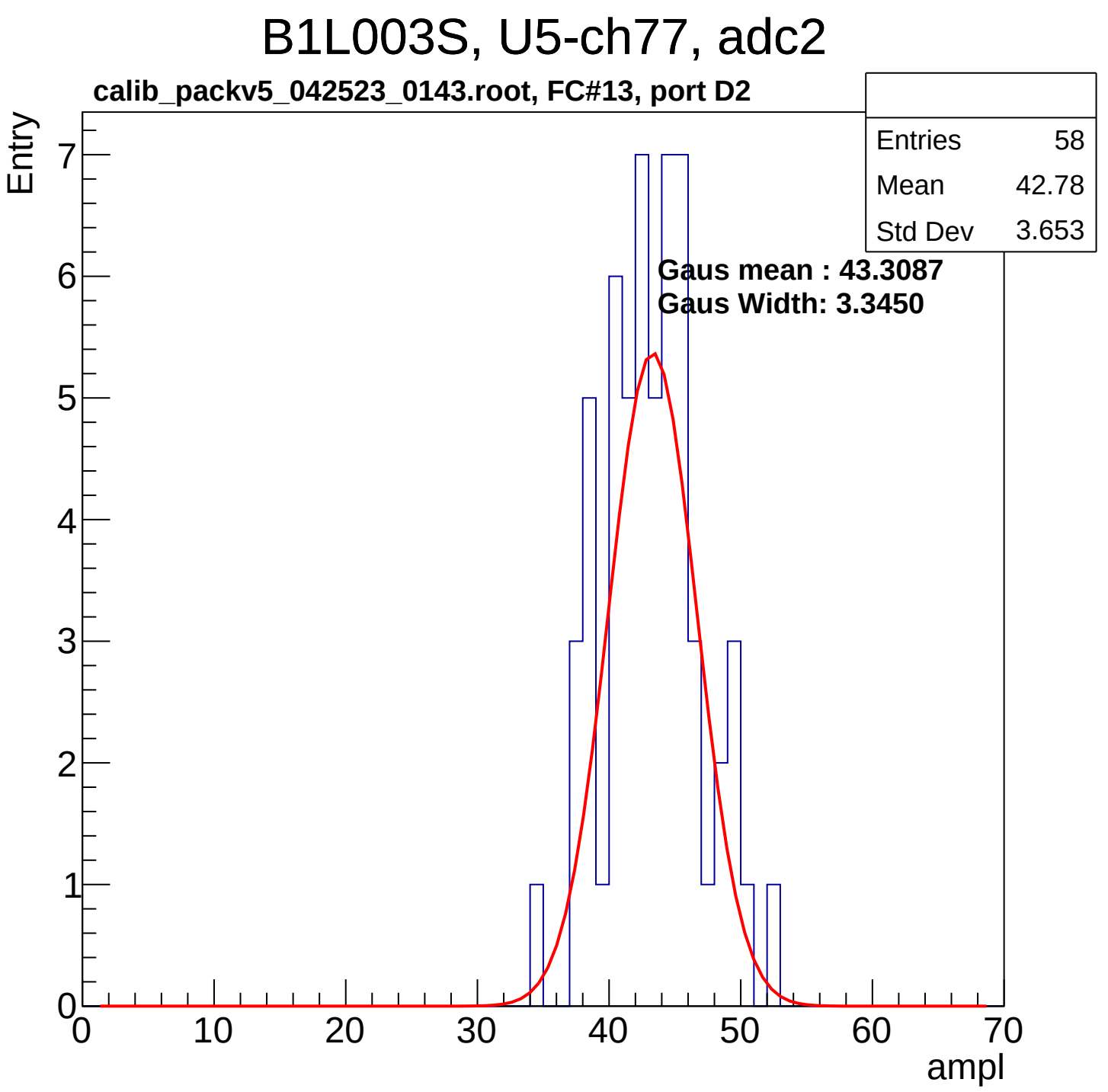
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 30.04
Std Dev 5.373
Gaus mean : 30.5566
Gaus Width: 3.7932
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U5-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.32
Std Dev 3.586
Gaus mean : 36.8564
Gaus Width: 4.0636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.78
Std Dev 3.653
Gaus mean : 43.3087
Gaus Width: 3.3450
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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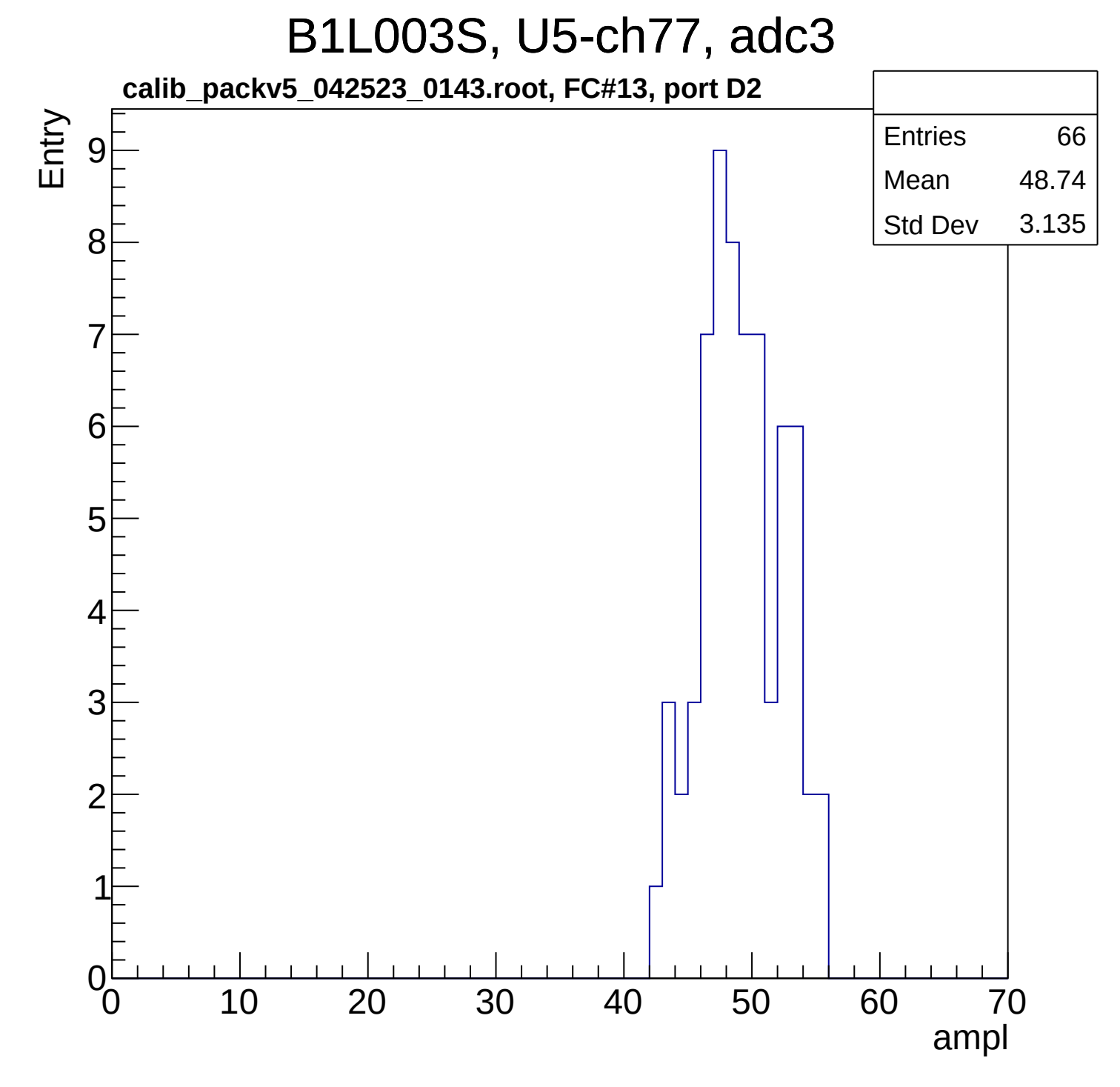
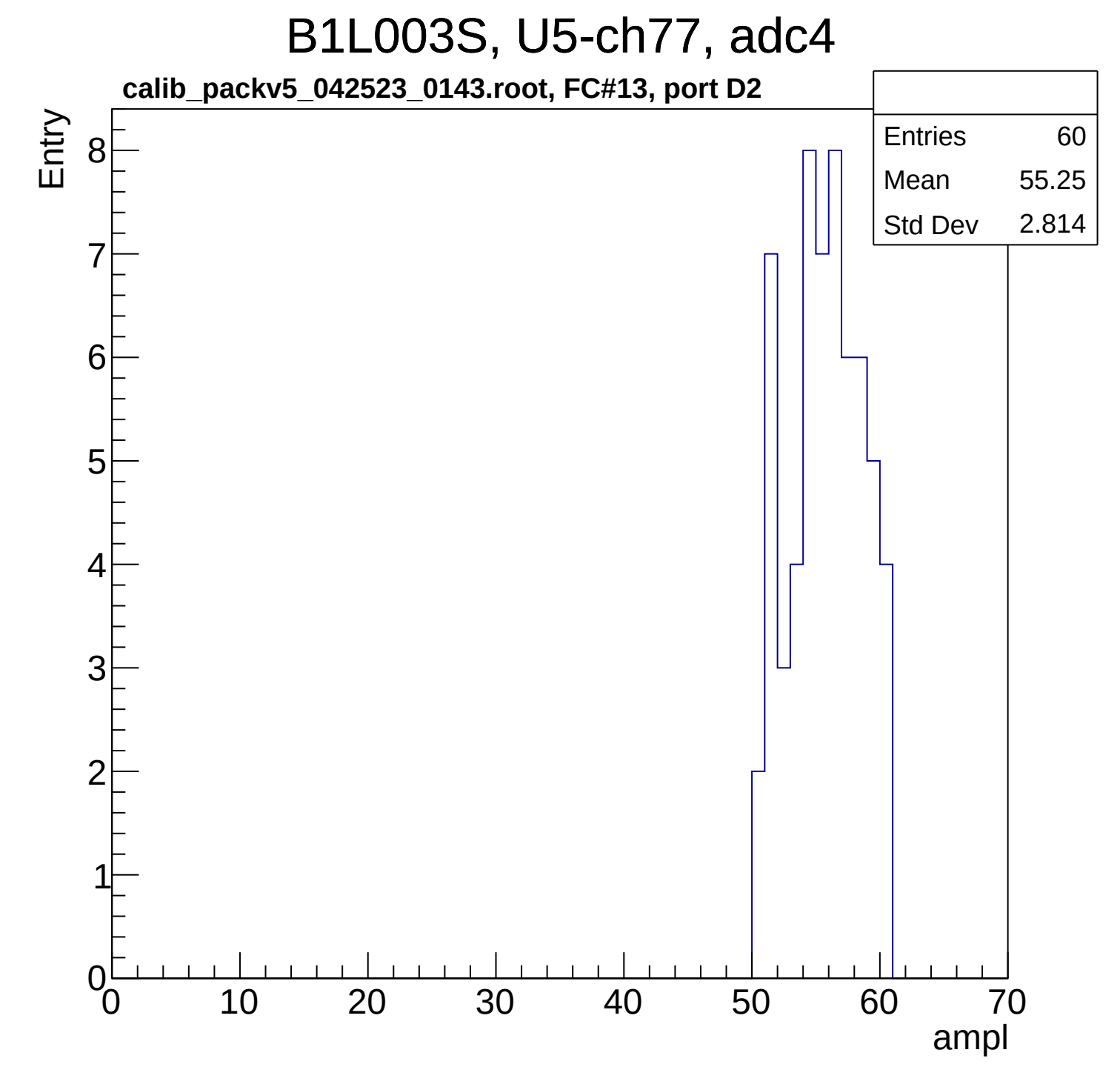
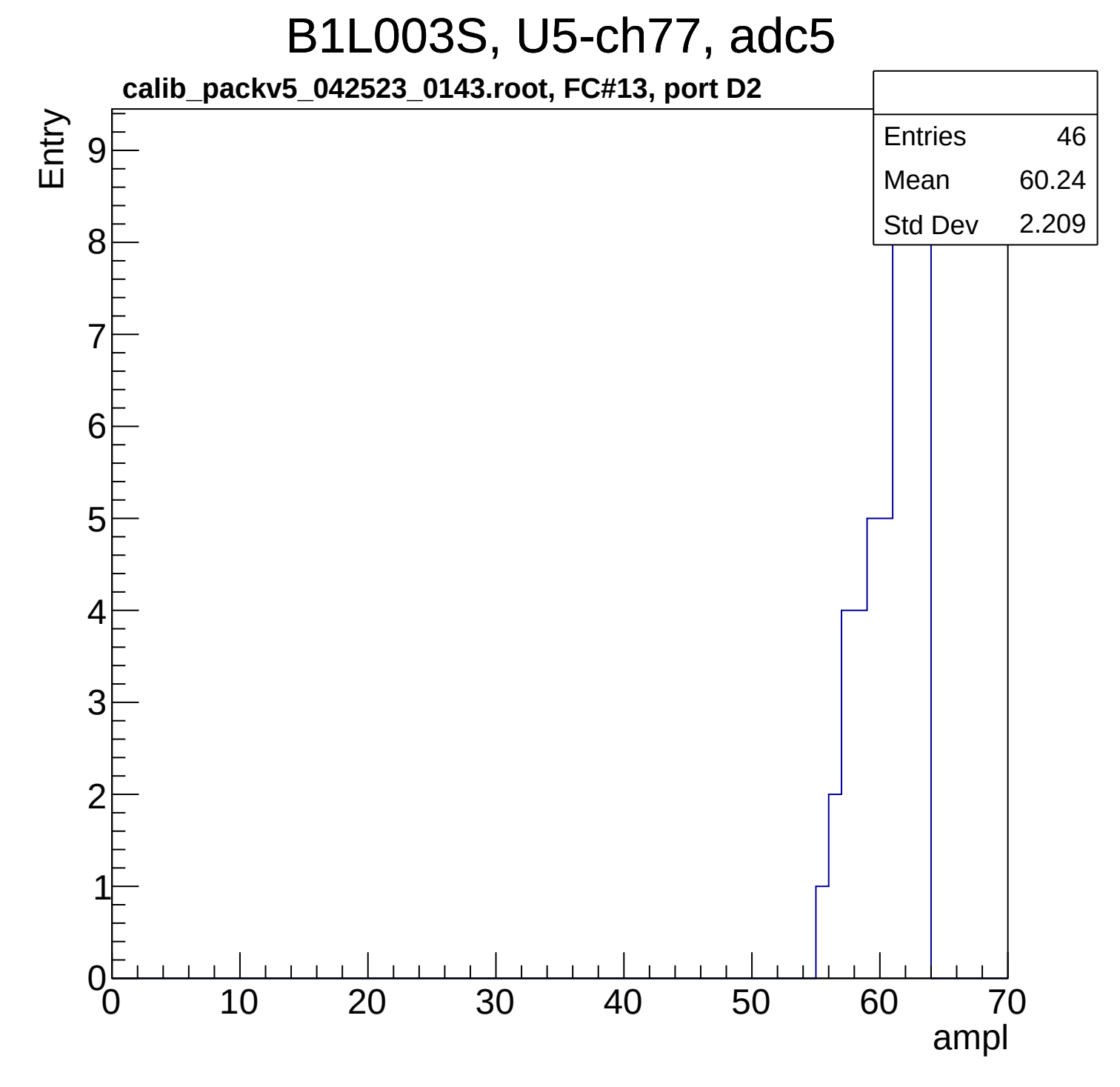
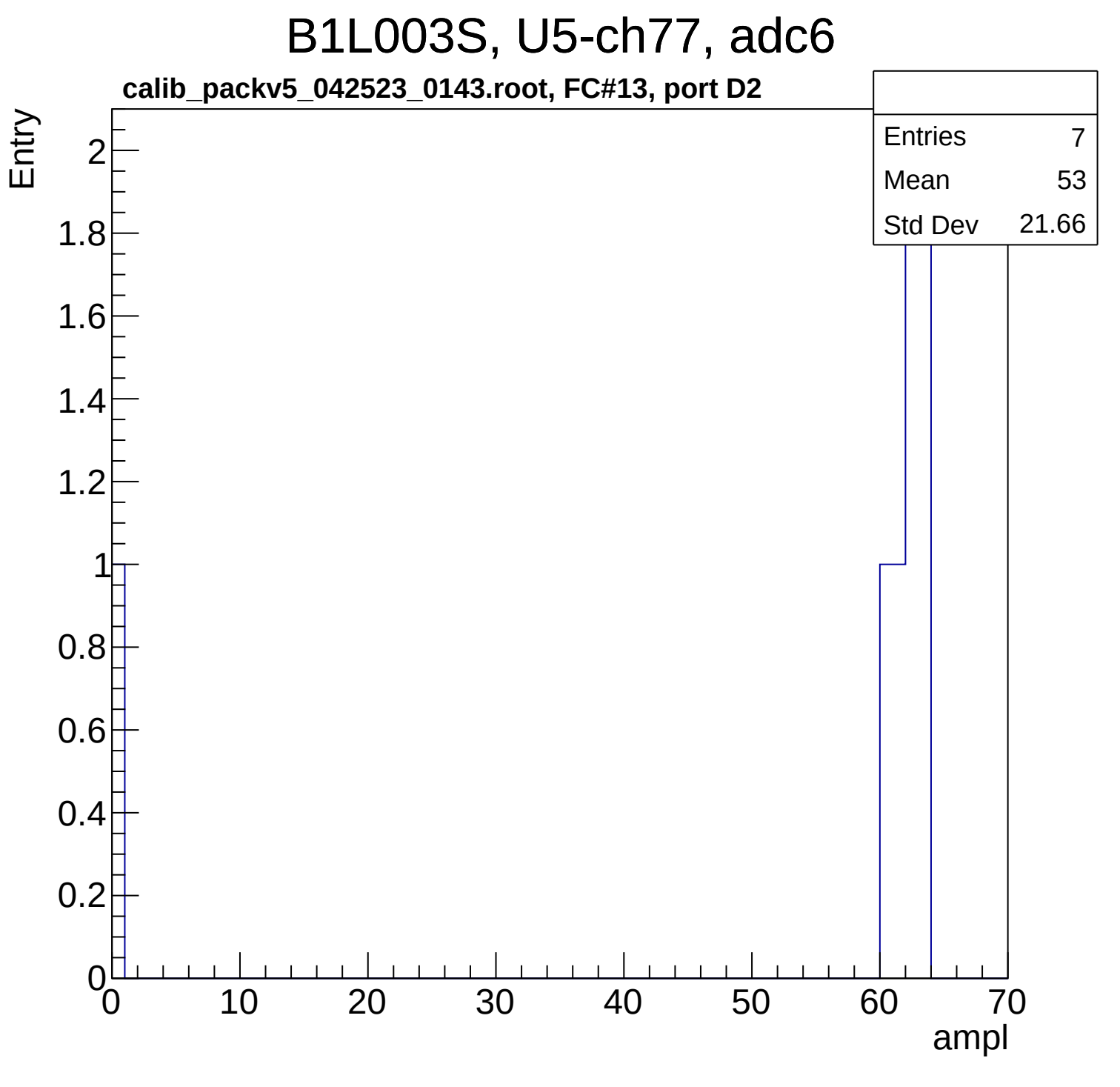
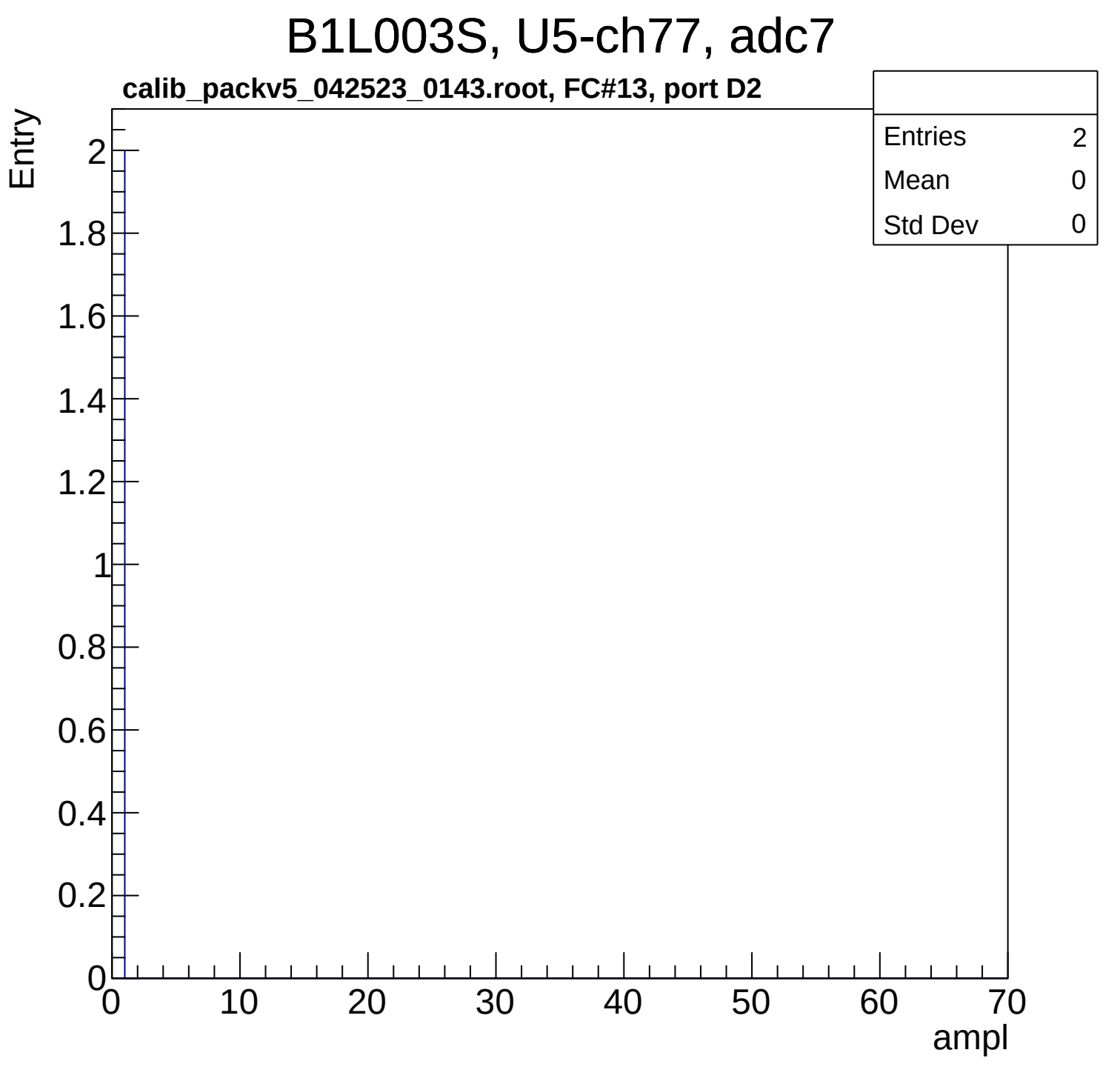
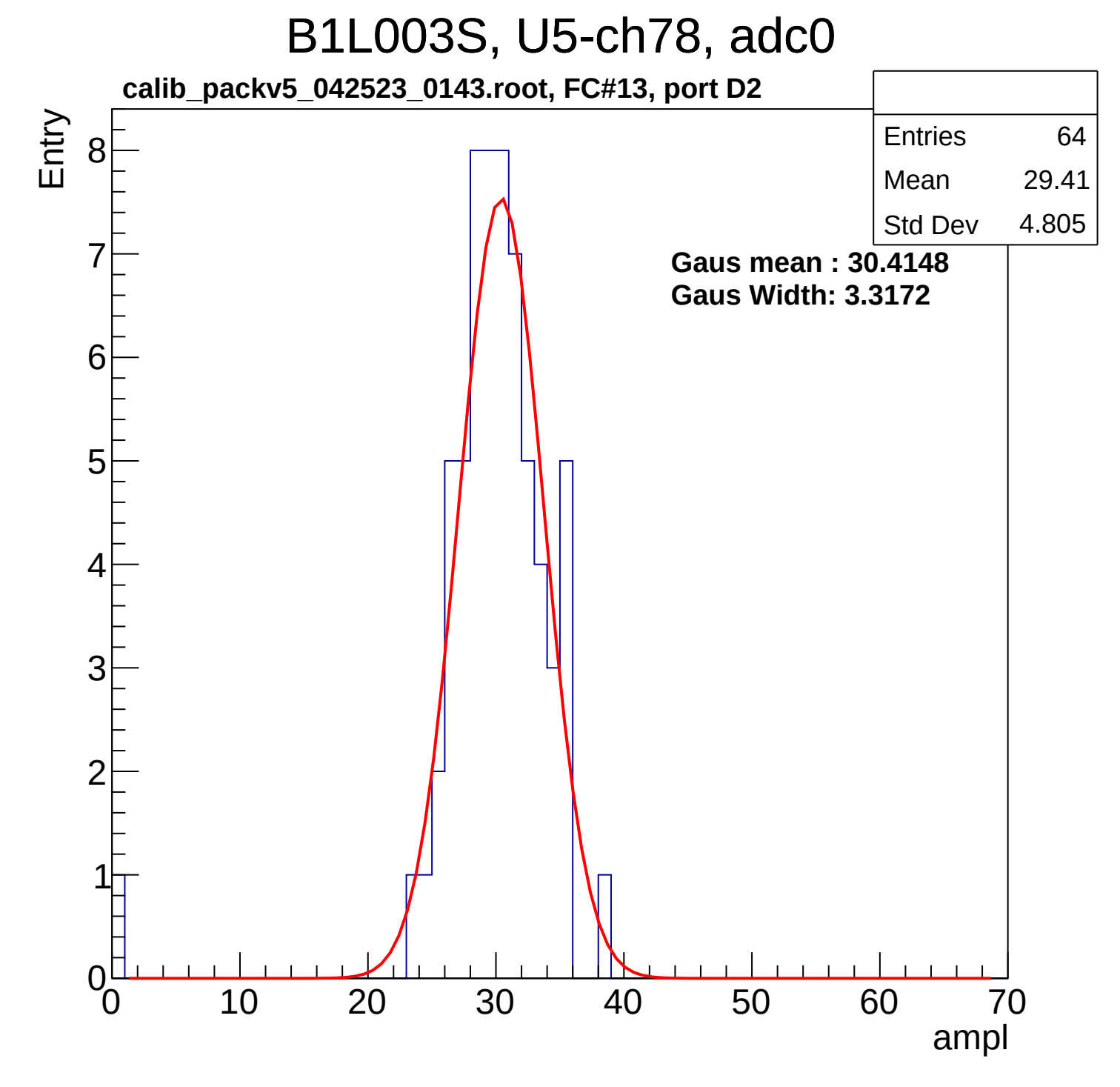

B1L003S, U5-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.74
Std Dev 3.135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

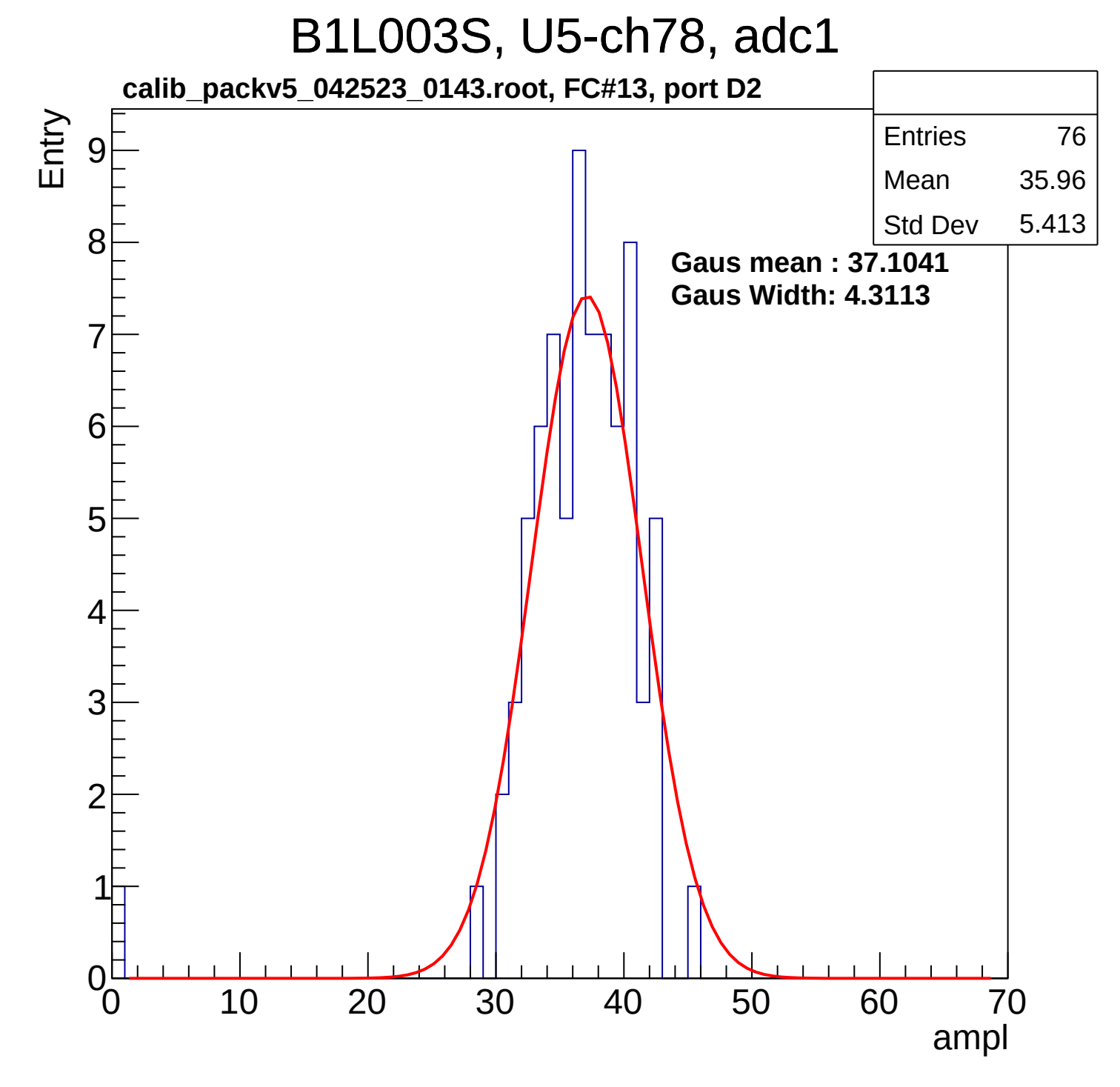

B1L003S, U5-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.25
Std Dev 2.814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

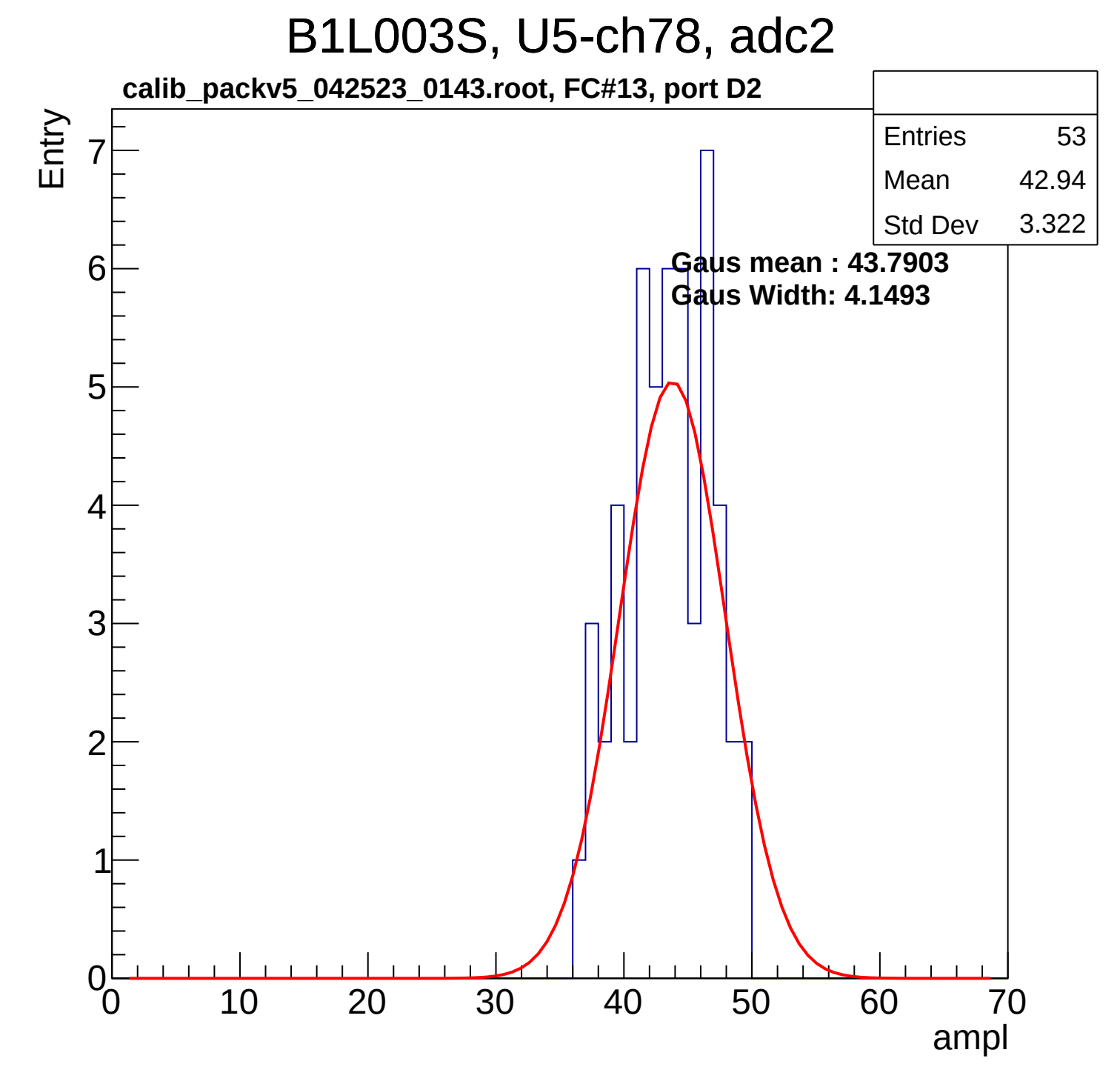

B1L003S, U5-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 60.24
Std Dev 2.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

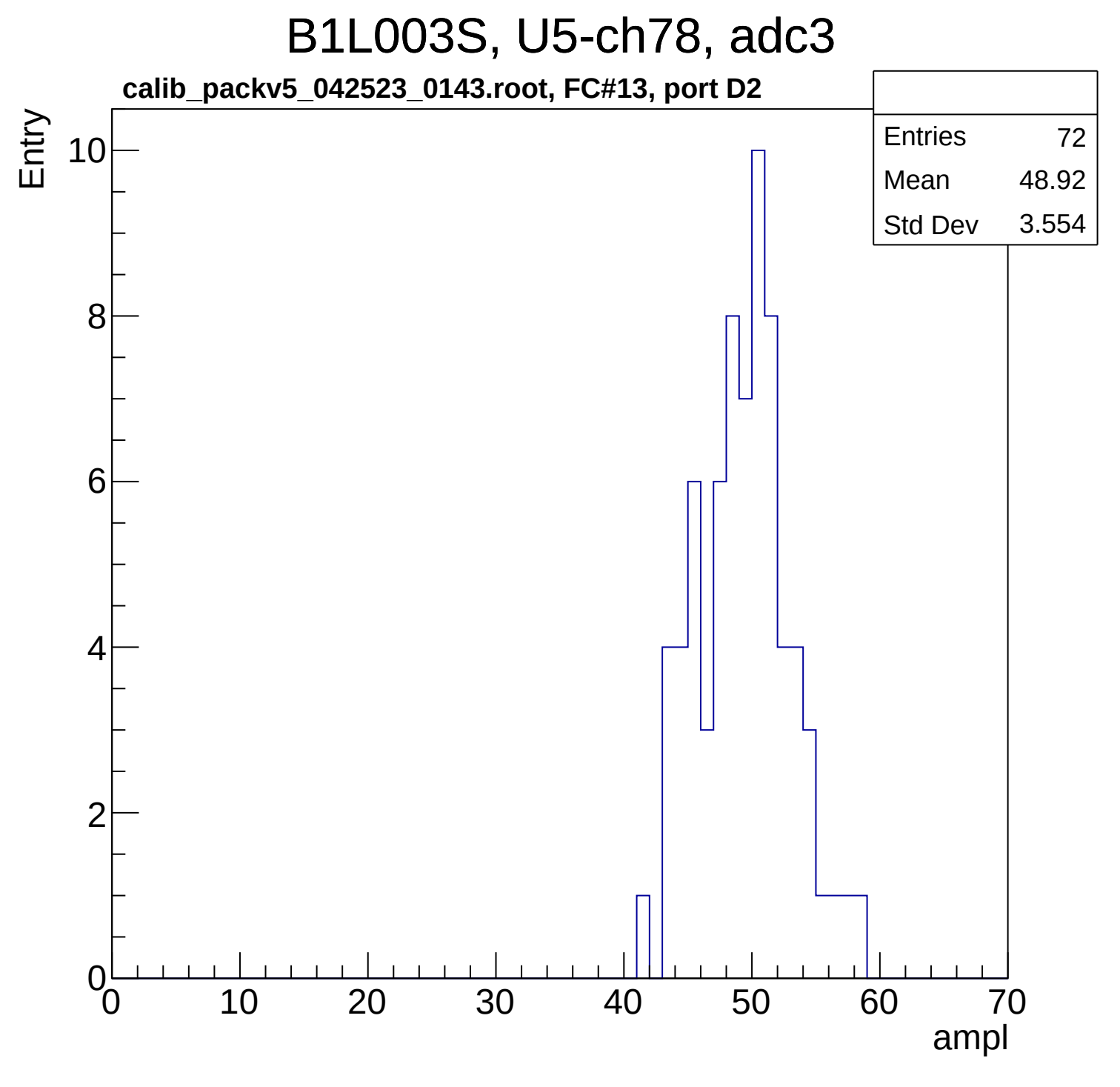

B1L003S, U5-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53
Std Dev 21.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

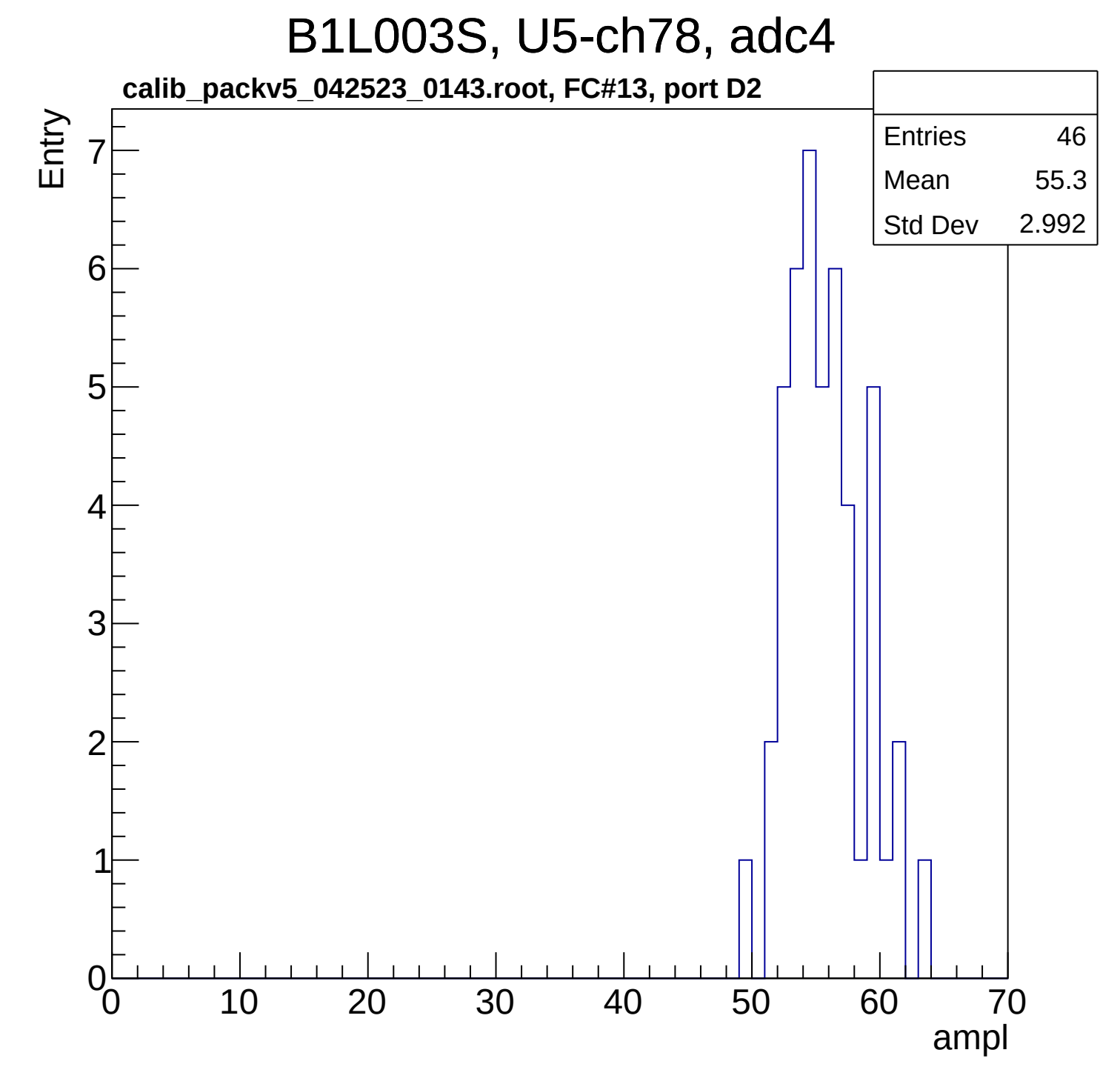

B1L003S, U5-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.41
Std Dev 4.805
Gaus mean : 30.4148
Gaus Width: 3.3172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.96
Std Dev 5.413
Gaus mean : 37.1041
Gaus Width: 4.3113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.94
Std Dev 3.322
Gaus mean : 43.7903
Gaus Width: 4.1493
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.92
Std Dev 3.554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.3
Std Dev 2.992
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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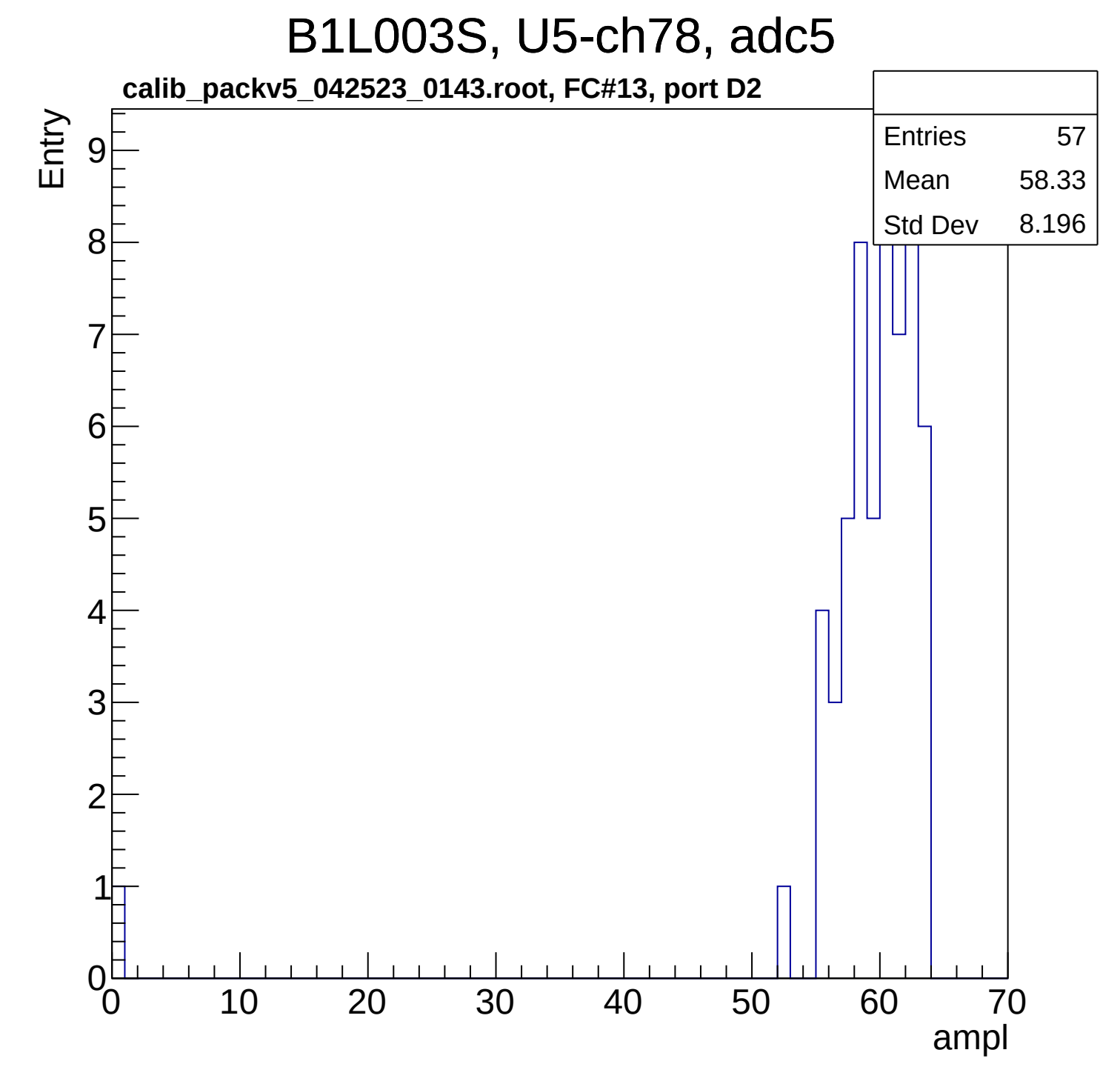
B1L003S, U5-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.33
Std Dev 8.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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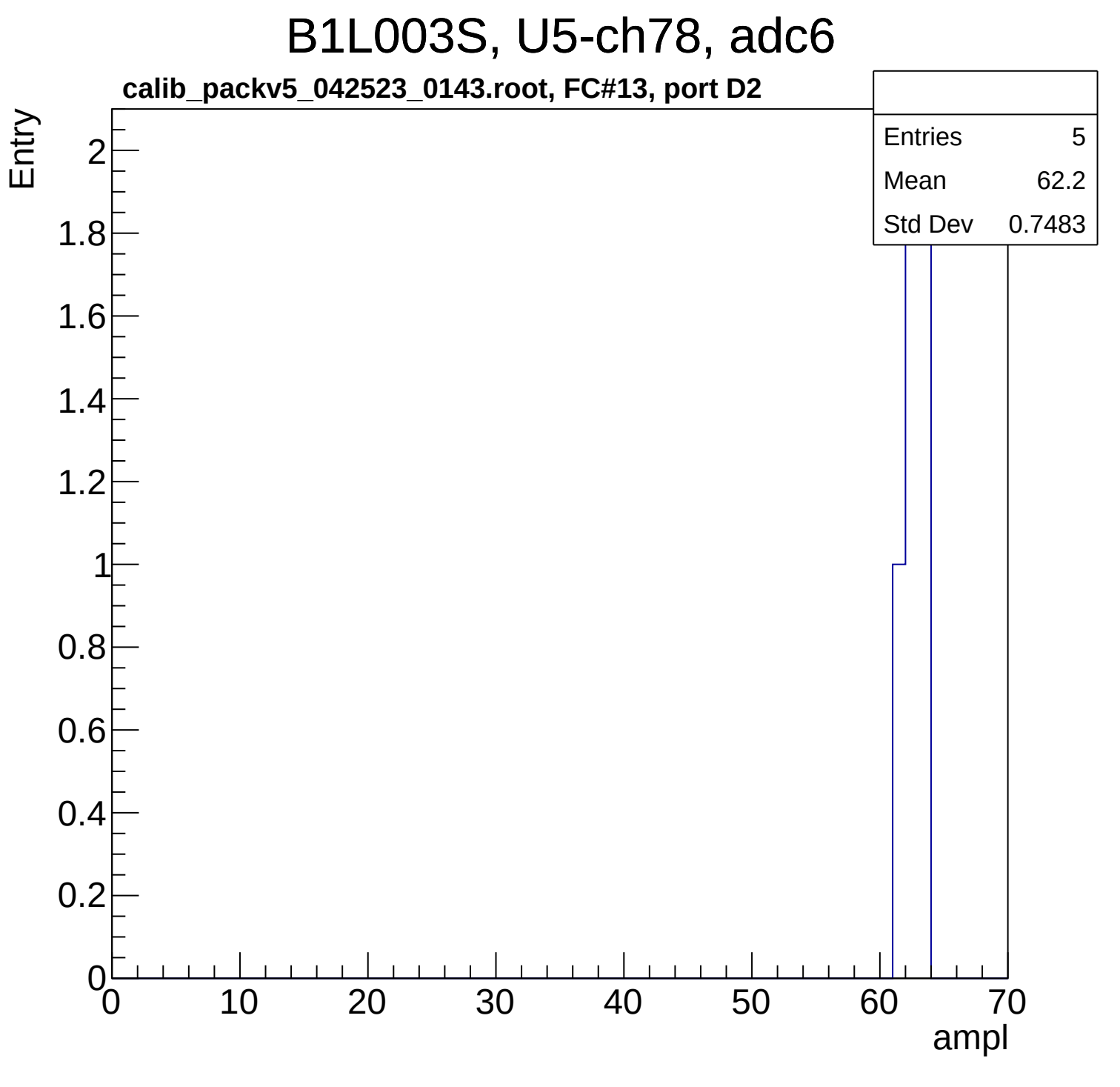
B1L003S, U5-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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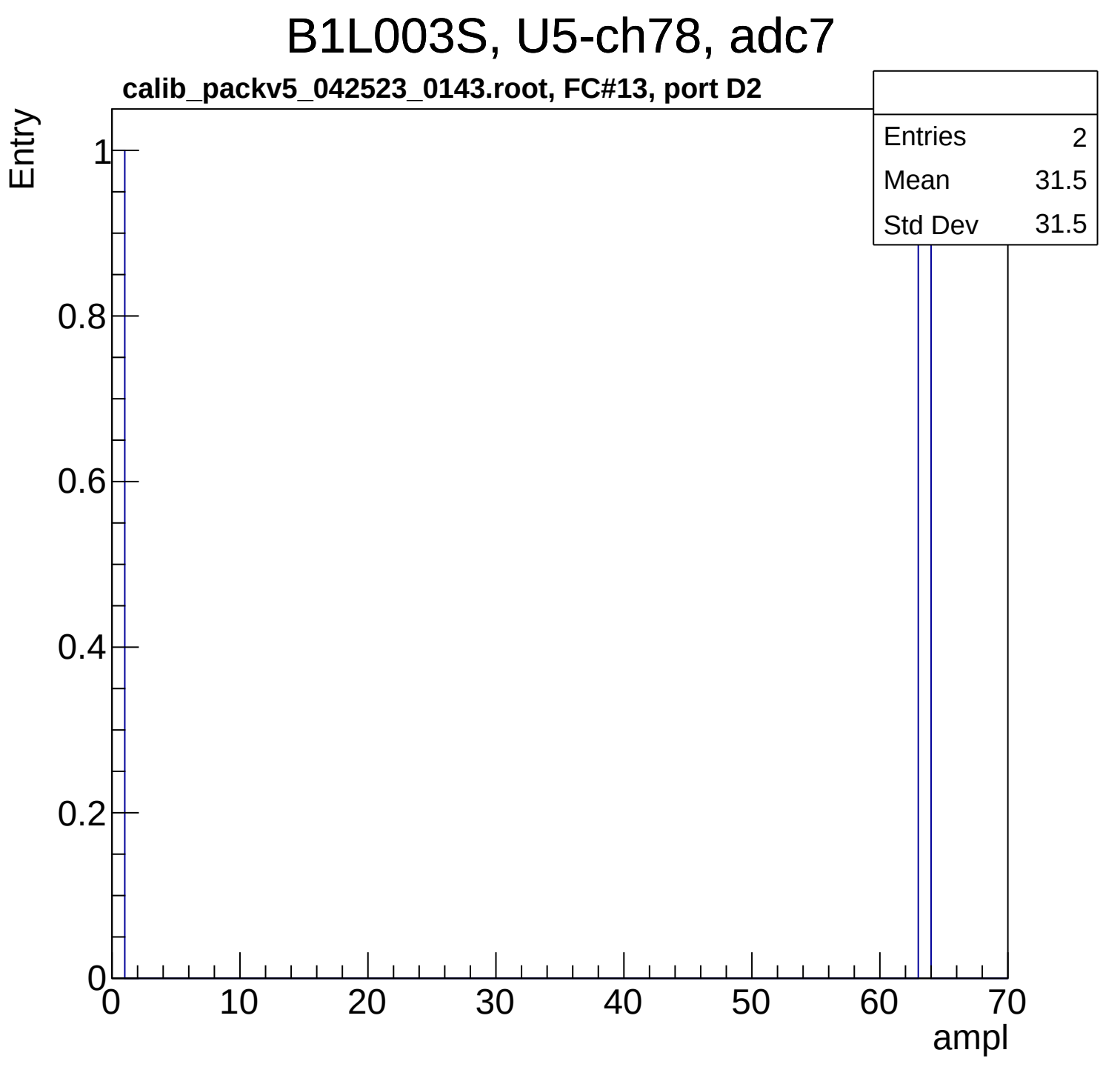
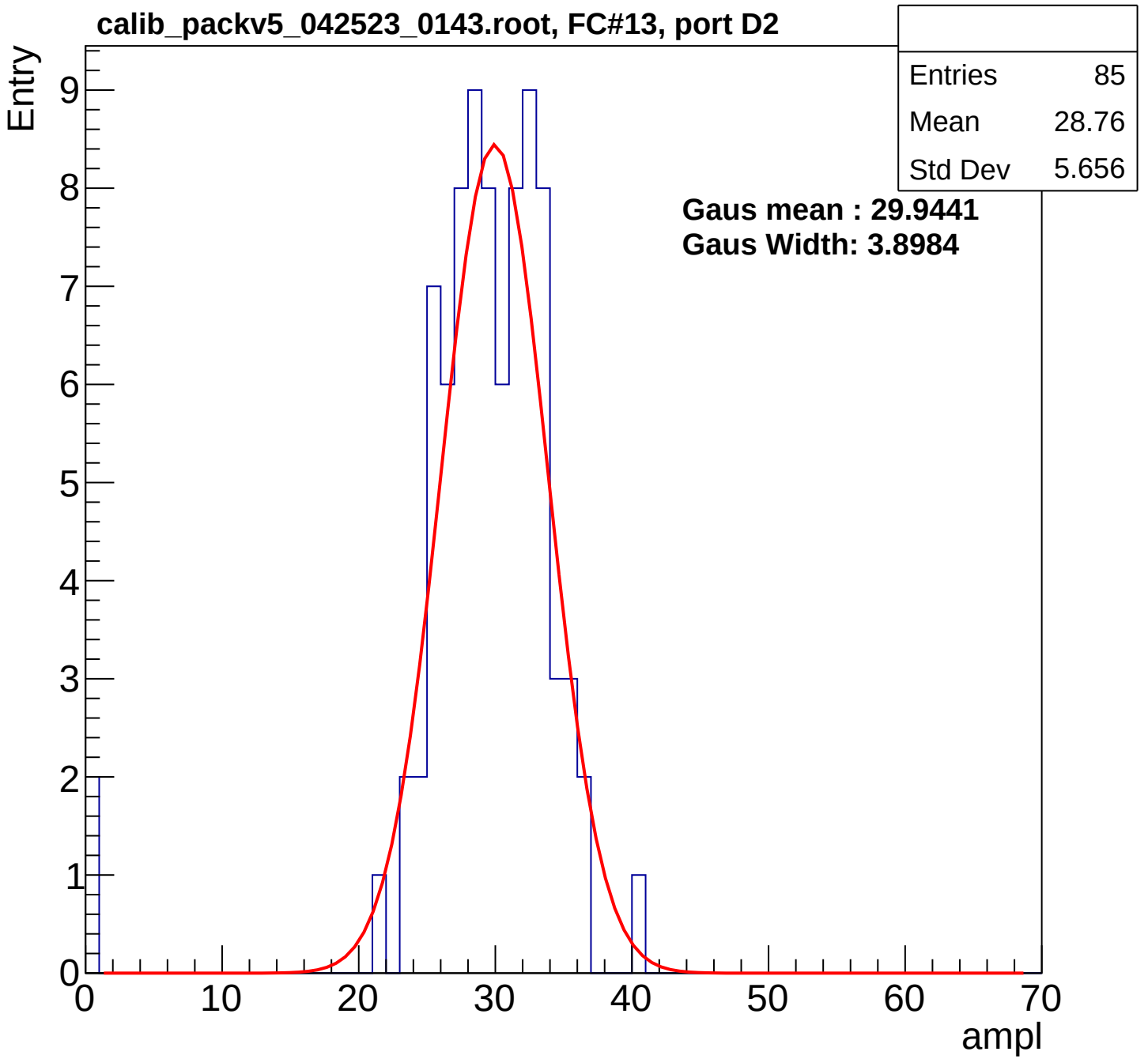
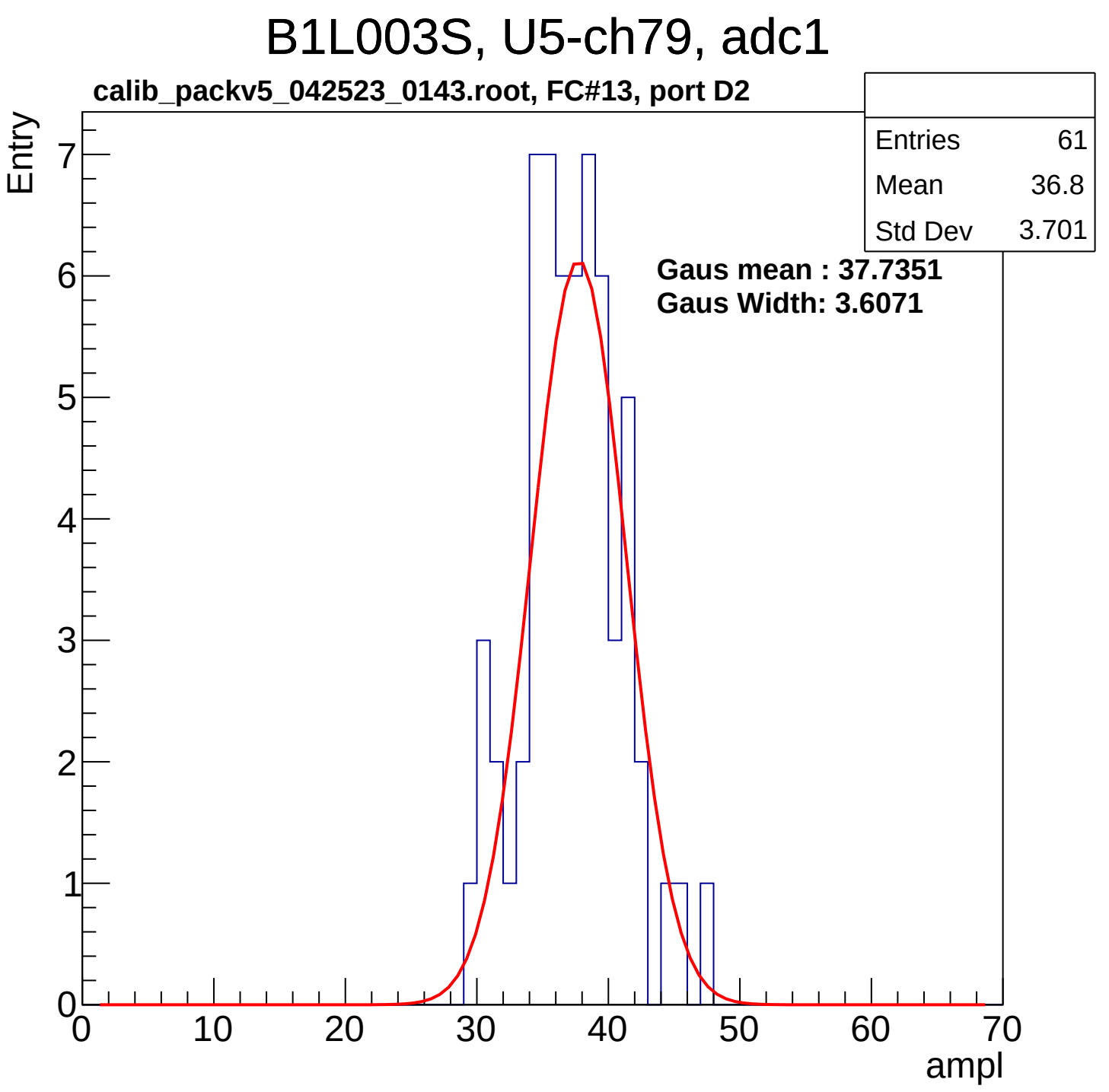
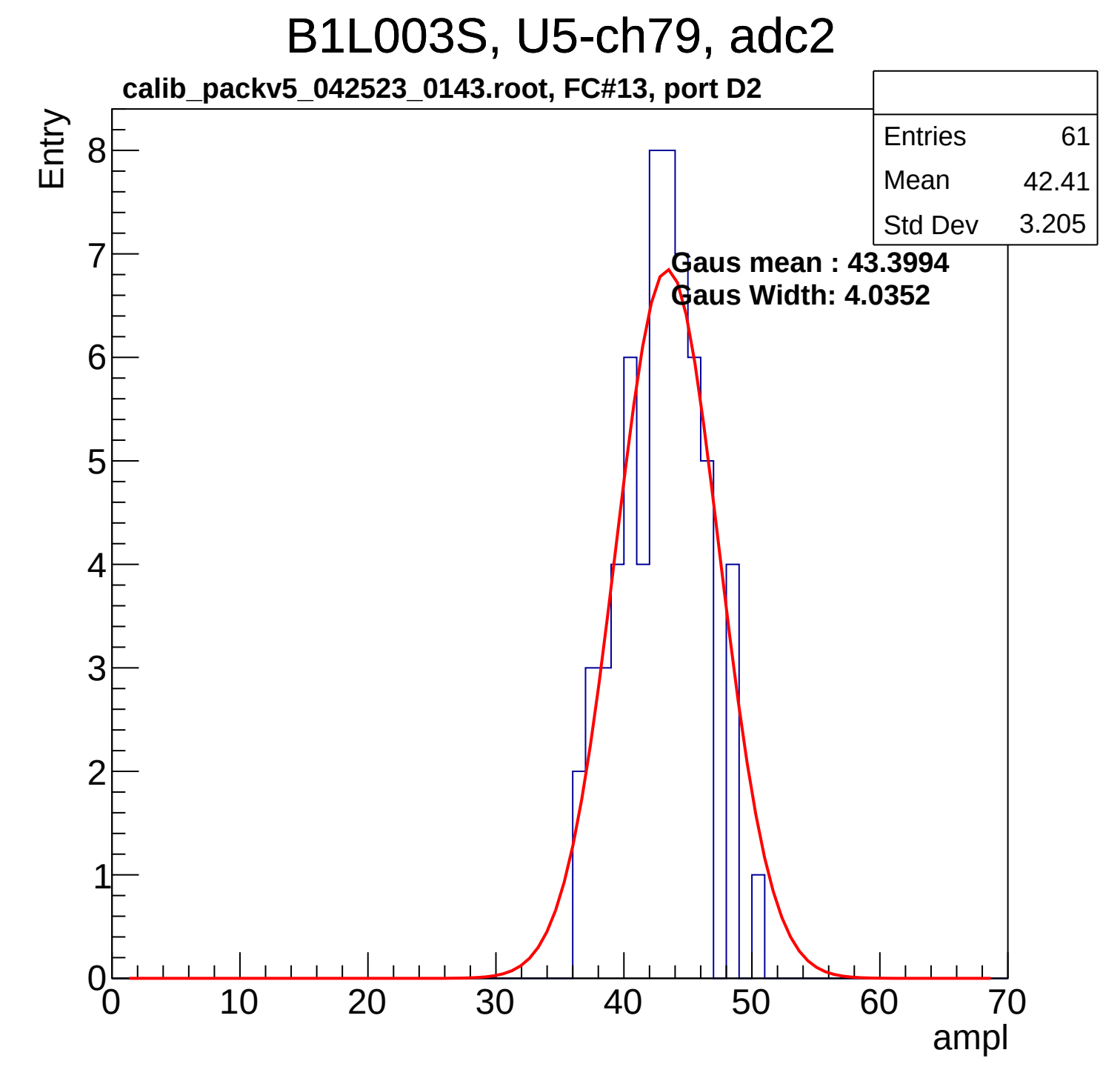

B1L003S, U5-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 28.76
Std Dev 5.656
Gaus mean : 29.9441
Gaus Width: 3.8984
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.8
Std Dev 3.701
Gaus mean : 37.7351
Gaus Width: 3.6071
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.41
Std Dev 3.205
Gaus mean : 43.3994
Gaus Width: 4.0352
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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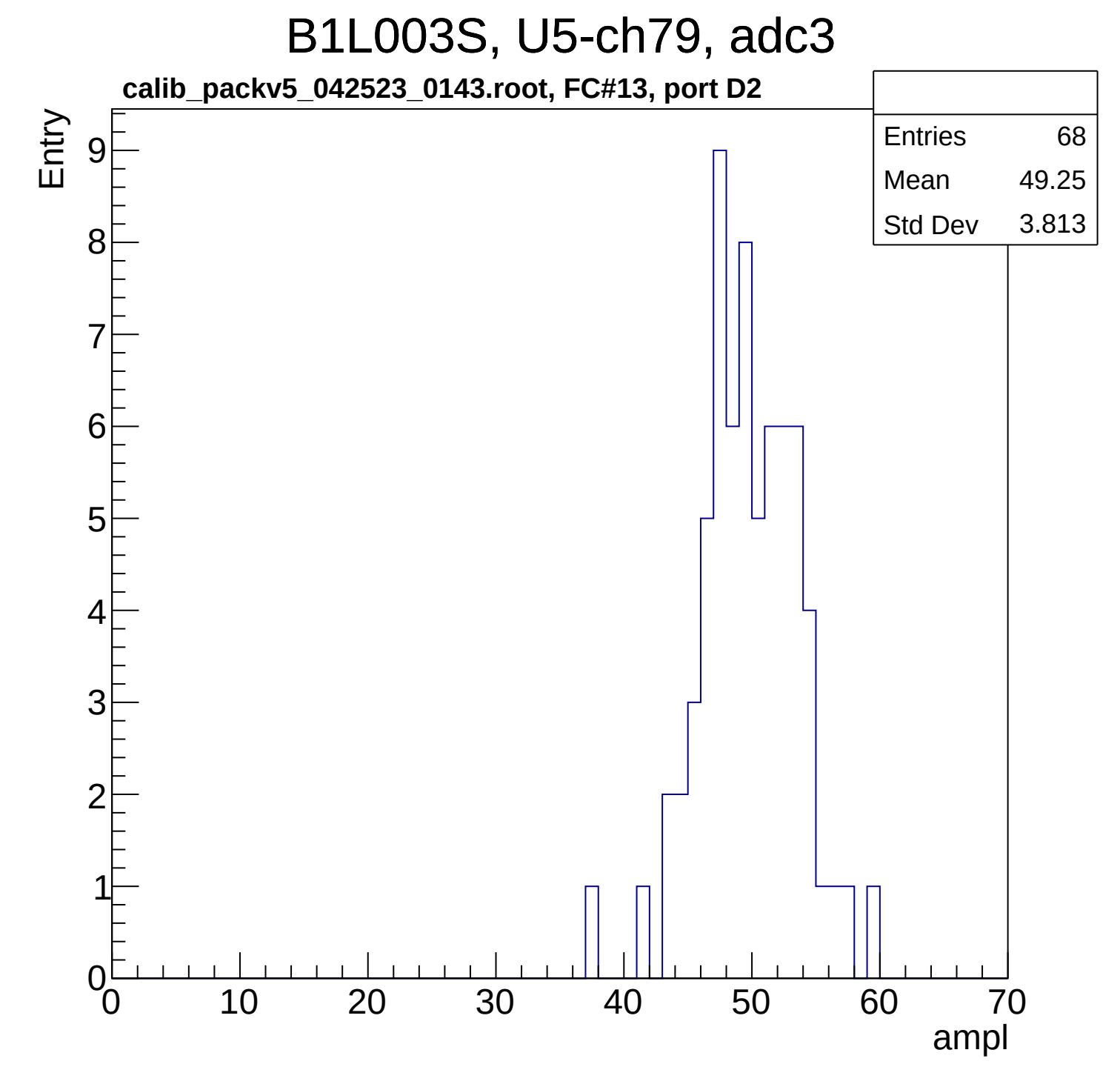
B1L003S, U5-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.25
Std Dev 3.813
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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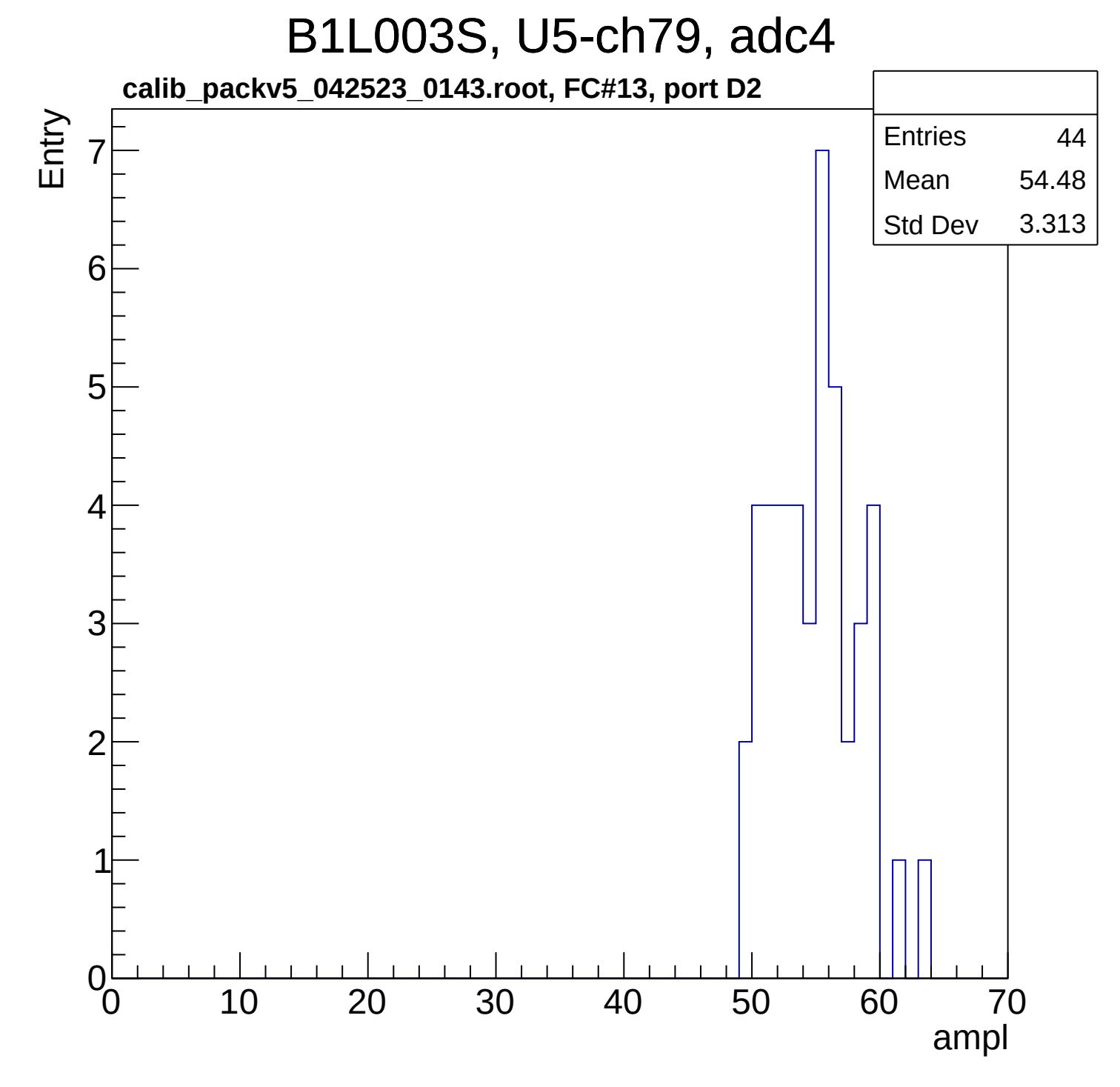
B1L003S, U5-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 54.48
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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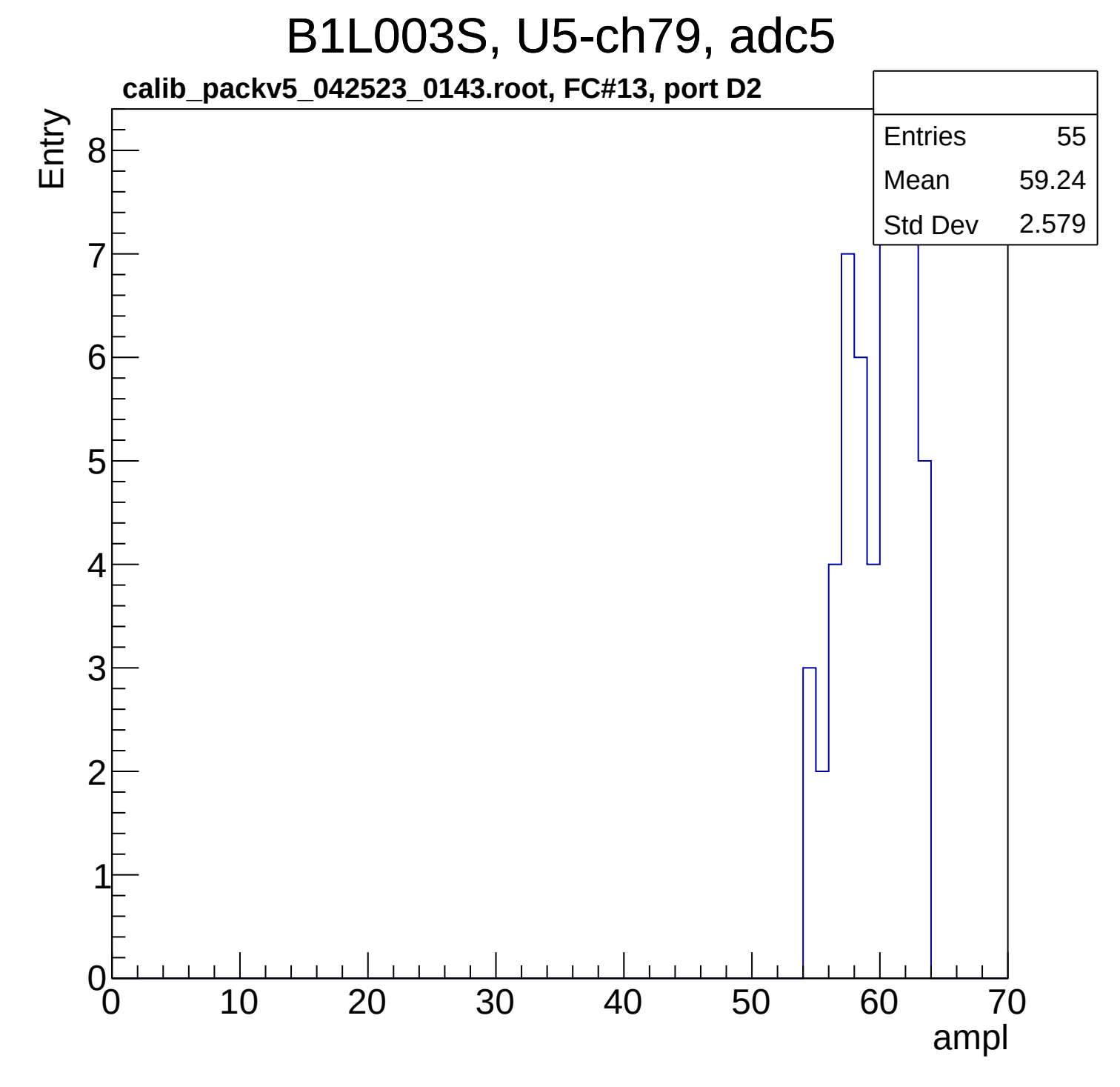
B1L003S, U5-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.24
Std Dev 2.579
Entry
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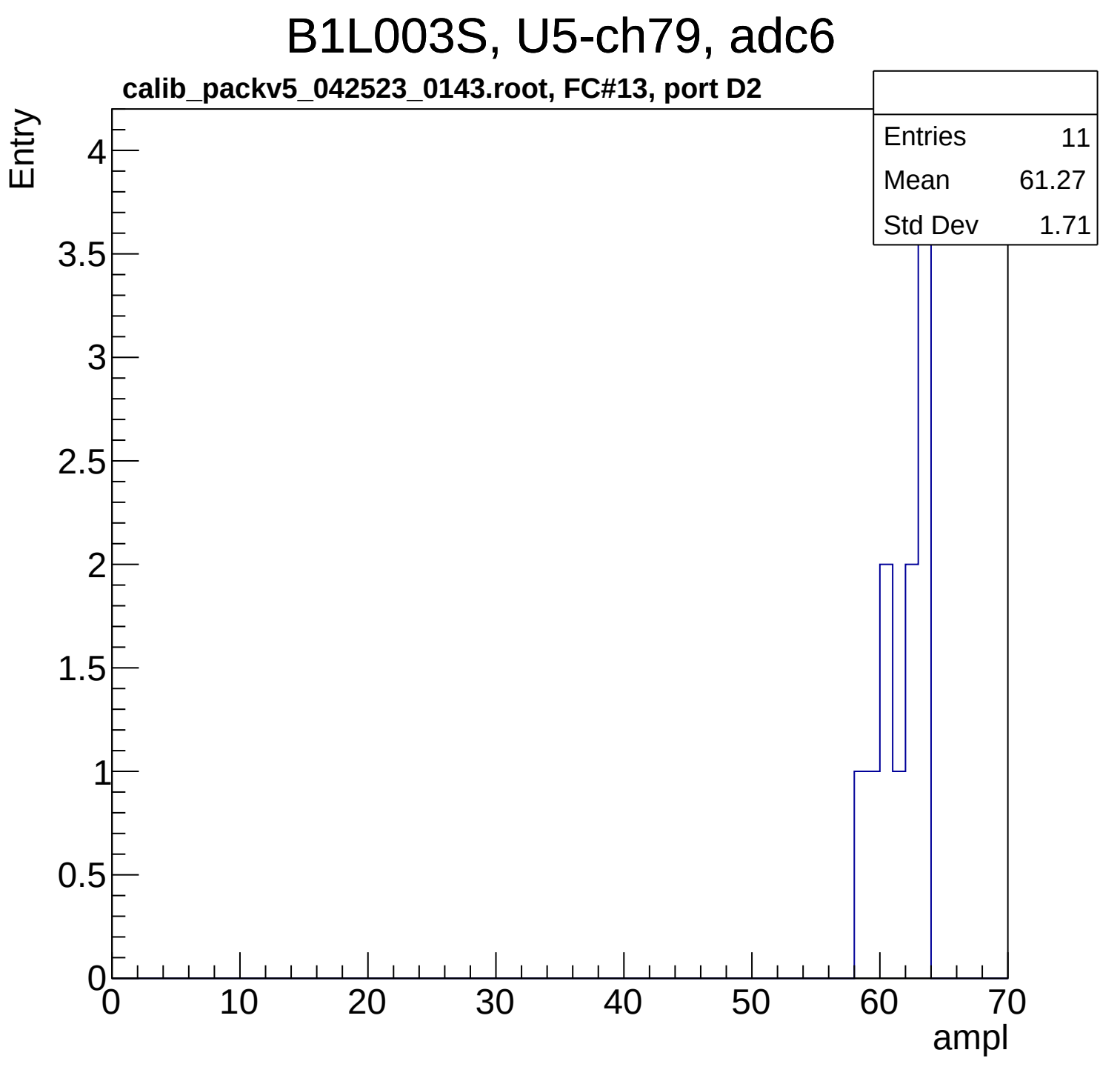
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.27
Std Dev 1.71
Entry
ampl
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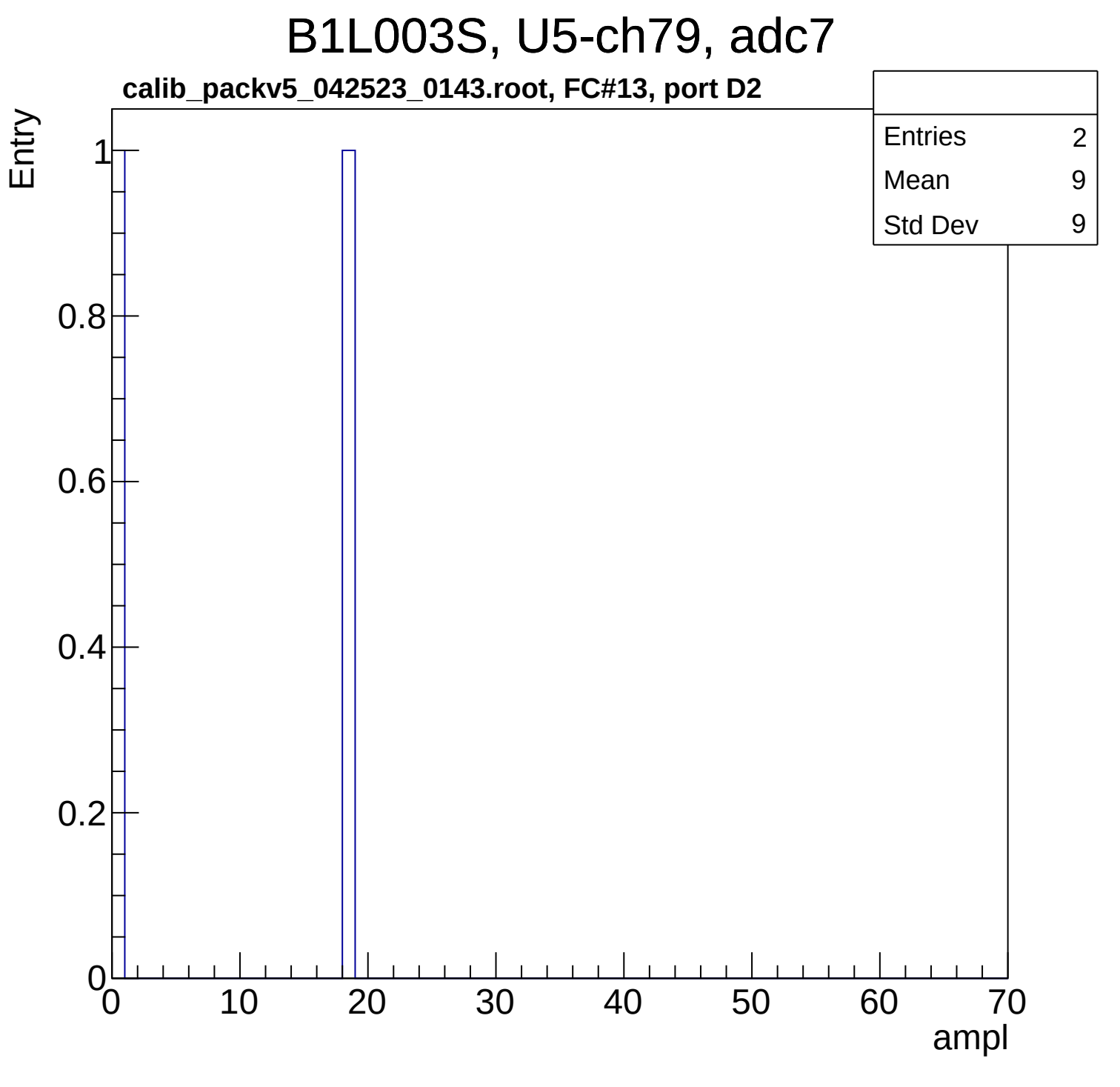
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 9
Std Dev 9
Entry
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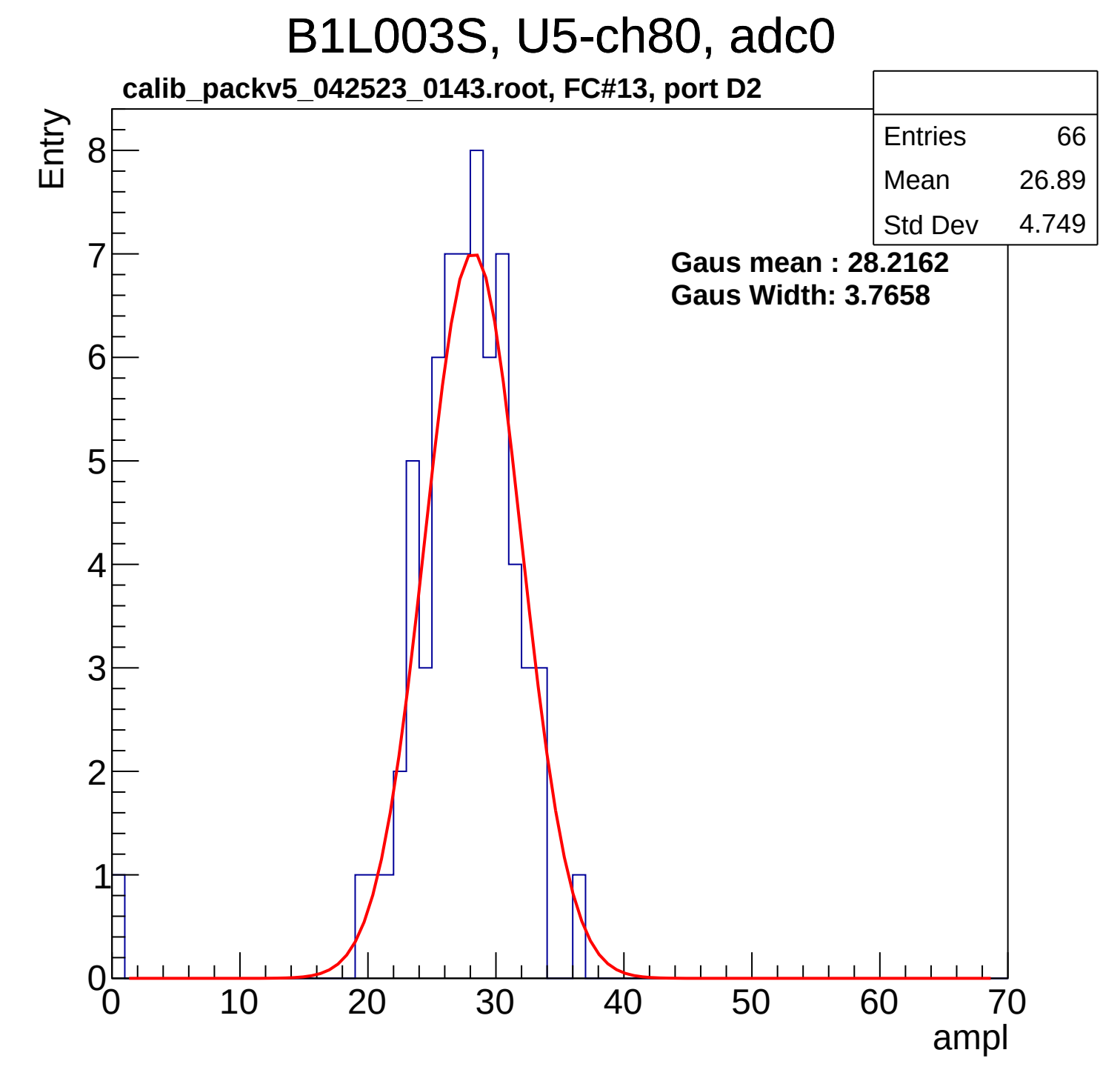
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 26.89
Std Dev 4.749
Gaus mean : 28.2162
Gaus Width: 3.7658
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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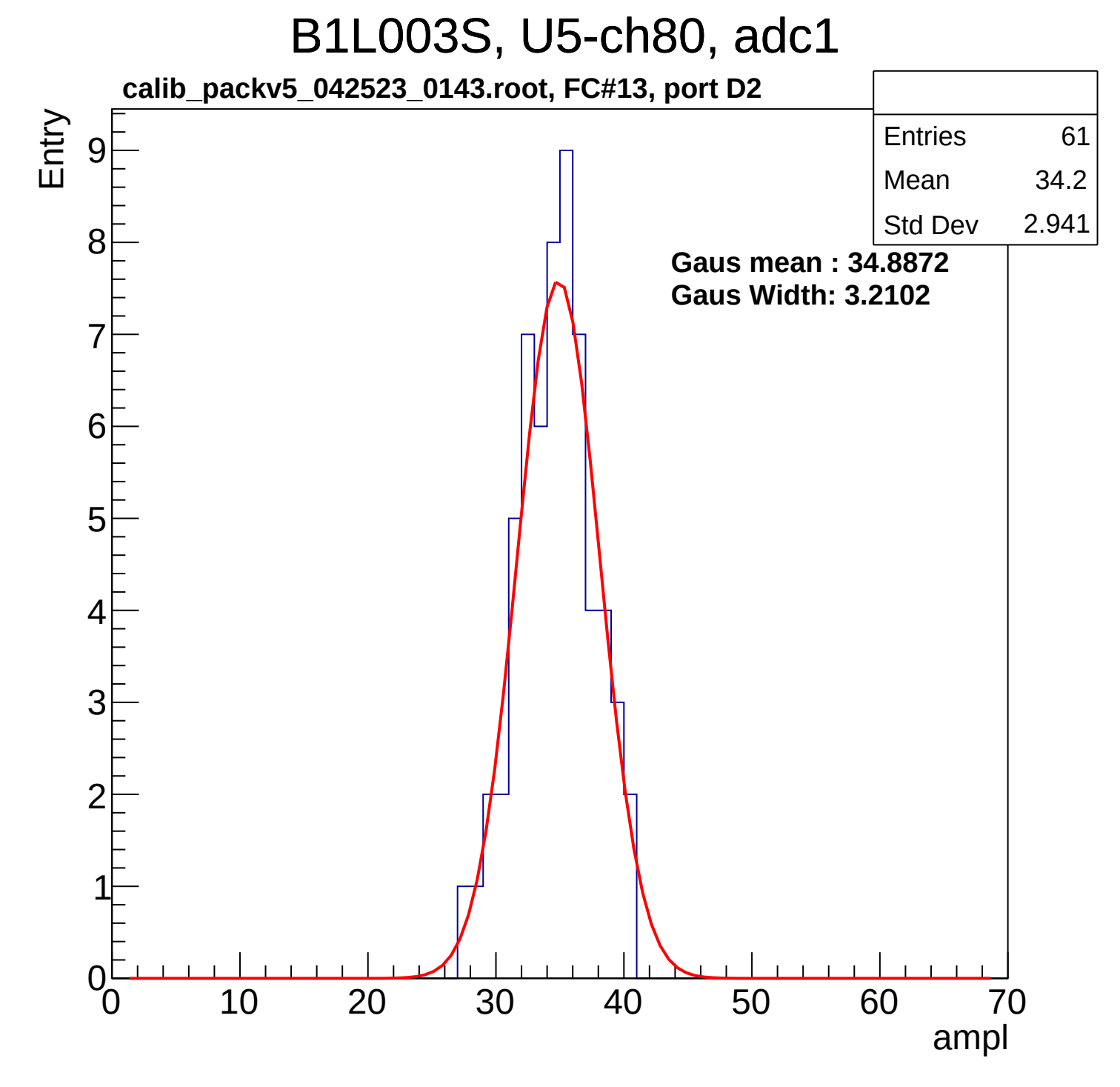
B1L003S, U5-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 34.2
Std Dev 2.941
Gaus mean : 34.8872
Gaus Width: 3.2102
Entry
ampl
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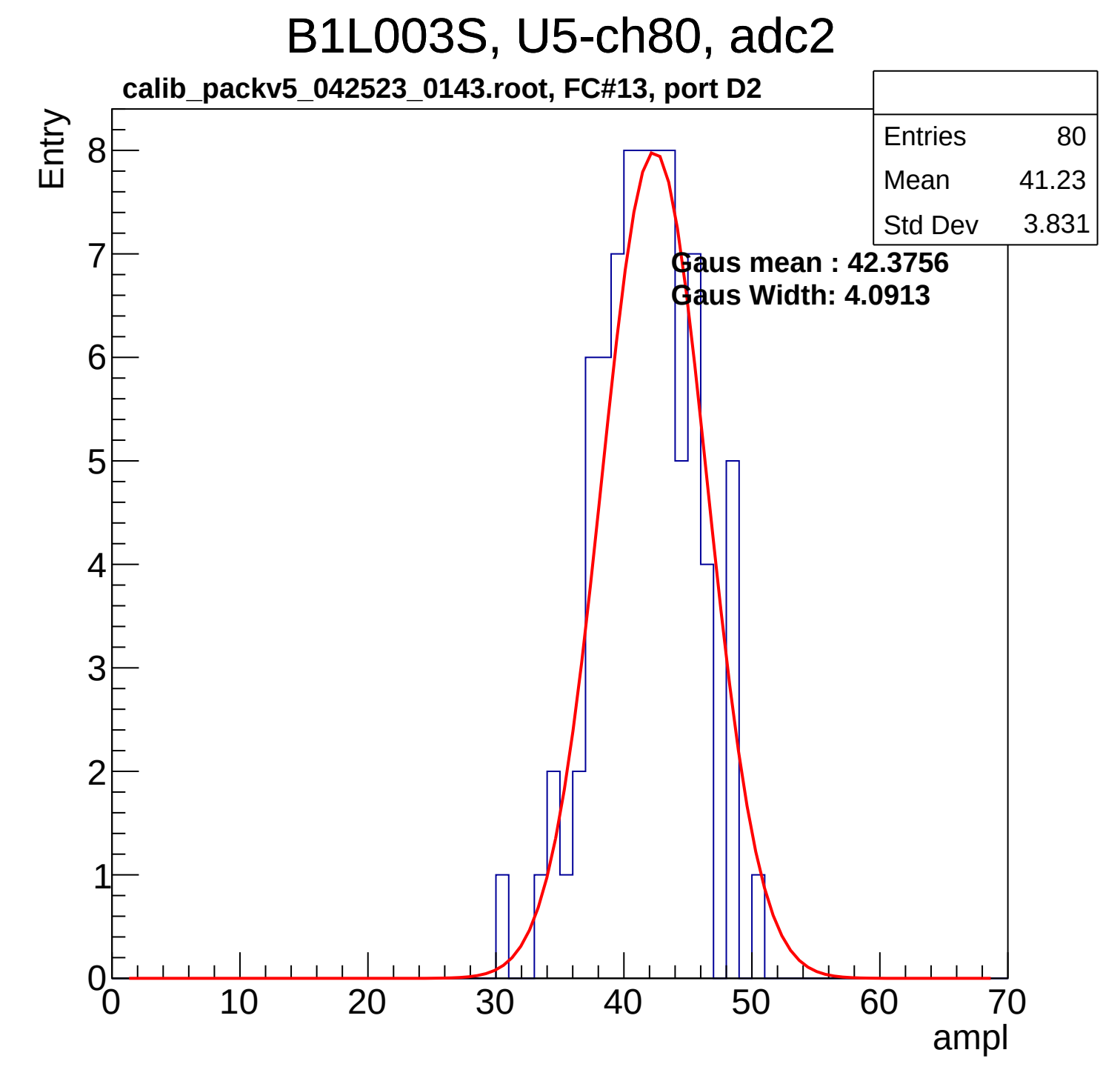
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 41.23
Std Dev 3.831
Gaus mean : 42.3756
Gaus Width: 4.0913
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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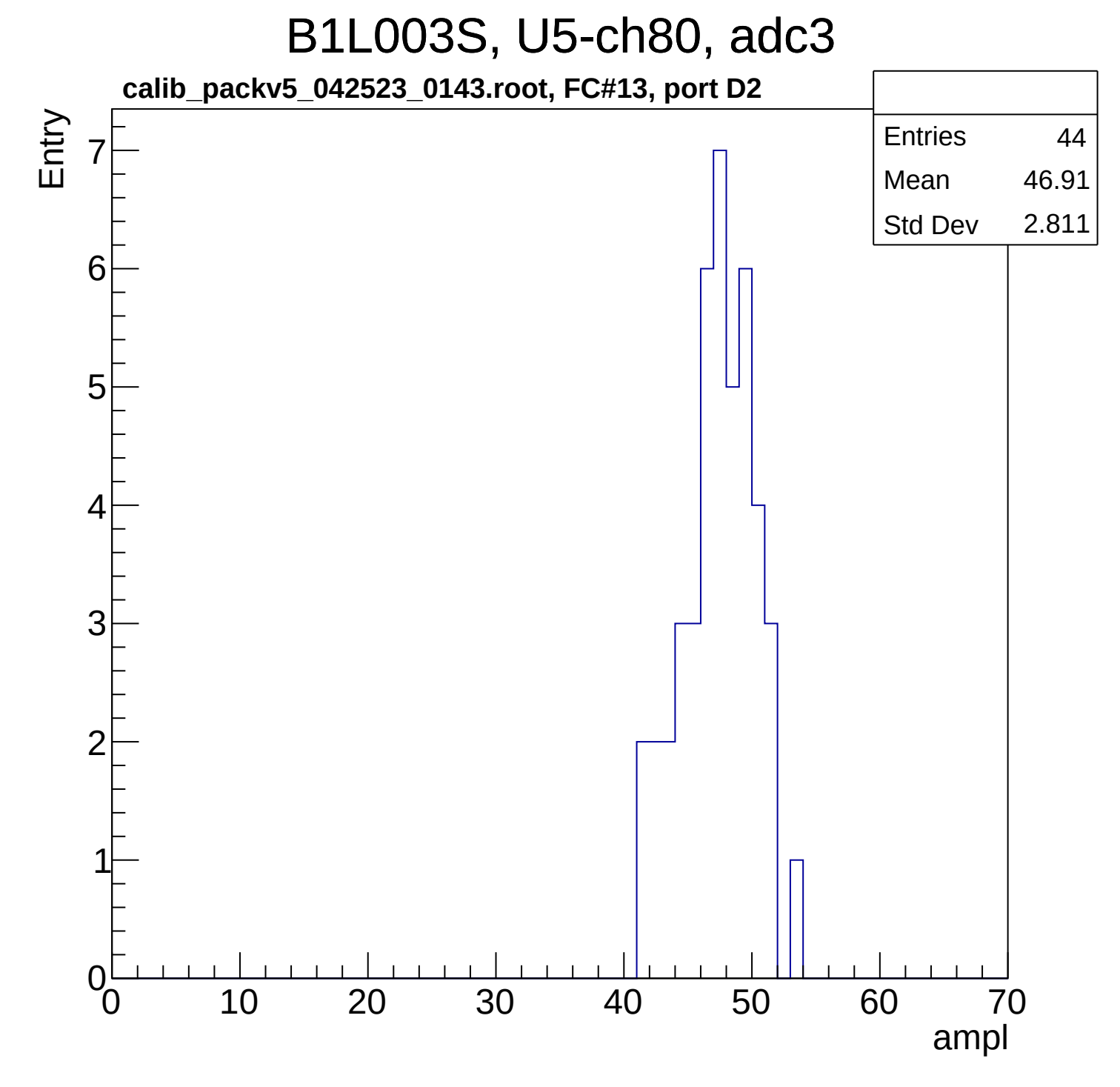
B1L003S, U5-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 46.91
Std Dev 2.811
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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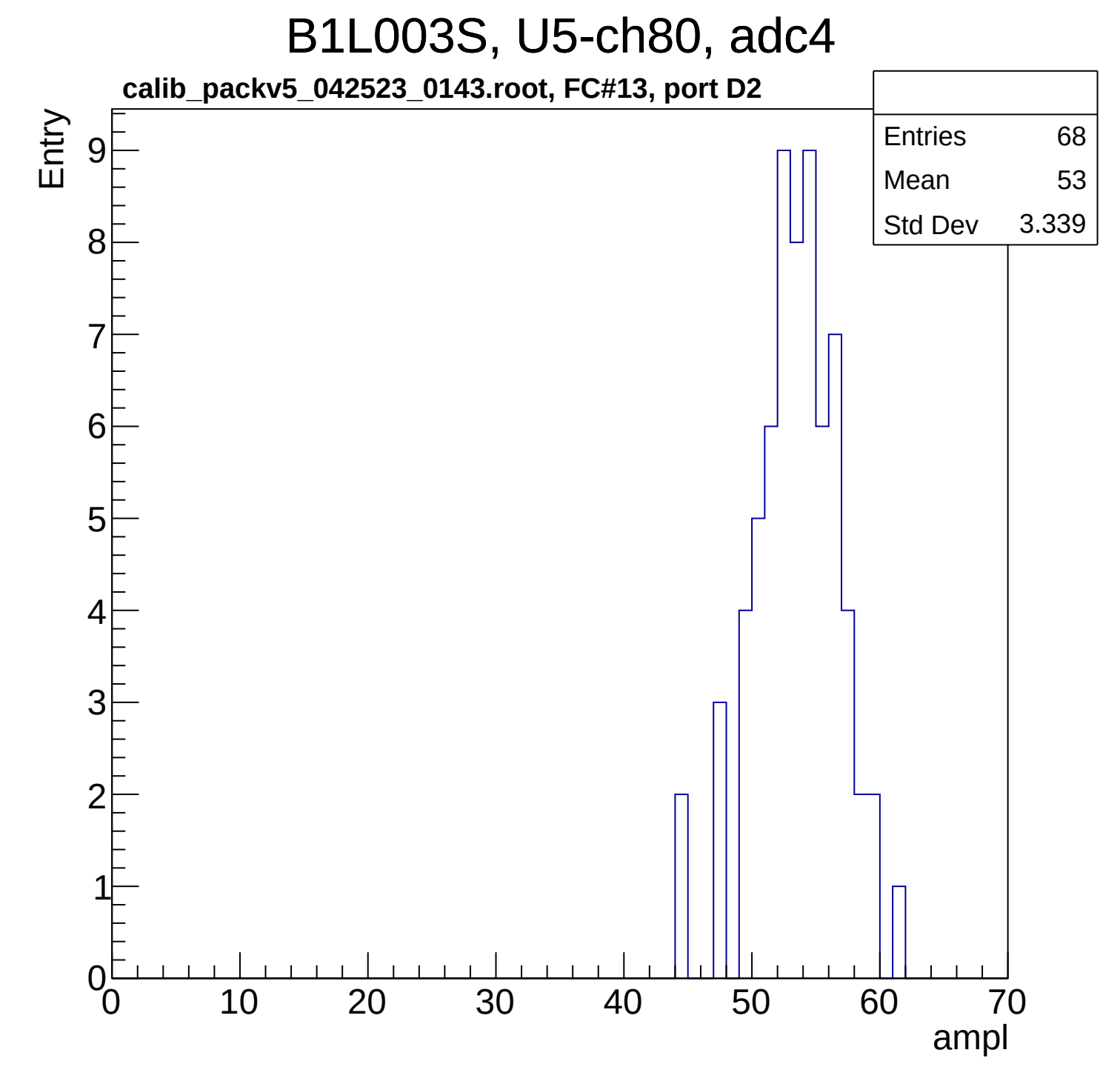
B1L003S, U5-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 53
Std Dev 3.339
Entry
ampl
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2
3
4
5
6
7
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9
0
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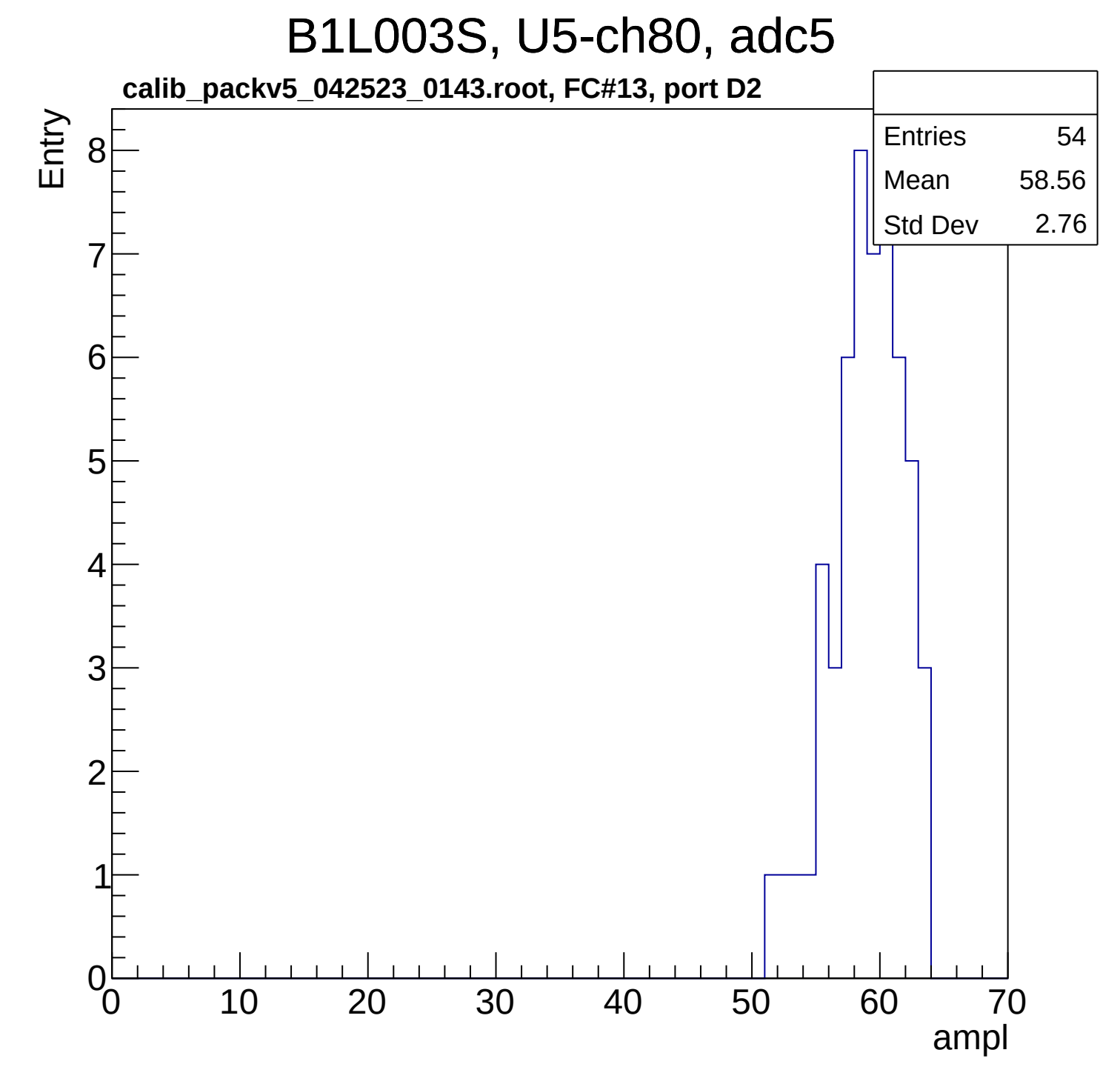
40
50
60
70


B1L003S, U5-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.56
Std Dev 2.76
Entry
ampl
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4
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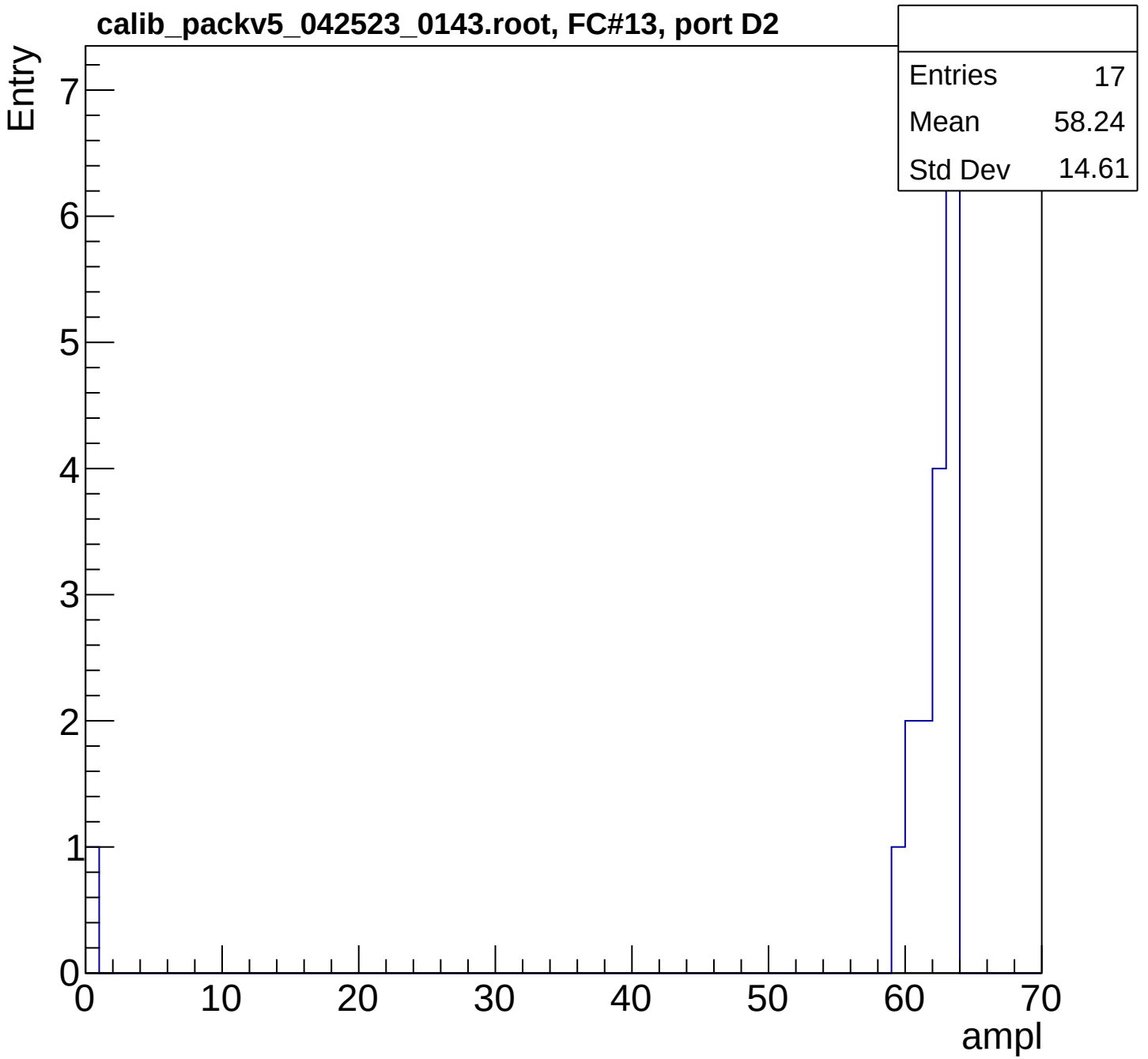
B1L003S, U5-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 58.24
Std Dev 14.61
Entry
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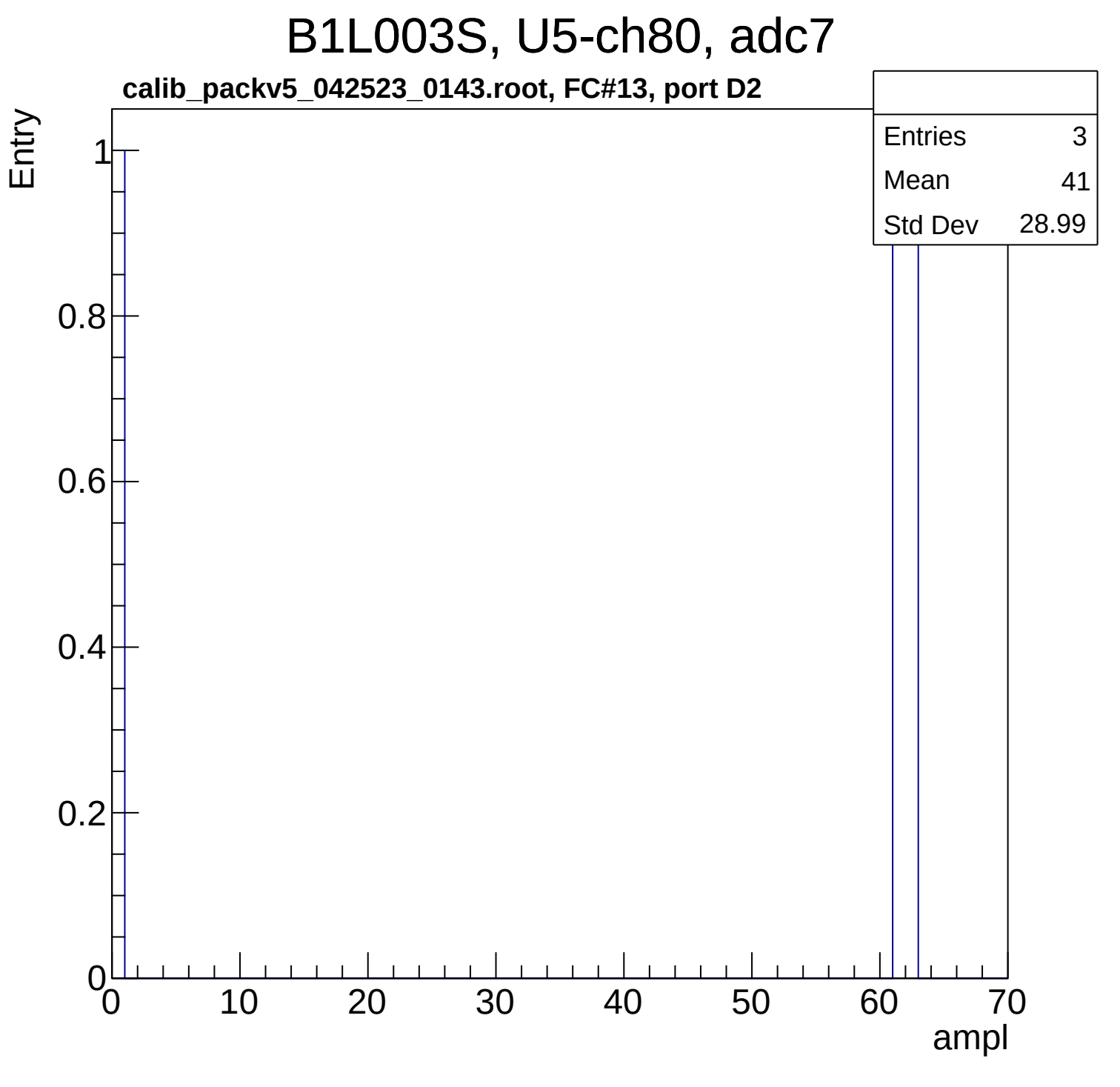
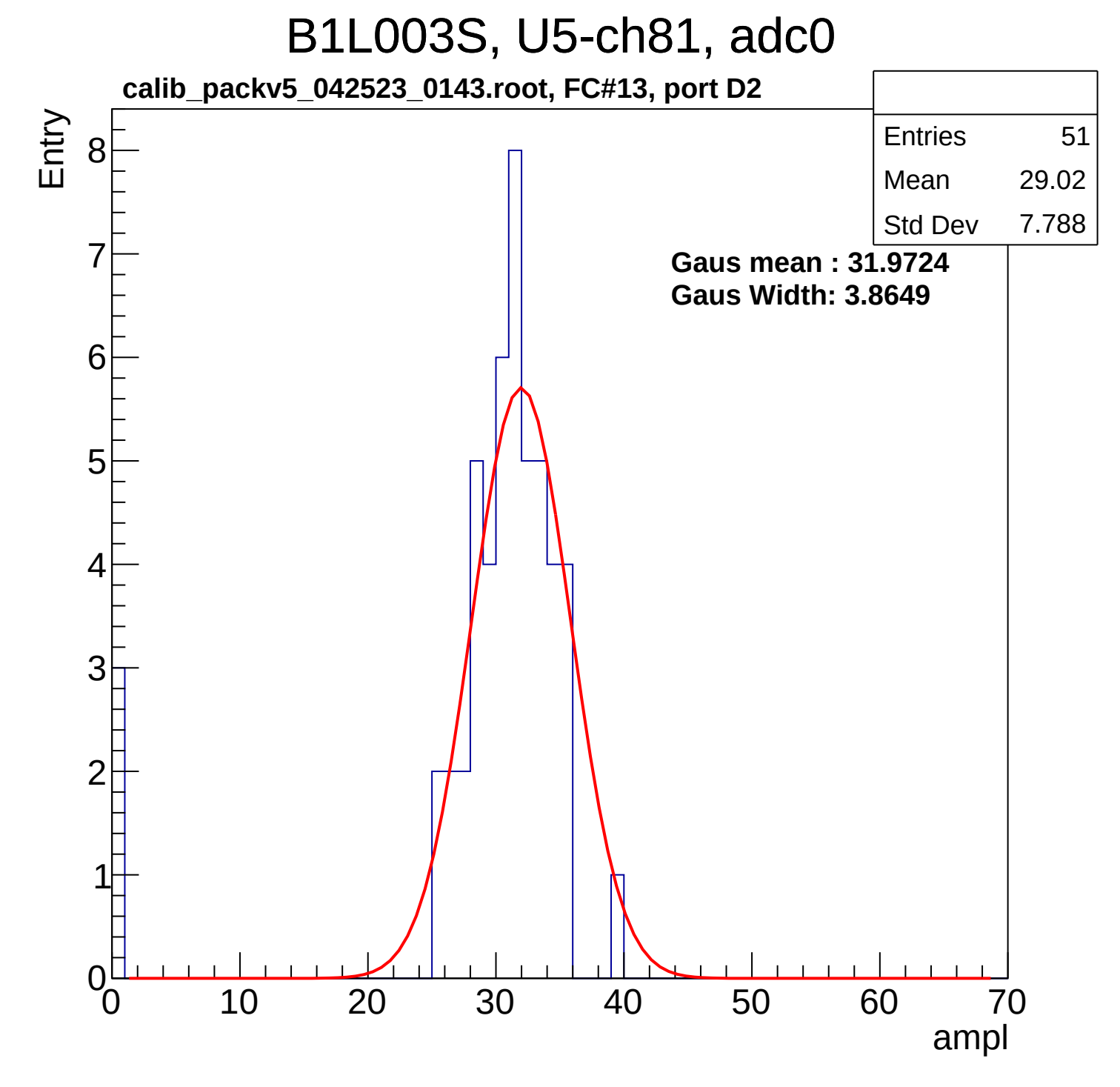
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 29.02
Std Dev 7.788
Gaus mean : 31.9724
Gaus Width: 3.8649
Entry
ampl
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2
3
4
5
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7
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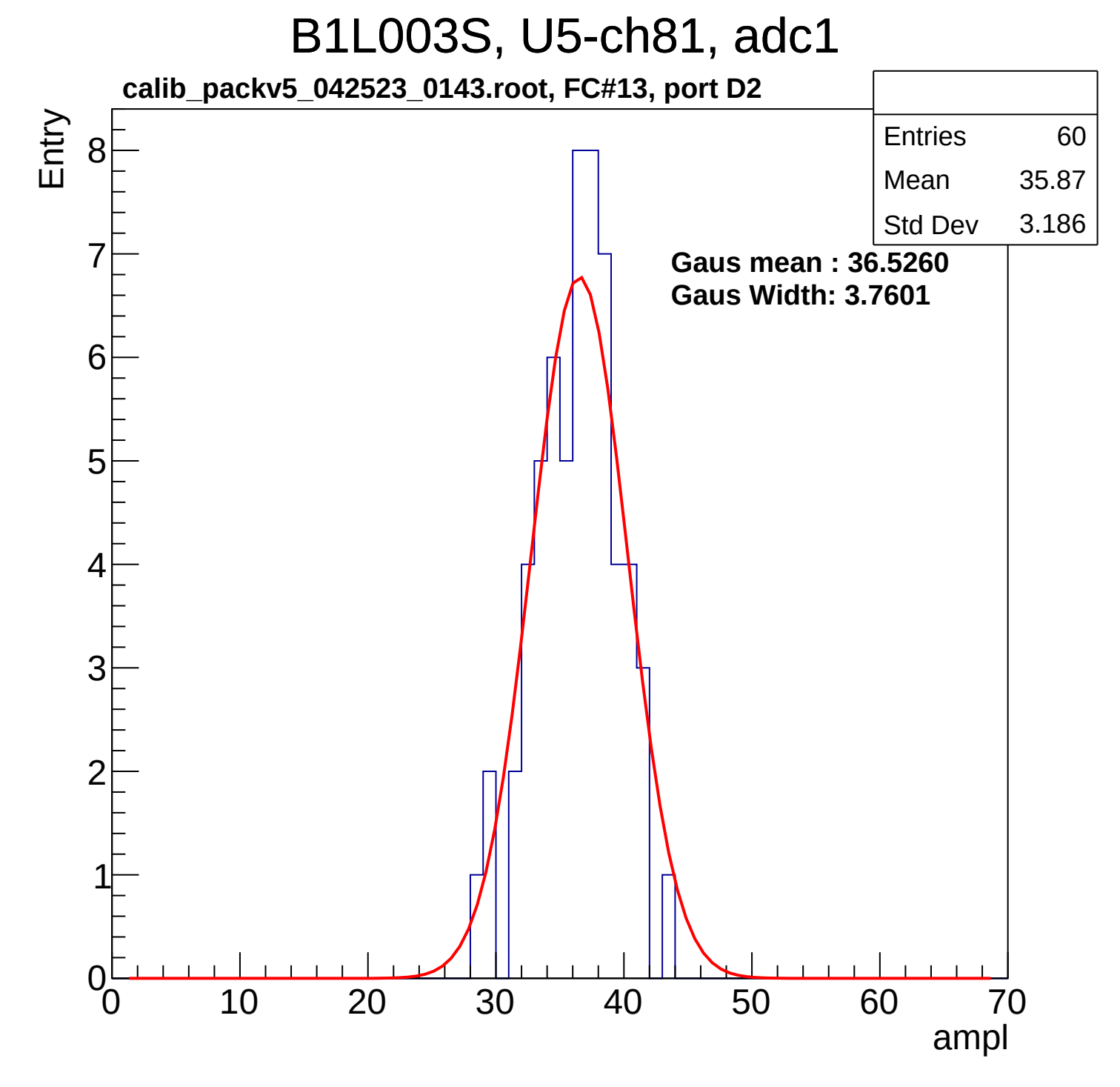
B1L003S, U5-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.87
Std Dev 3.186
Gaus mean : 36.5260
Gaus Width: 3.7601
Entry
ampl
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1
2
3
4
5
6
7
8
0
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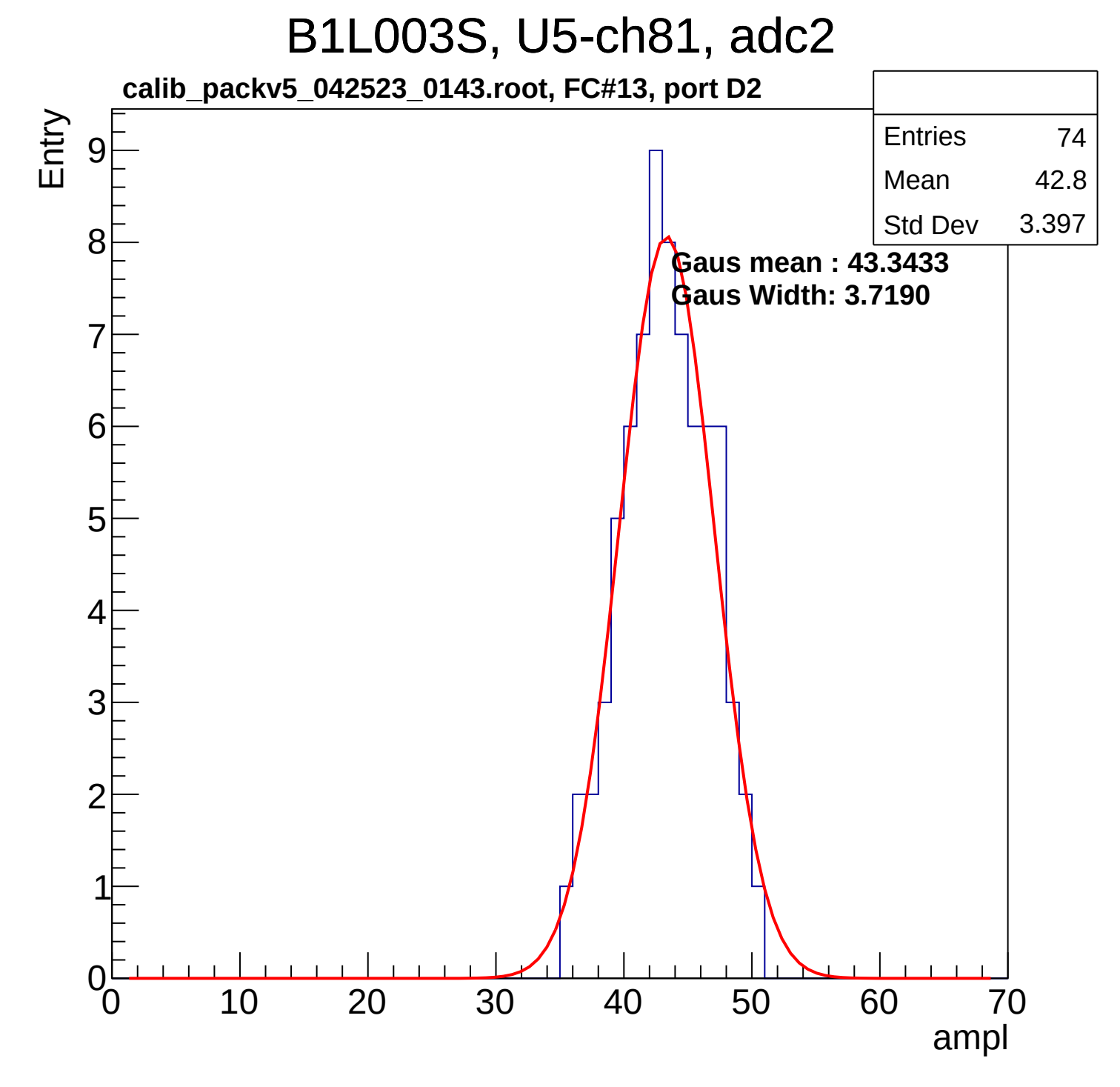
B1L003S, U5-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.8
Std Dev 3.397
Gaus mean : 43.3433
Gaus Width: 3.7190
Entry
ampl
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3
4
5
6
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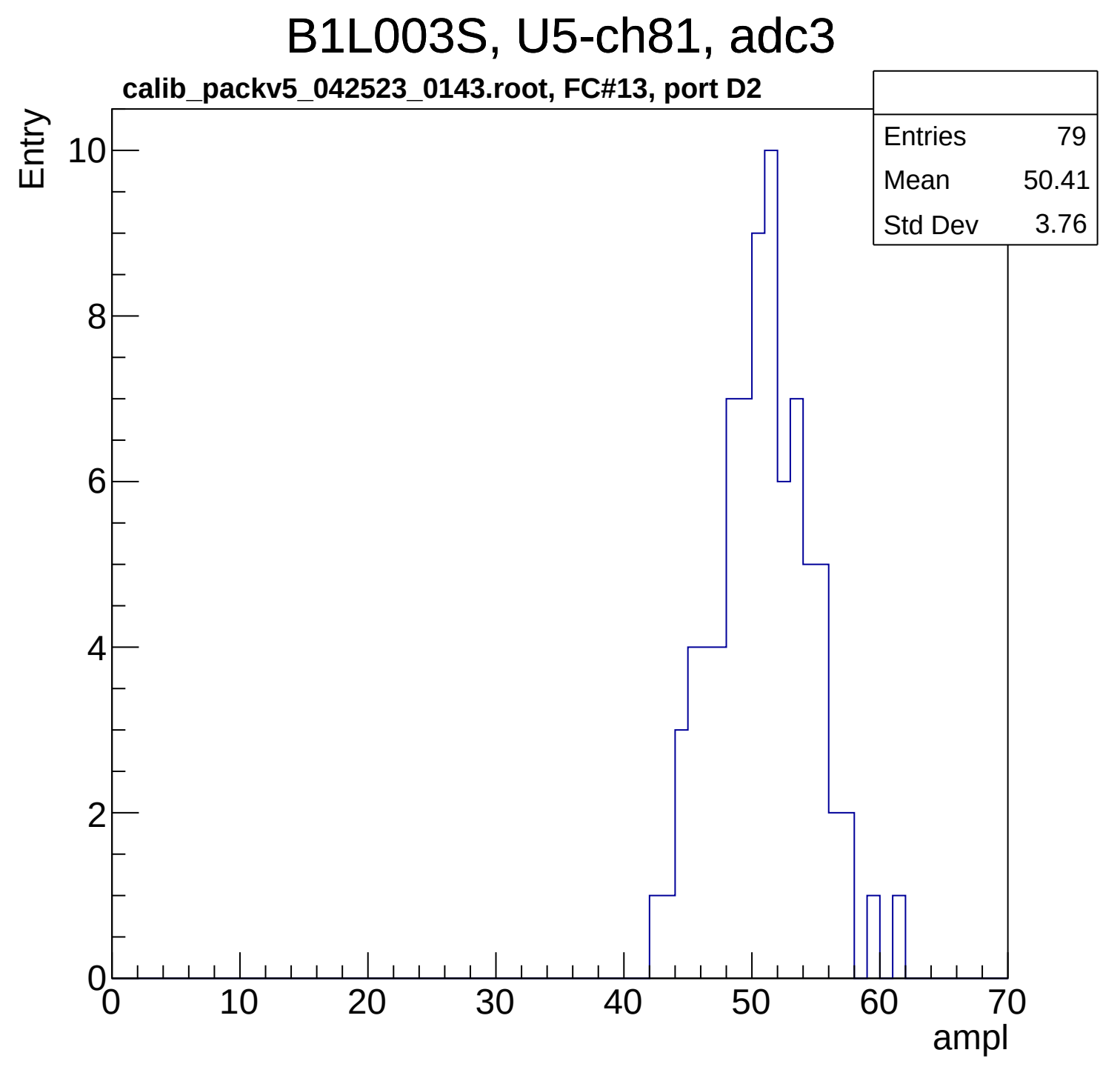
B1L003S, U5-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 50.41
Std Dev 3.76
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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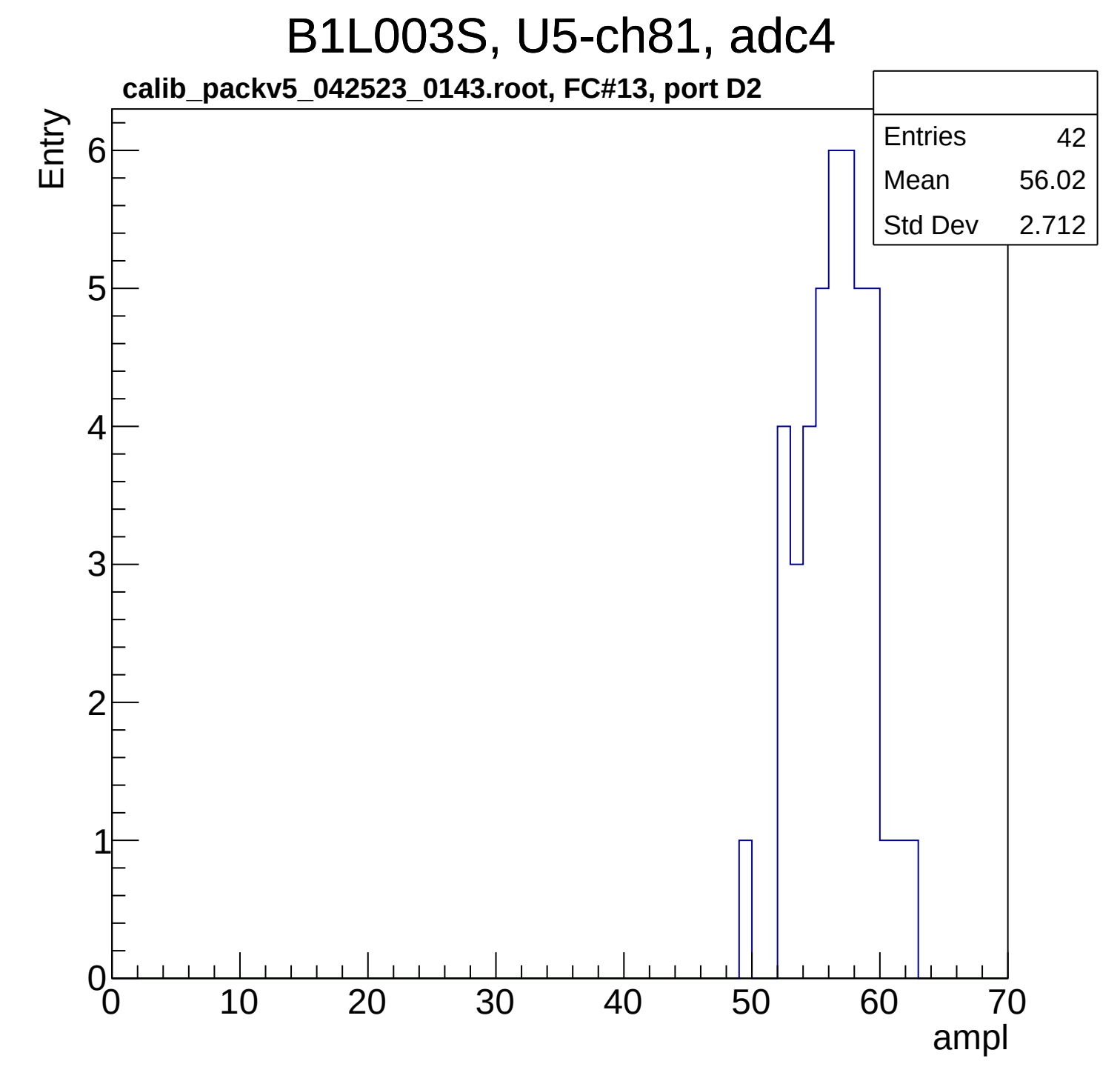
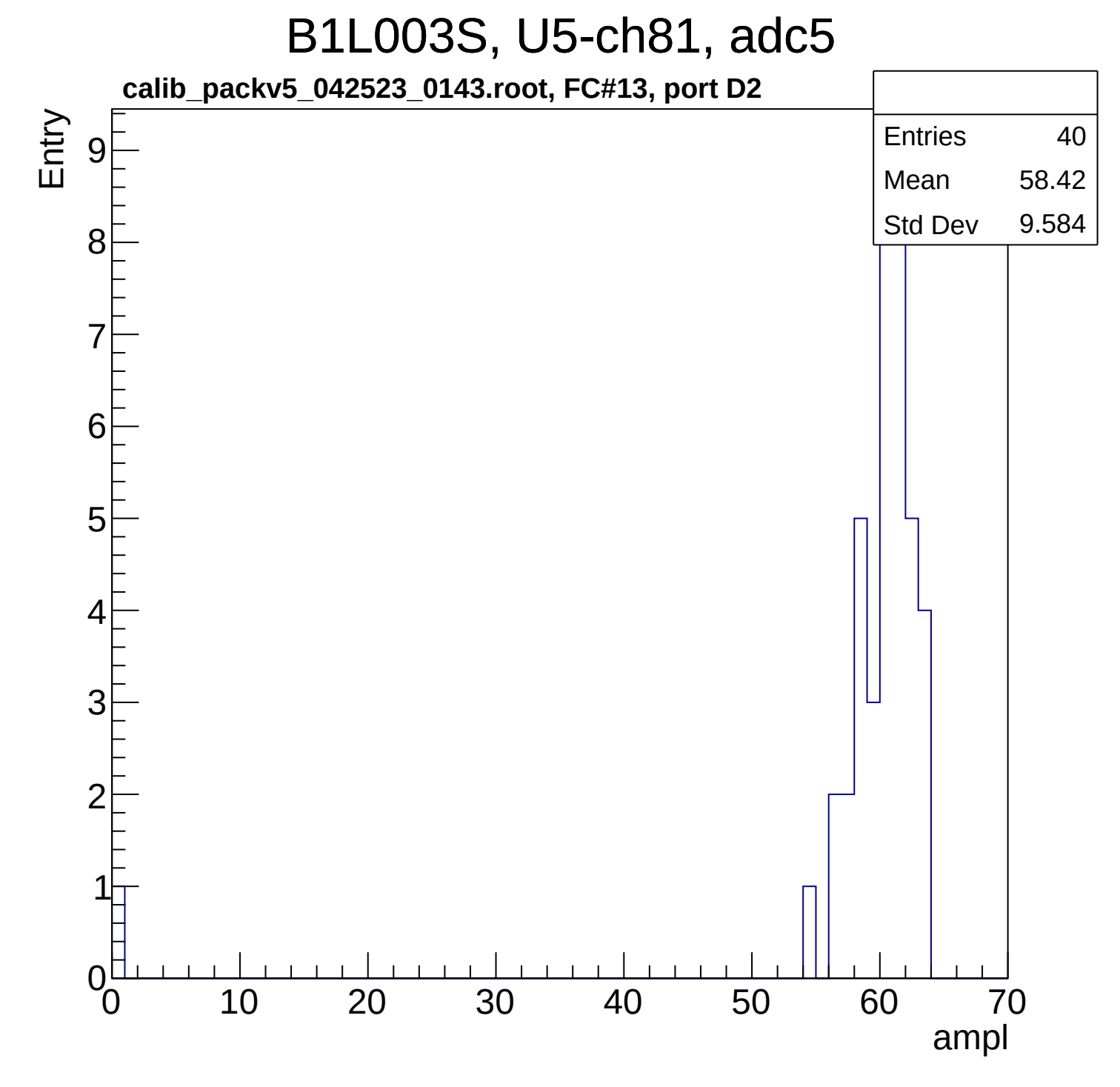

B1L003S, U5-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 56.02
Std Dev 2.712
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.42
Std Dev 9.584
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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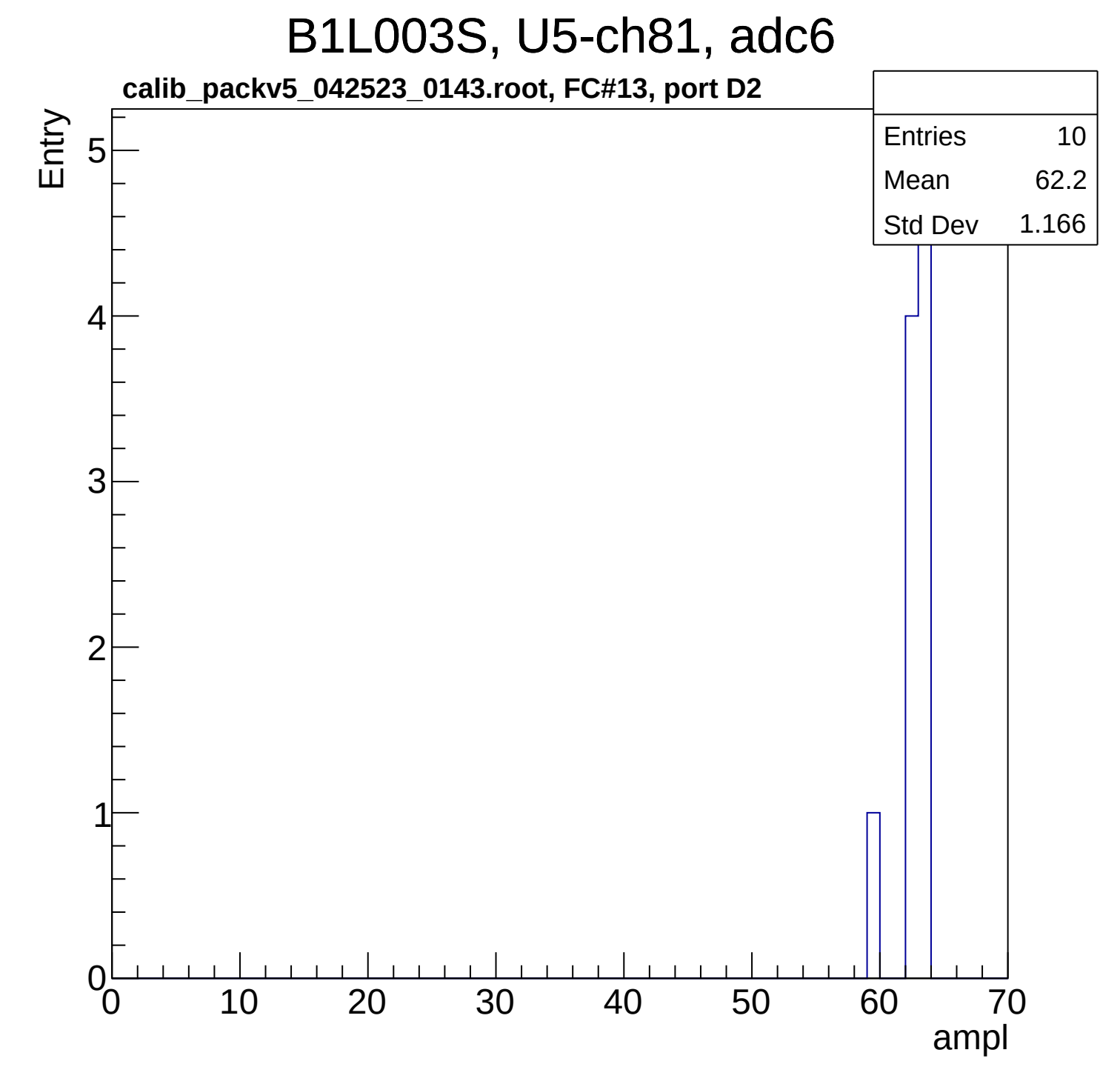
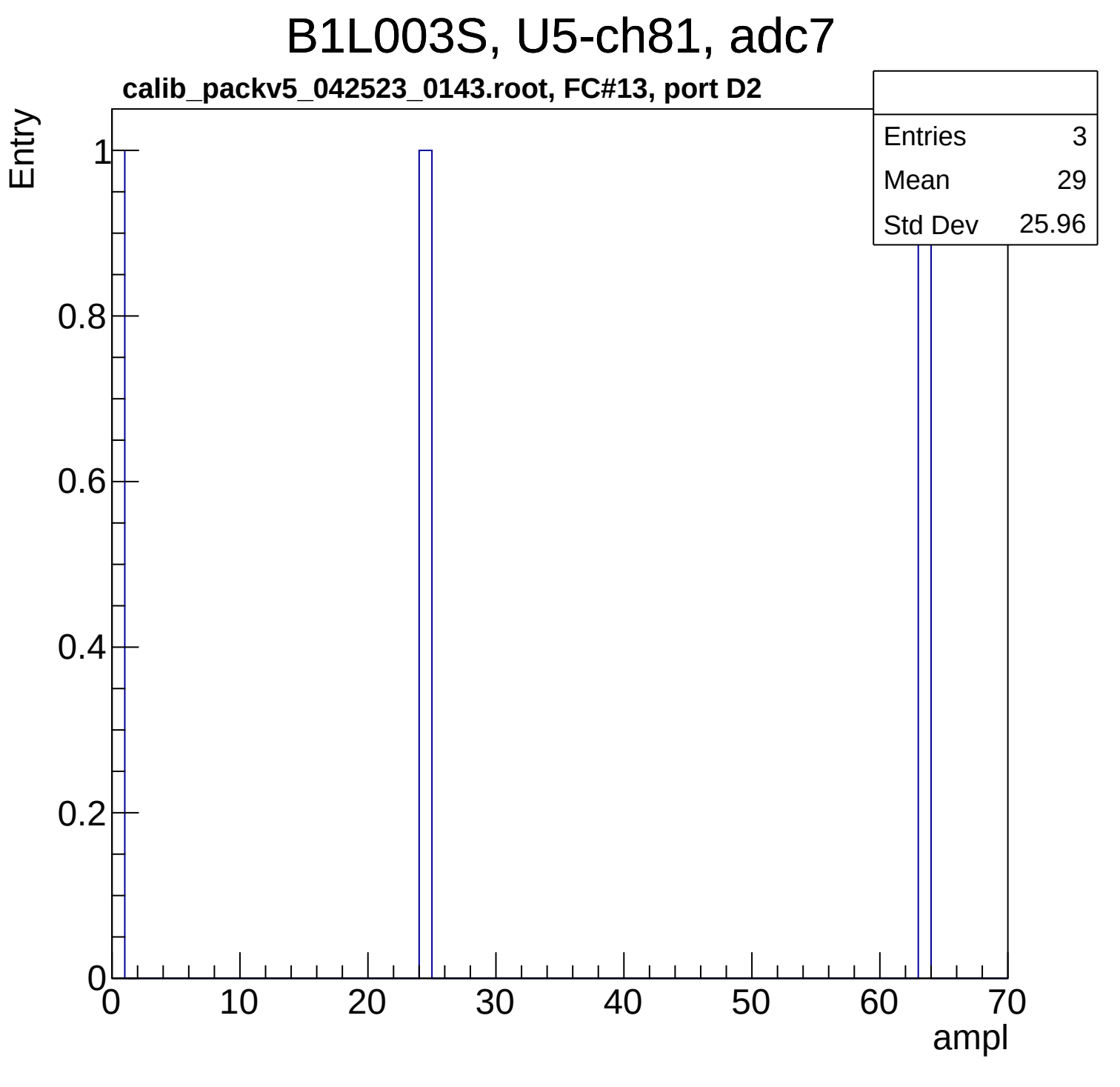

B1L003S, U5-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.2
Std Dev 1.166
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 29
Std Dev 25.96
Entry
ampl
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0.4
0.6
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1
0
10
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40
50
60
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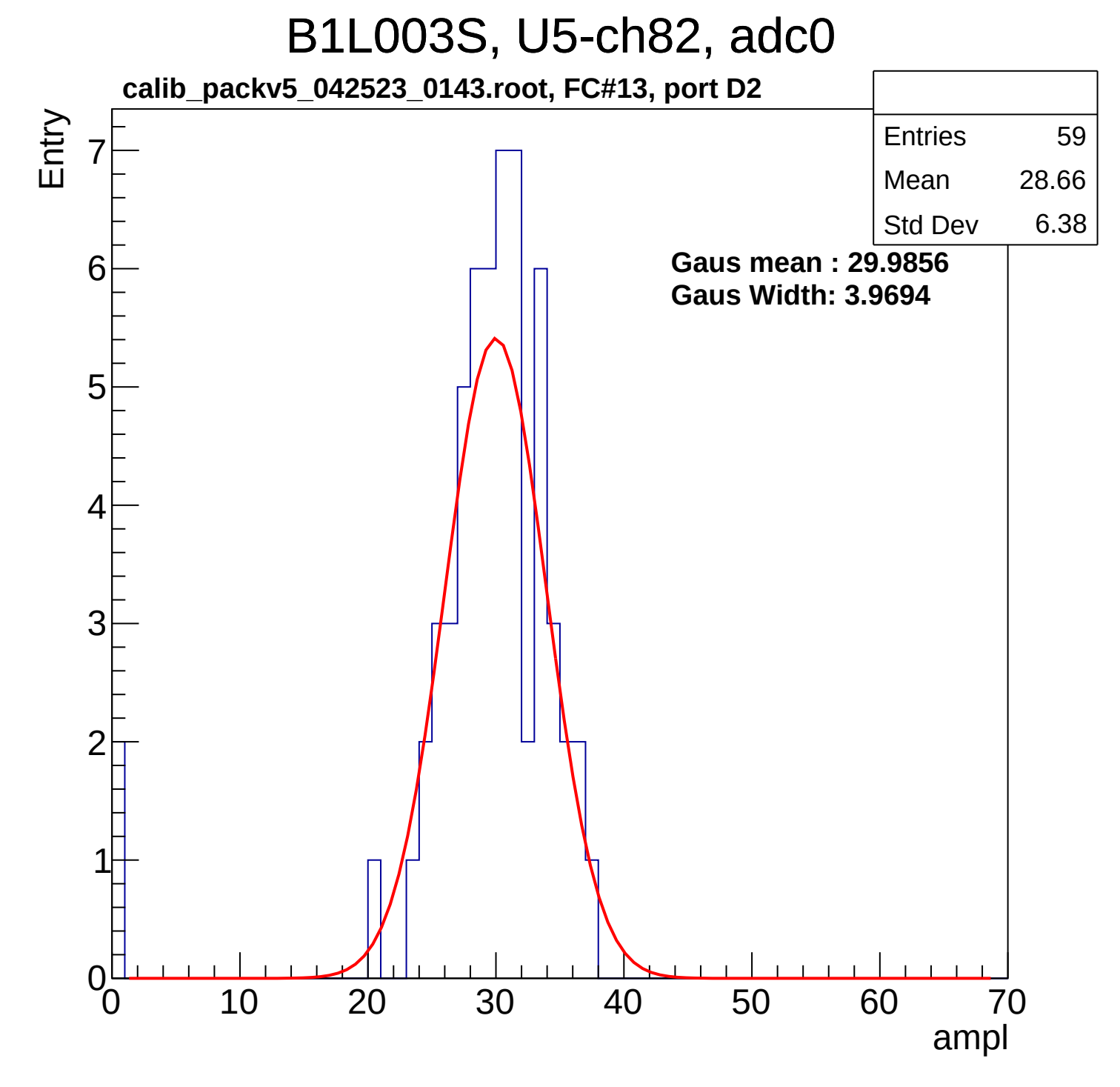
B1L003S, U5-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.66
Std Dev 6.38
Gaus mean : 29.9856
Gaus Width: 3.9694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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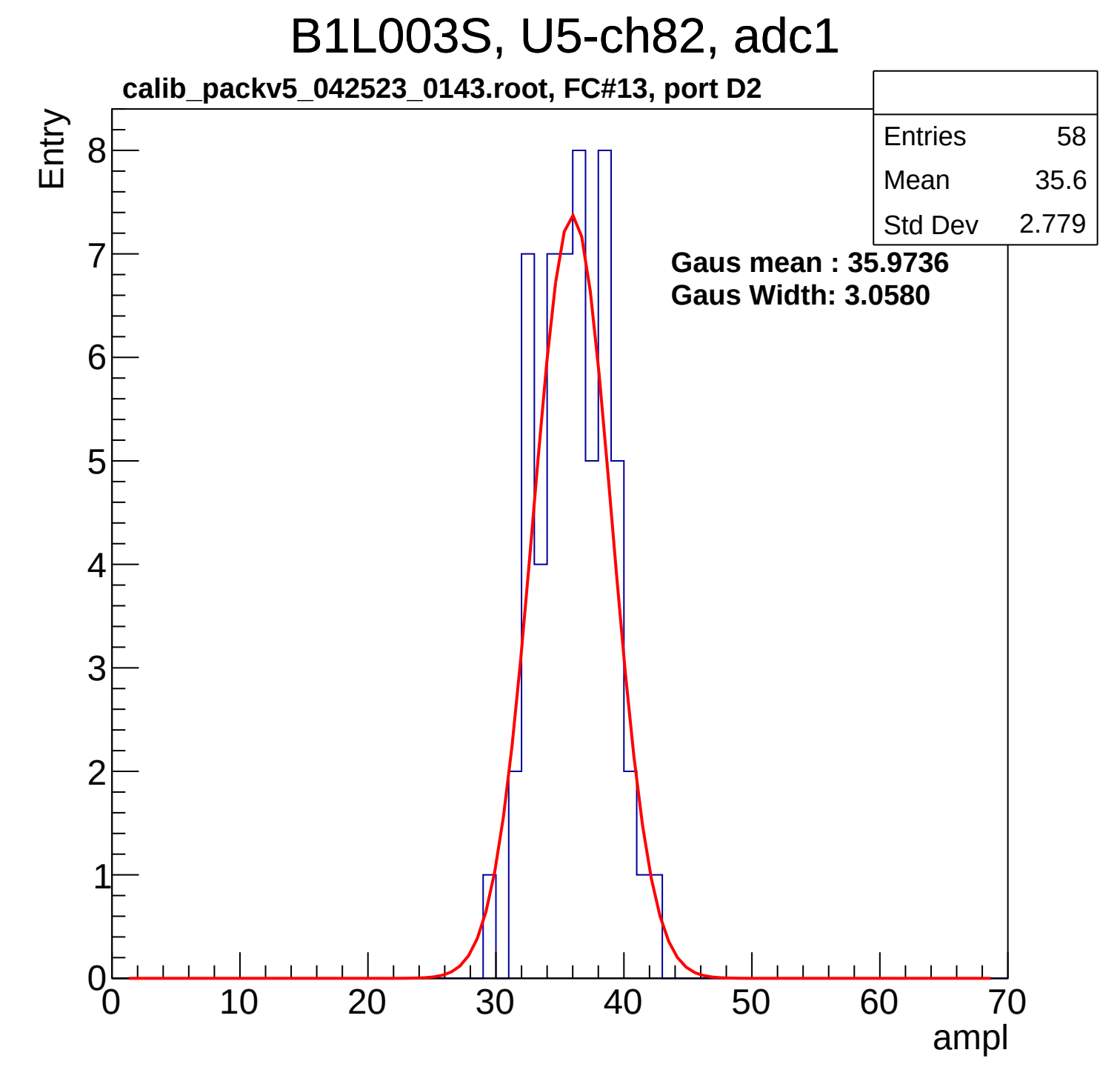
B1L003S, U5-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.6
Std Dev 2.779
Gaus mean : 35.9736
Gaus Width: 3.0580
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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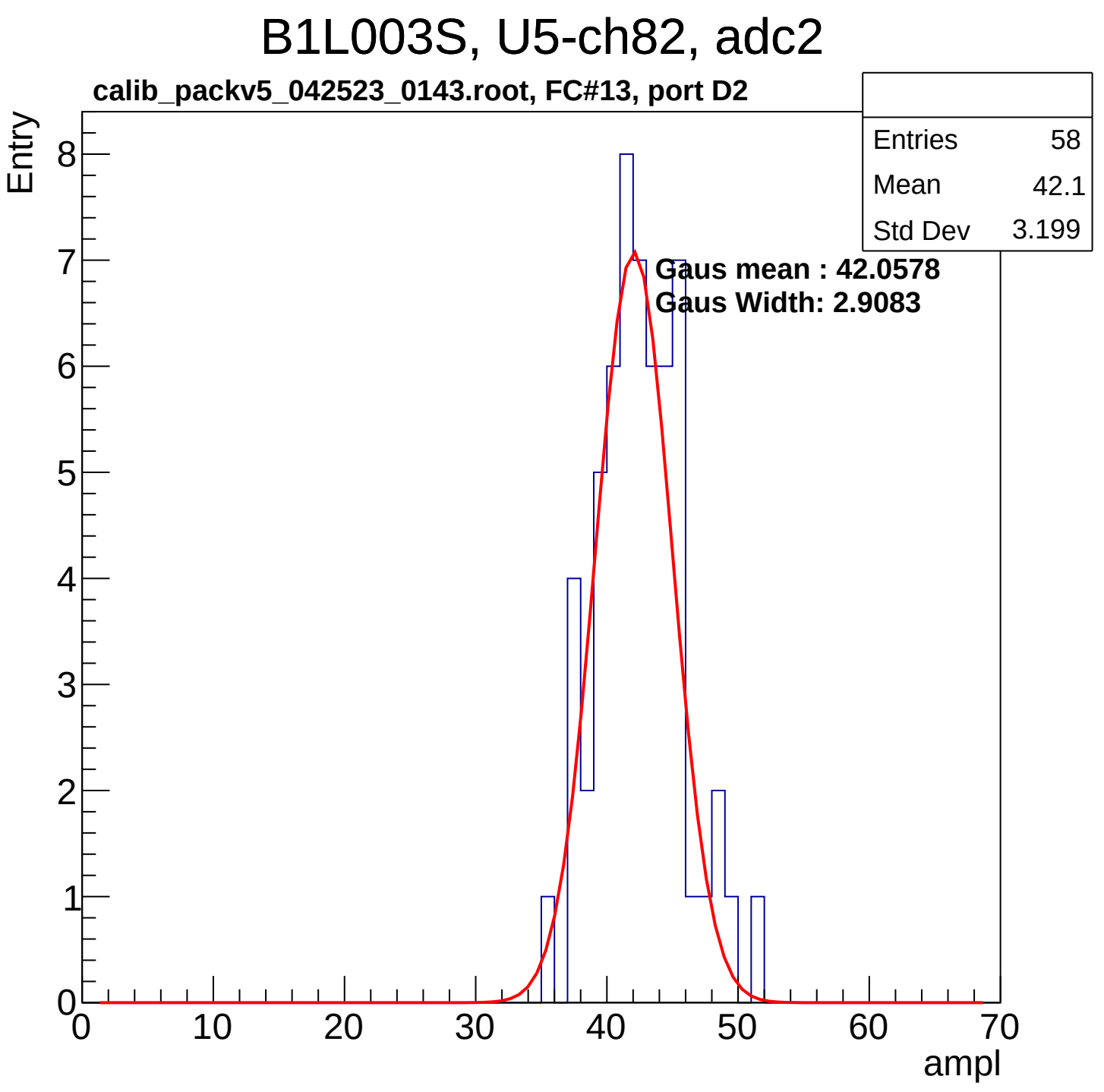
B1L003S, U5-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.1
Std Dev 3.199
Gaus mean : 42.0578
Gaus Width: 2.9083
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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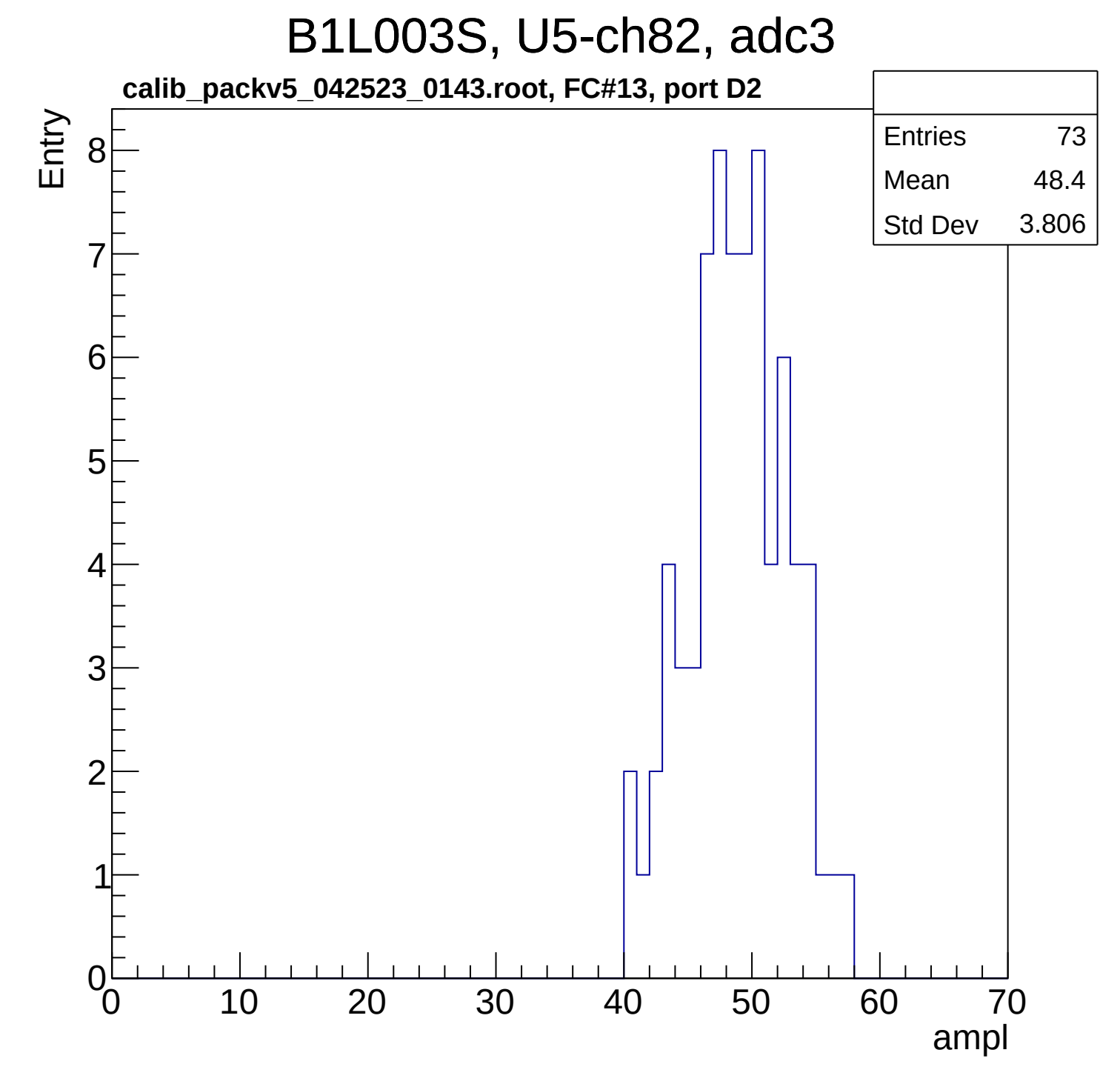
B1L003S, U5-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 48.4
Std Dev 3.806
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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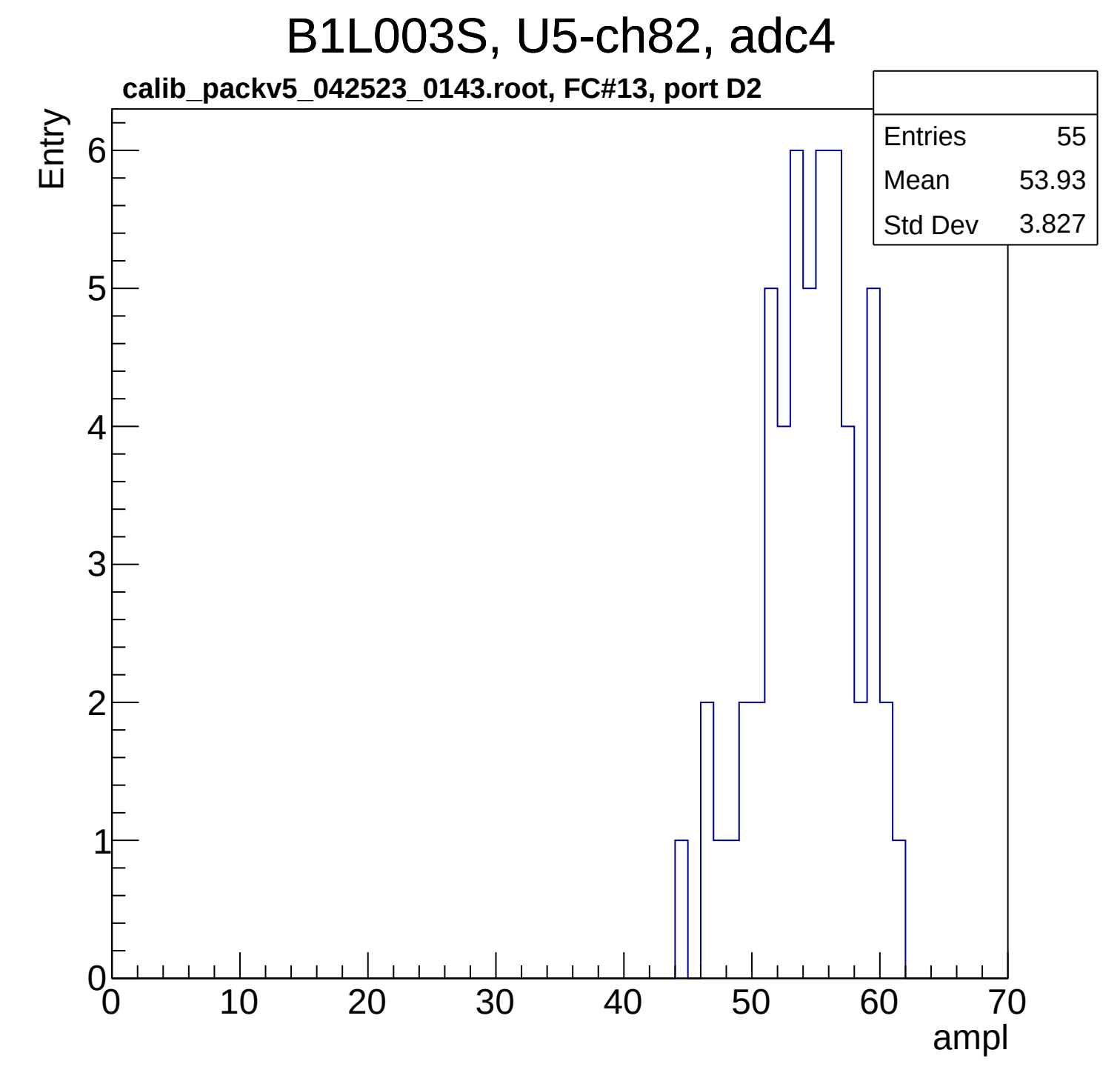
B1L003S, U5-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.93
Std Dev 3.827
Entry
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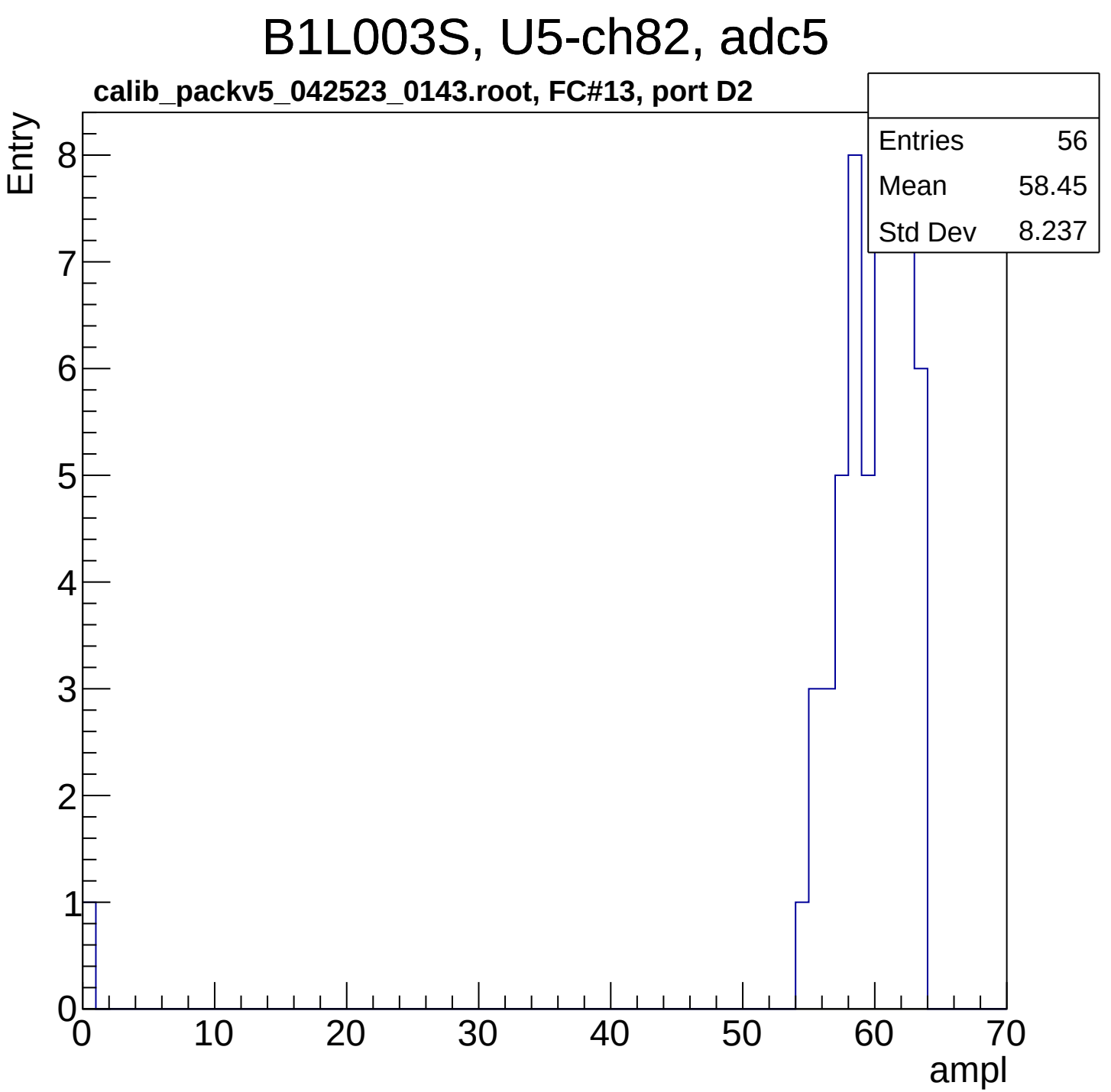
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.45
Std Dev 8.237
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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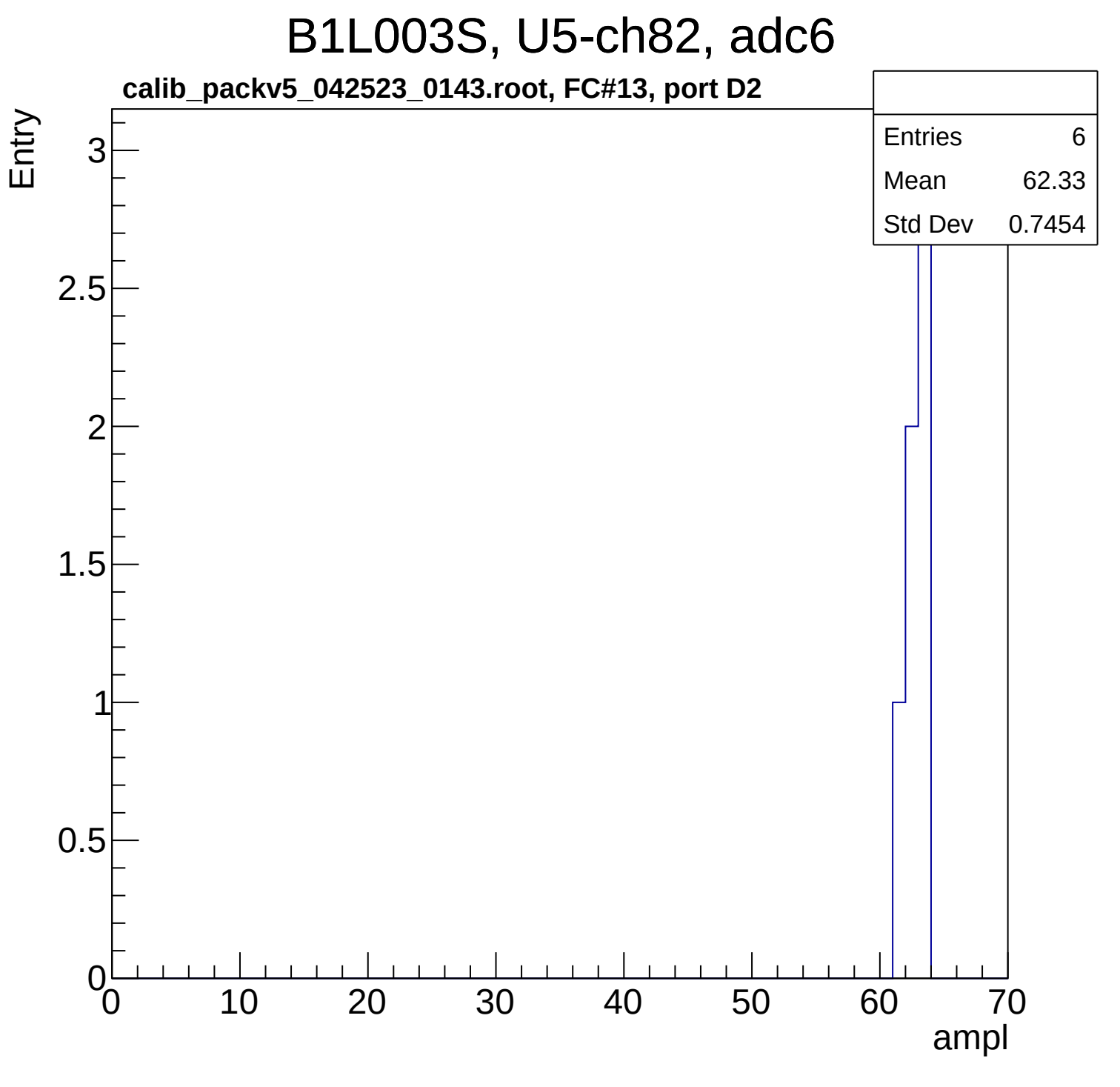
40
50
60
70


B1L003S, U5-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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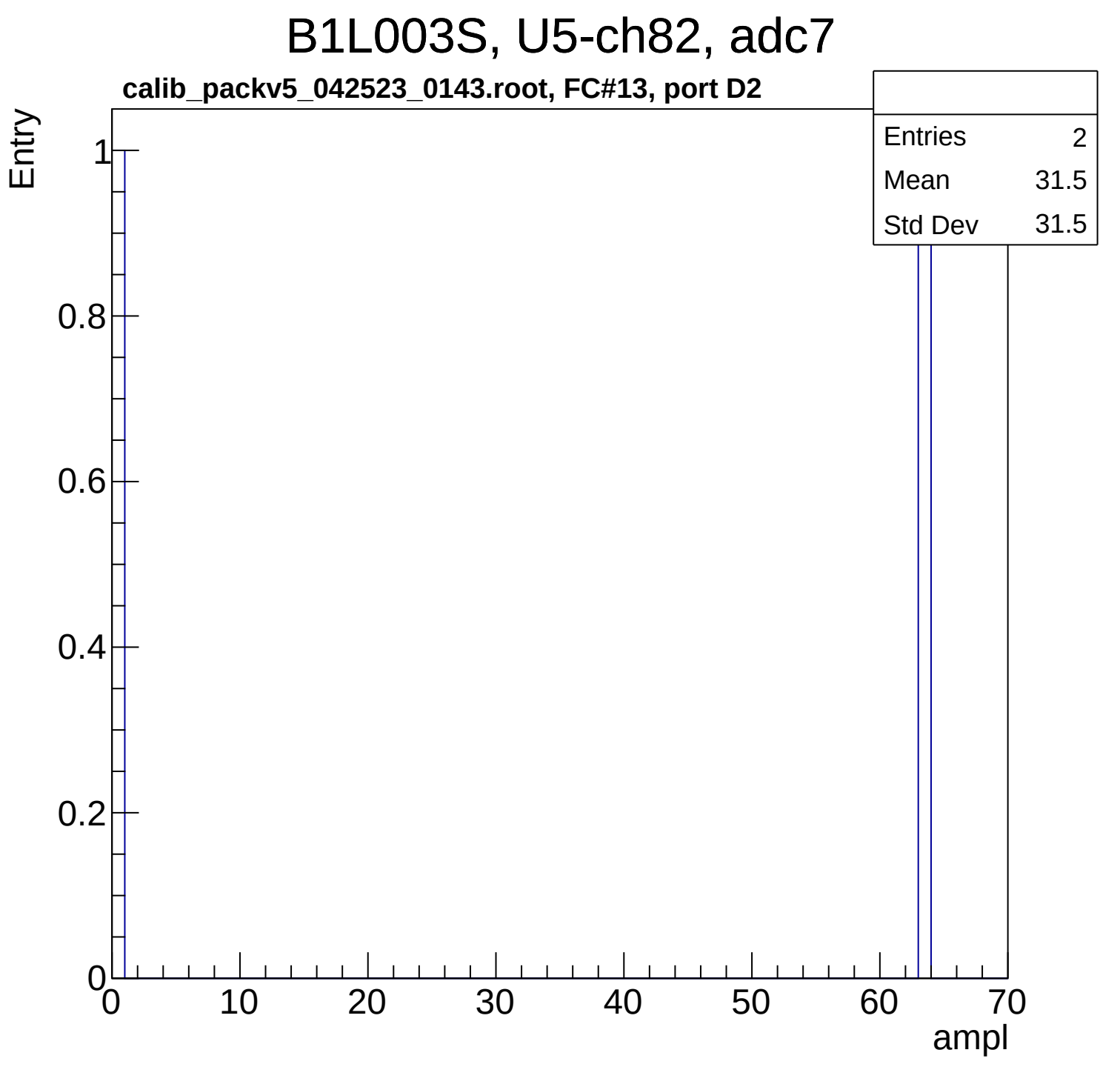
B1L003S, U5-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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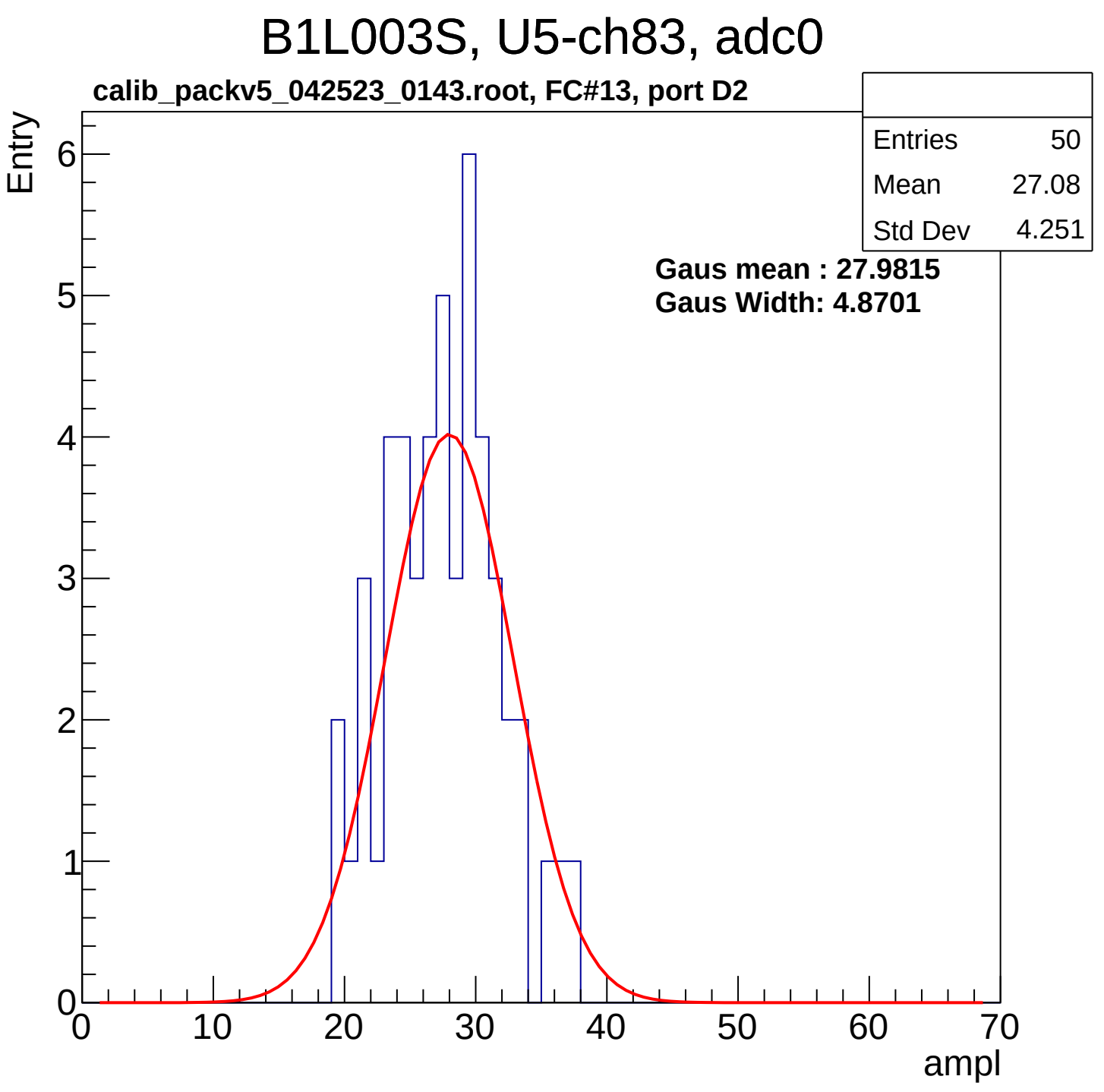
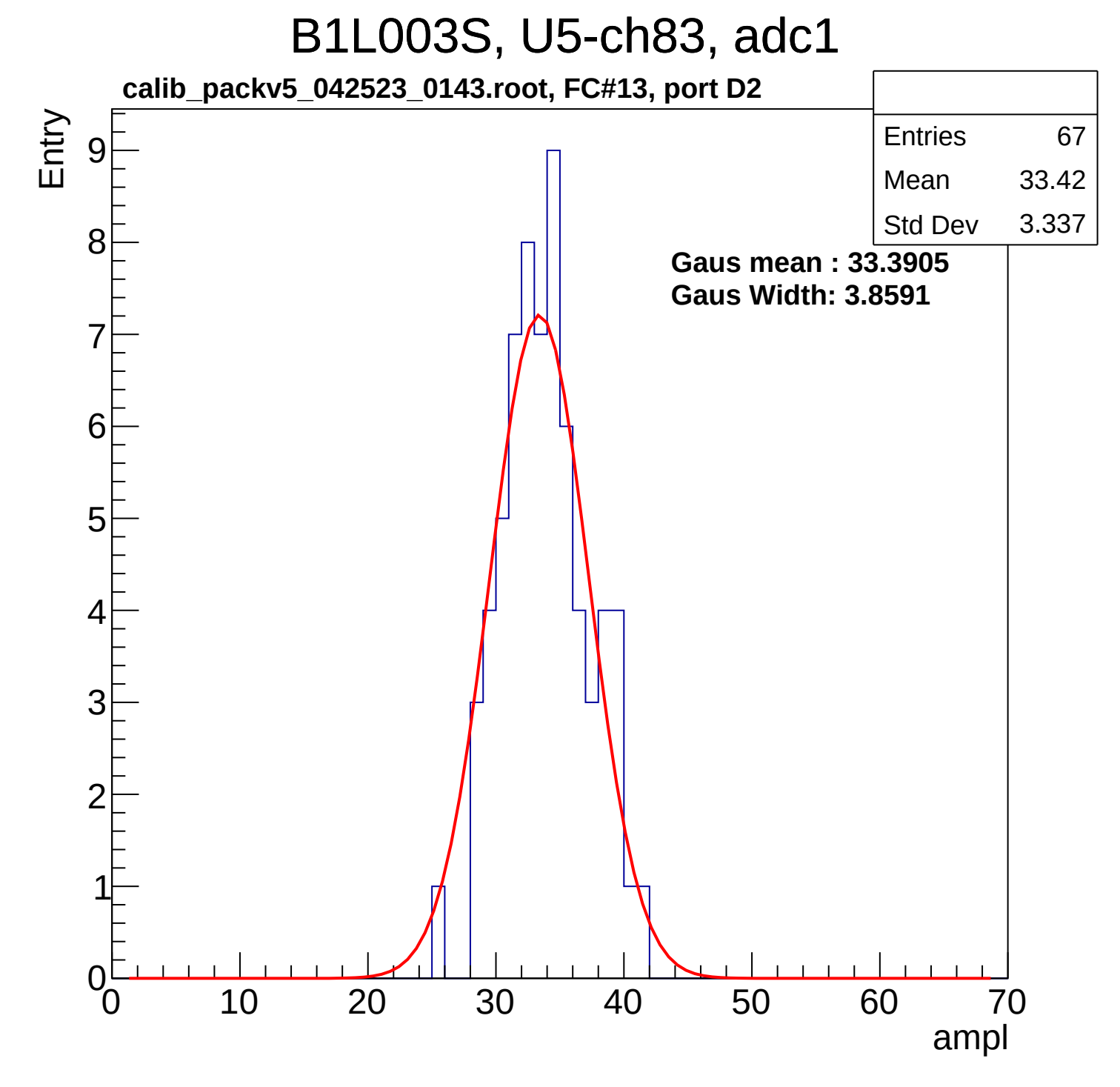
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 27.08
Std Dev 4.251
Gaus mean : 27.9815
Gaus Width: 4.8701
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U5-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 33.42
Std Dev 3.337
Gaus mean : 33.3905
Gaus Width: 3.8591
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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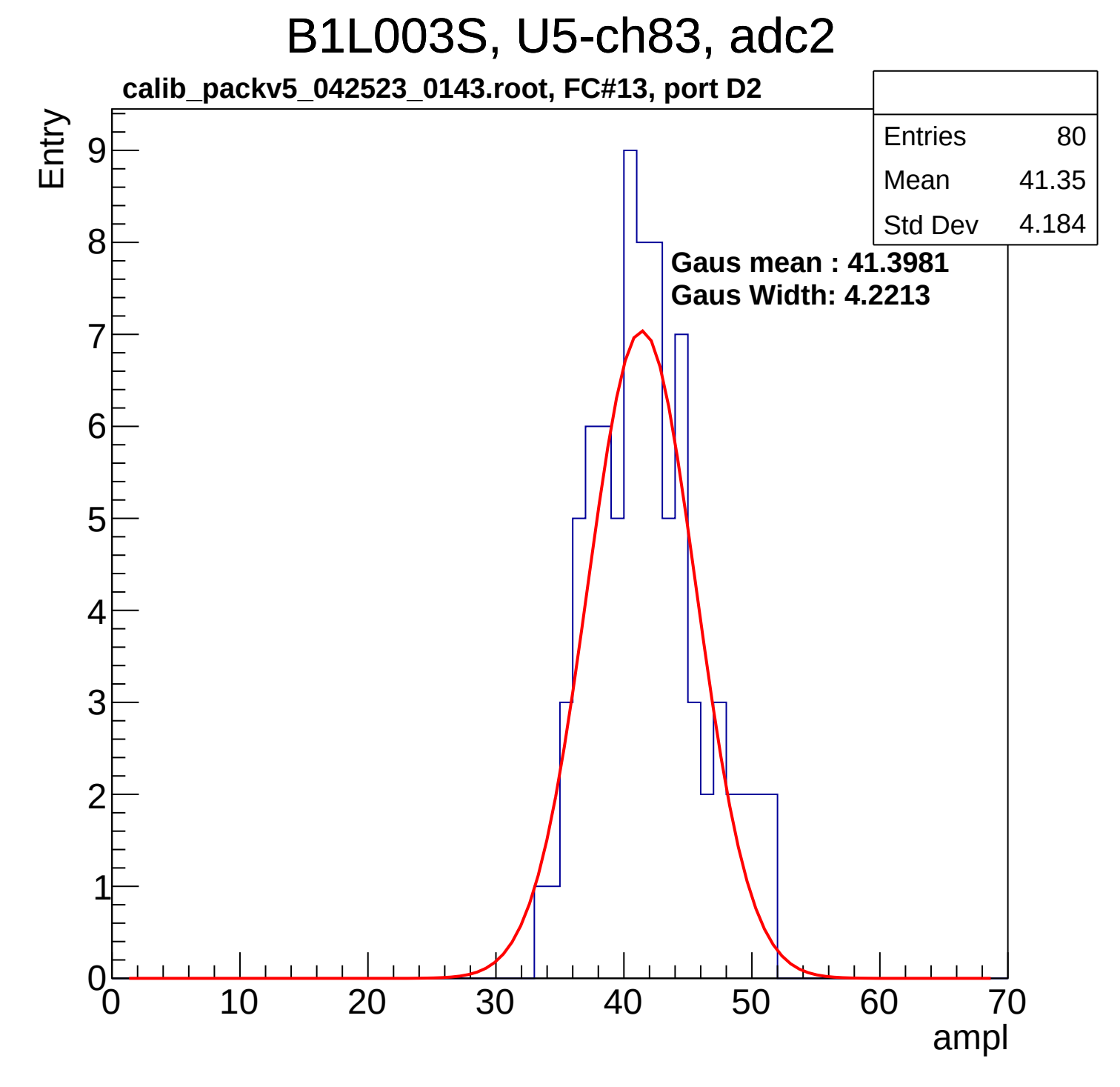
B1L003S, U5-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 41.35
Std Dev 4.184
Gaus mean : 41.3981
Gaus Width: 4.2213
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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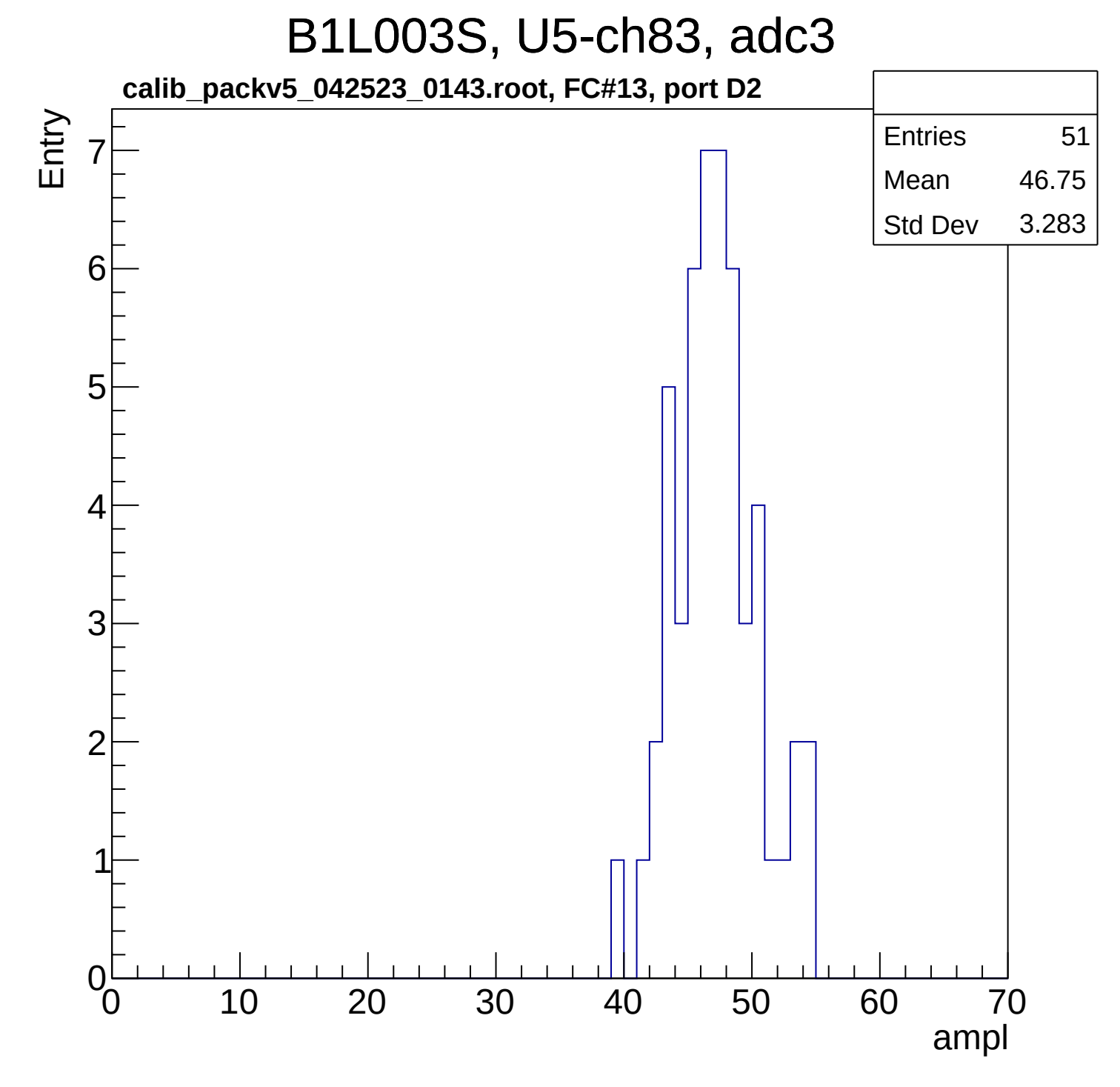
B1L003S, U5-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 46.75
Std Dev 3.283
Entry
ampl
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0
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60
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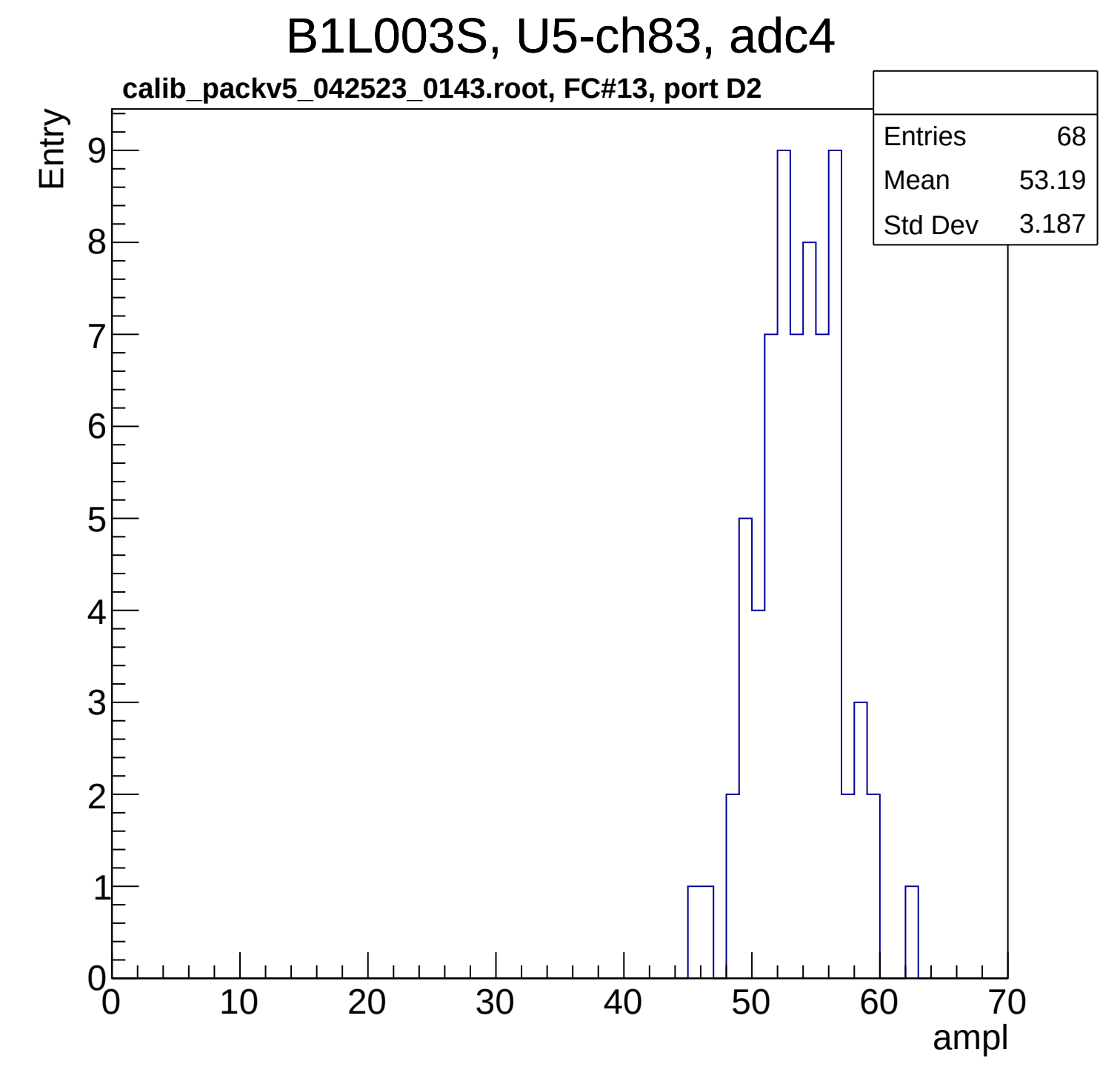
B1L003S, U5-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 53.19
Std Dev 3.187
Entry
ampl
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5
6
7
8
9
0
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40
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60
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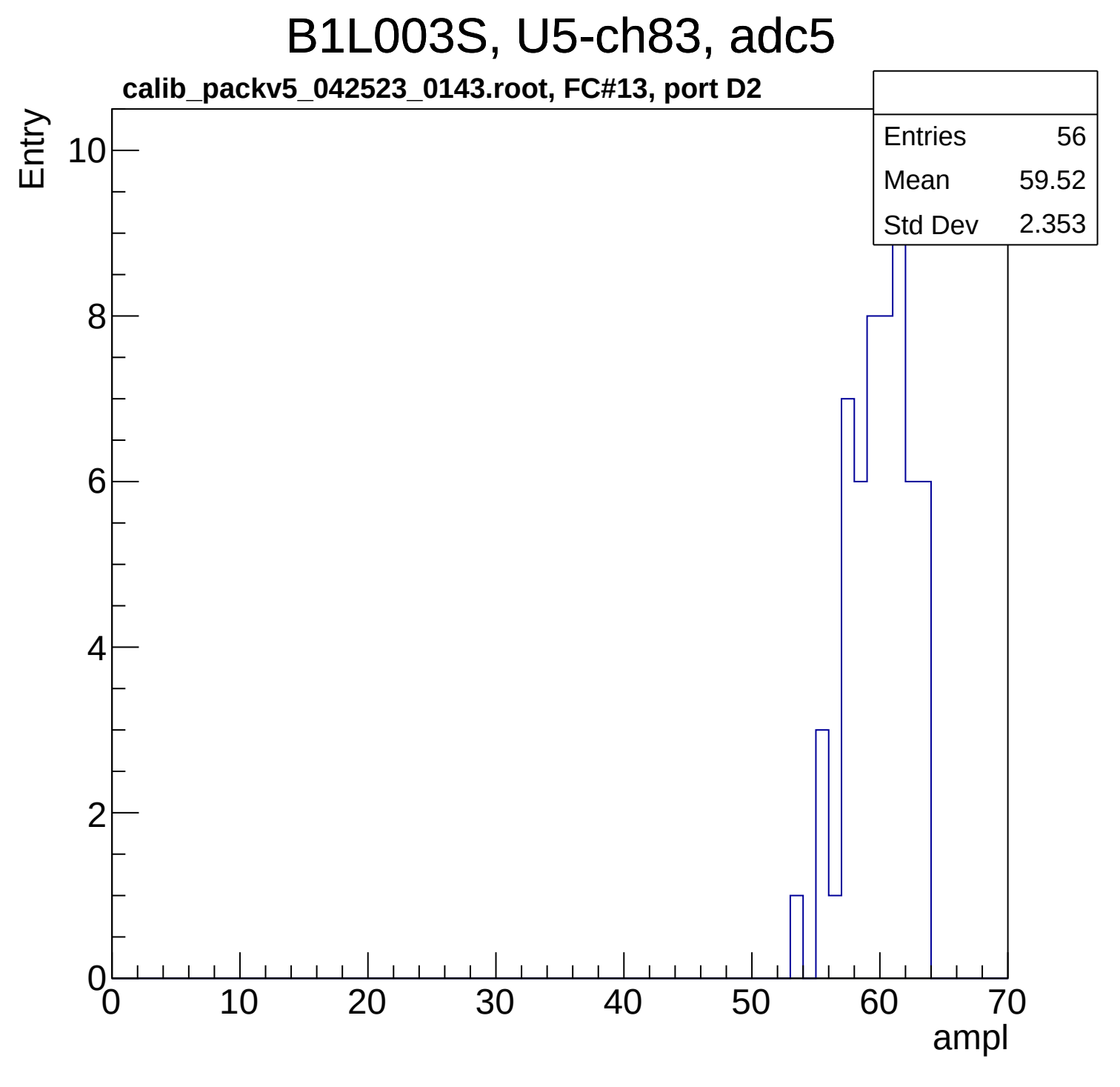
B1L003S, U5-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.52
Std Dev 2.353
Entry
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2
4
6
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10
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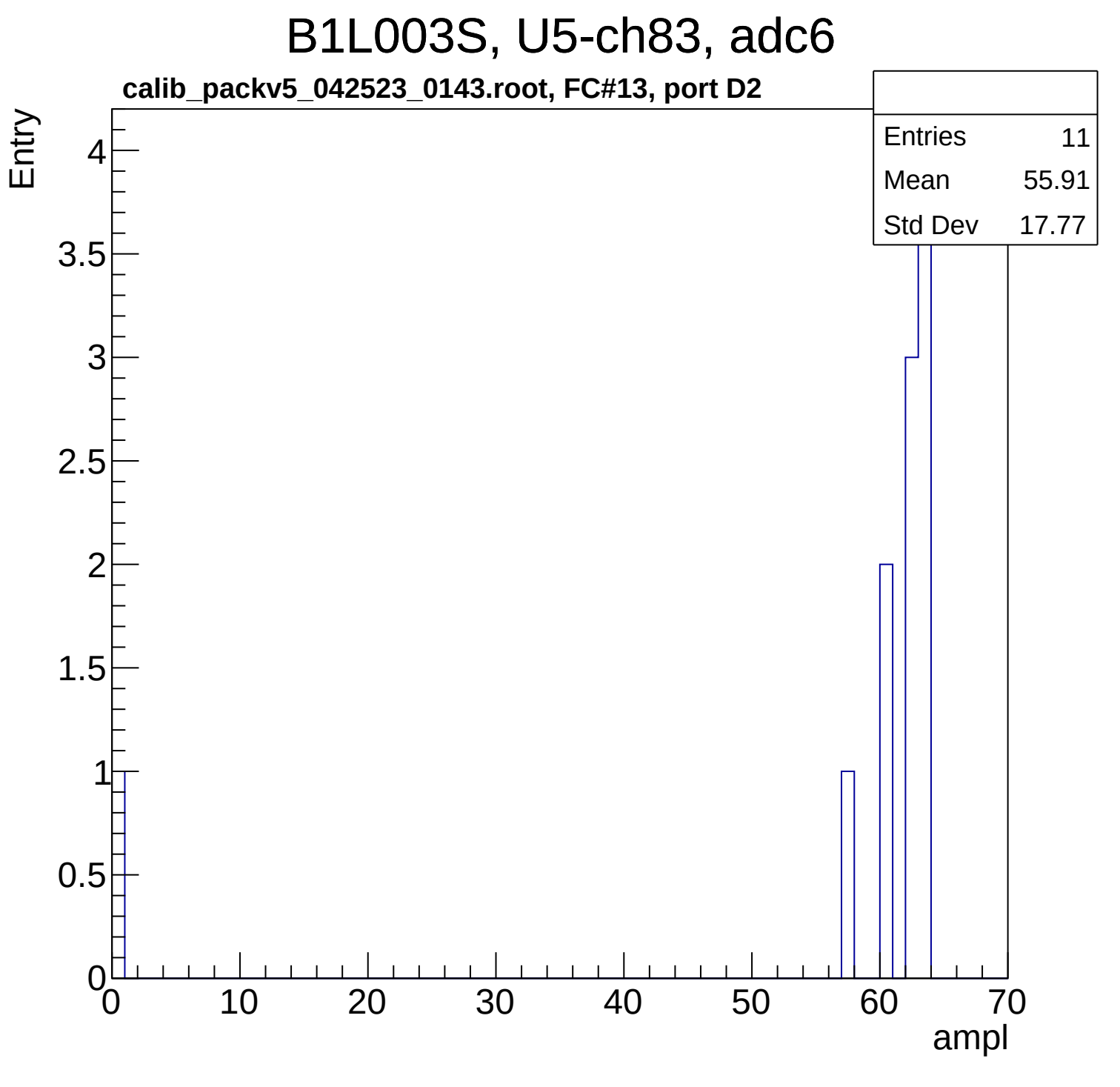
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.91
Std Dev 17.77
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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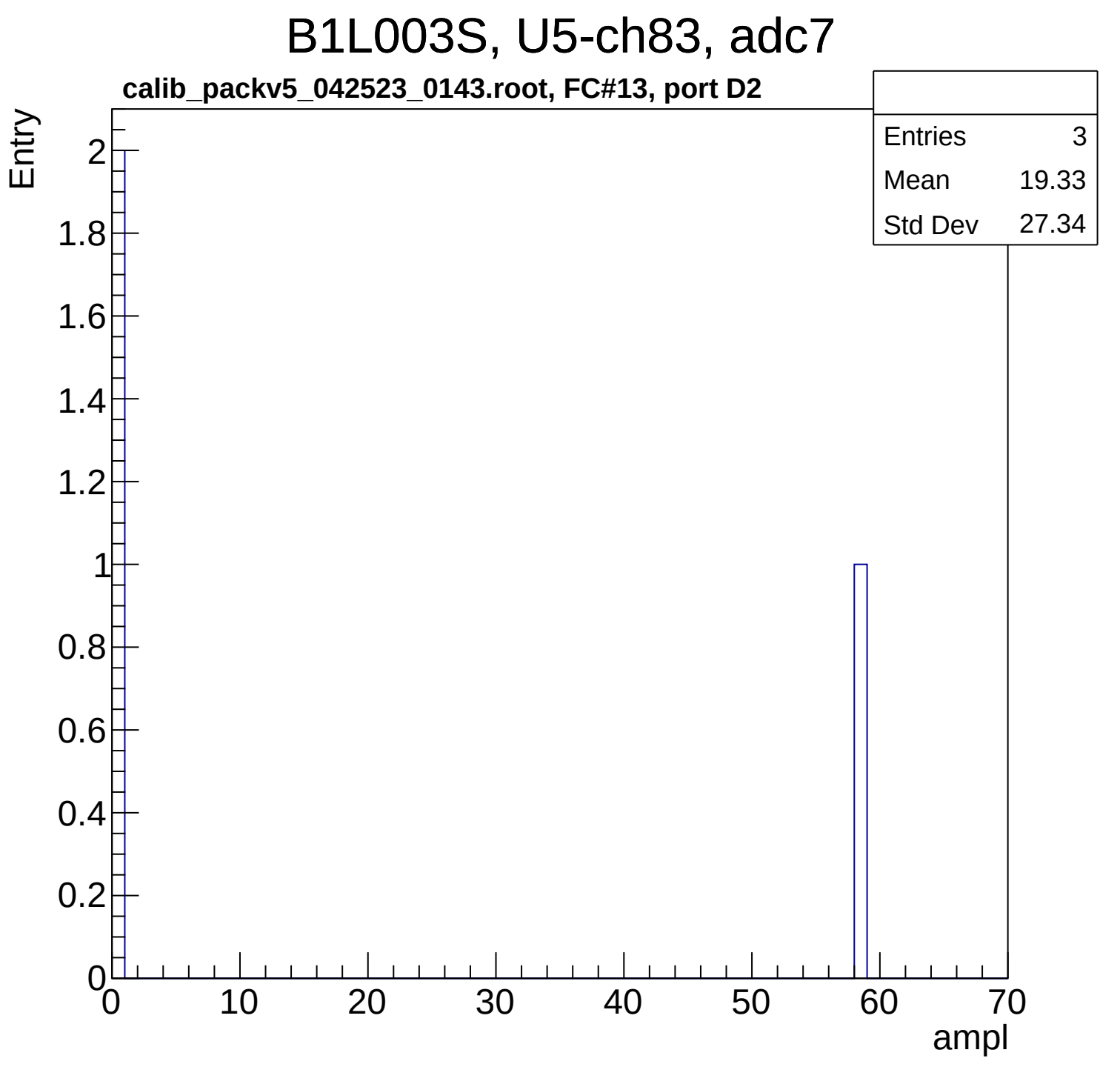
B1L003S, U5-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 19.33
Std Dev 27.34
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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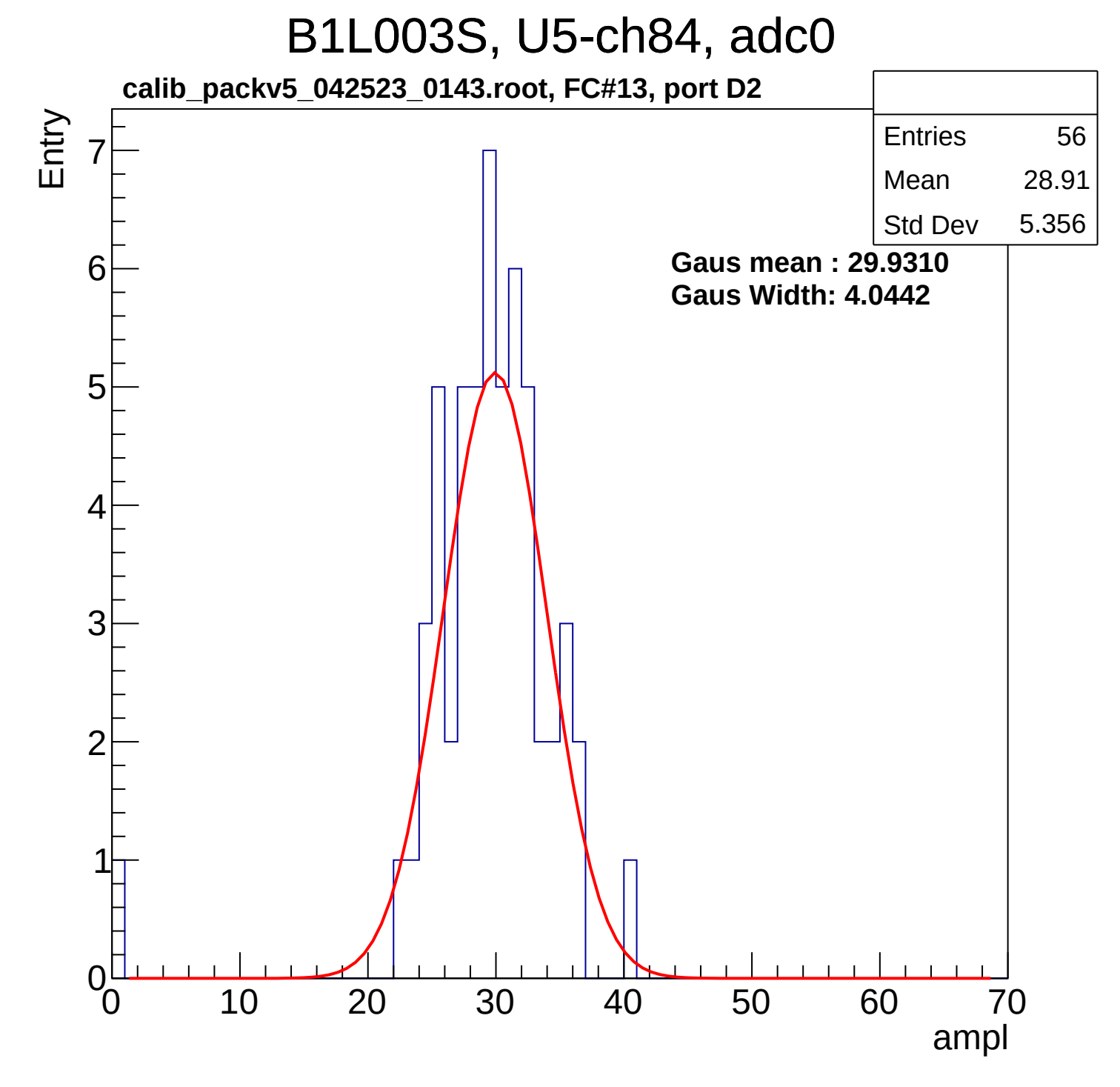
B1L003S, U5-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 28.91
Std Dev 5.356
Gaus mean : 29.9310
Gaus Width: 4.0442
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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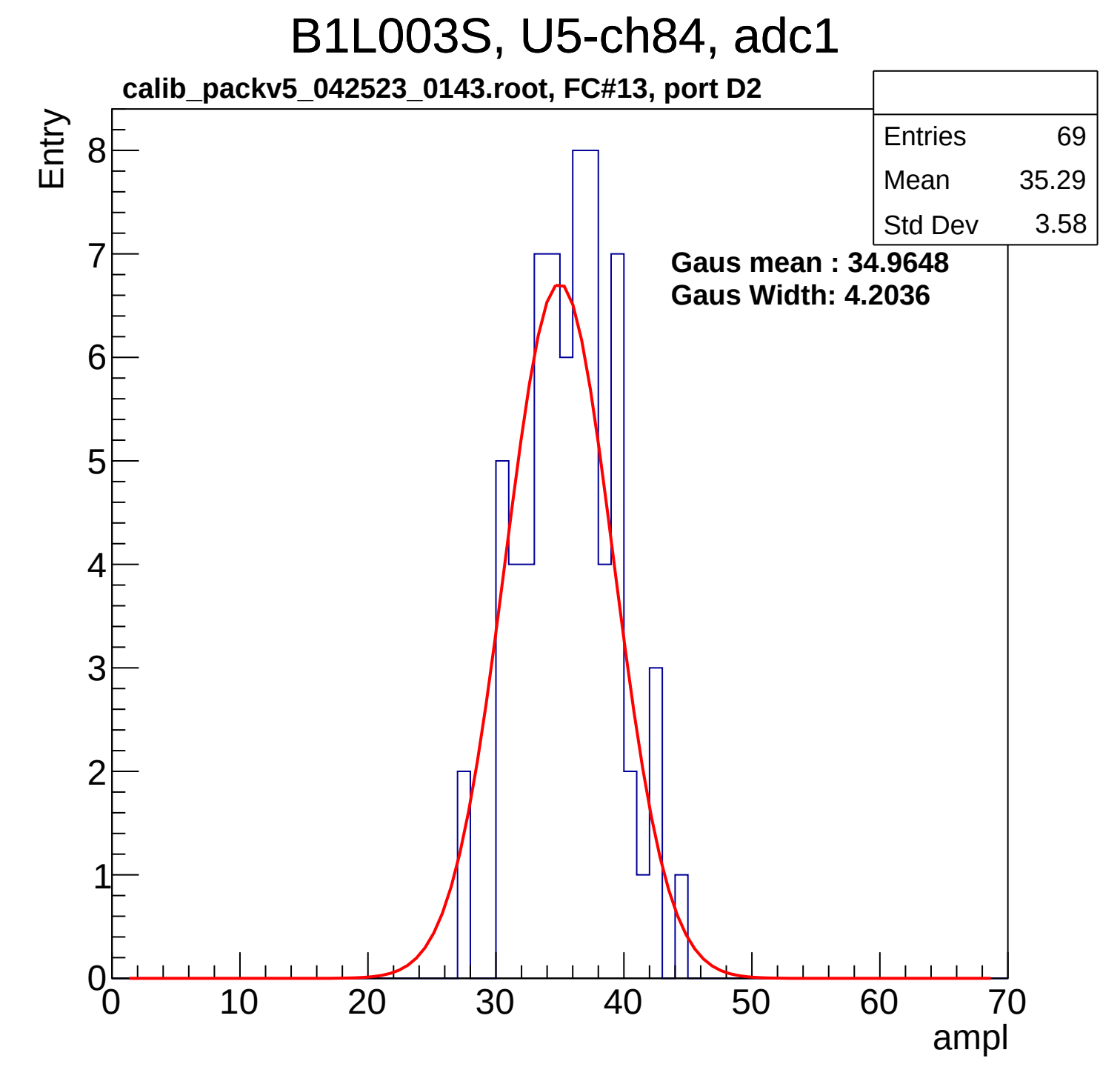
40
50
60
70


B1L003S, U5-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.29
Std Dev 3.58
Gaus mean : 34.9648
Gaus Width: 4.2036
Entry
ampl
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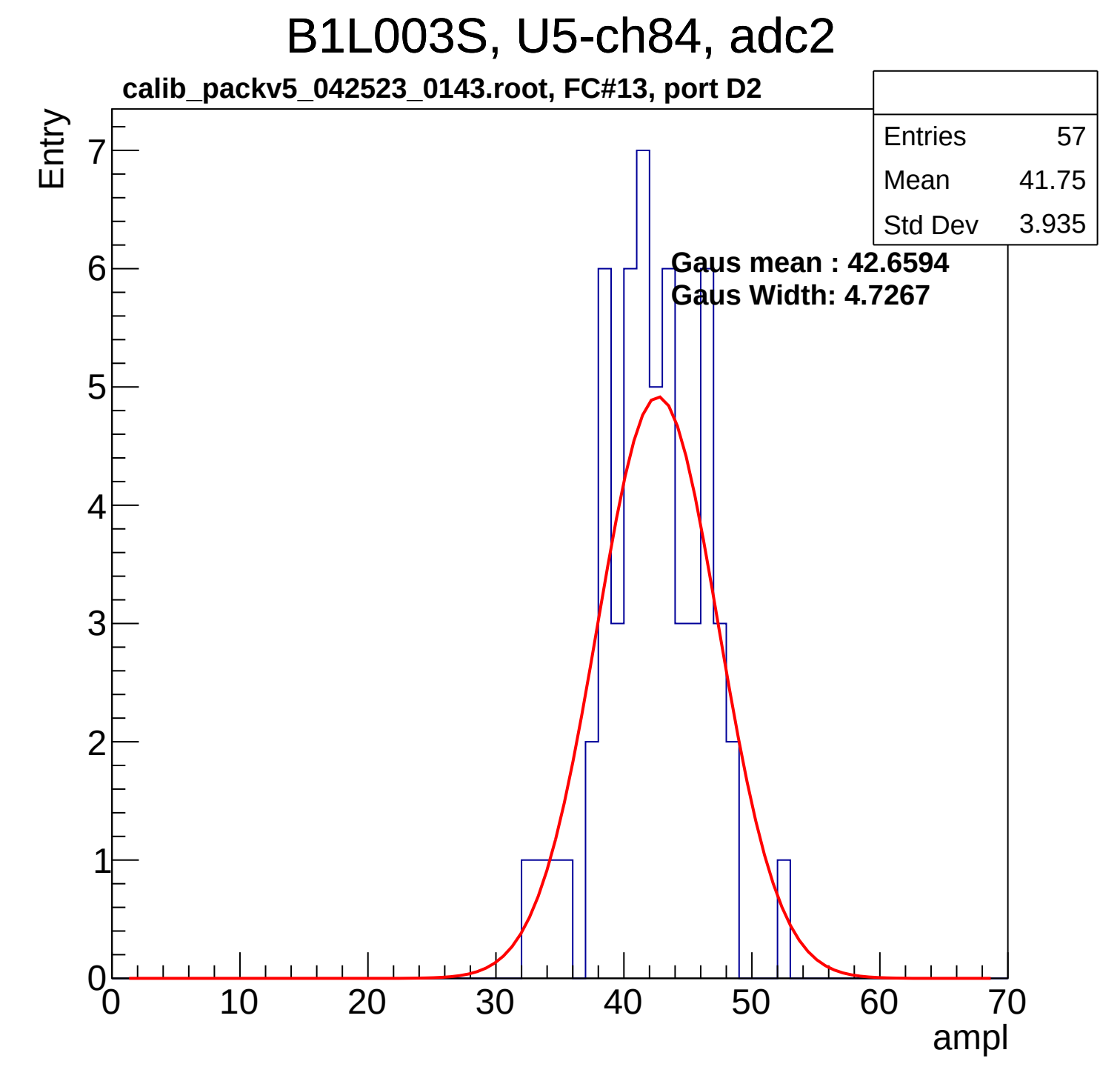
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.75
Std Dev 3.935
Gaus mean : 42.6594
Gaus Width: 4.7267
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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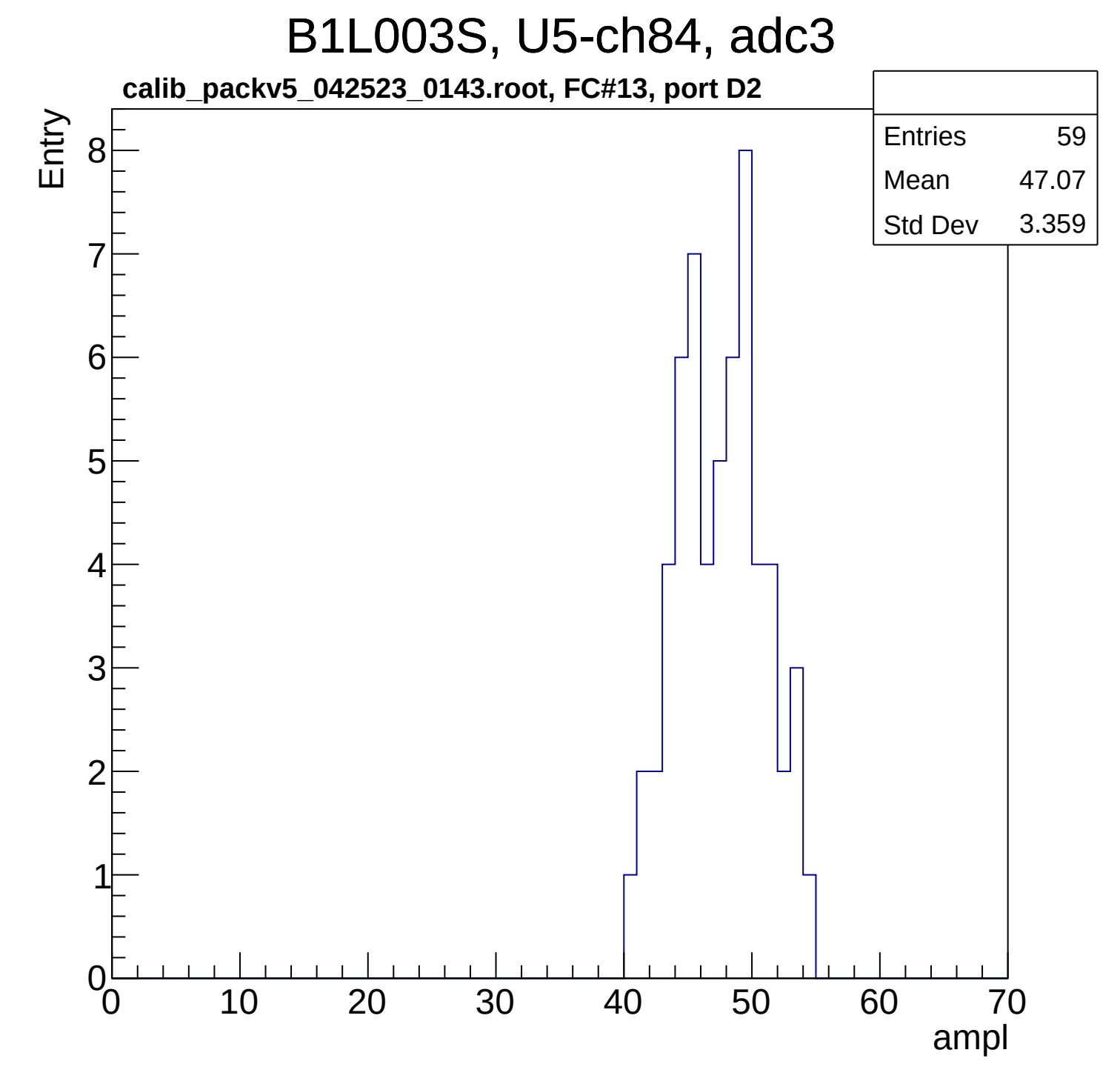
B1L003S, U5-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 47.07
Std Dev 3.359
Entry
ampl
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1
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3
4
5
6
7
8
0
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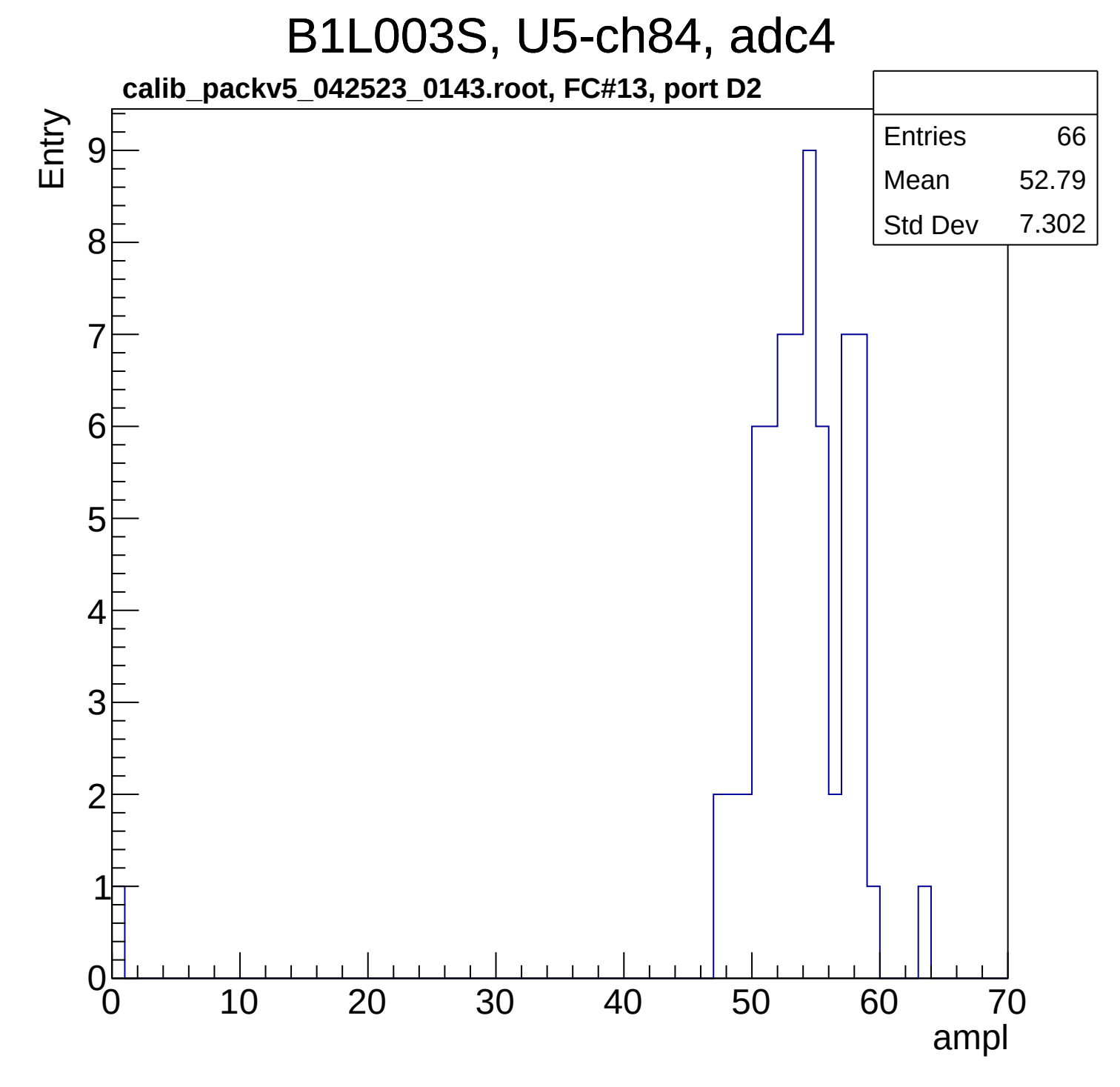
B1L003S, U5-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 52.79
Std Dev 7.302
Entry
ampl
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3
4
5
6
7
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60
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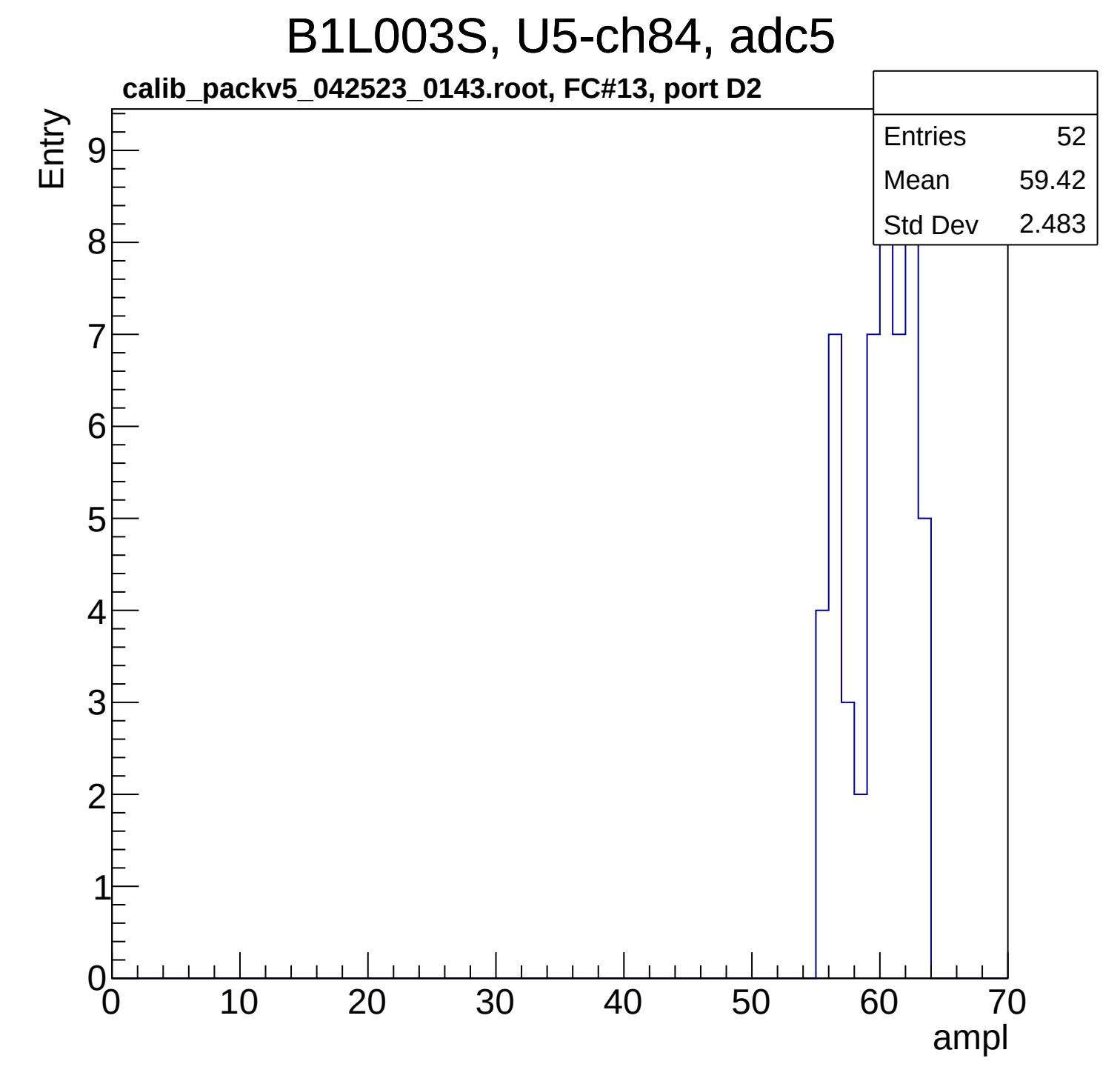
B1L003S, U5-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.42
Std Dev 2.483
Entry
ampl
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3
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40
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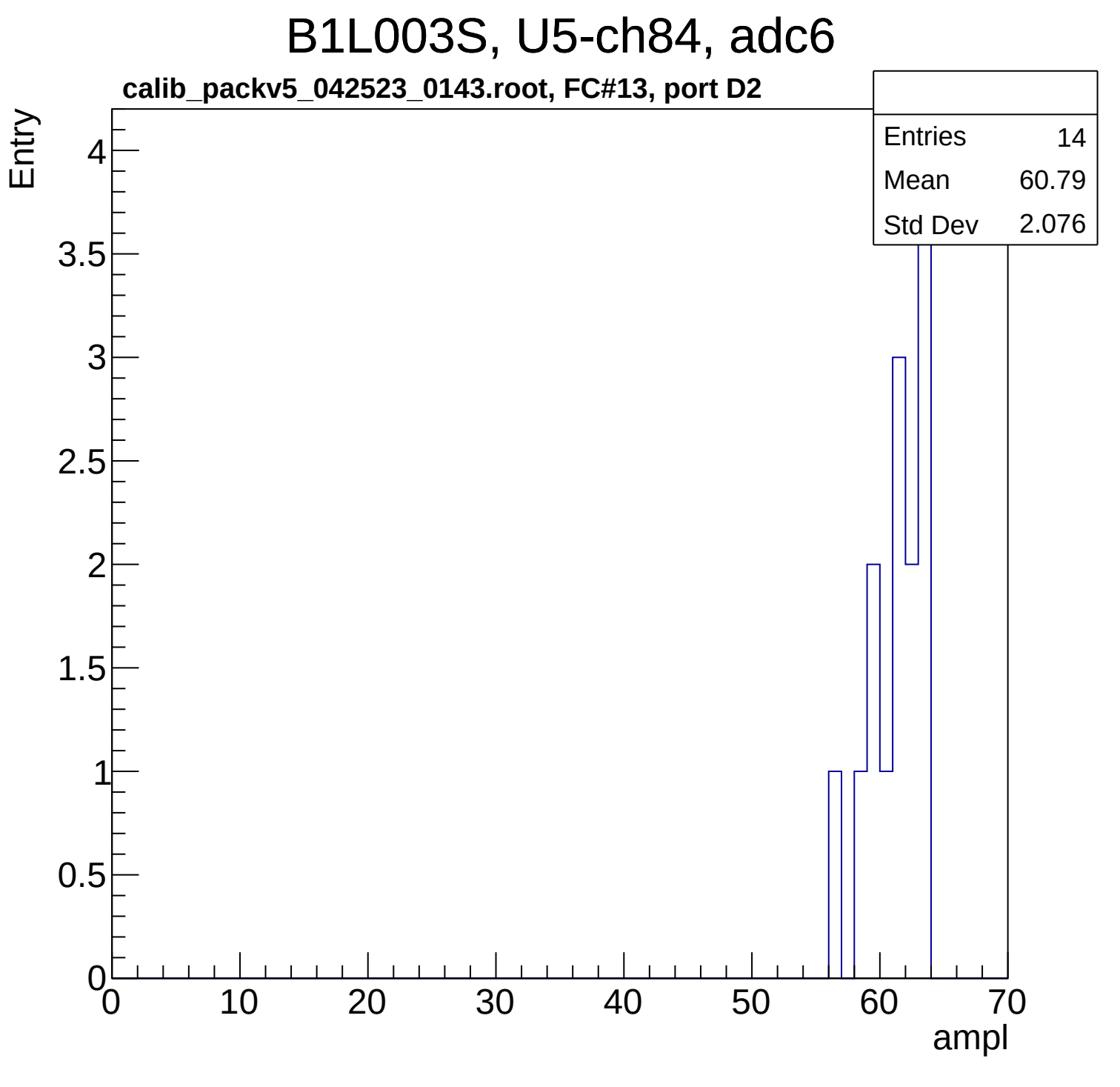
B1L003S, U5-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 60.79
Std Dev 2.076
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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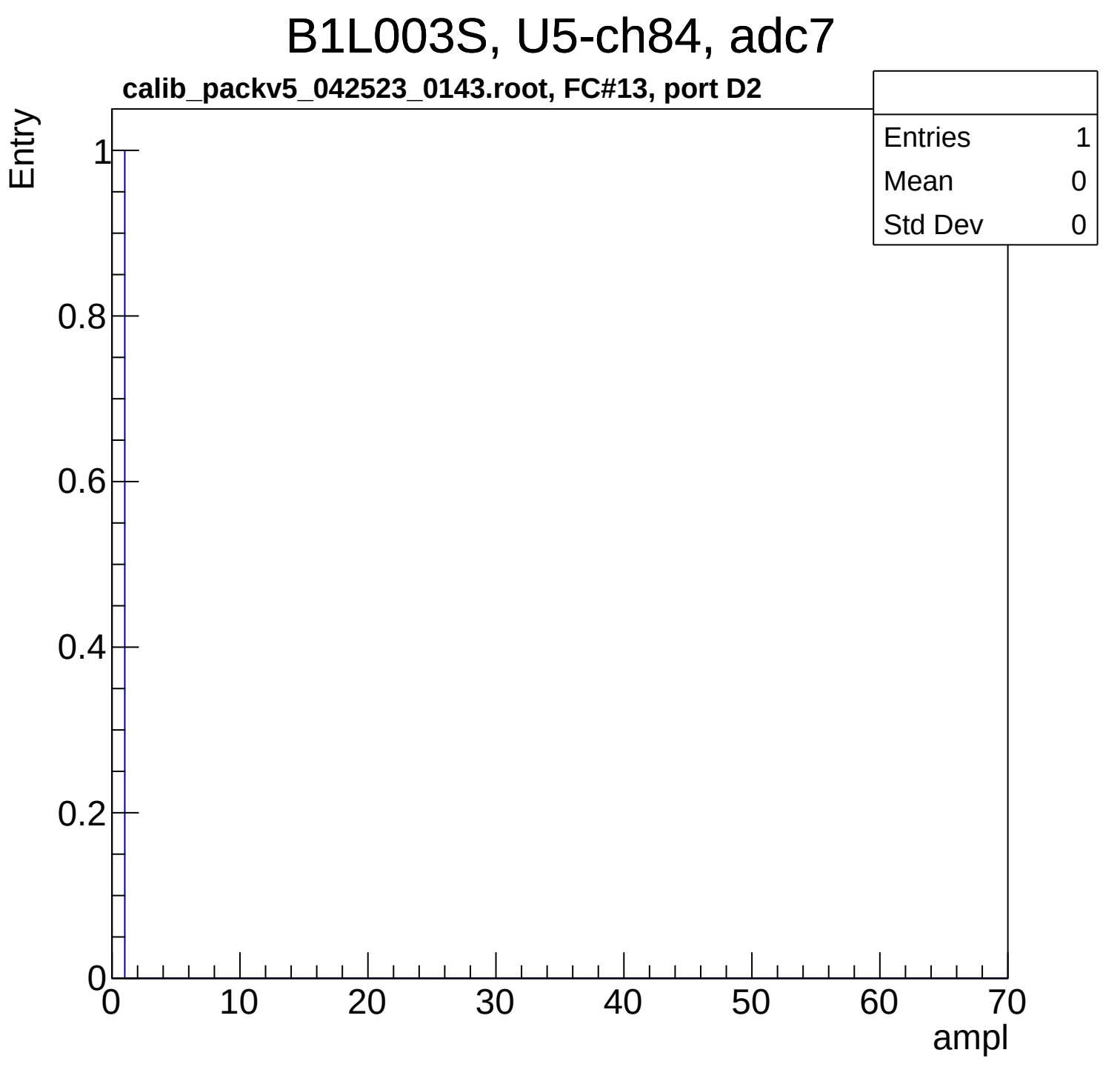
B1L003S, U5-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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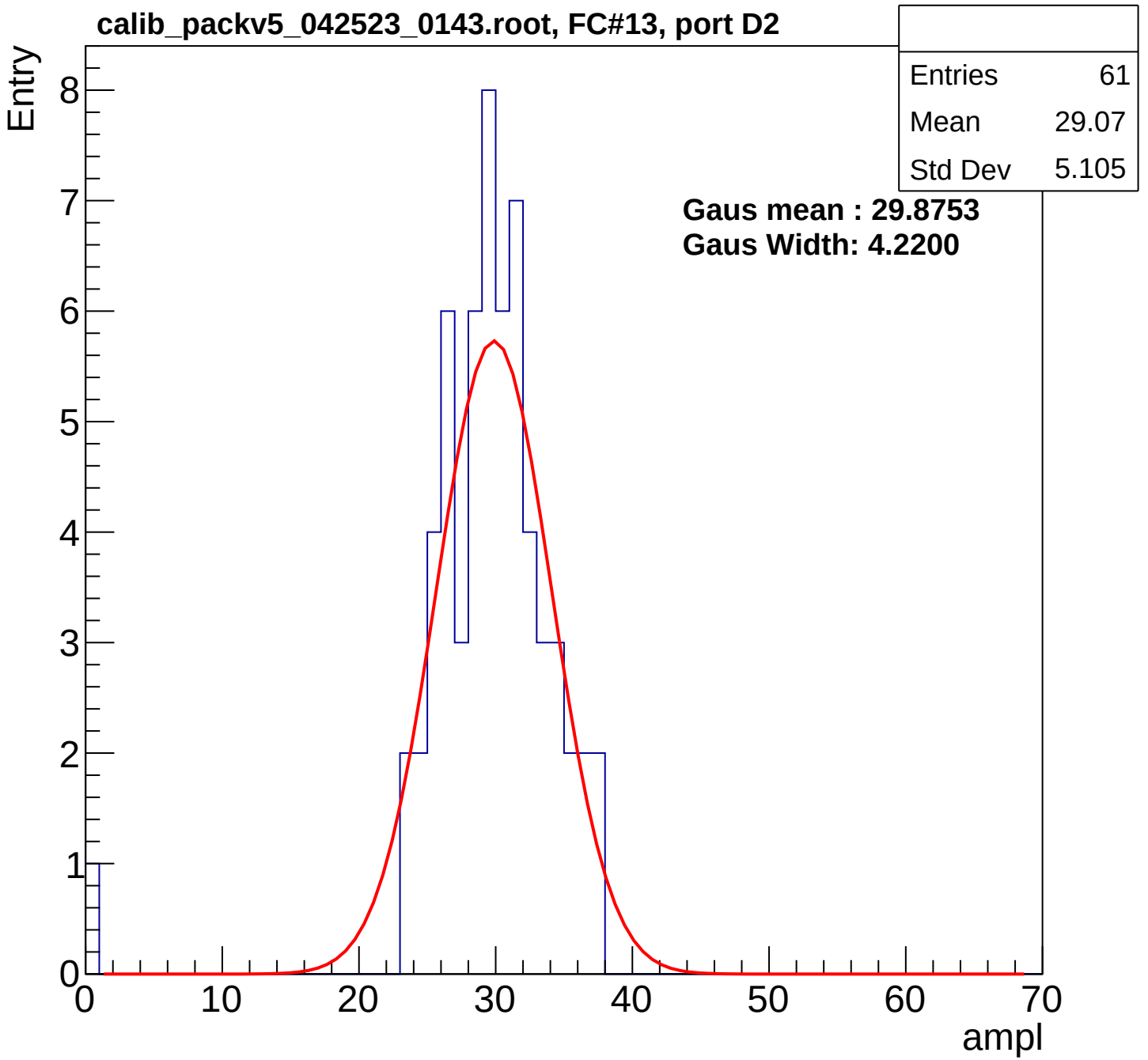
50
60
70
ampl


B1L003S, U5-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.07
Std Dev 5.105
Gaus mean : 29.8753
Gaus Width: 4.2200
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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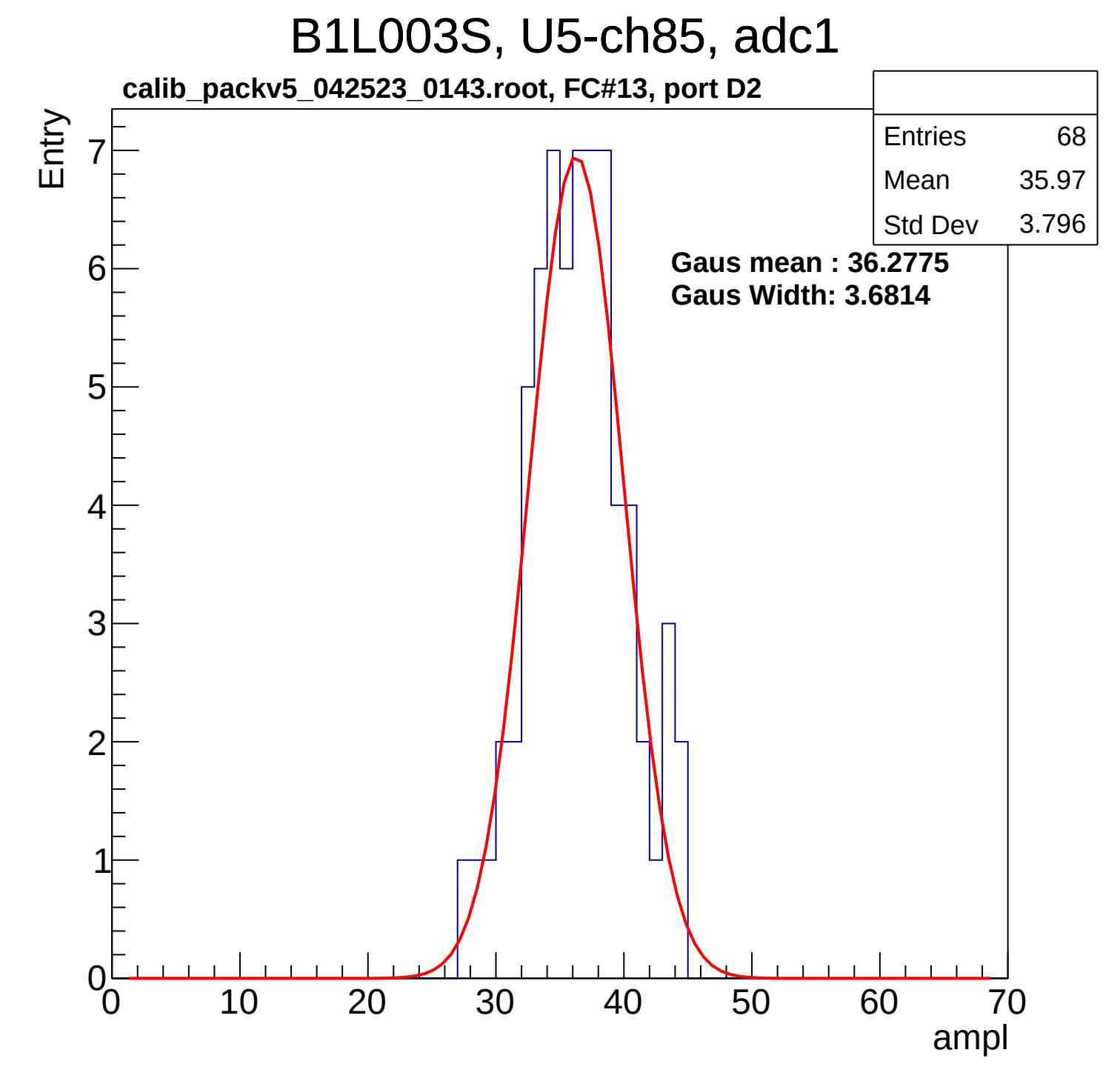
B1L003S, U5-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.97
Std Dev 3.796
Gaus mean : 36.2775
Gaus Width: 3.6814
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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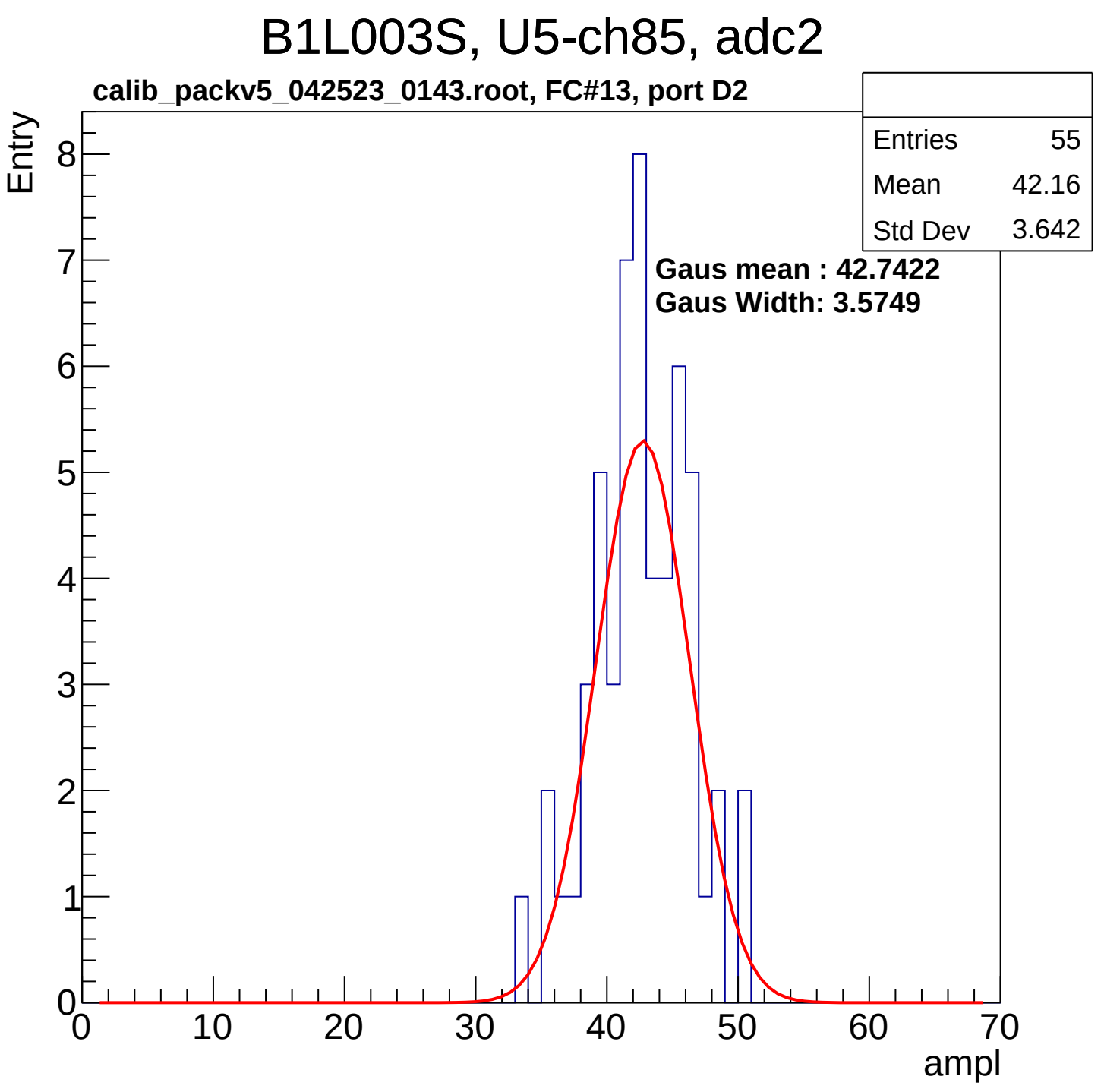
B1L003S, U5-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.16
Std Dev 3.642
Gaus mean : 42.7422
Gaus Width: 3.5749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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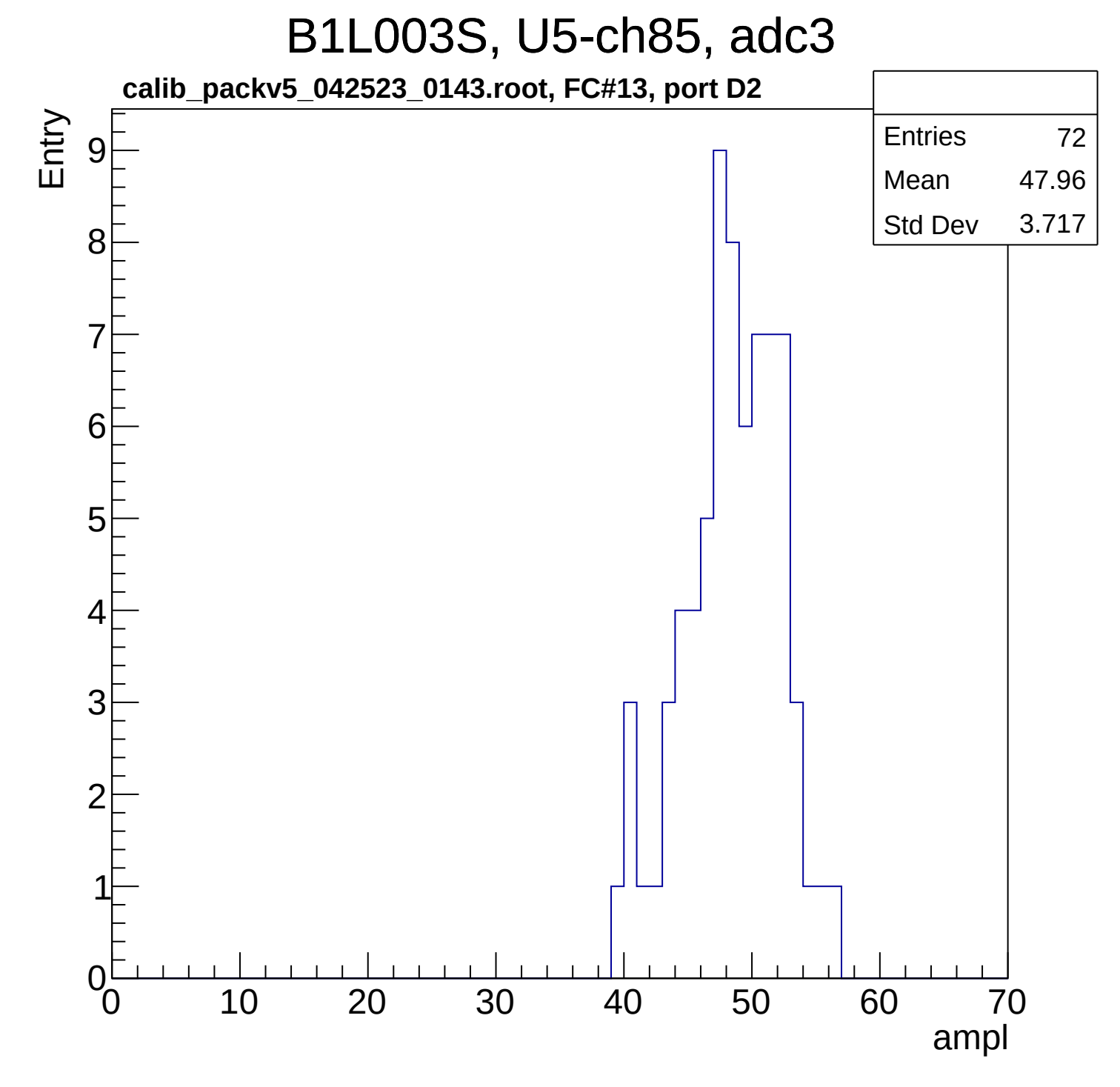
B1L003S, U5-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 47.96
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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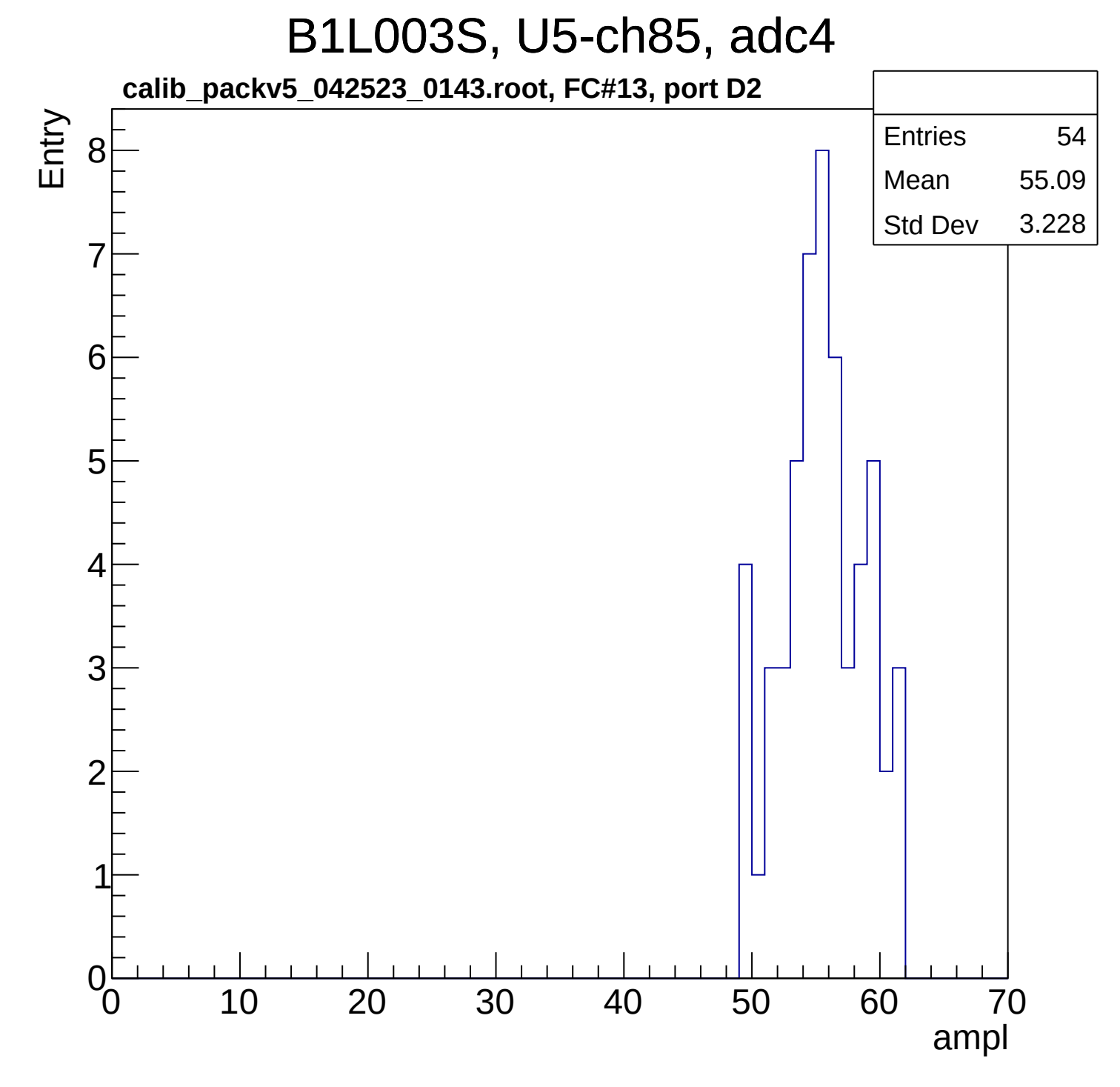
B1L003S, U5-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.09
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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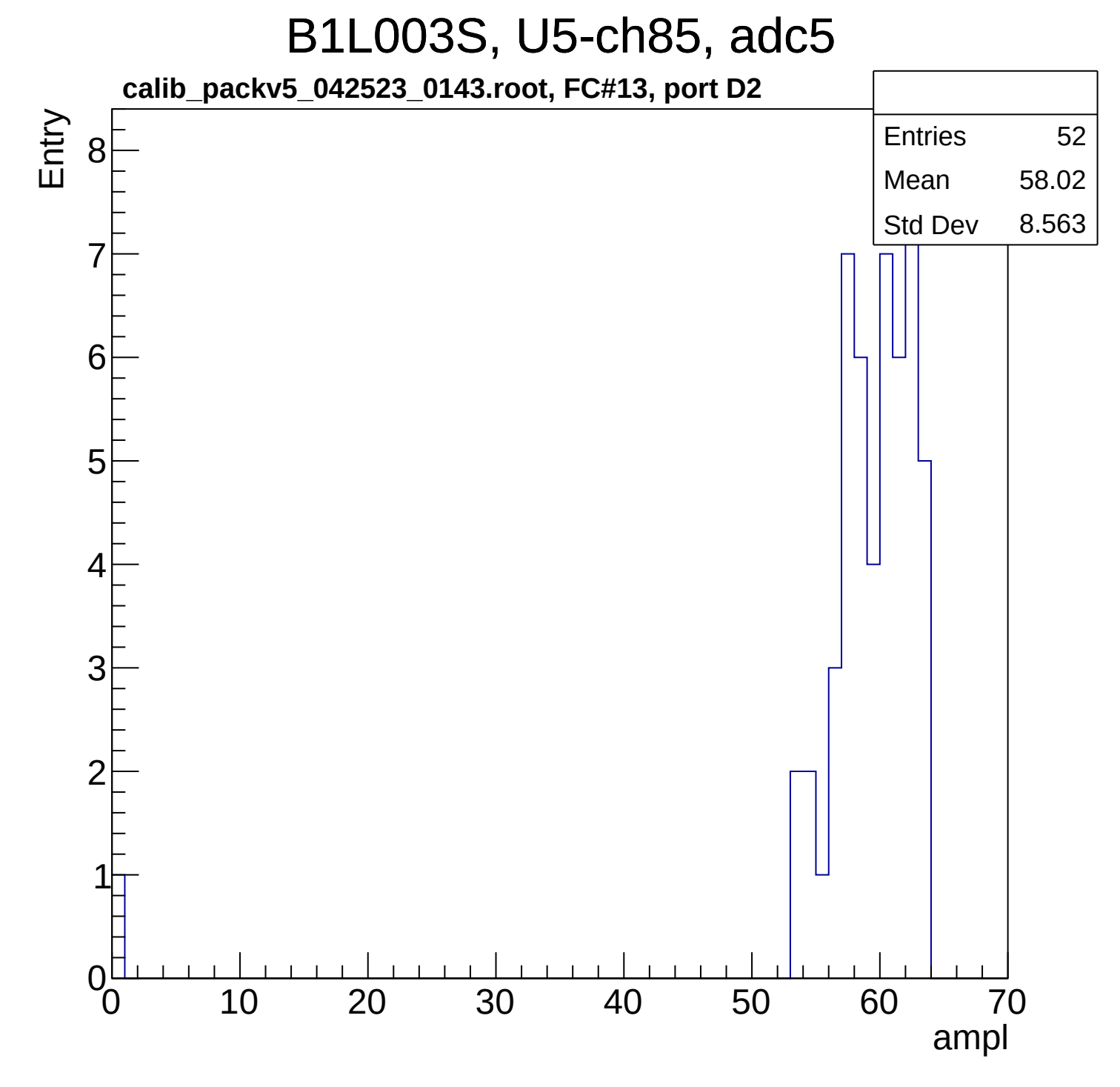
B1L003S, U5-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.02
Std Dev 8.563
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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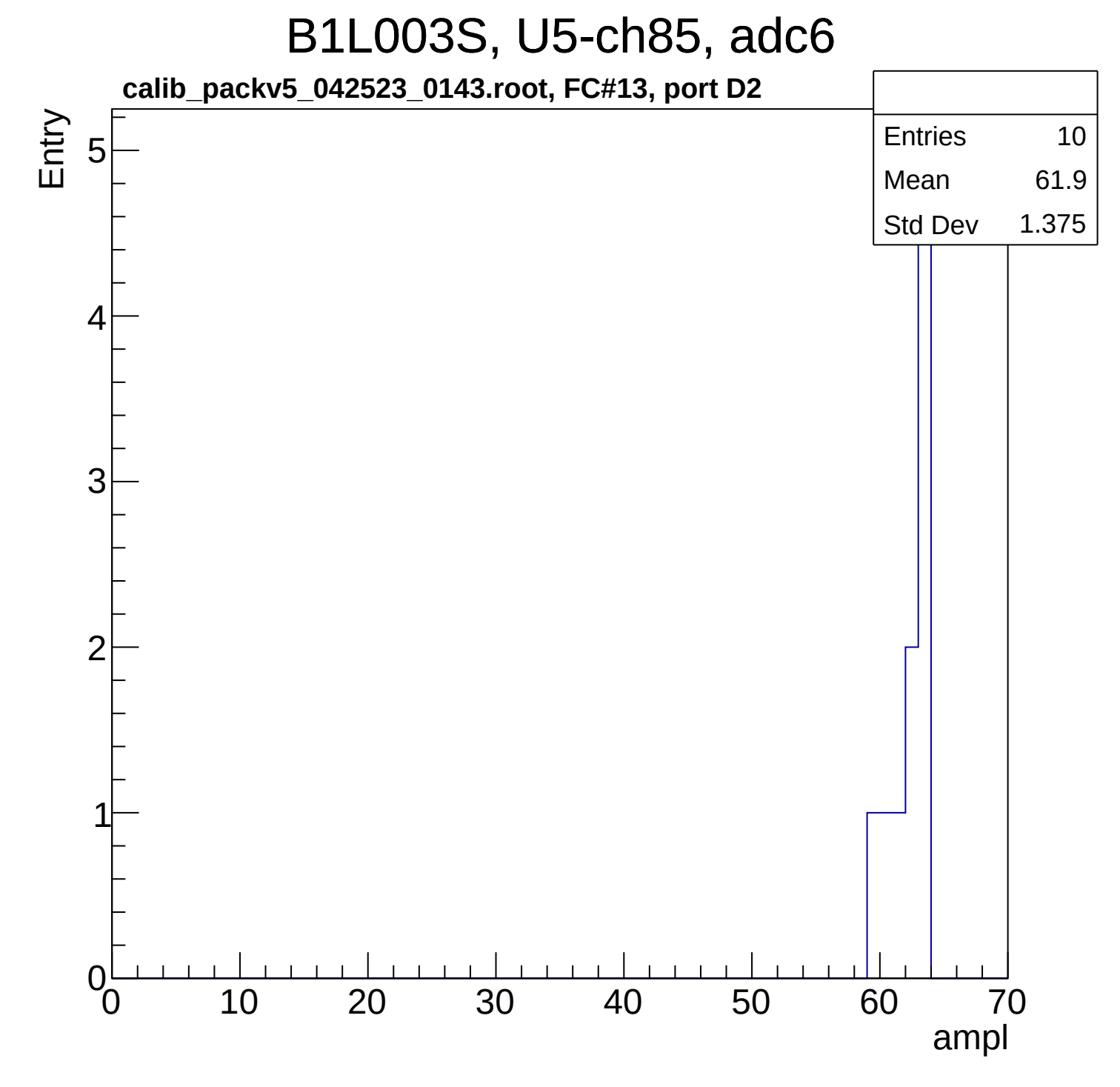
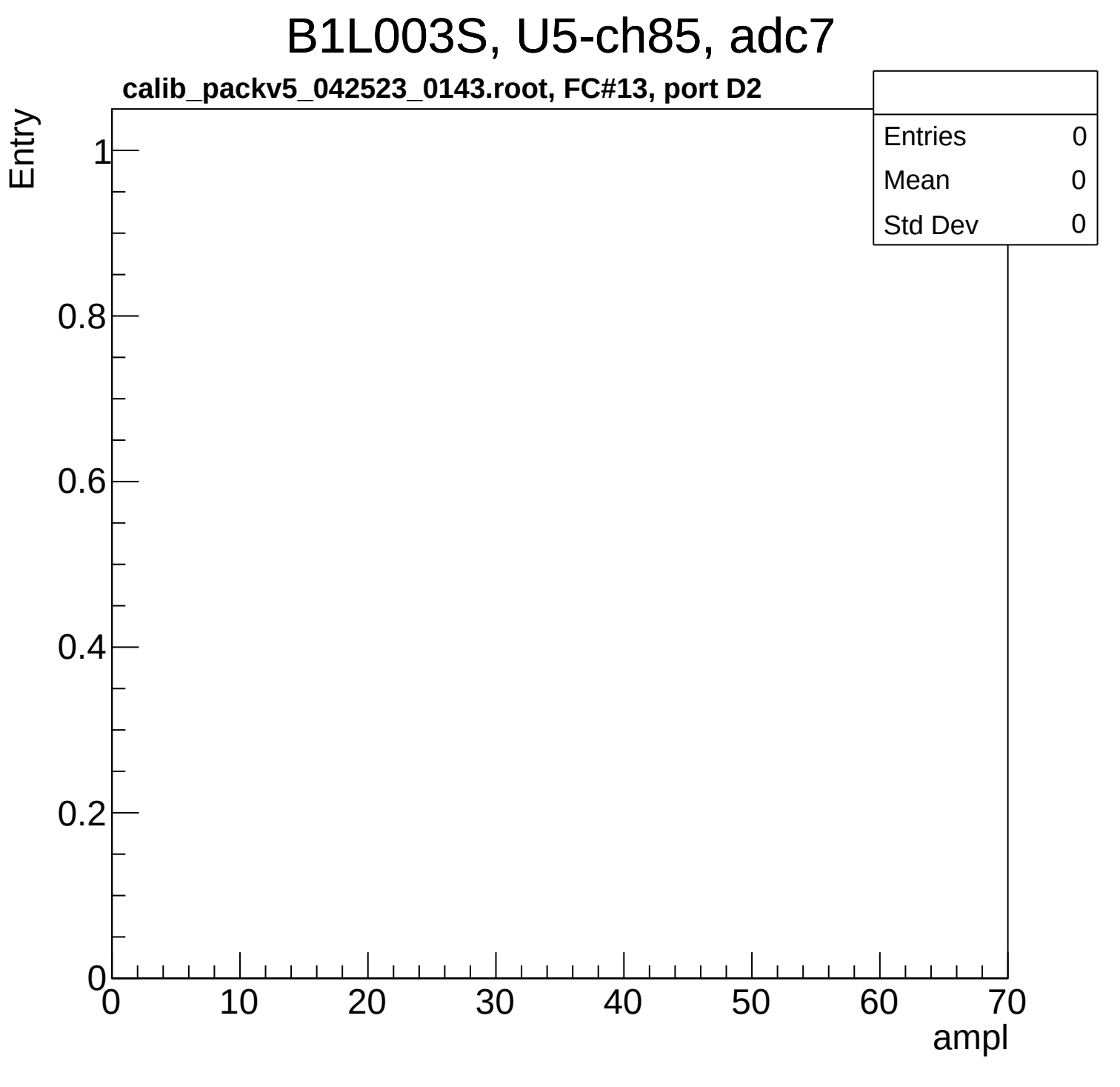
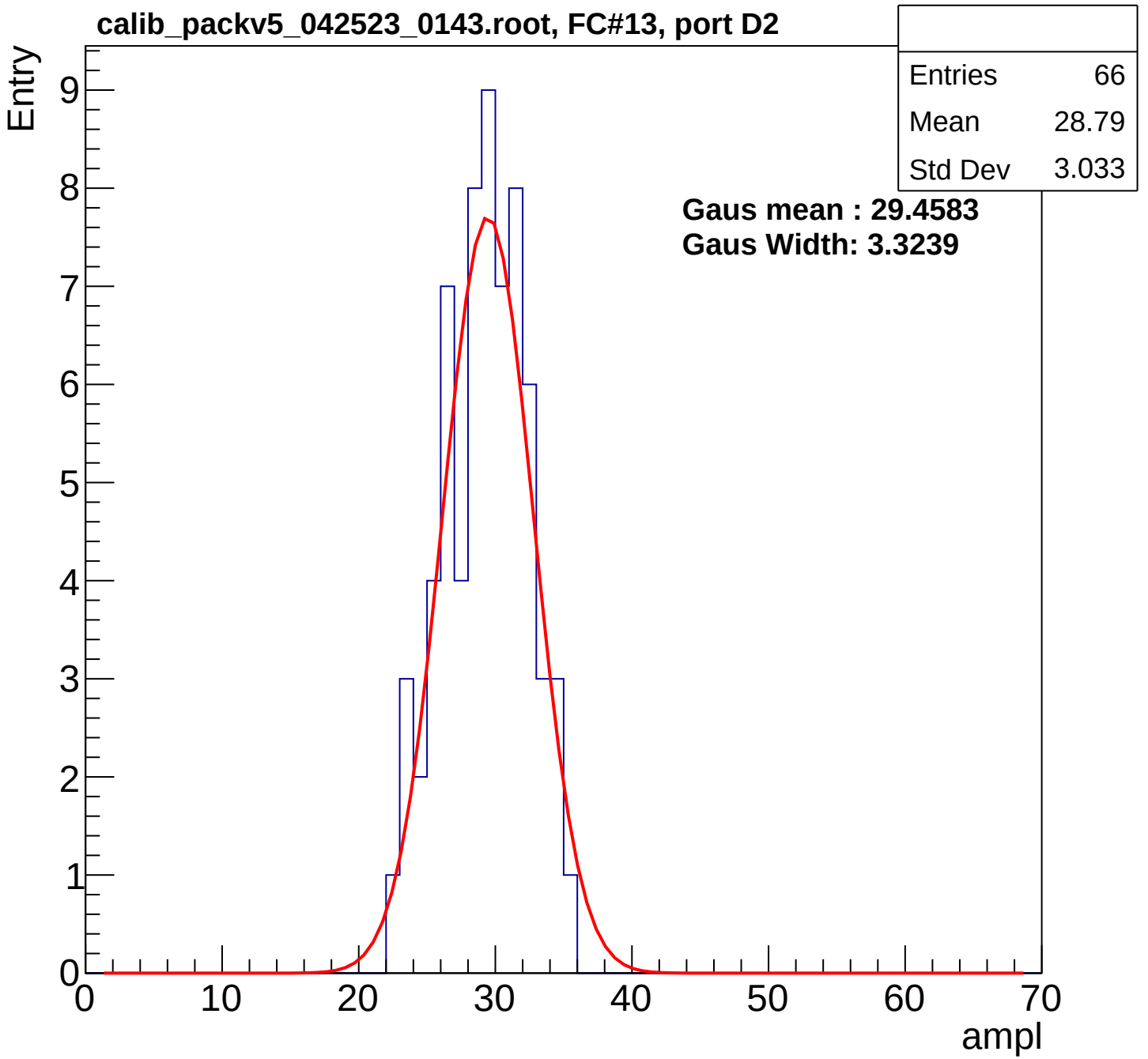
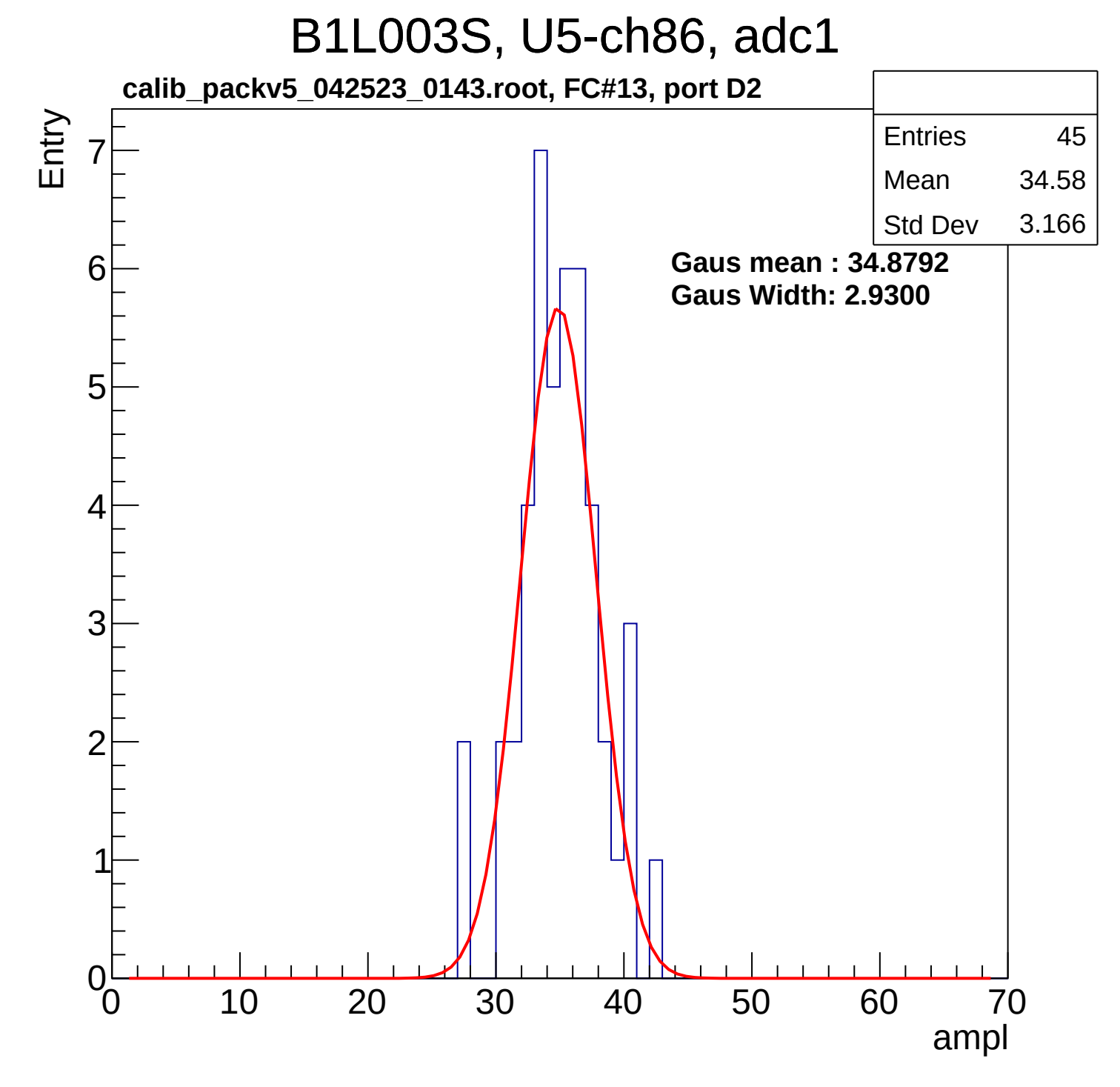

B1L003S, U5-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.9
Std Dev 1.375
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.79
Std Dev 3.033
Gaus mean : 29.4583
Gaus Width: 3.3239
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 34.58
Std Dev 3.166
Gaus mean : 34.8792
Gaus Width: 2.9300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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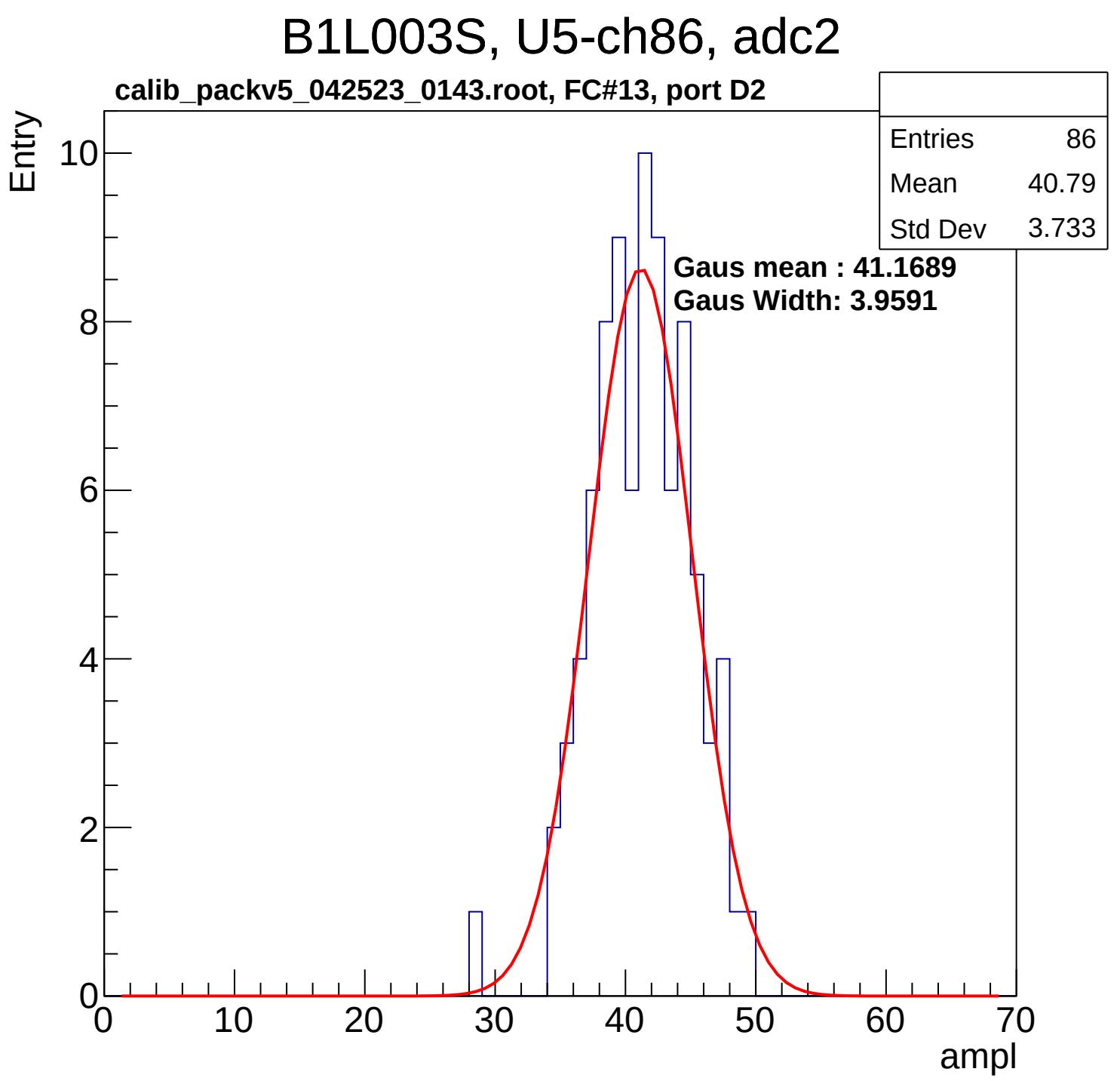
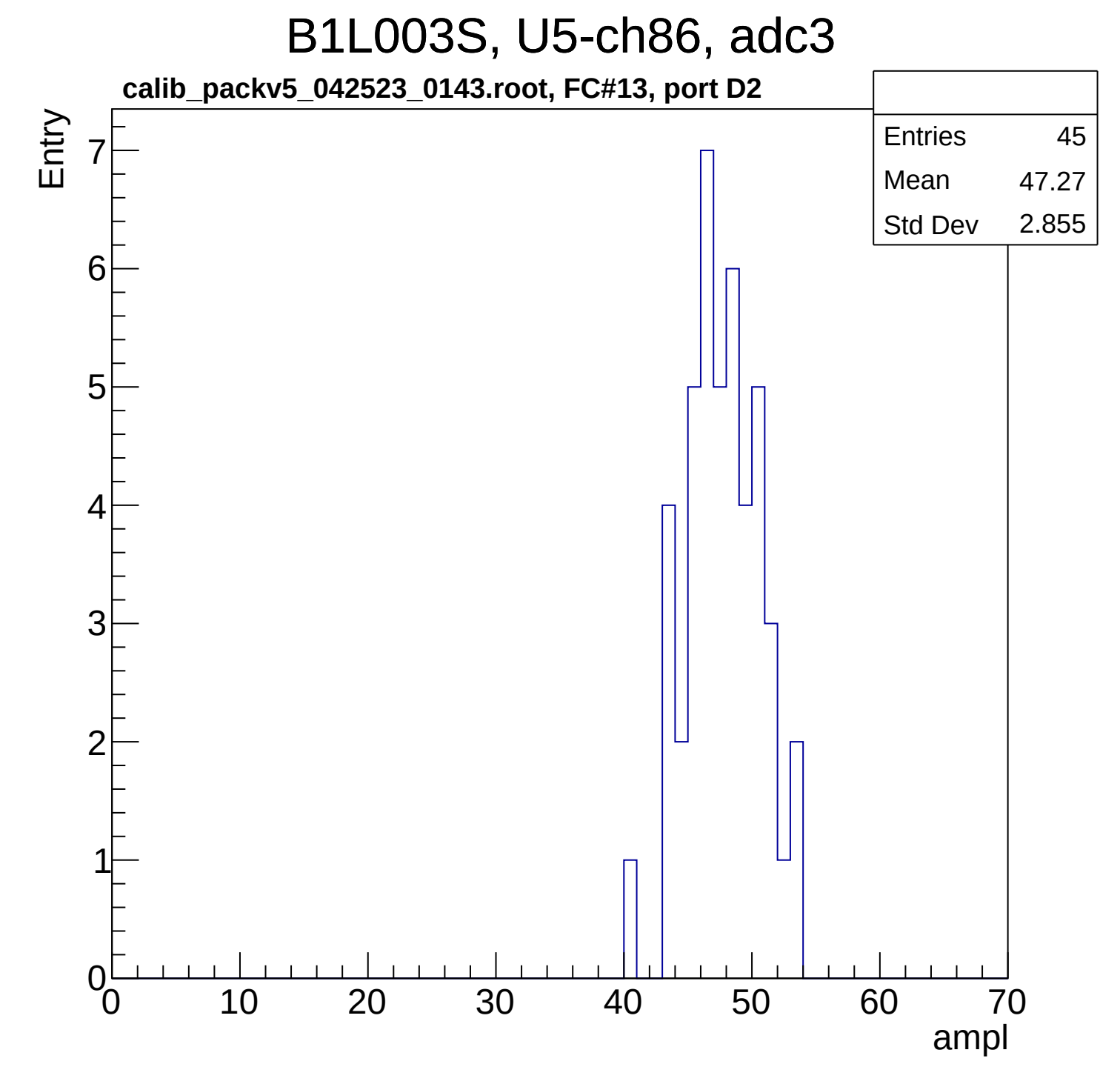

B1L003S, U5-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 40.79
Std Dev 3.733
Gaus mean : 41.1689
Gaus Width: 3.9591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 47.27
Std Dev 2.855
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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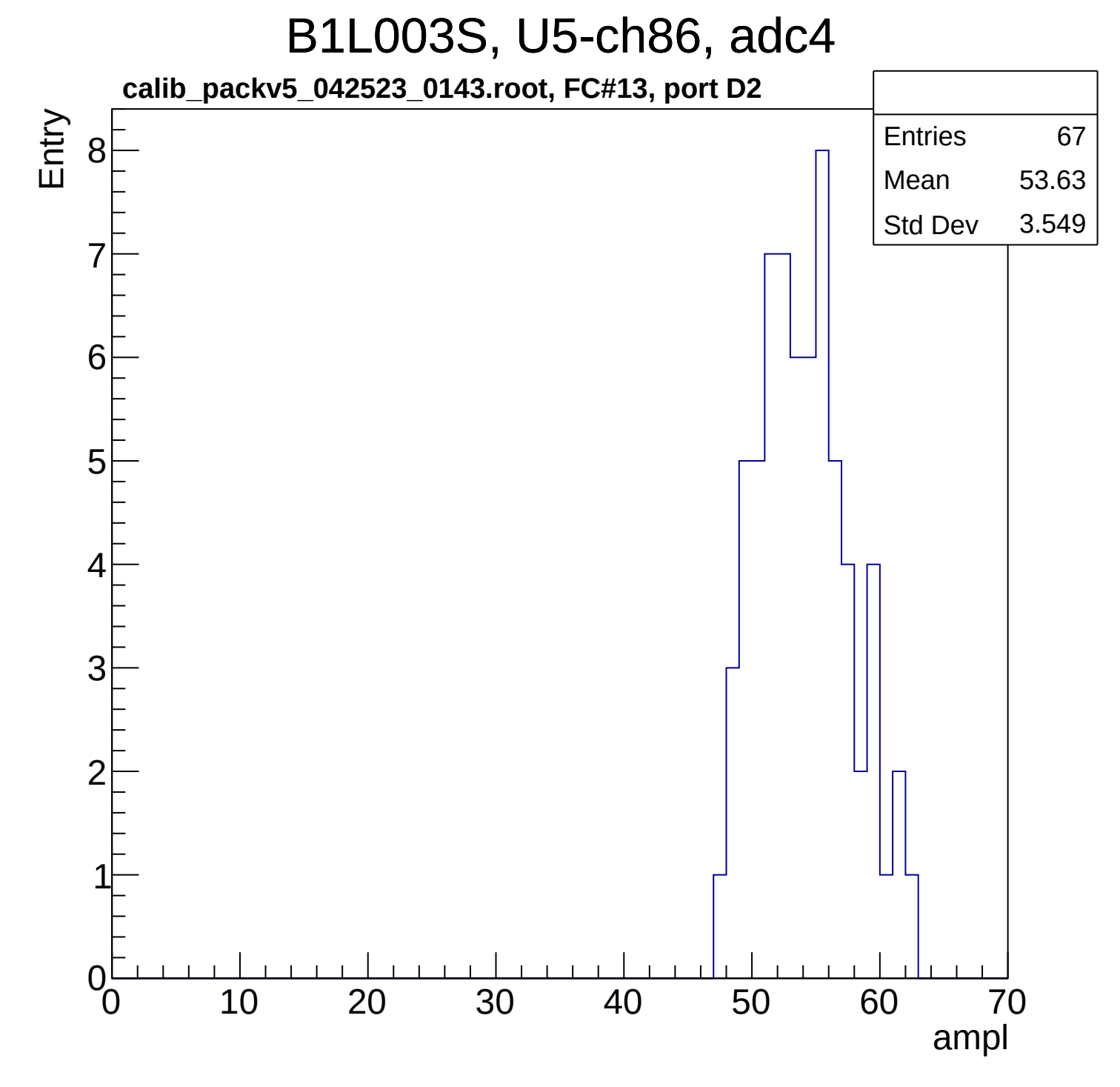
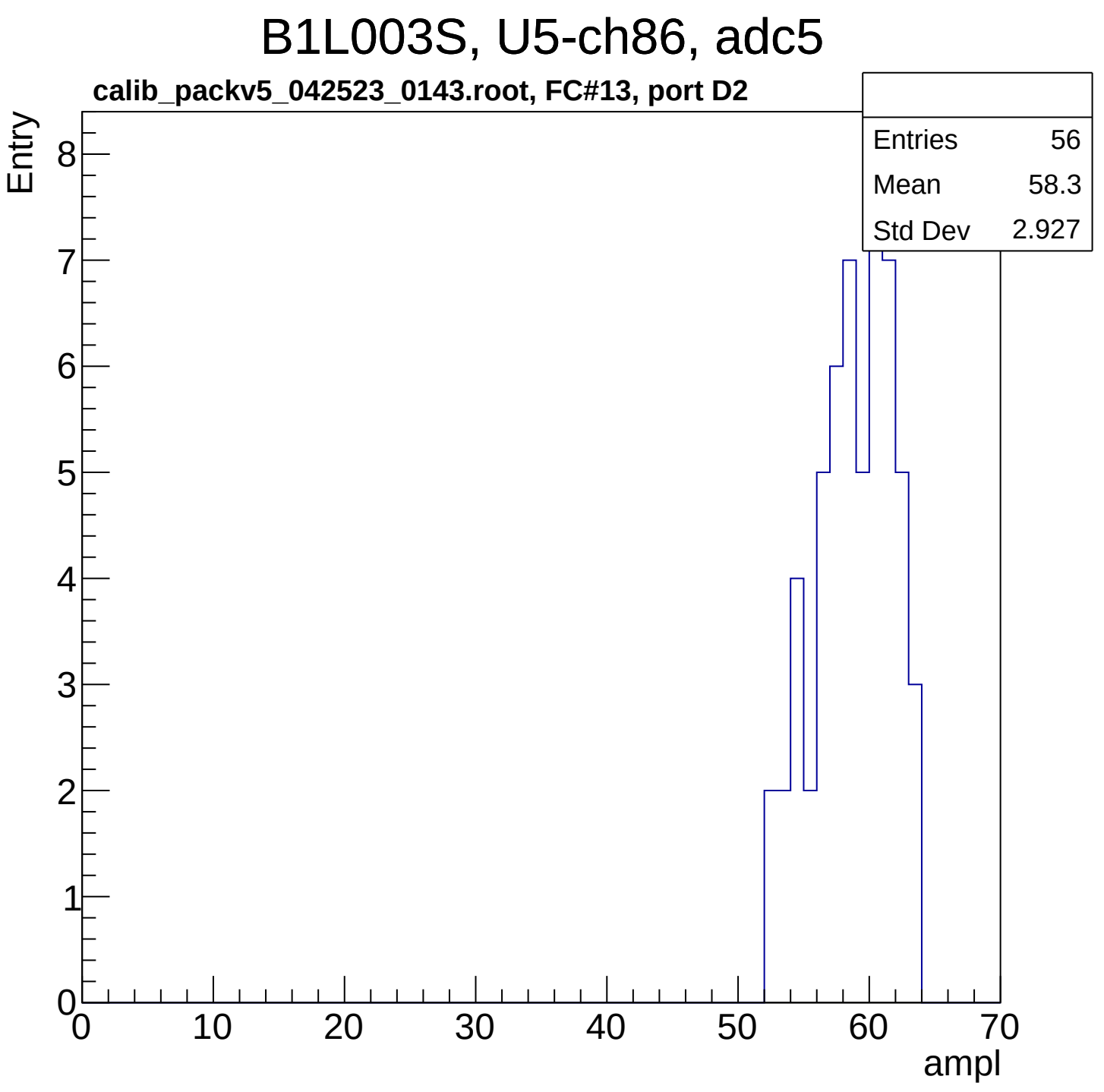
40
50
60
70


B1L003S, U5-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 53.63
Std Dev 3.549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.3
Std Dev 2.927
Entry
ampl
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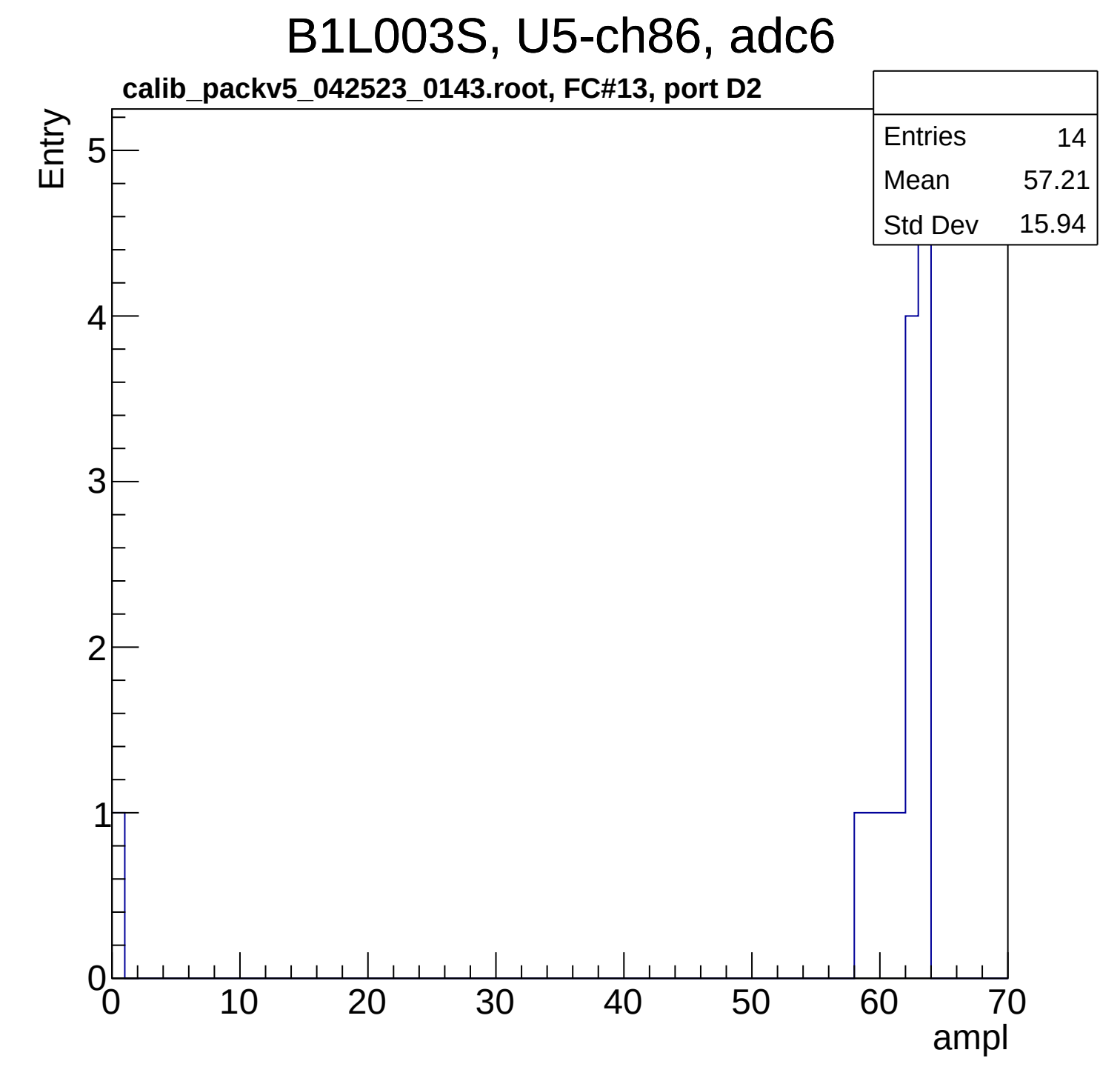
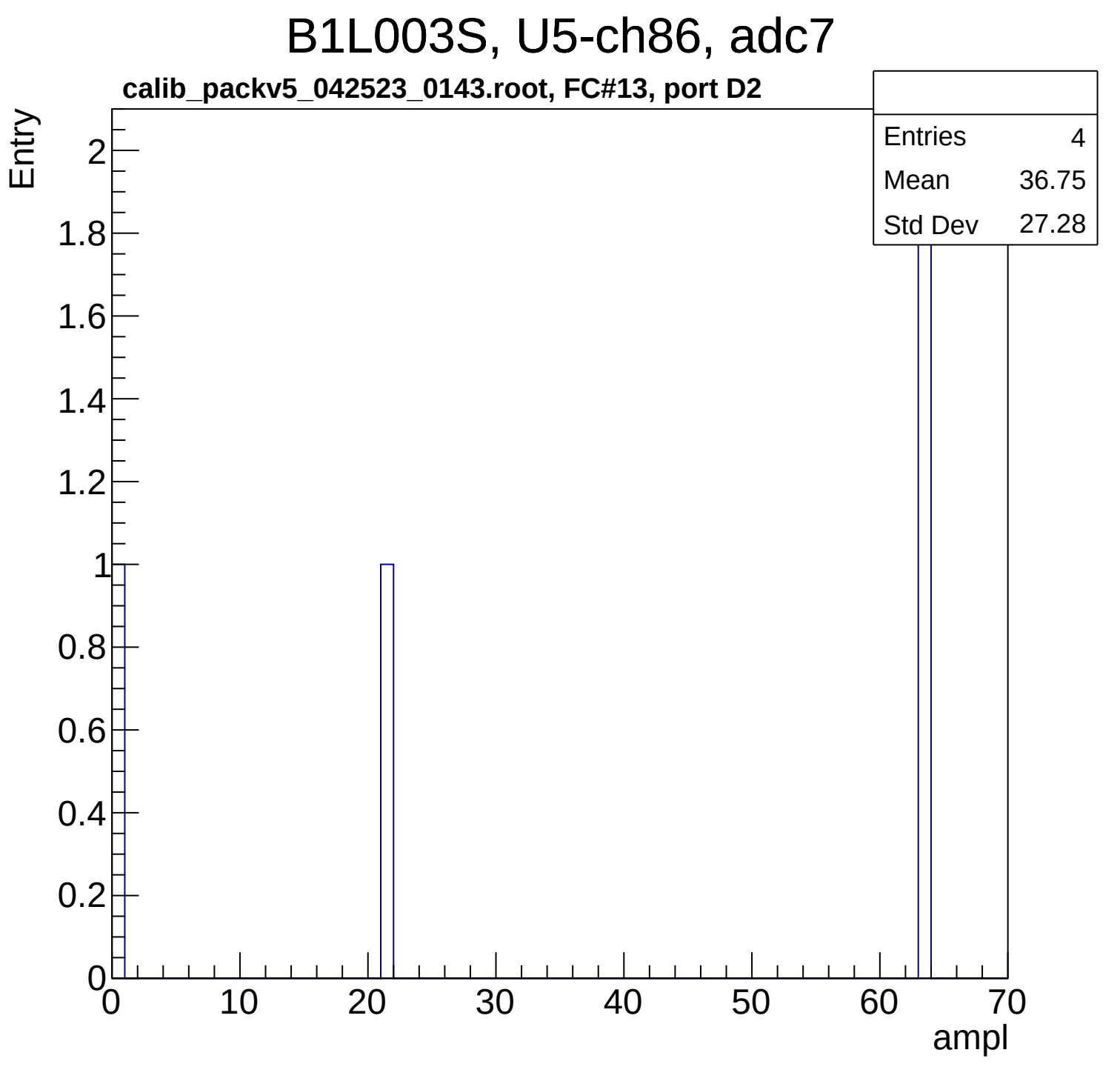
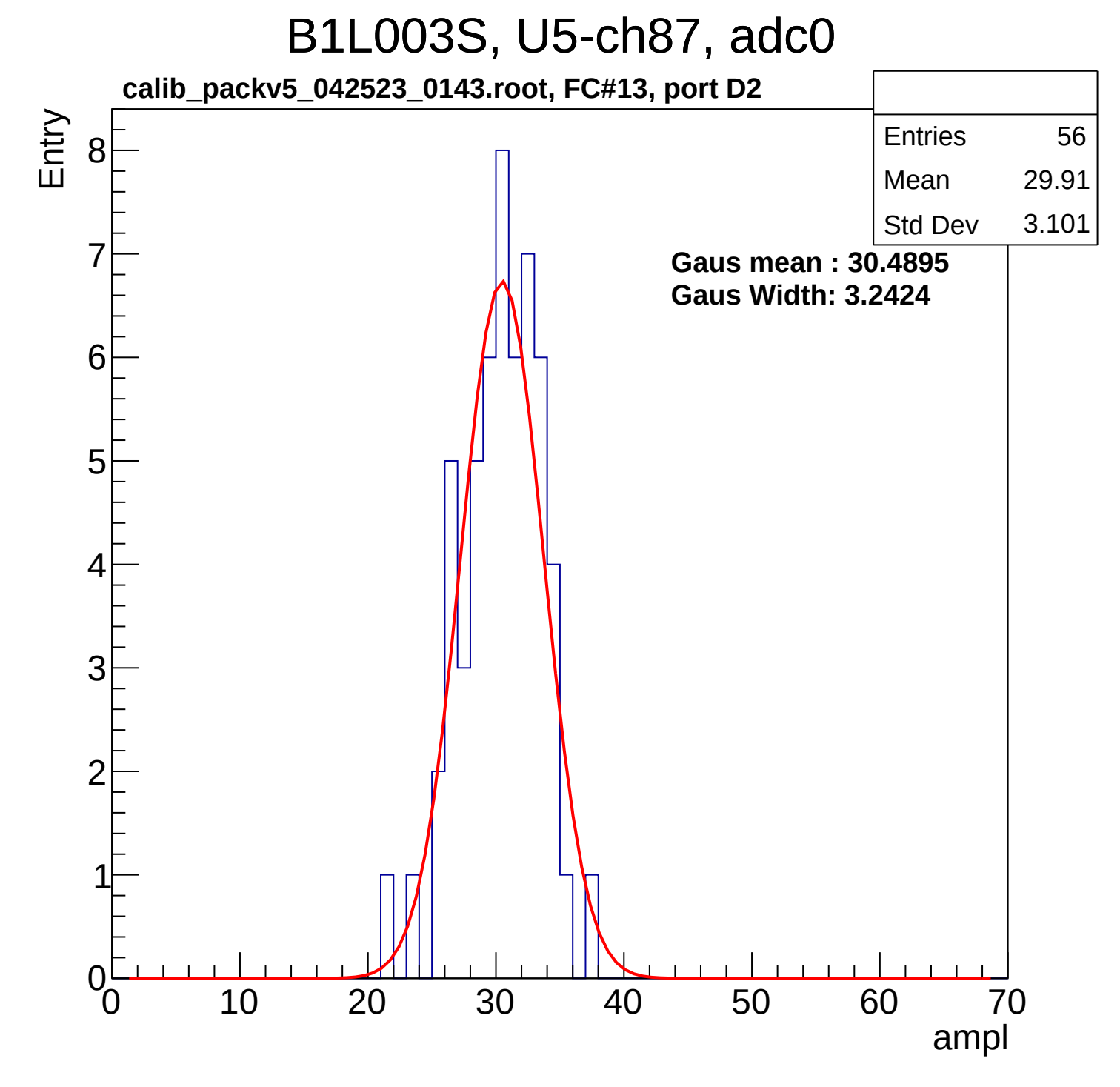
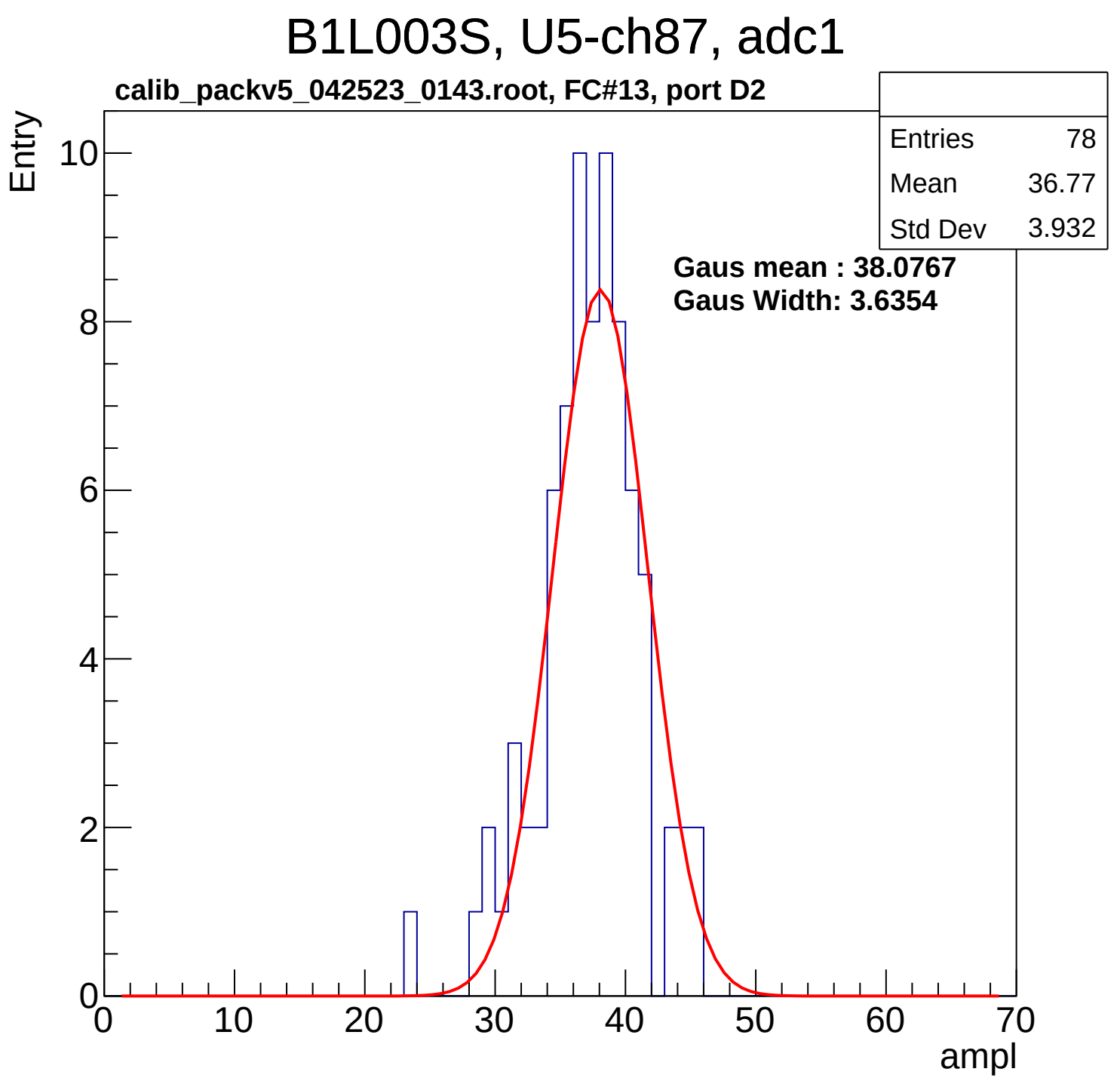
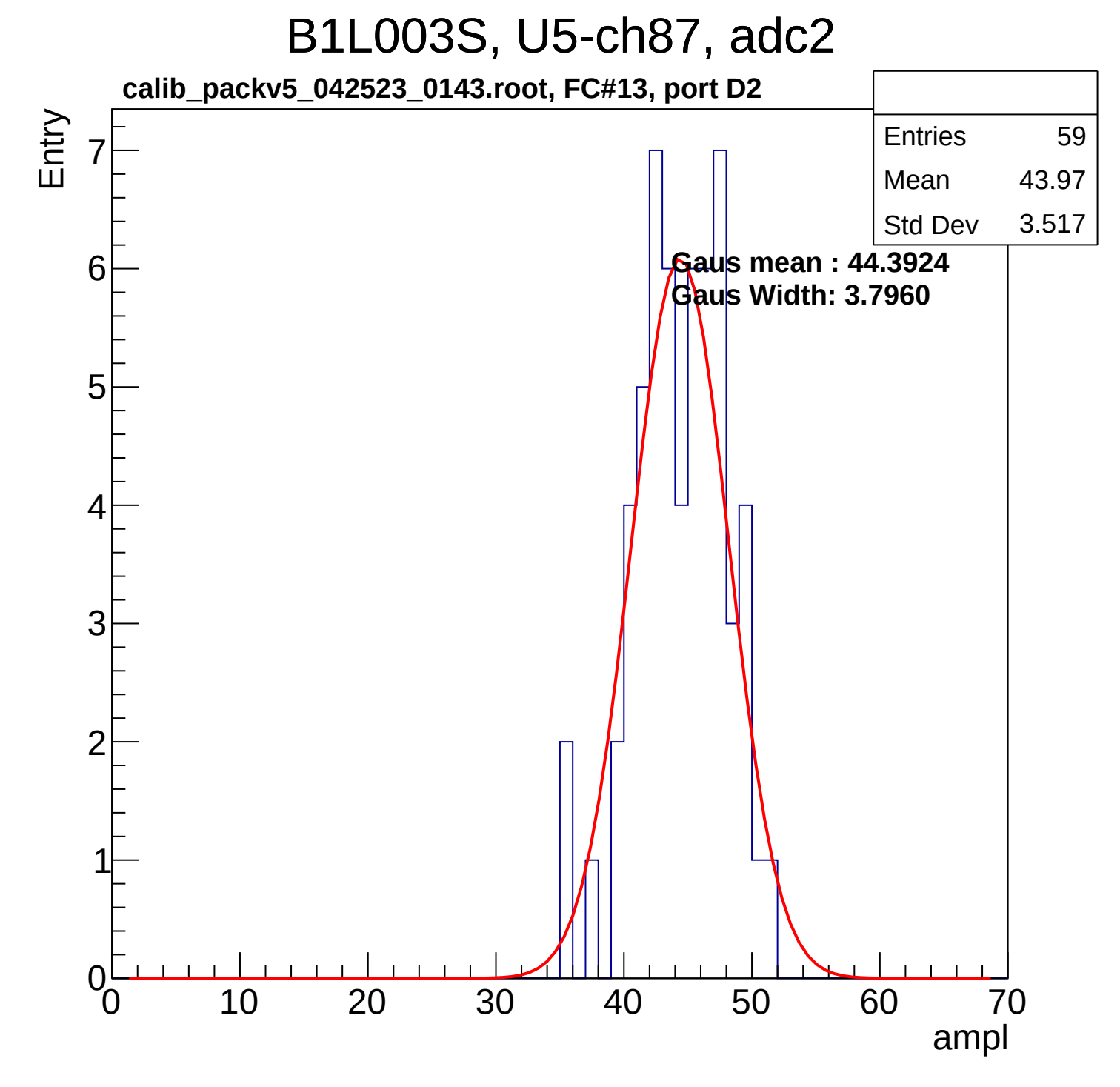
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

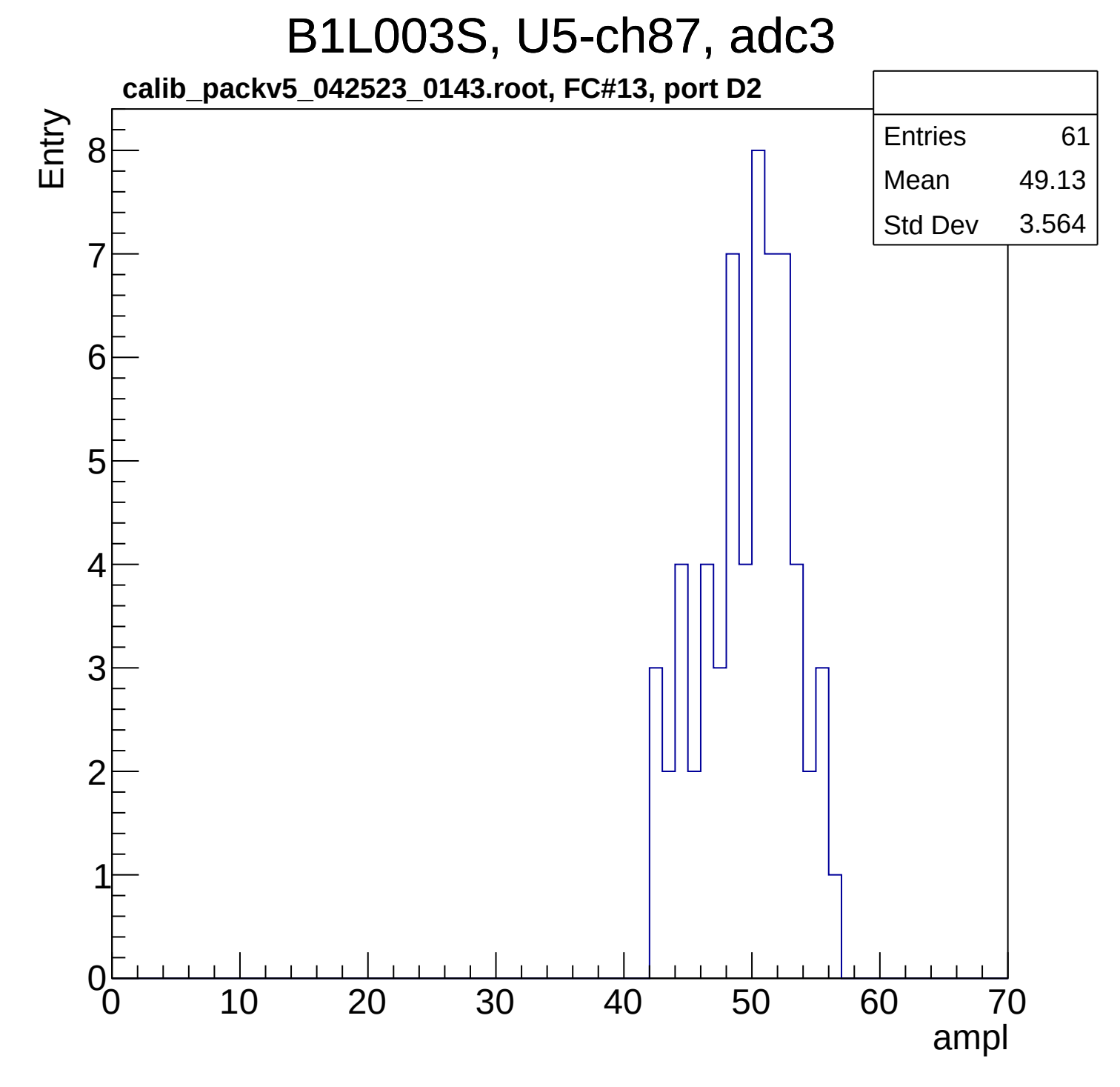

B1L003S, U5-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.21
Std Dev 15.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 29.91
Std Dev 3.101
Gaus mean : 30.4895
Gaus Width: 3.2424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.77
Std Dev 3.932
Gaus mean : 38.0767
Gaus Width: 3.6354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.97
Std Dev 3.517
Gaus mean : 44.3924
Gaus Width: 3.7960
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.13
Std Dev 3.564
Entry
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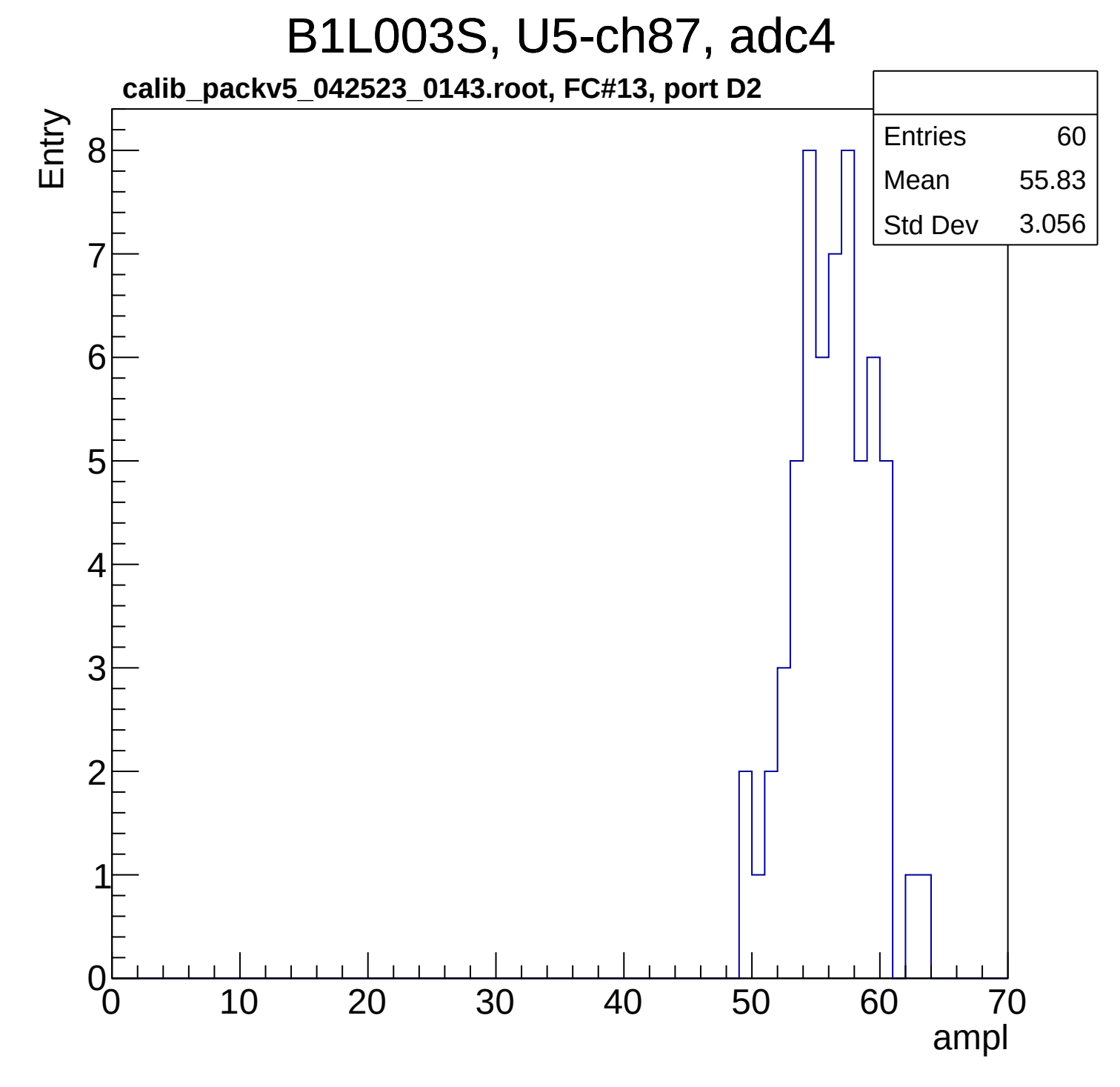
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.83
Std Dev 3.056
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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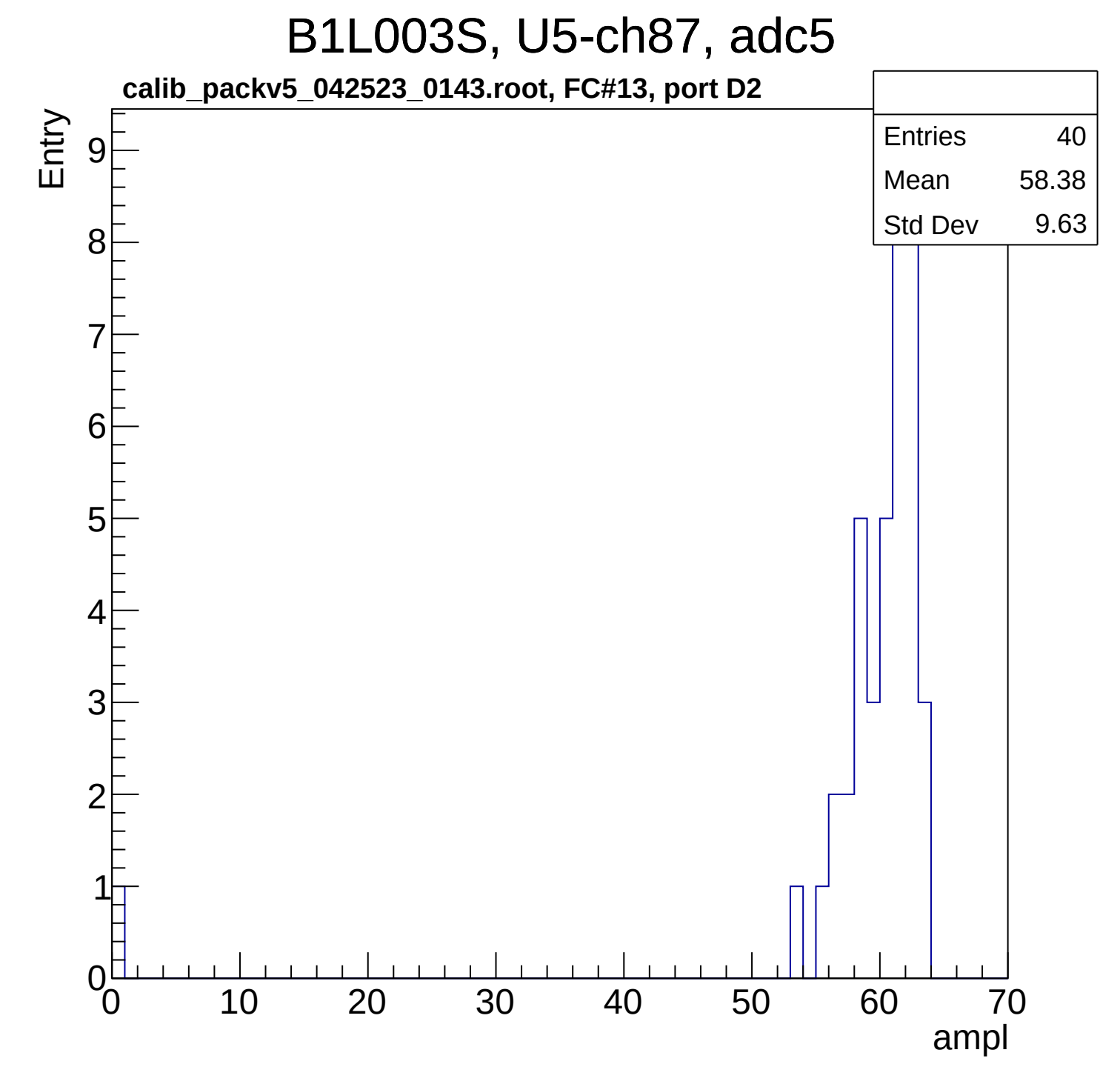
60
70


B1L003S, U5-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.38
Std Dev 9.63
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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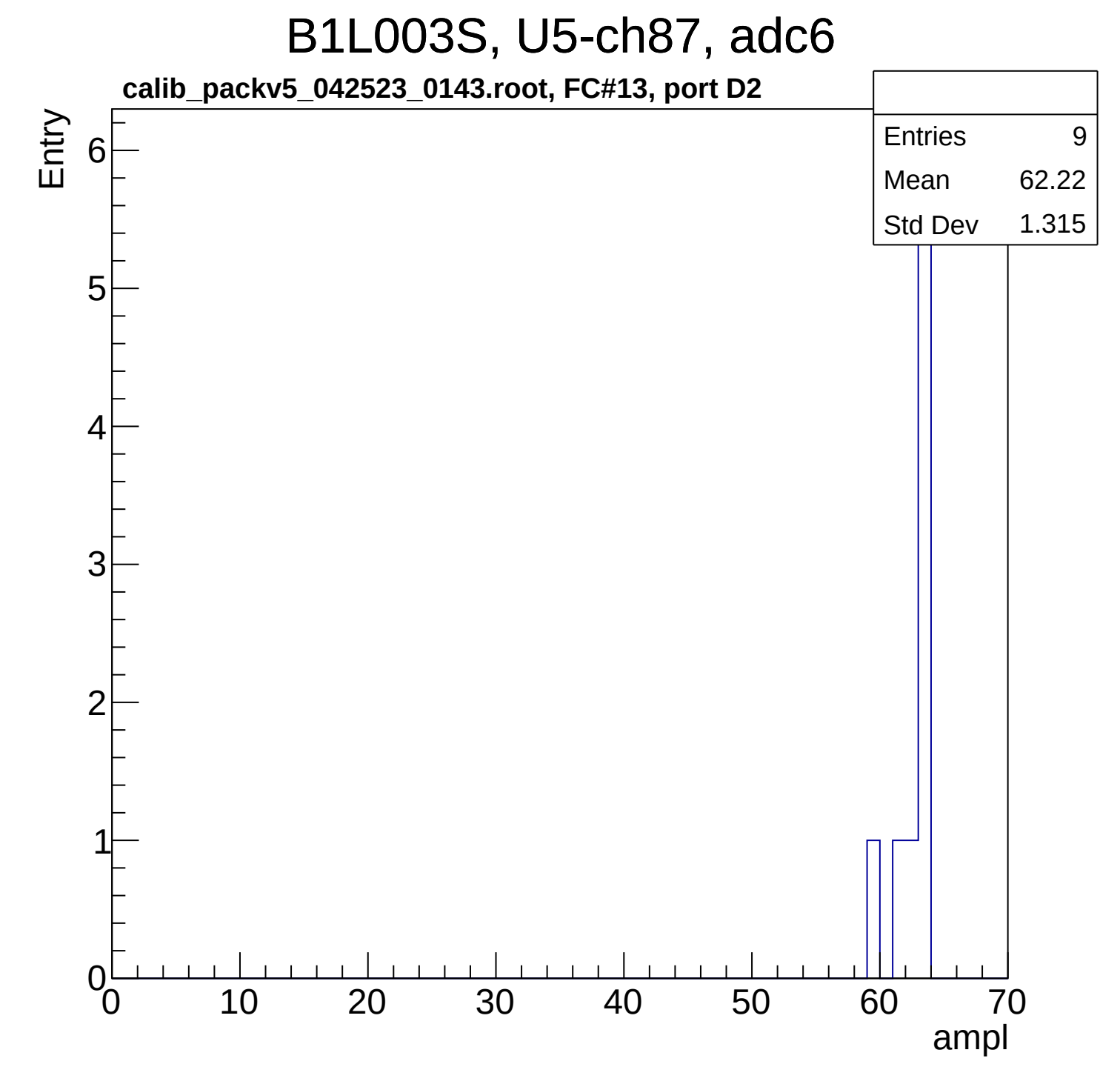
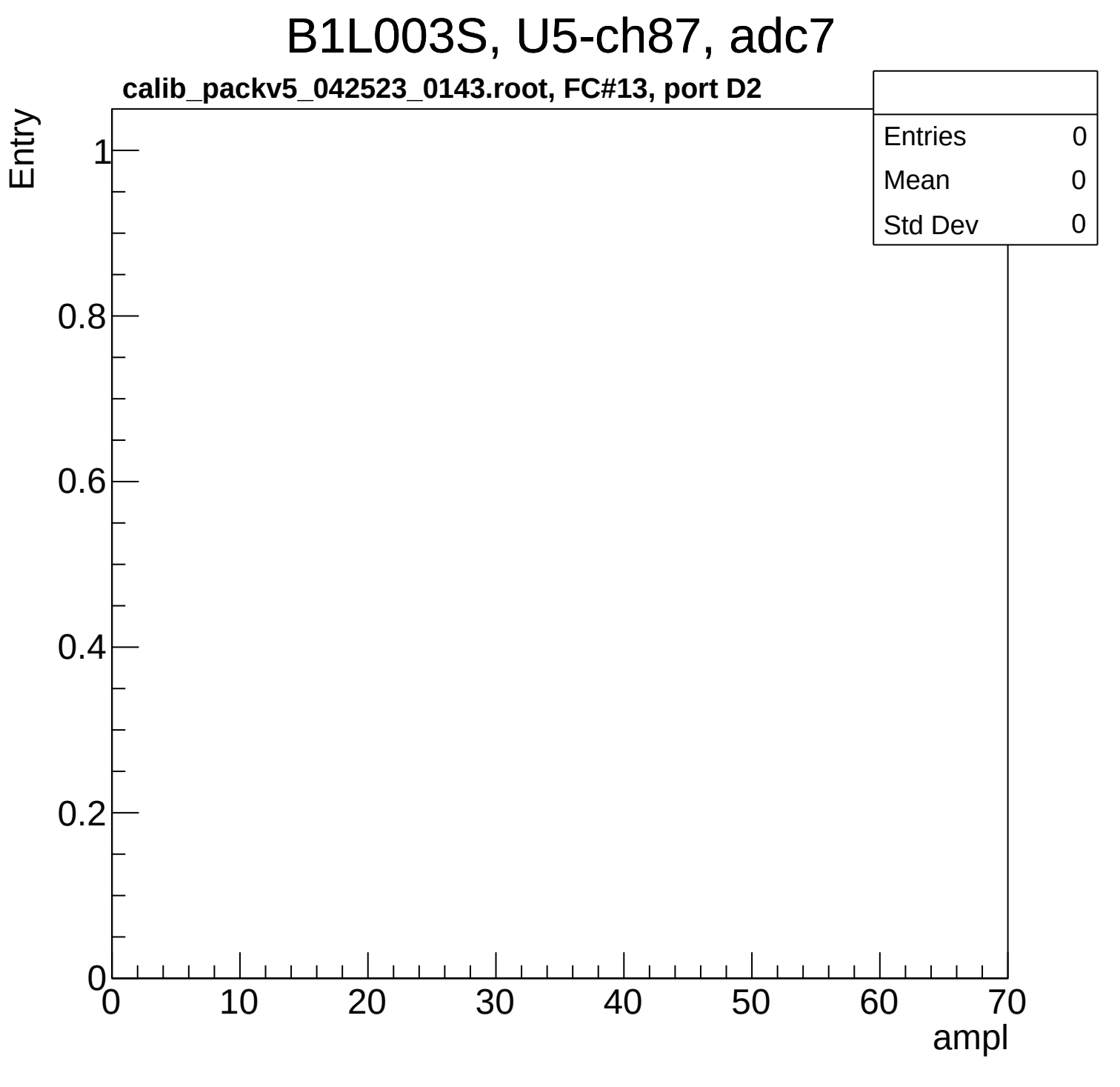

B1L003S, U5-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.22
Std Dev 1.315
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U5-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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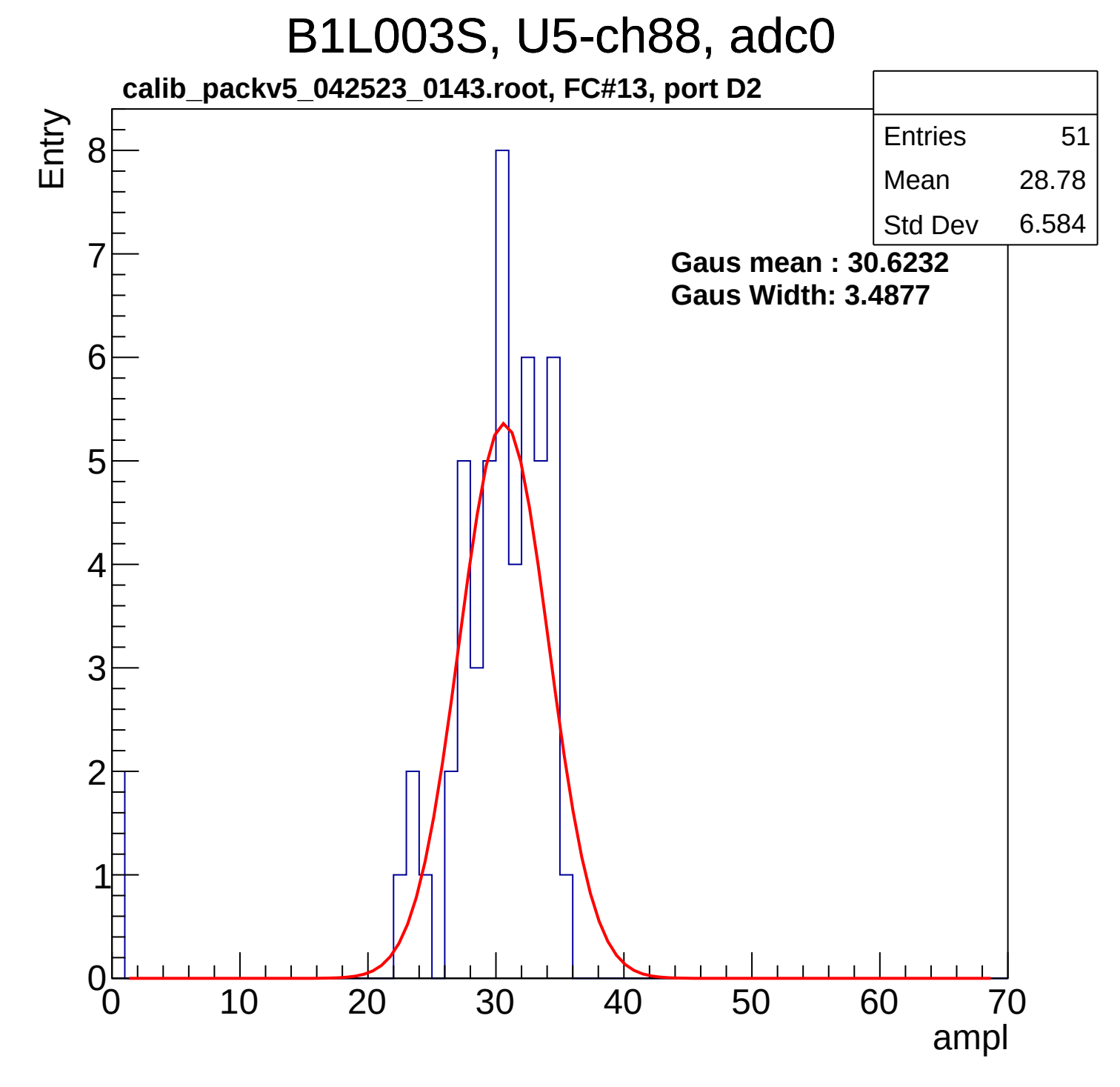
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.78
Std Dev 6.584
Gaus mean : 30.6232
Gaus Width: 3.4877
Entry
ampl
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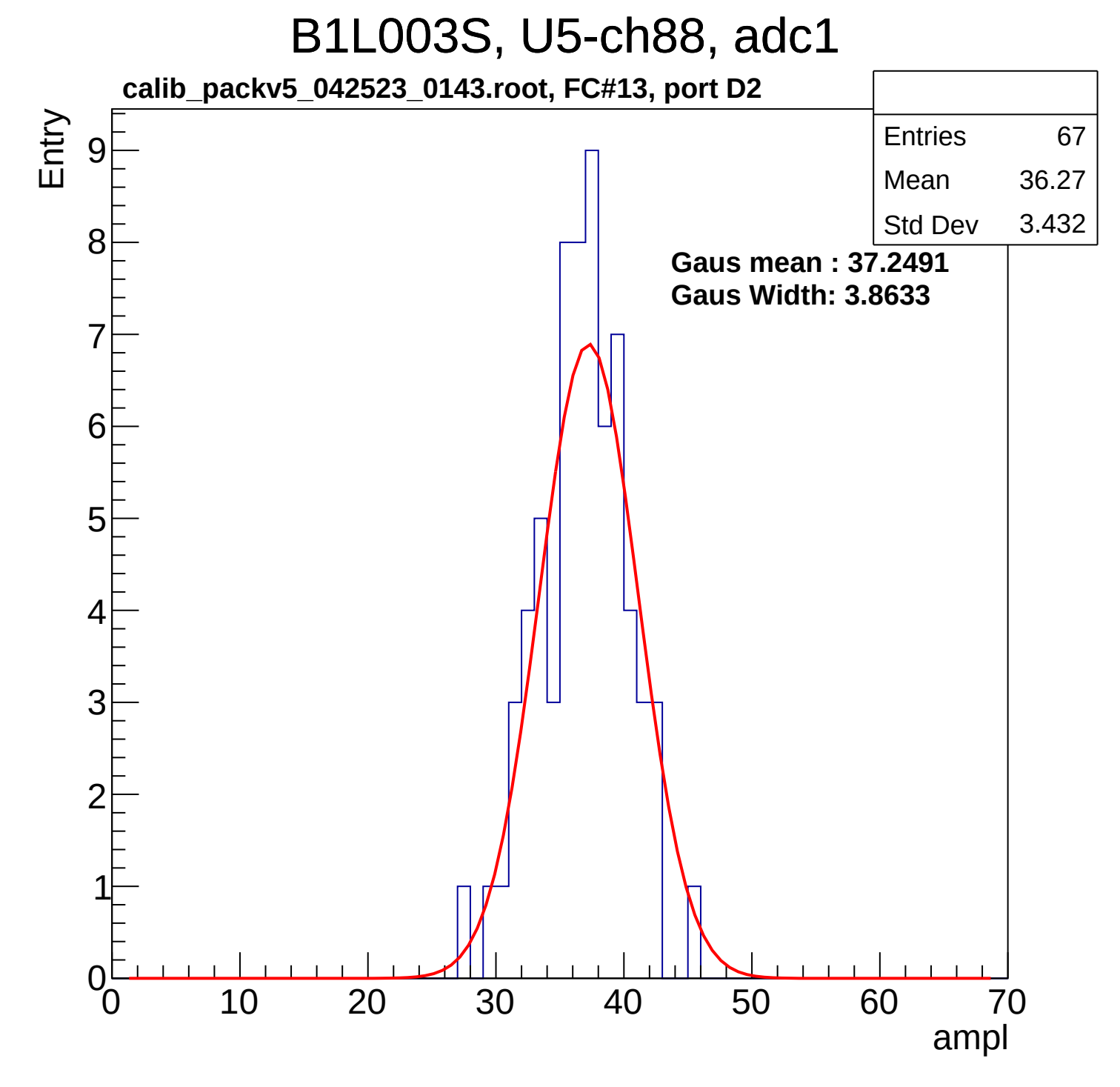
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.27
Std Dev 3.432
Gaus mean : 37.2491
Gaus Width: 3.8633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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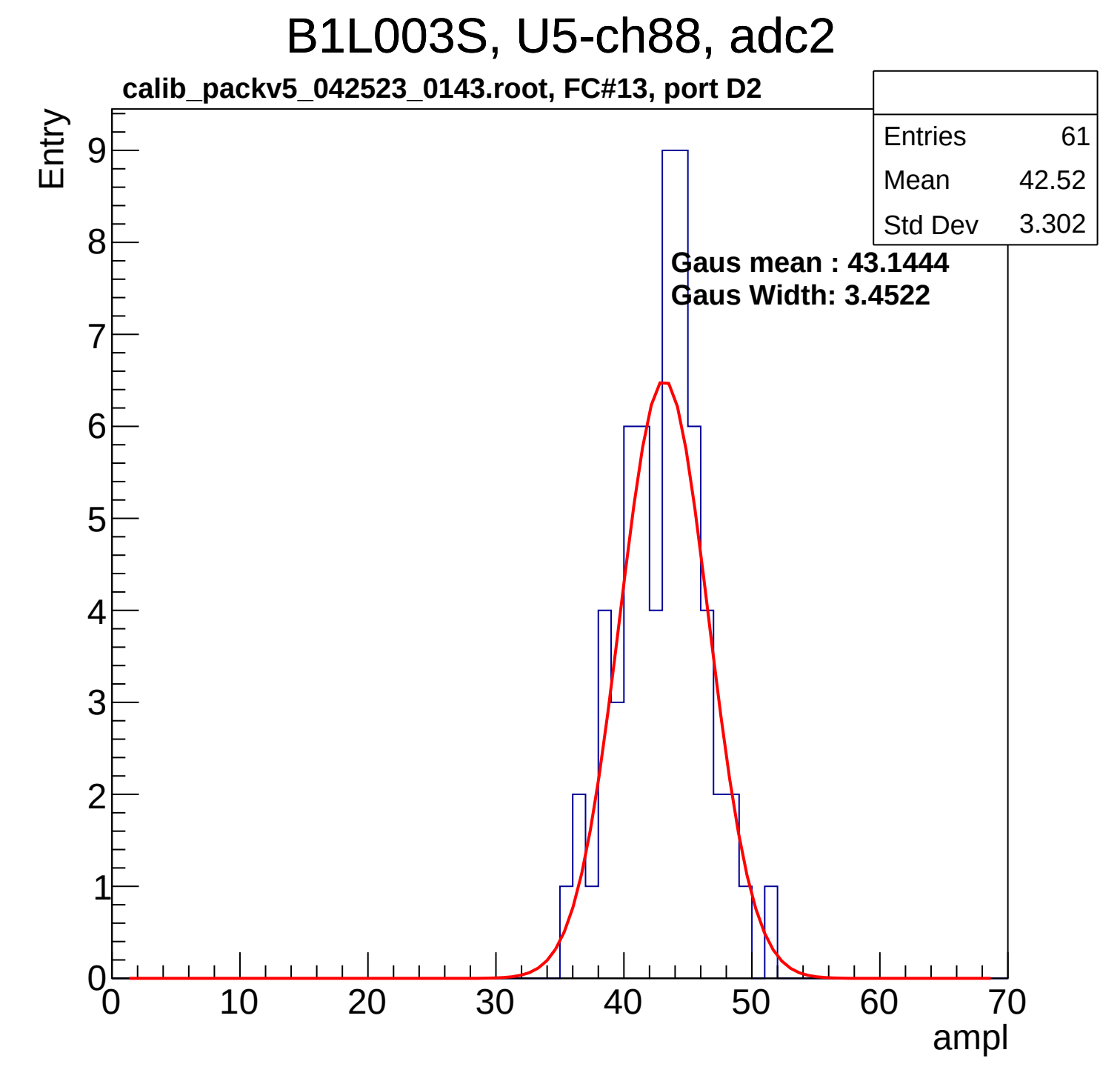
B1L003S, U5-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.52
Std Dev 3.302
Gaus mean : 43.1444
Gaus Width: 3.4522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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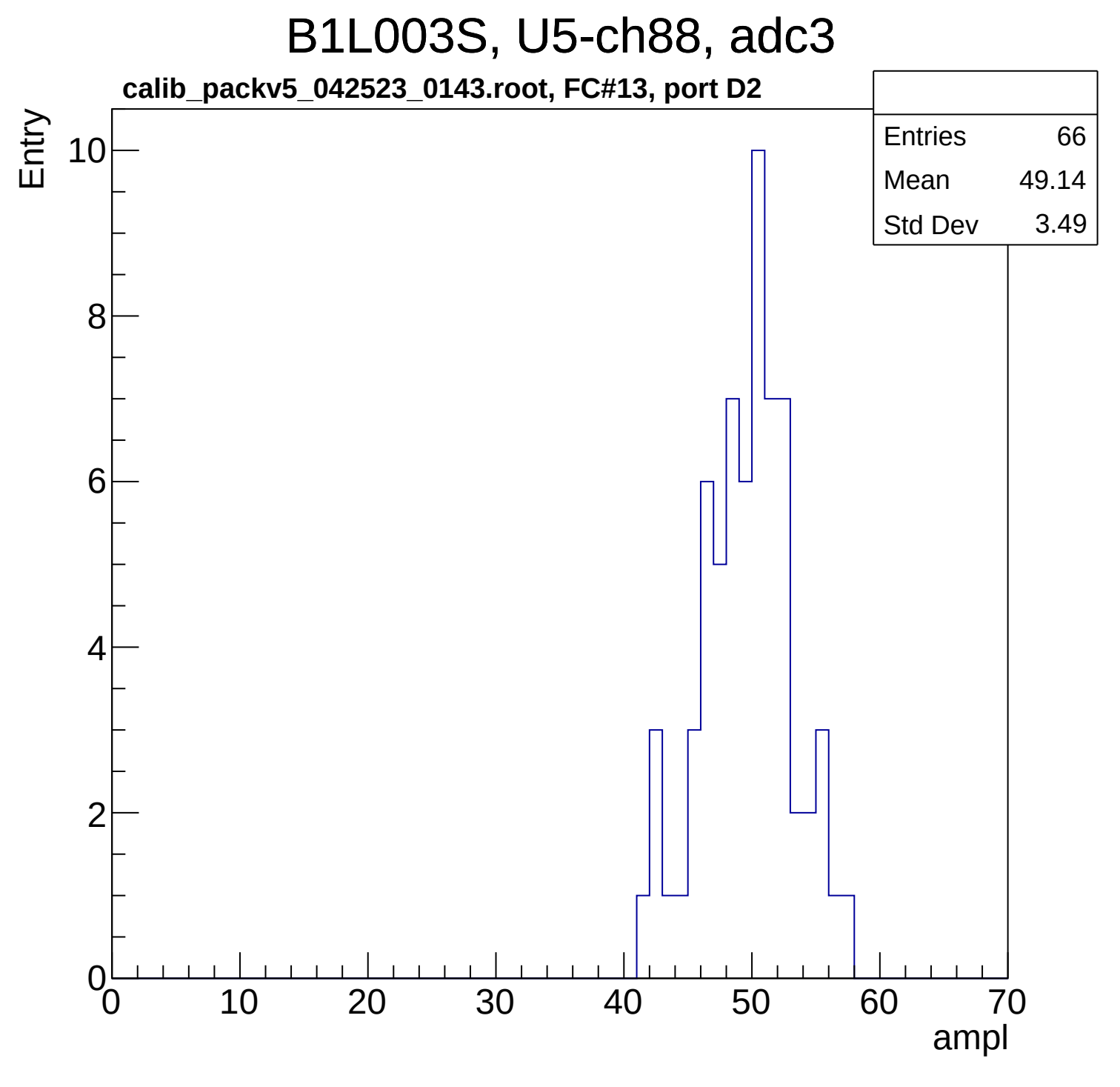
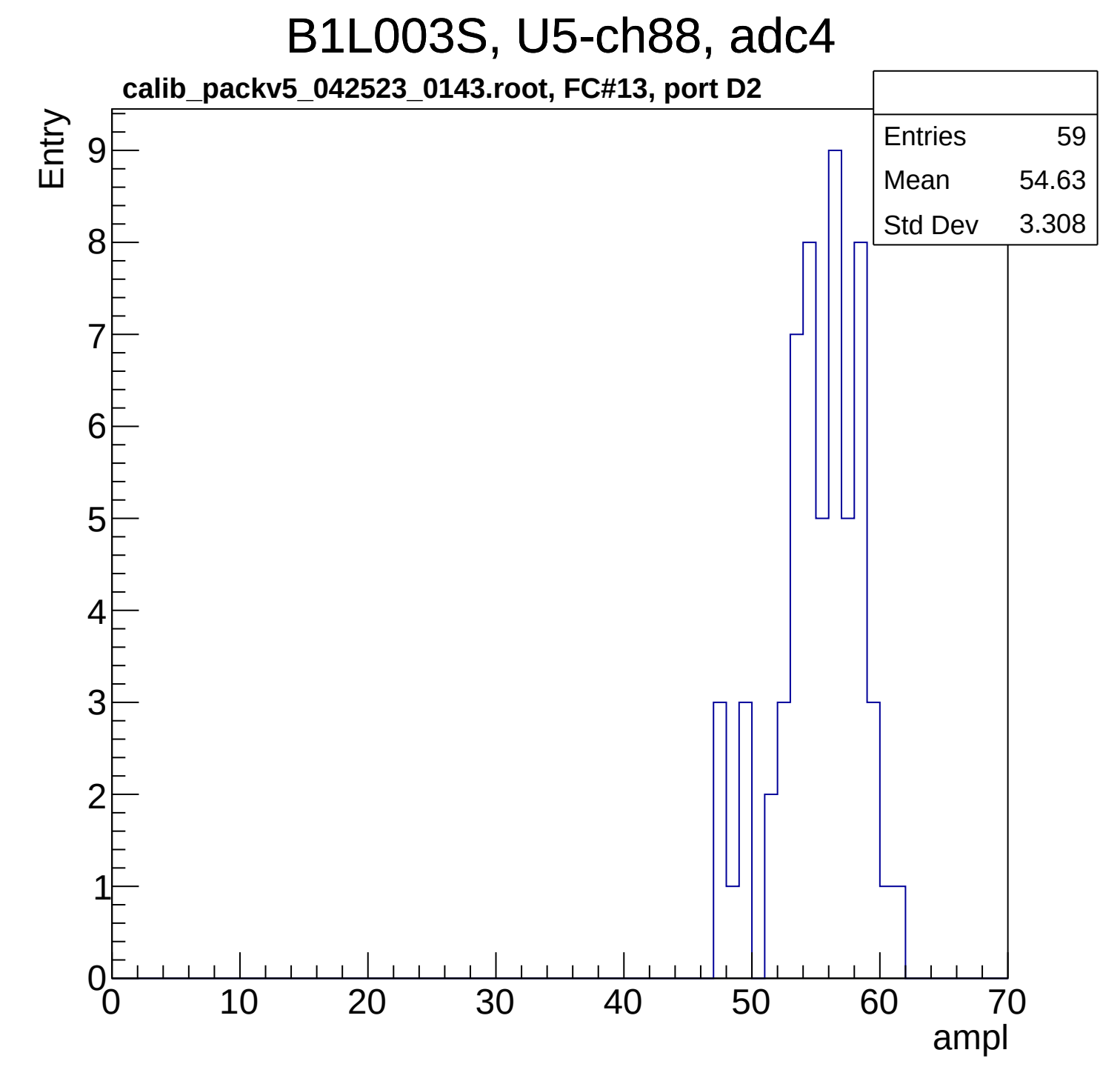

B1L003S, U5-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.14
Std Dev 3.49
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.63
Std Dev 3.308
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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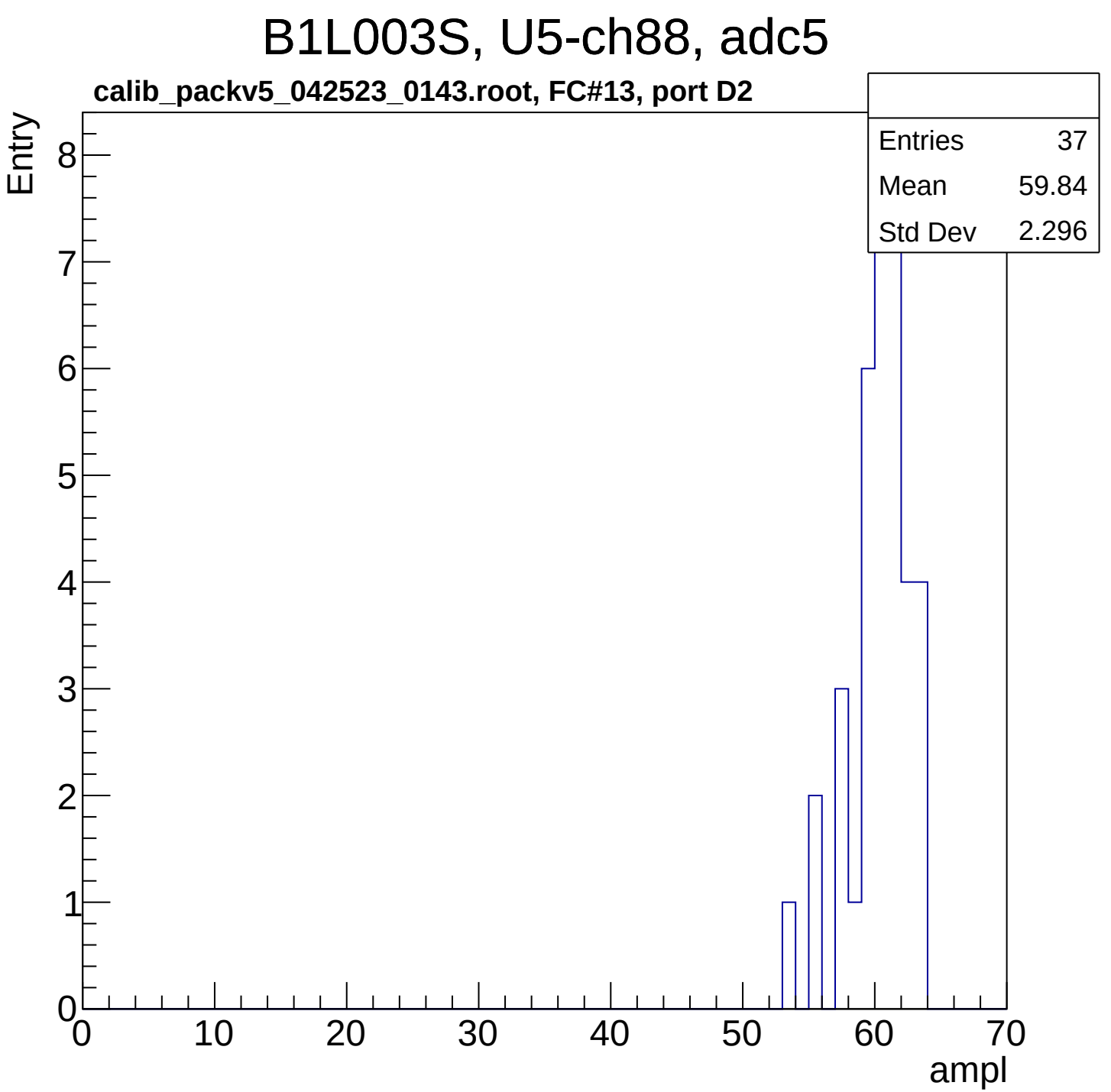
B1L003S, U5-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 59.84
Std Dev 2.296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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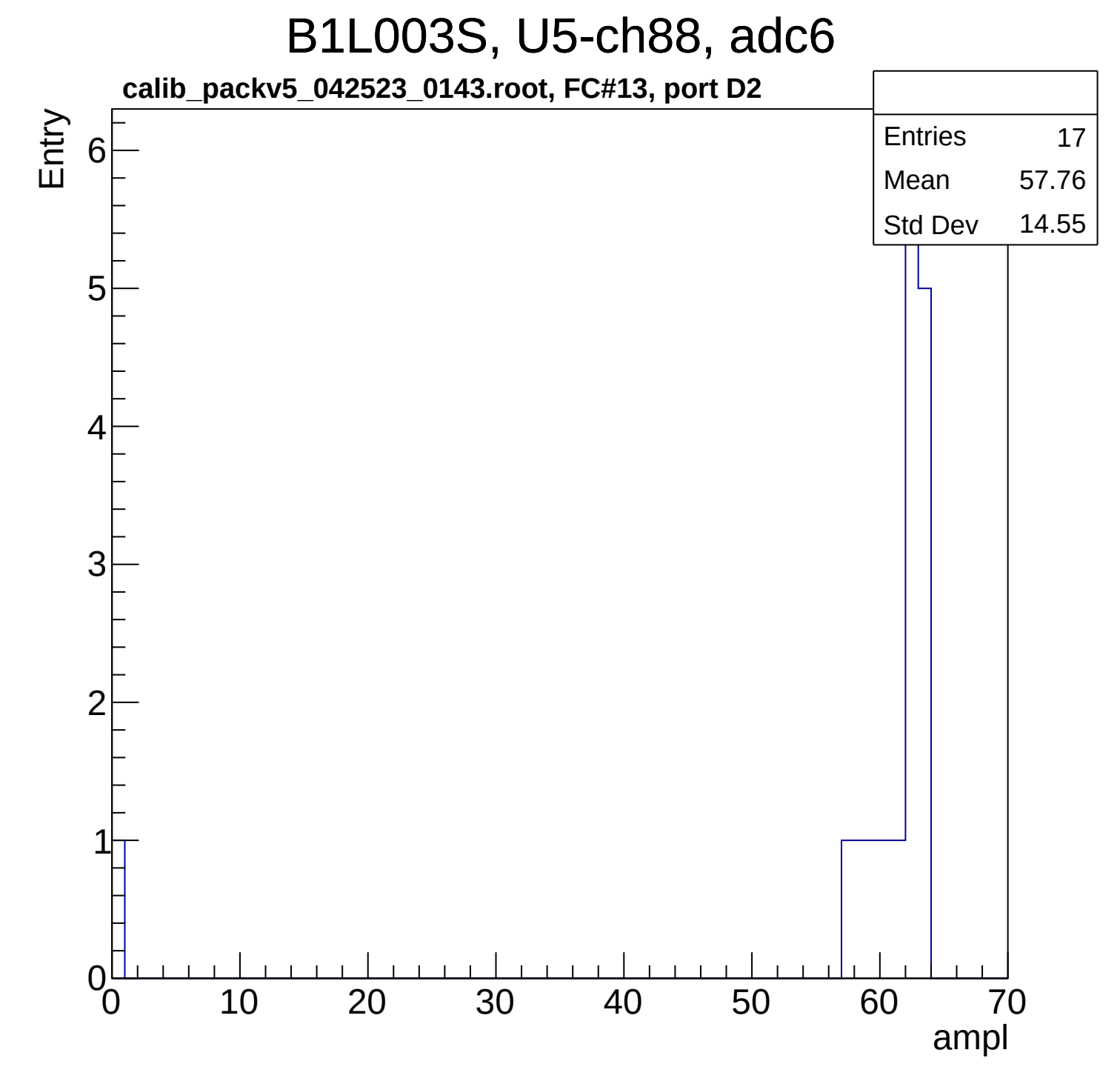
B1L003S, U5-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.76
Std Dev 14.55
Entry
0
1
2
3
4
5
6
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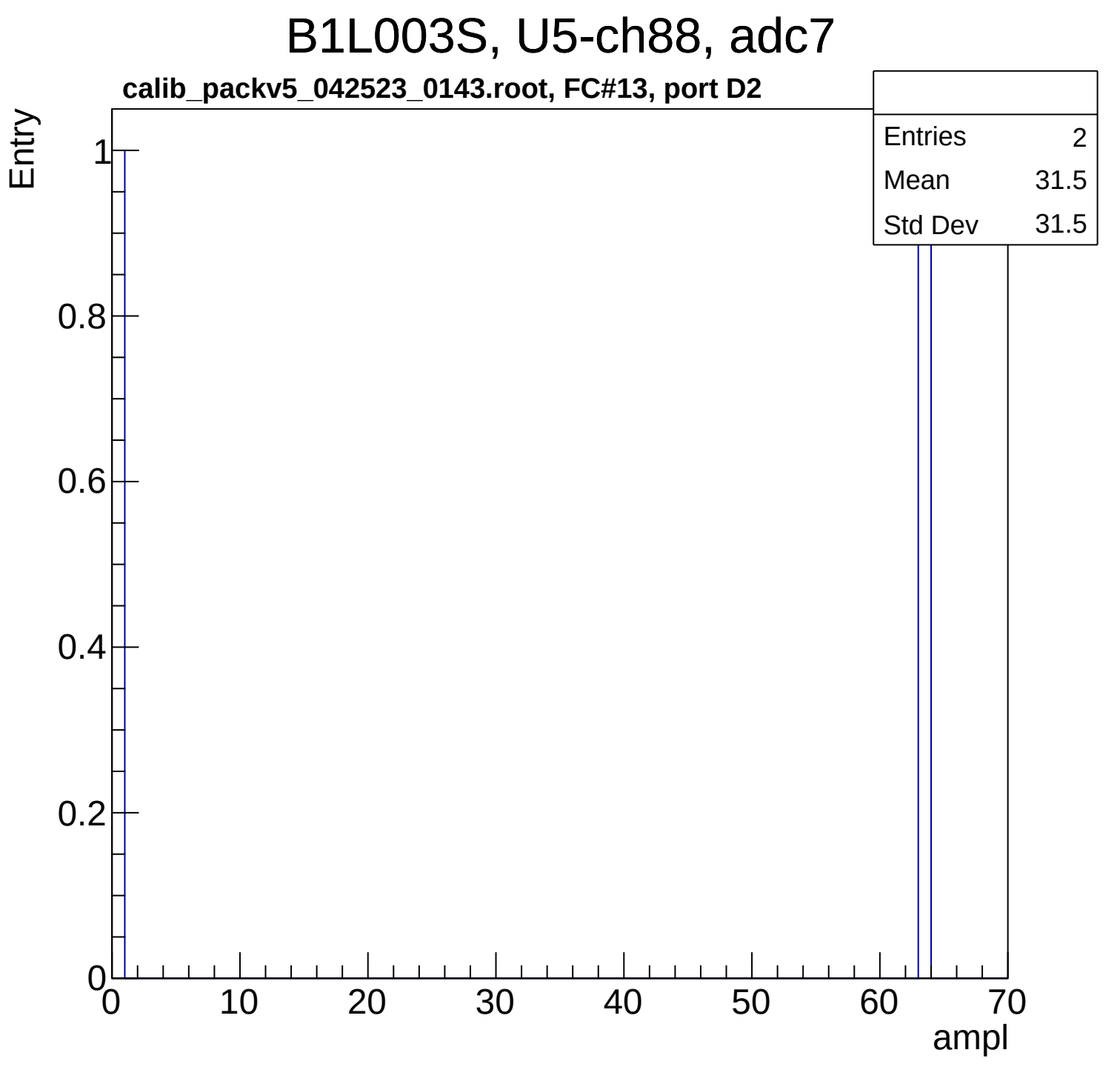
70
ampl


B1L003S, U5-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
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10
20
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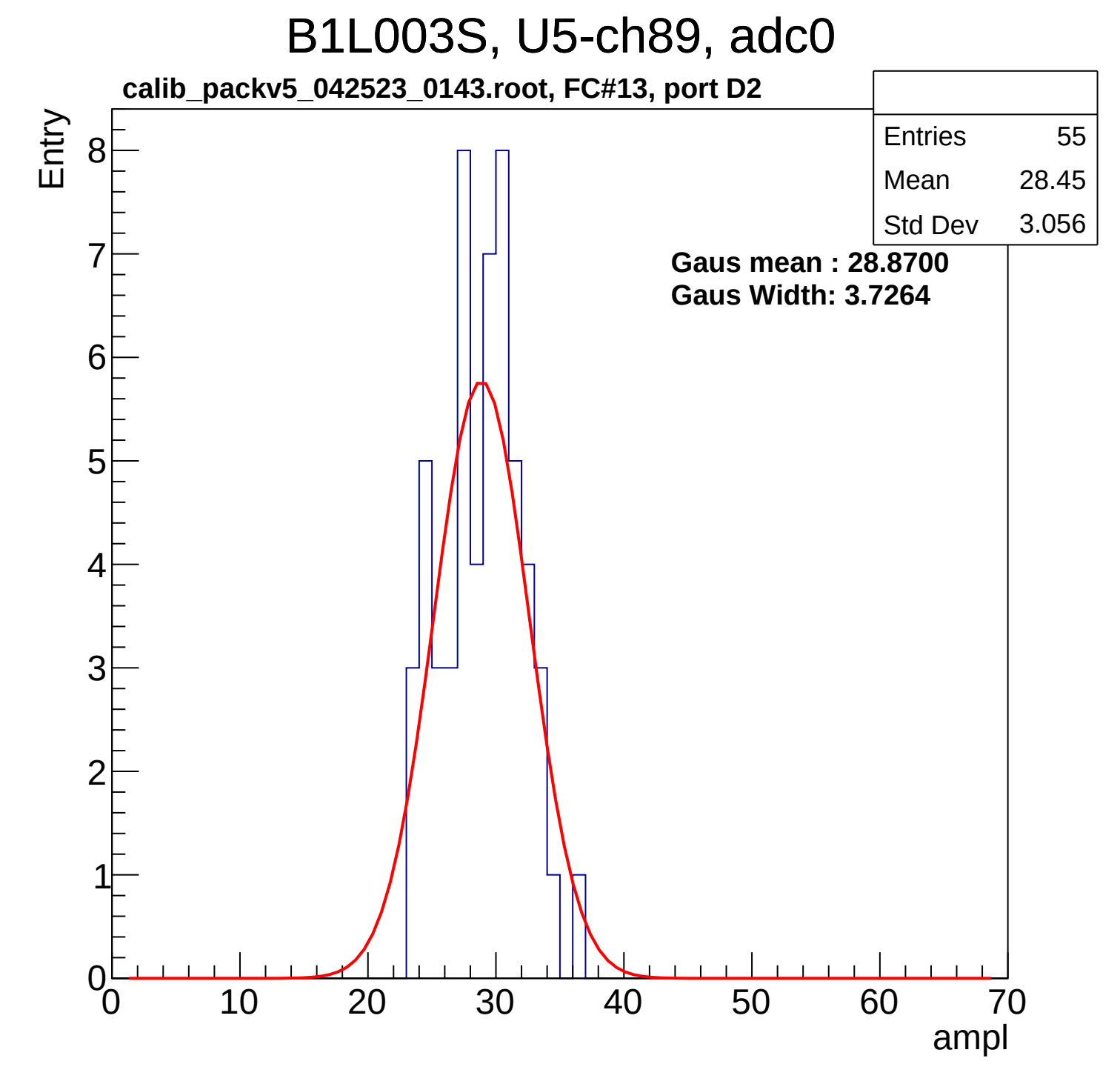
50
60
70
ampl


B1L003S, U5-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 28.45
Std Dev 3.056
Gaus mean : 28.8700
Gaus Width: 3.7264
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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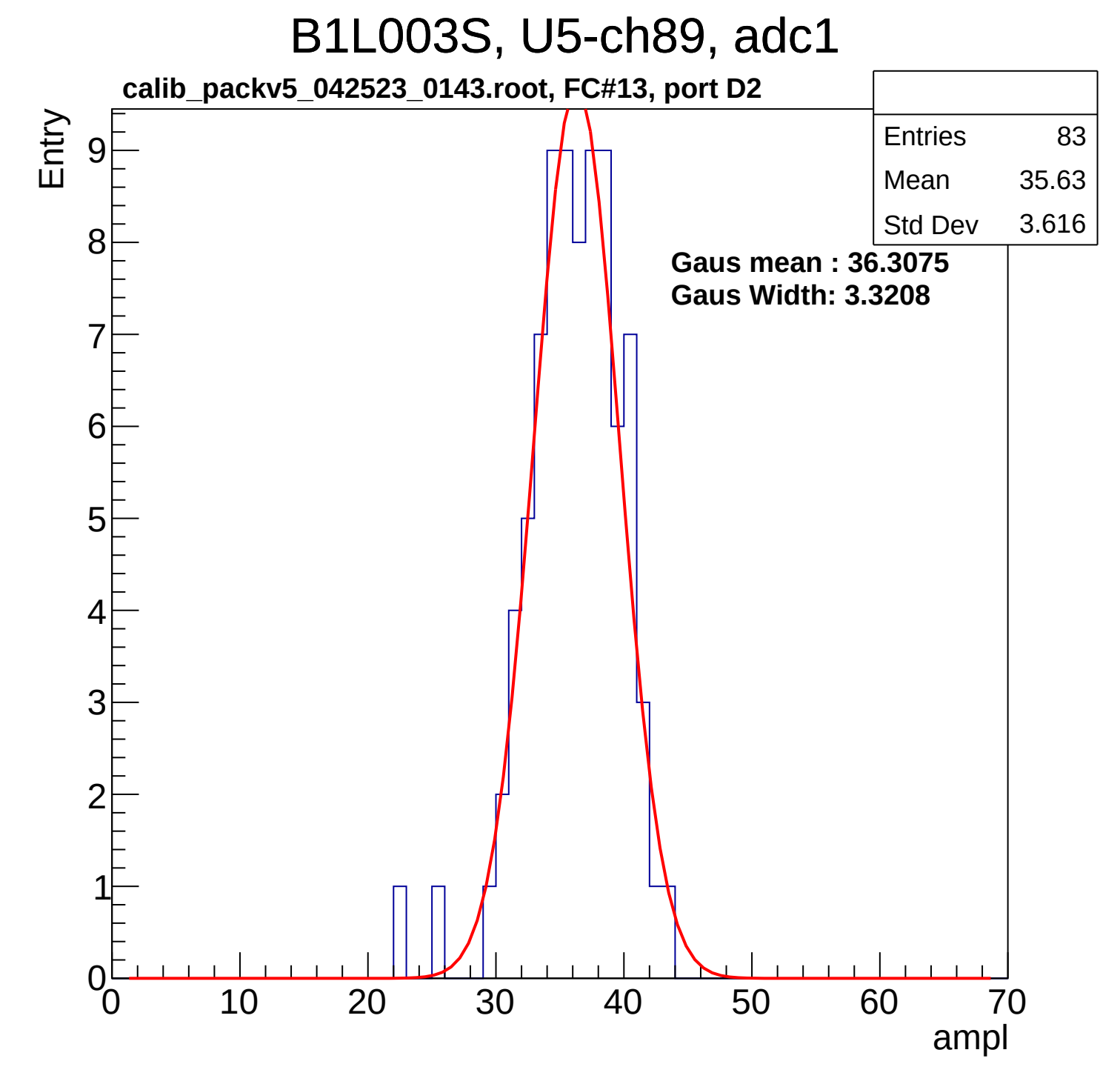
B1L003S, U5-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 35.63
Std Dev 3.616
Gaus mean : 36.3075
Gaus Width: 3.3208
Entry
ampl
0
10
20
30
40
50
60
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1
2
3
4
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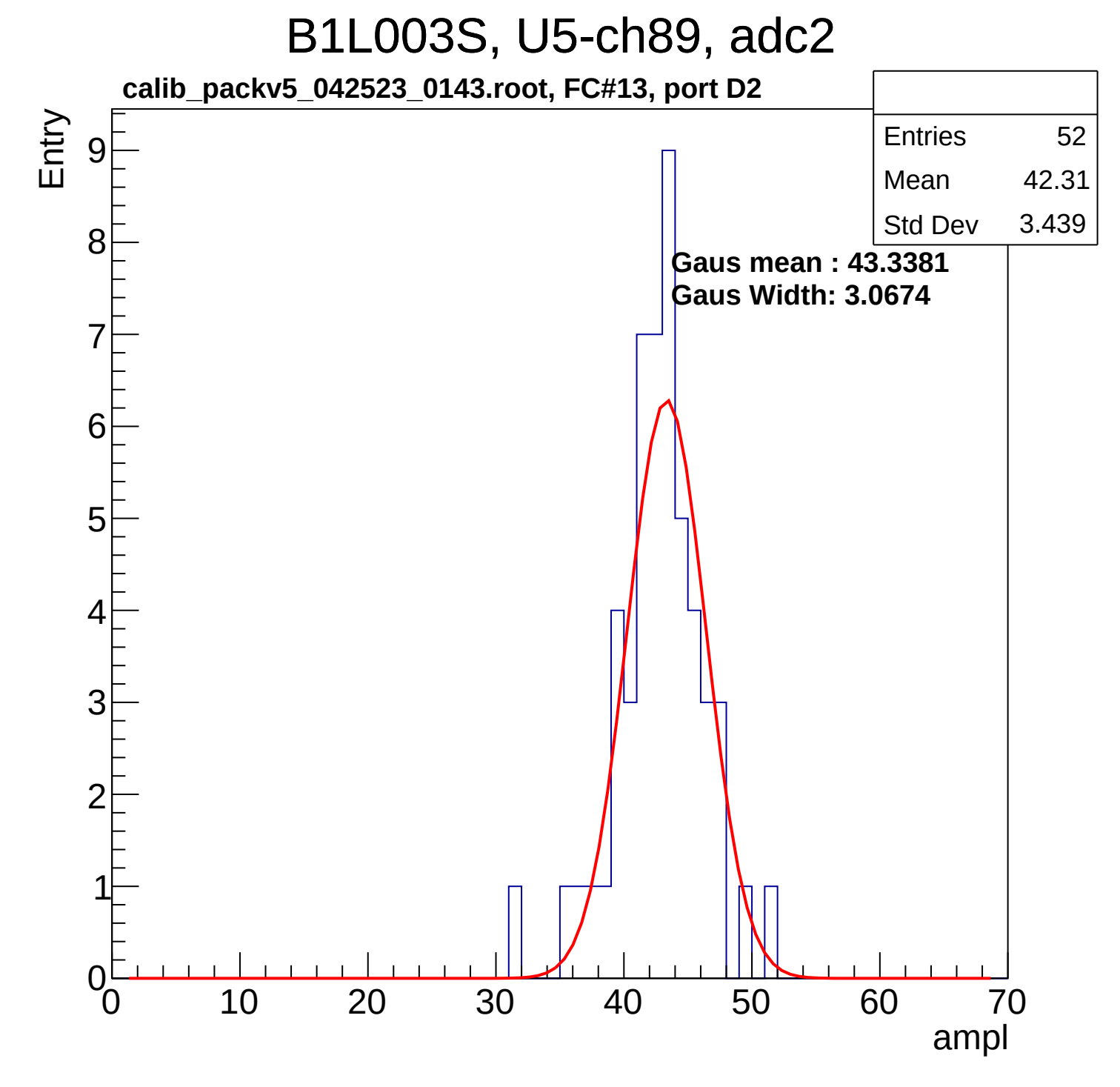
6
7
8
9


B1L003S, U5-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 42.31
Std Dev 3.439
Gaus mean : 43.3381
Gaus Width: 3.0674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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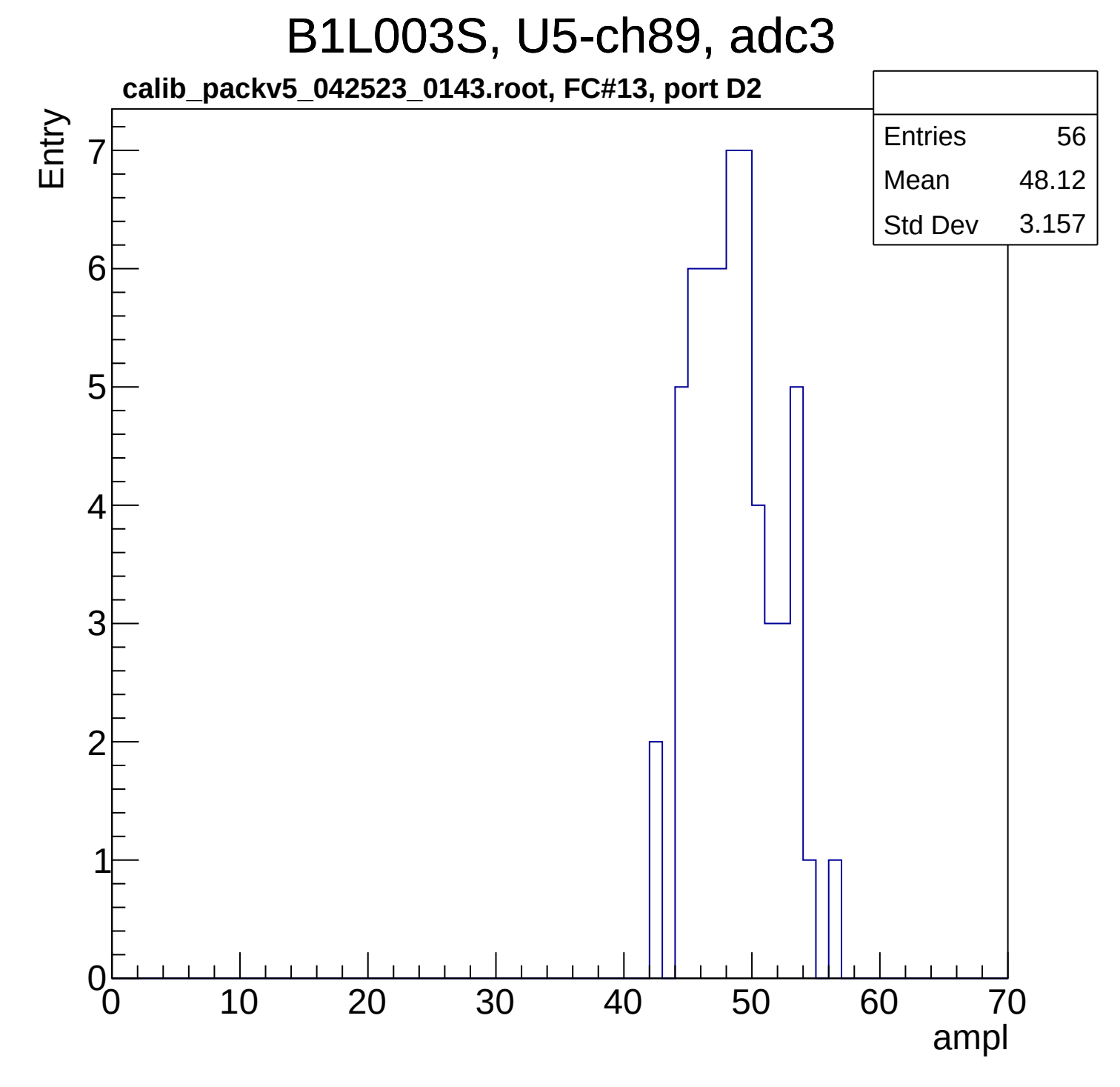
B1L003S, U5-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.12
Std Dev 3.157
Entry
ampl
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1
2
3
4
5
6
7
0
10
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40
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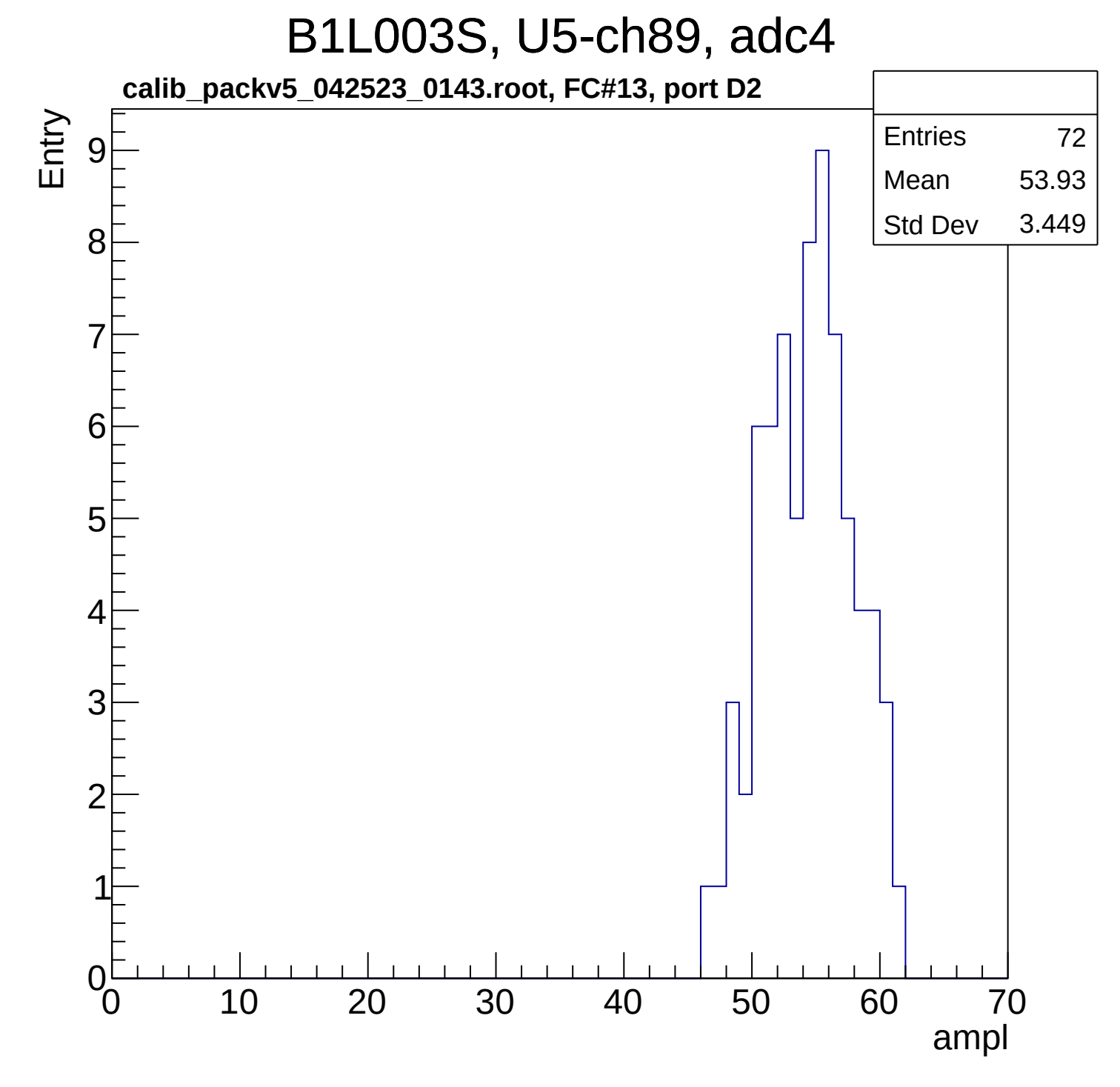
B1L003S, U5-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 53.93
Std Dev 3.449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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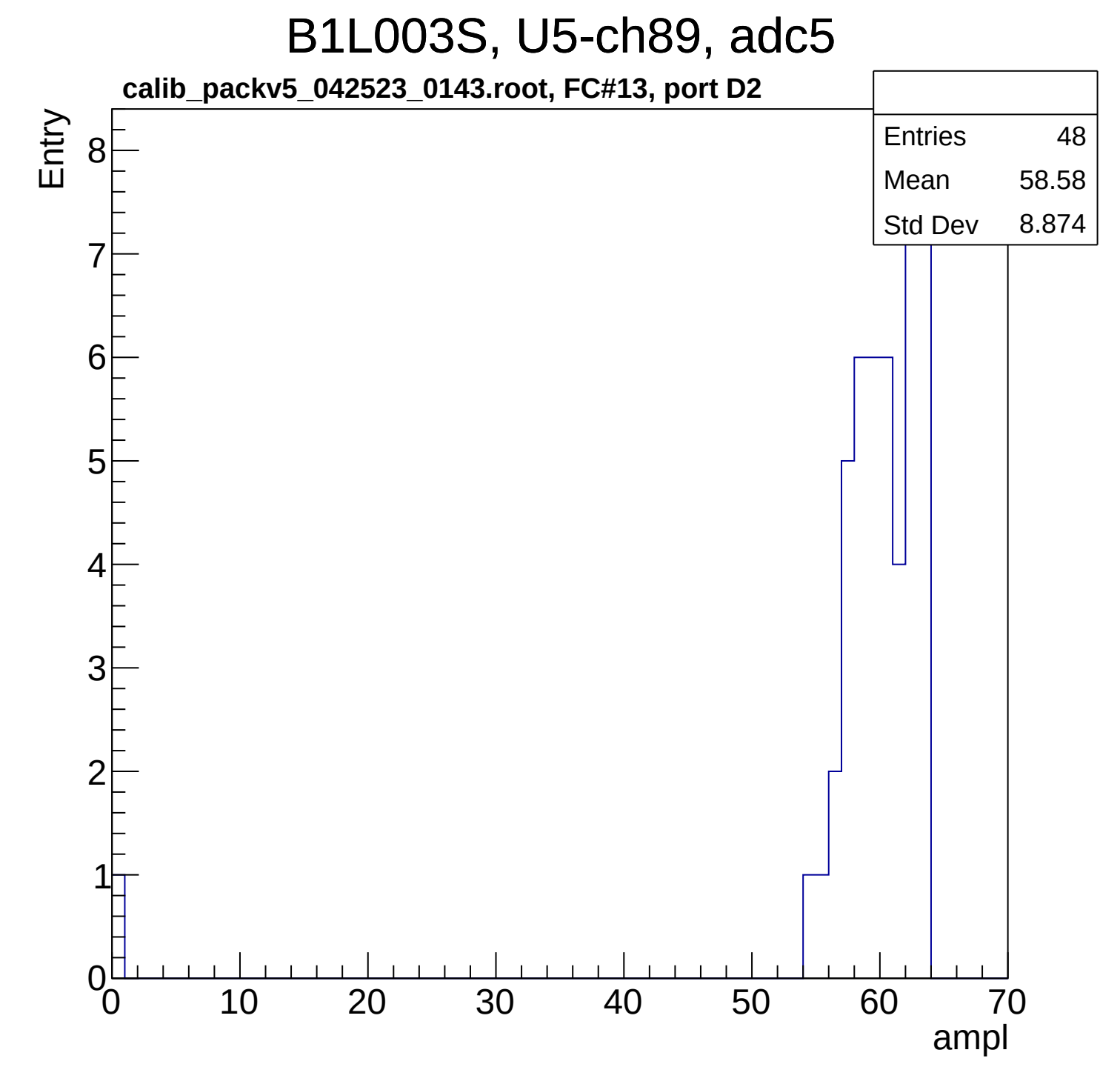
B1L003S, U5-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.58
Std Dev 8.874
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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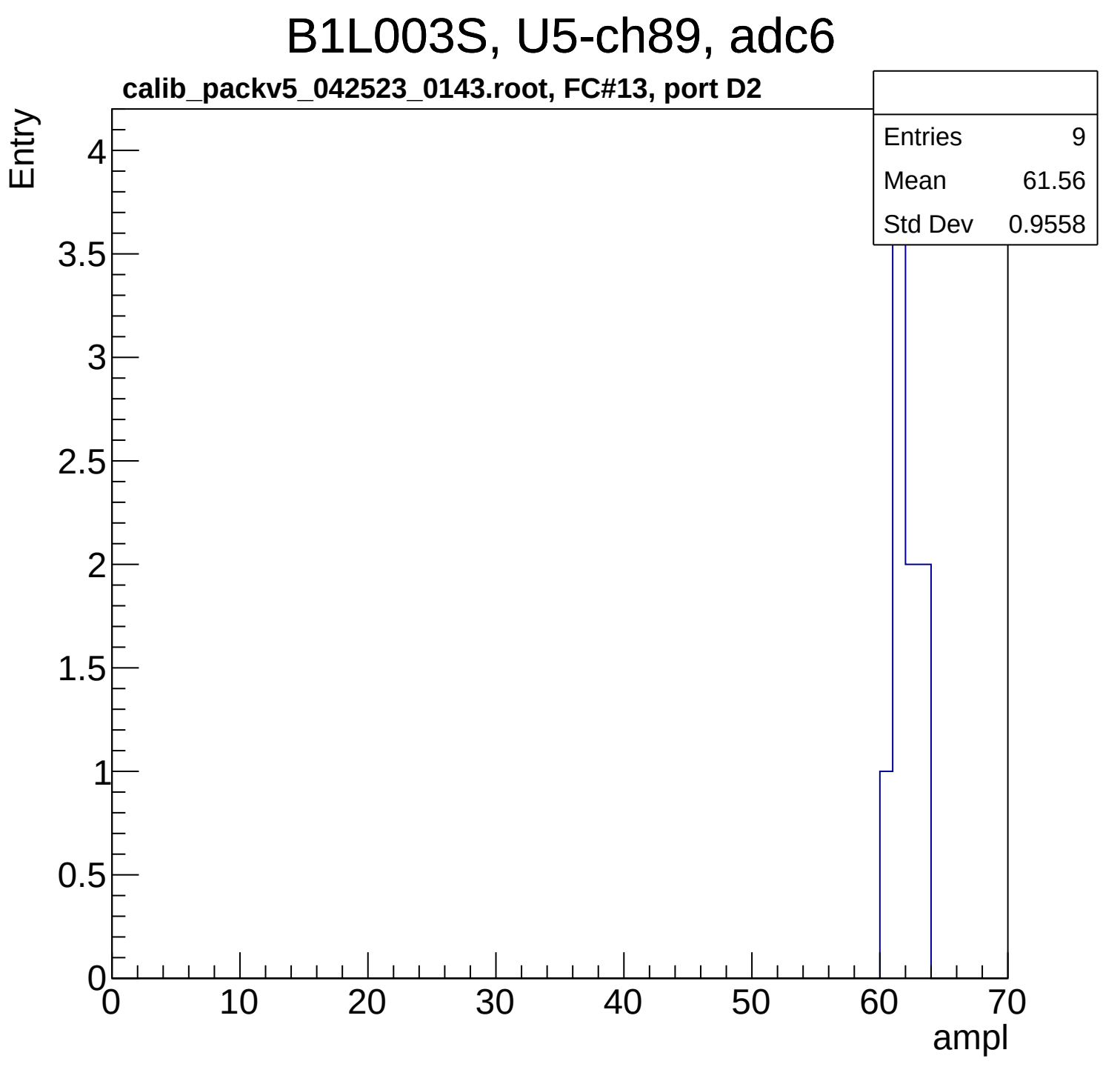
B1L003S, U5-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.56
Std Dev 0.9558
Entry
ampl
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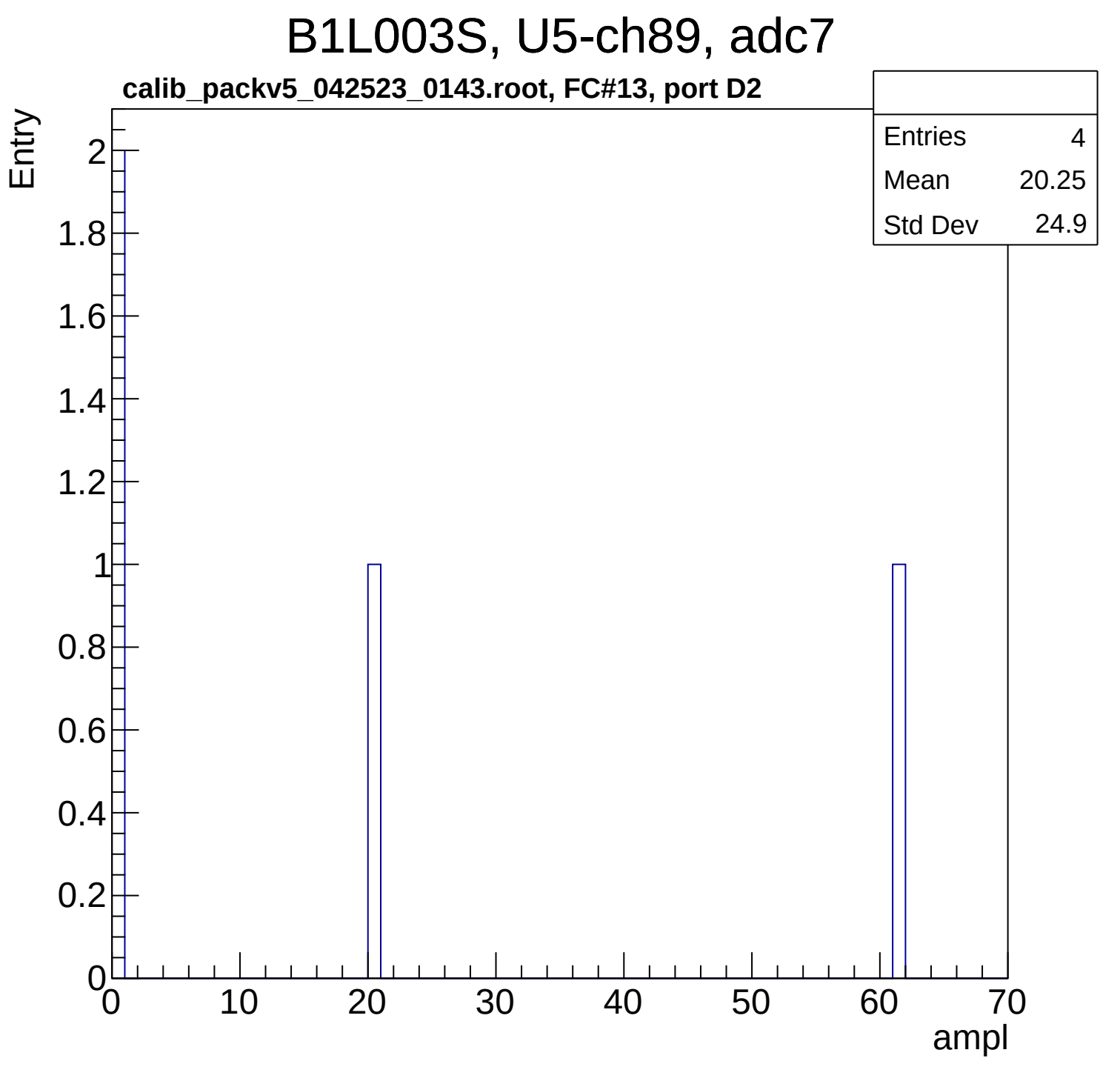
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U5-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 20.25
Std Dev 24.9
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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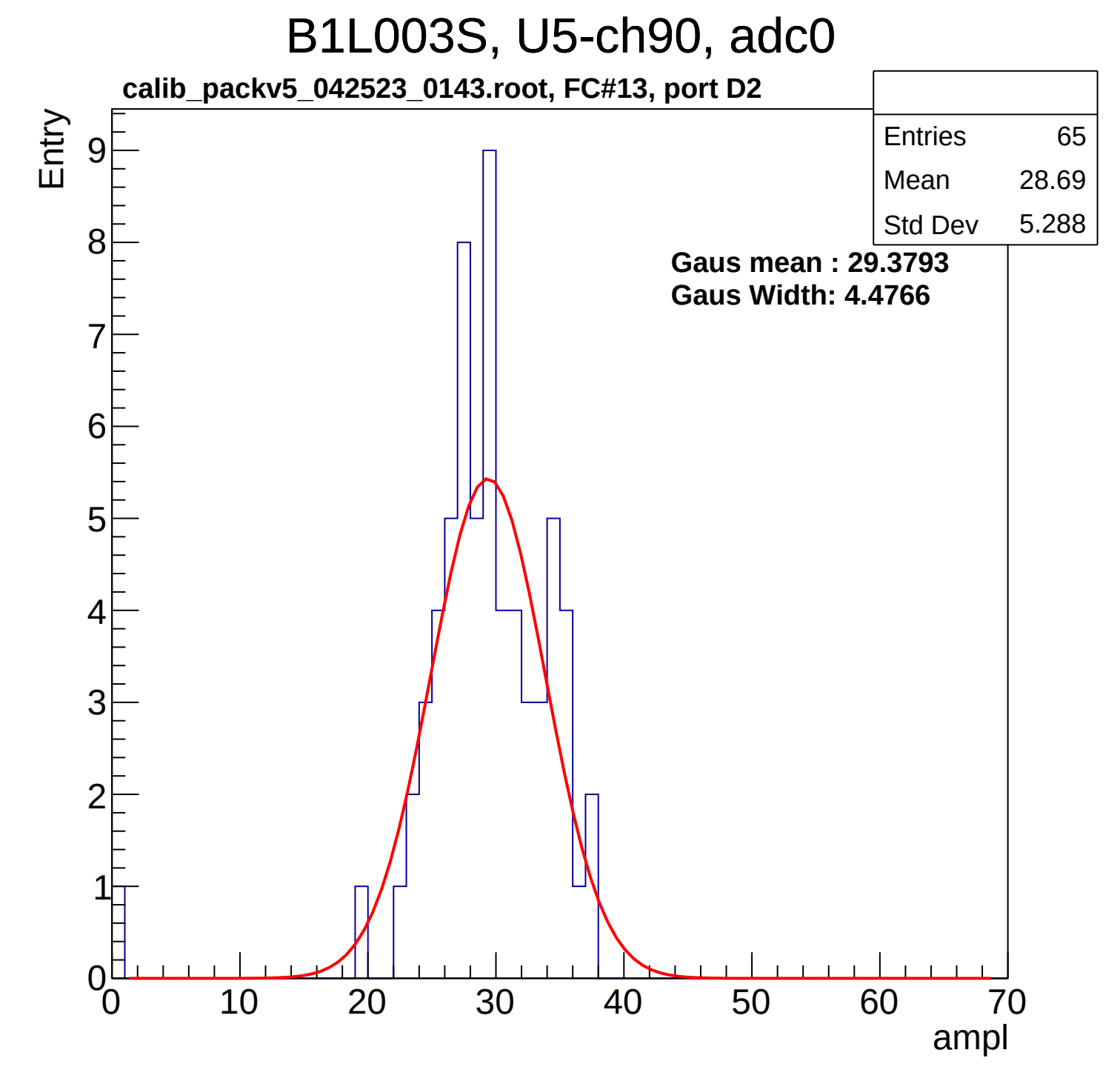
B1L003S, U5-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.69
Std Dev 5.288
Gaus mean : 29.3793
Gaus Width: 4.4766
Entry
ampl
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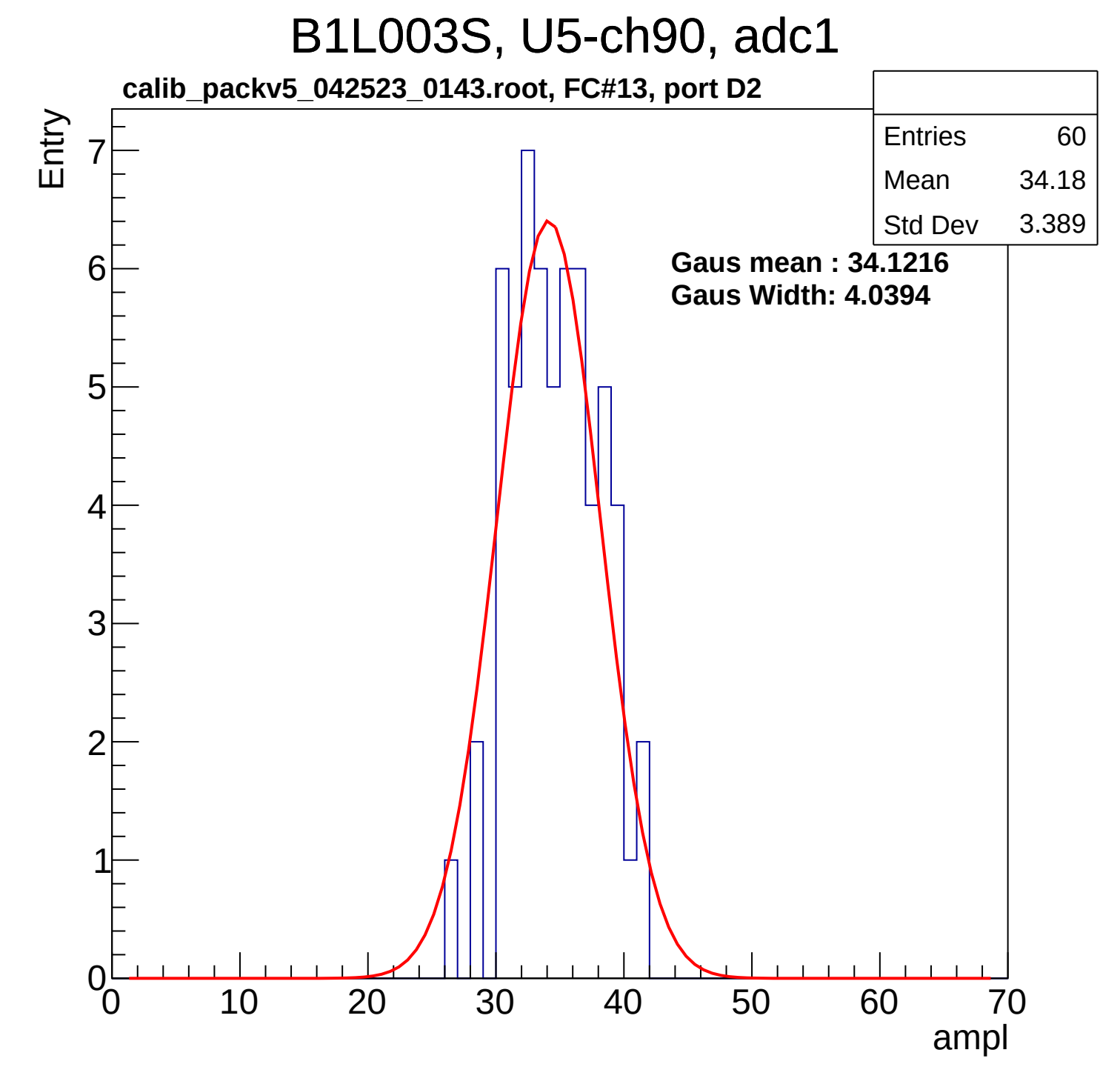
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 34.18
Std Dev 3.389
Gaus mean : 34.1216
Gaus Width: 4.0394
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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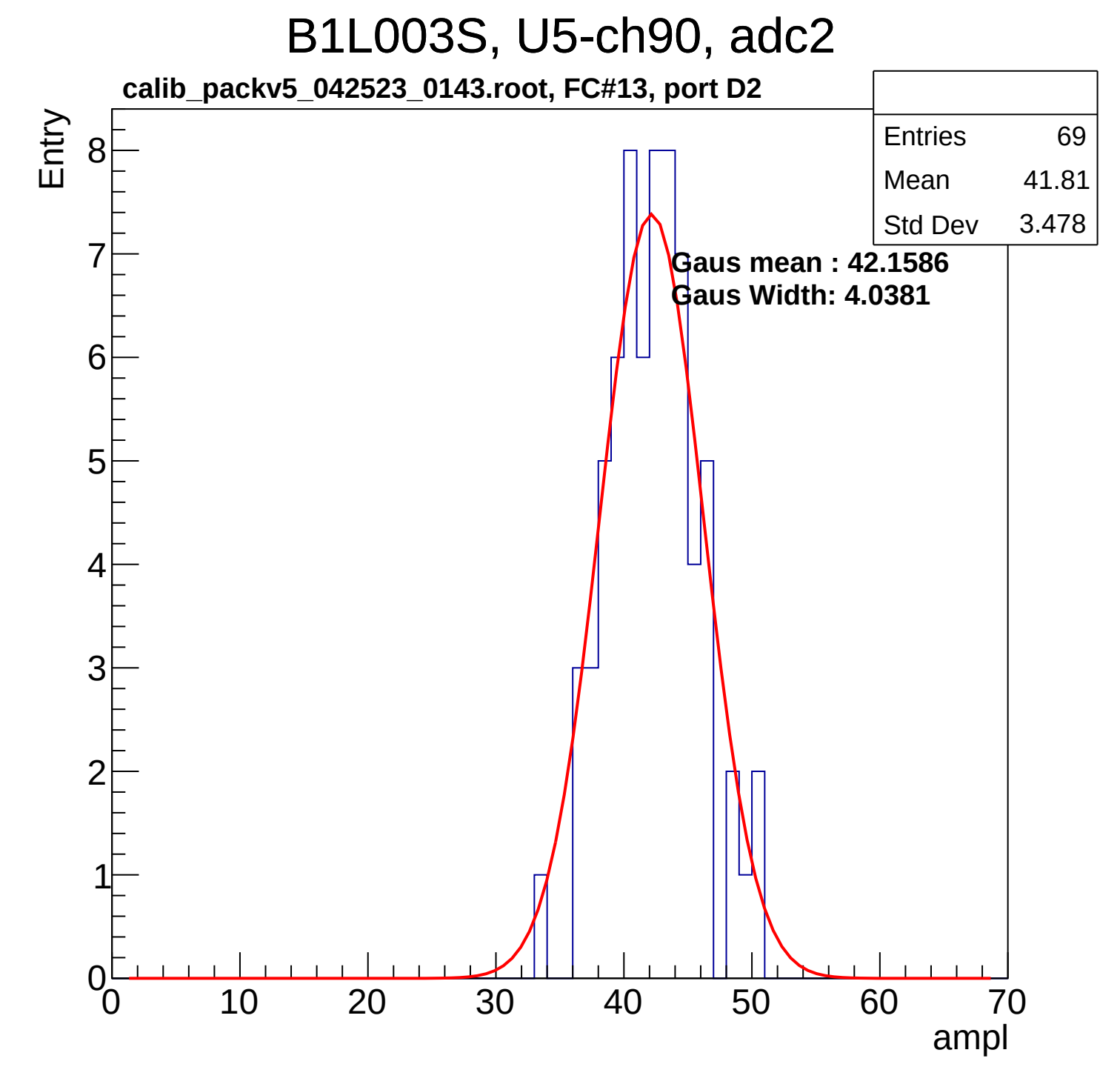
B1L003S, U5-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.81
Std Dev 3.478
Gaus mean : 42.1586
Gaus Width: 4.0381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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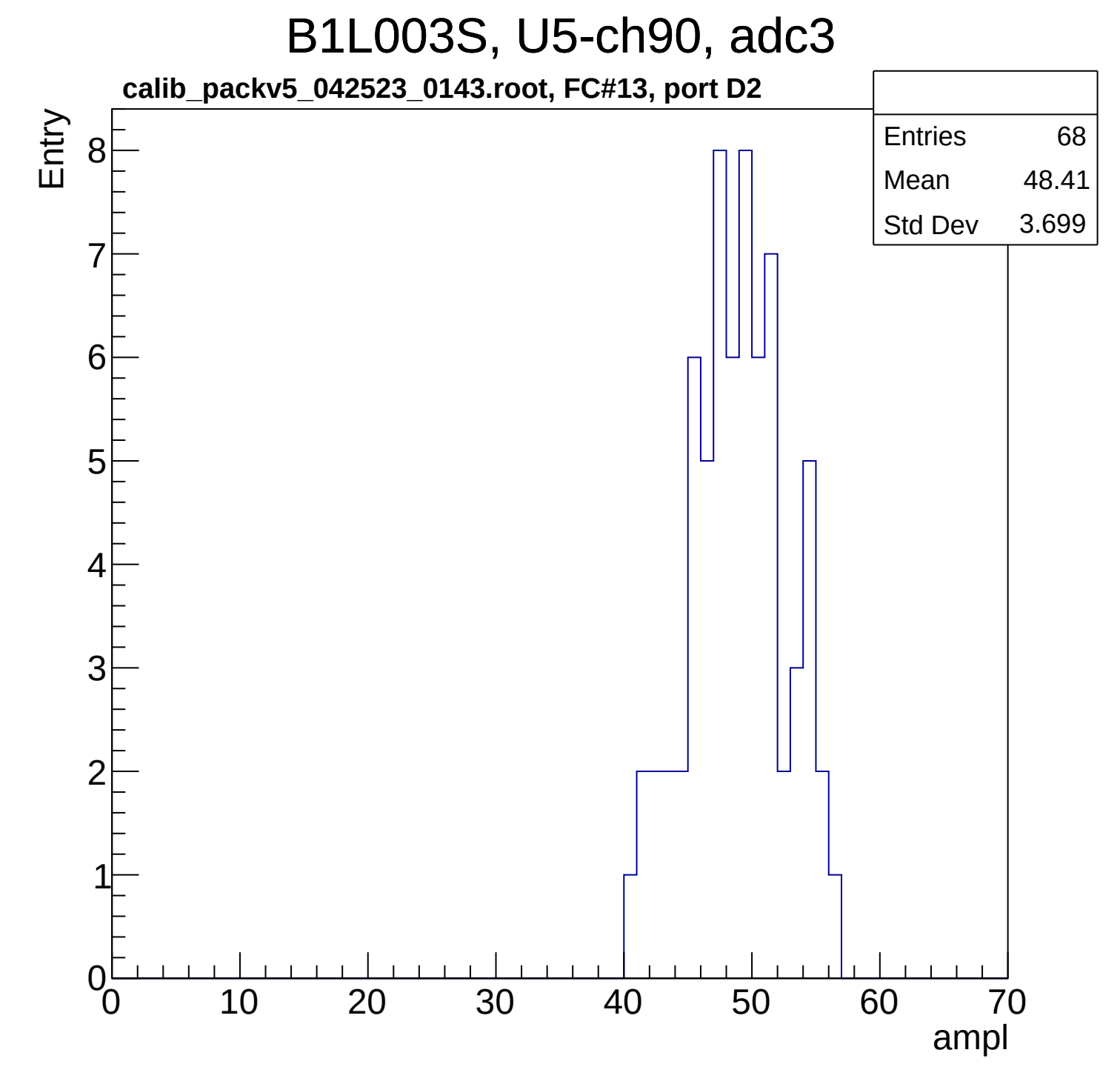
B1L003S, U5-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.41
Std Dev 3.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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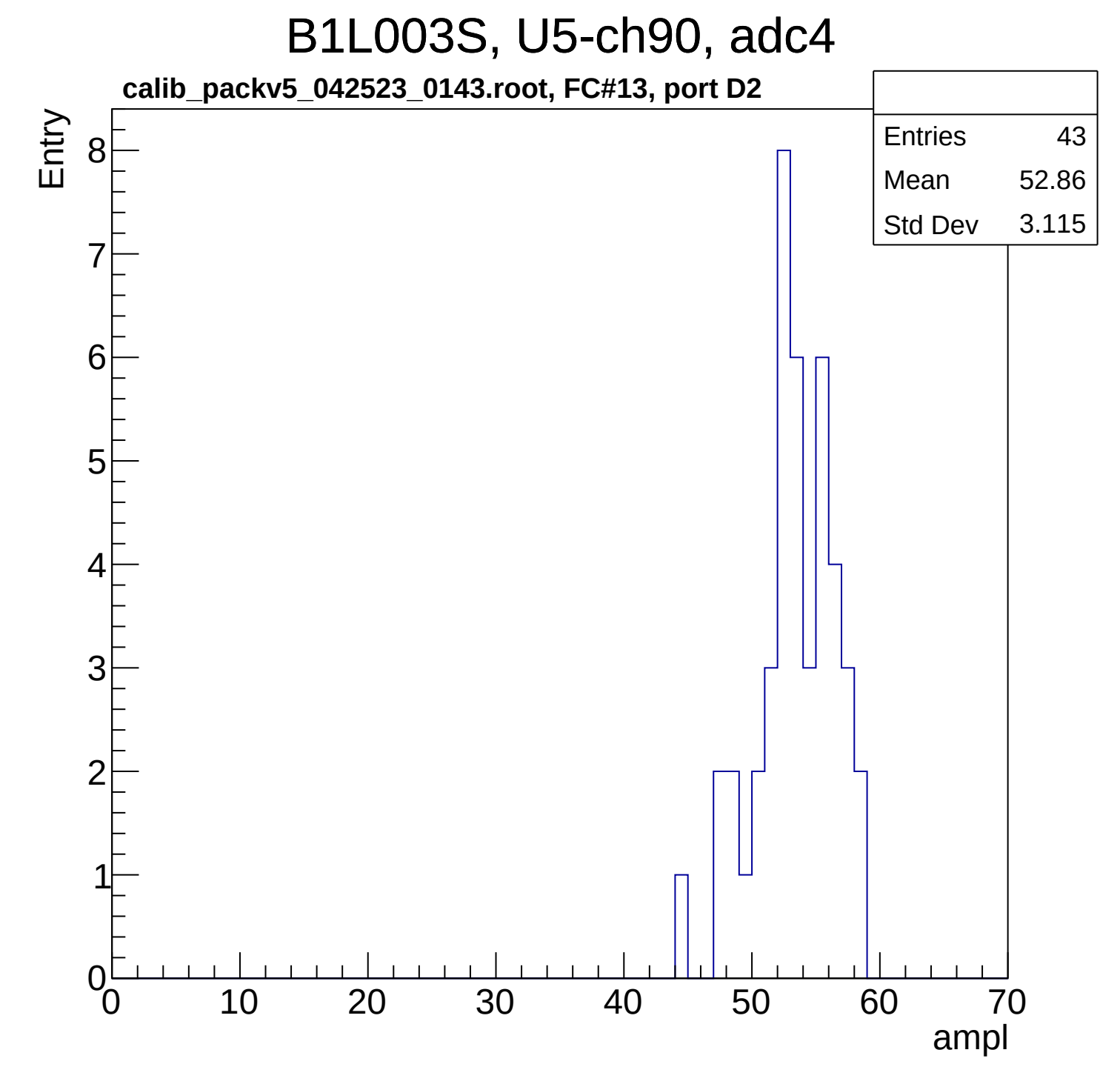
B1L003S, U5-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 52.86
Std Dev 3.115
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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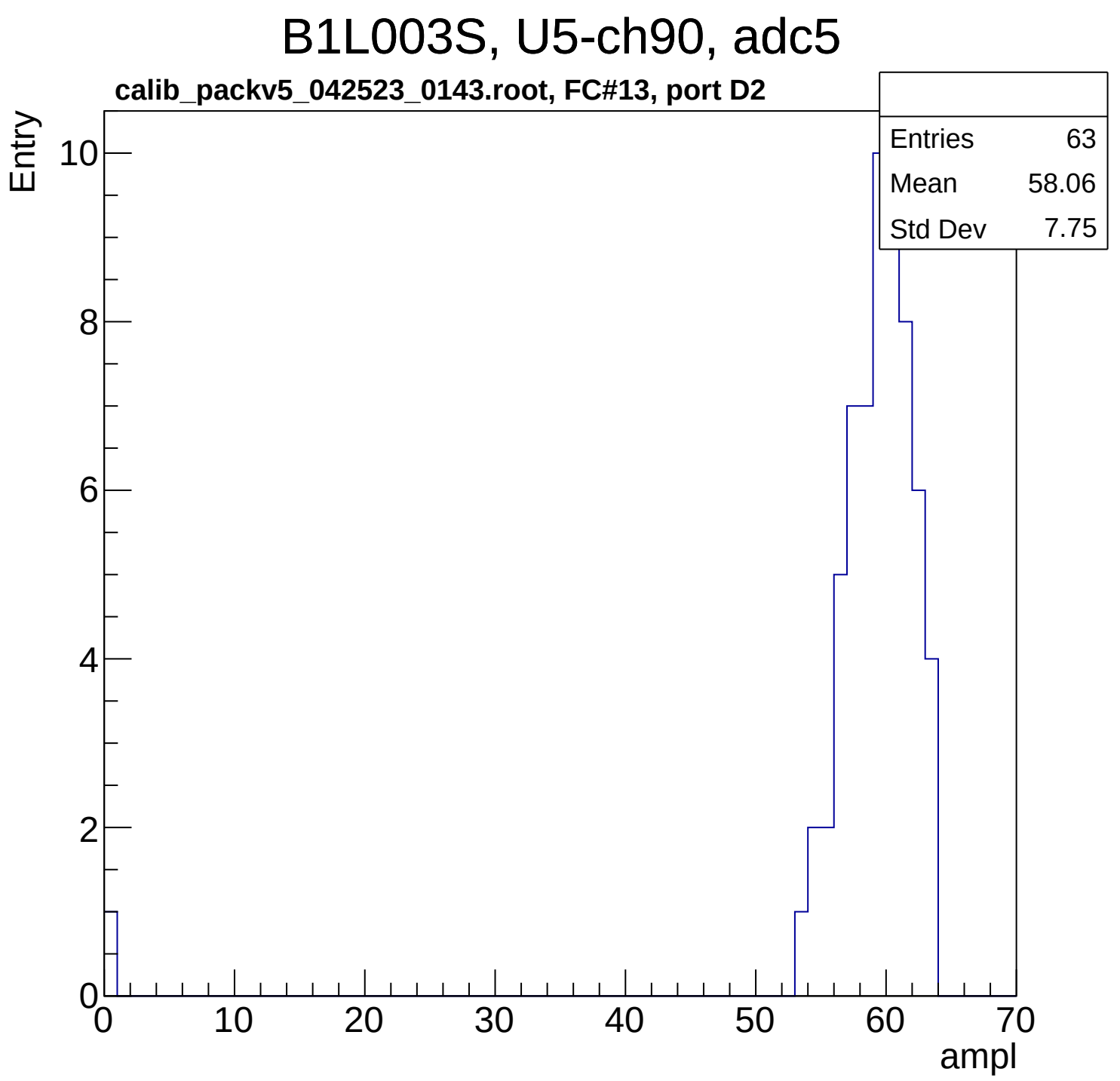
B1L003S, U5-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.06
Std Dev 7.75
Entry
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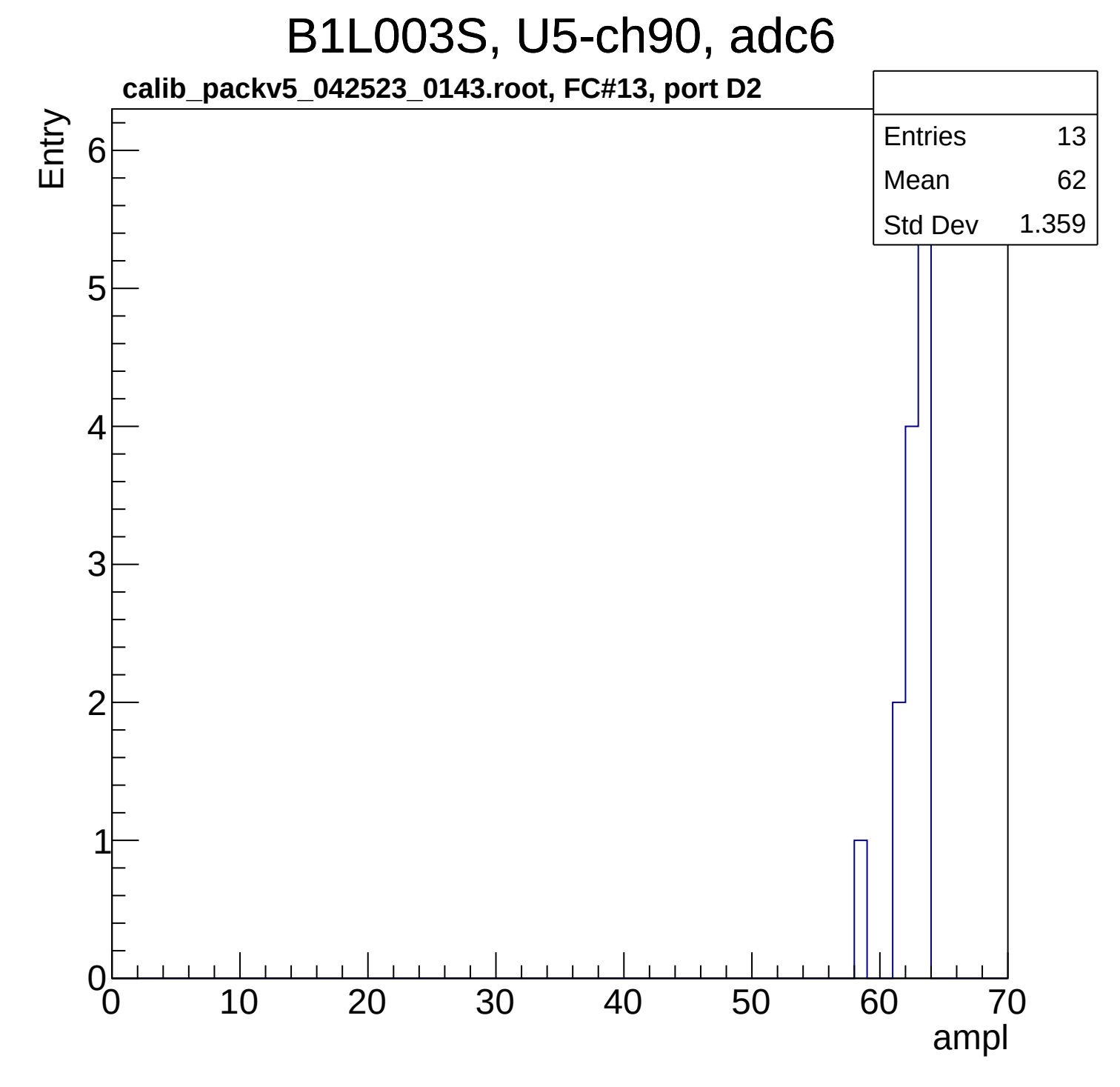
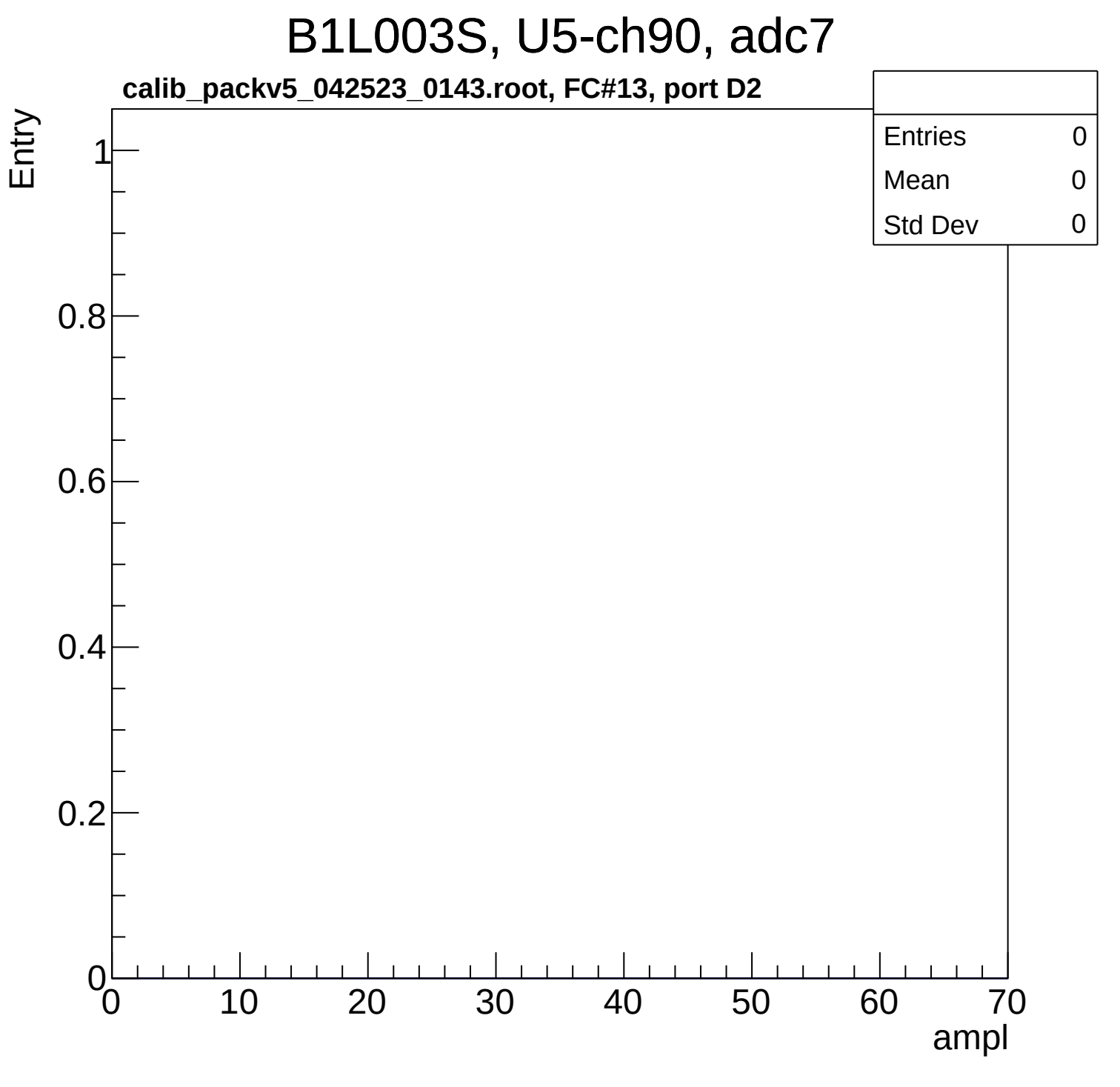
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 62
Std Dev 1.359
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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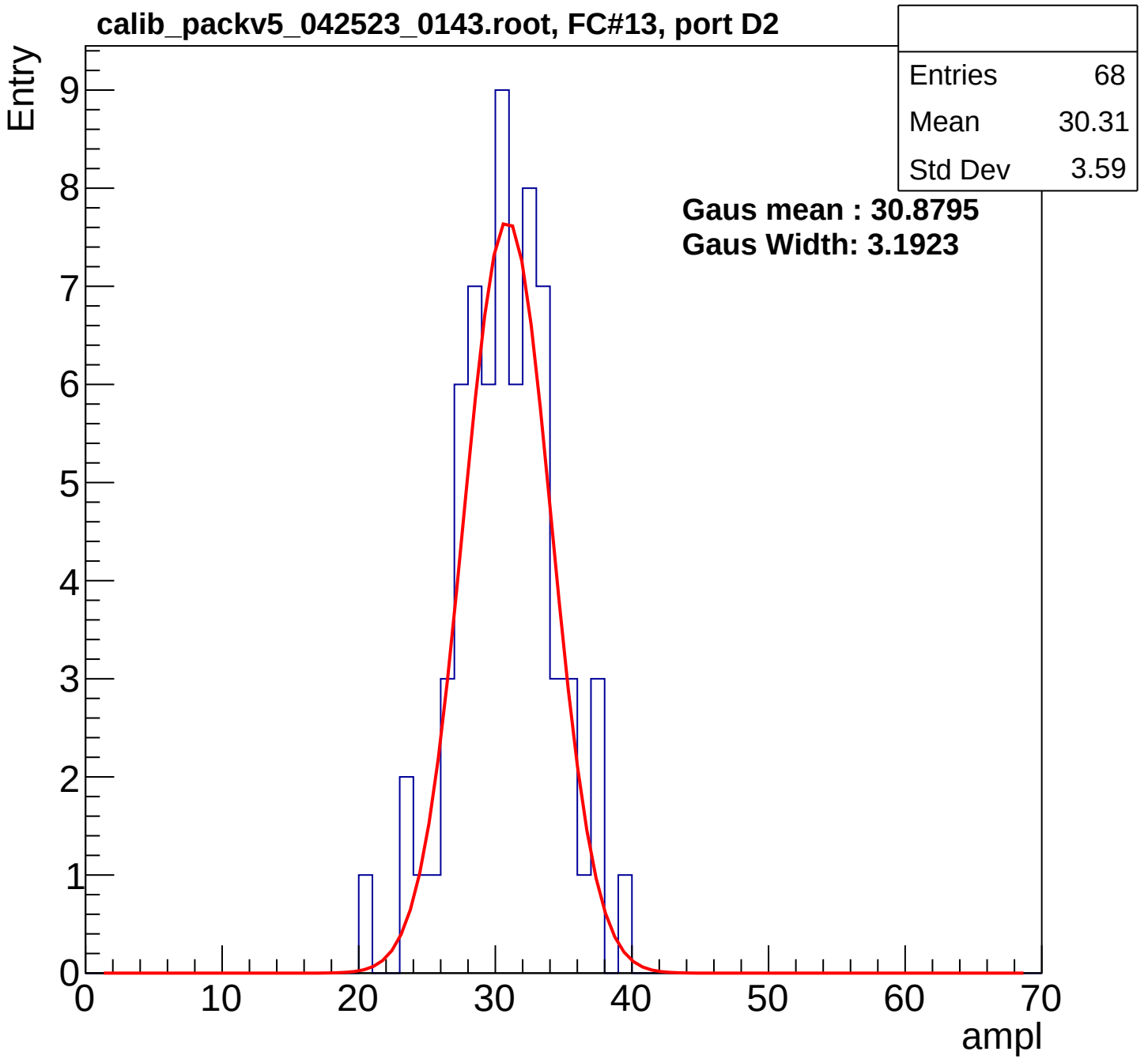
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.31
Std Dev 3.59
Gaus mean : 30.8795
Gaus Width: 3.1923
Entry
ampl
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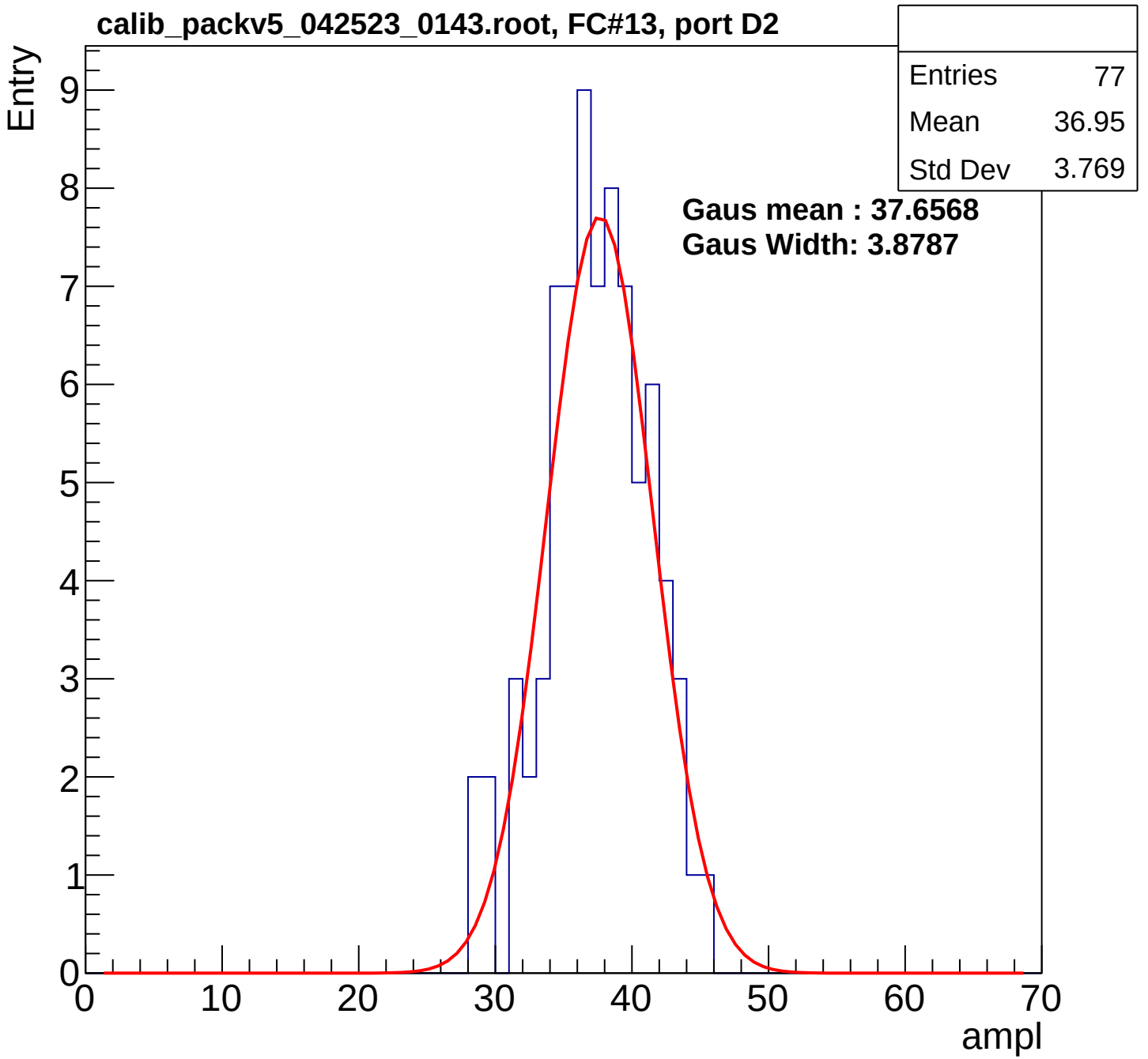
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.95
Std Dev 3.769
Gaus mean : 37.6568
Gaus Width: 3.8787
Entry
ampl
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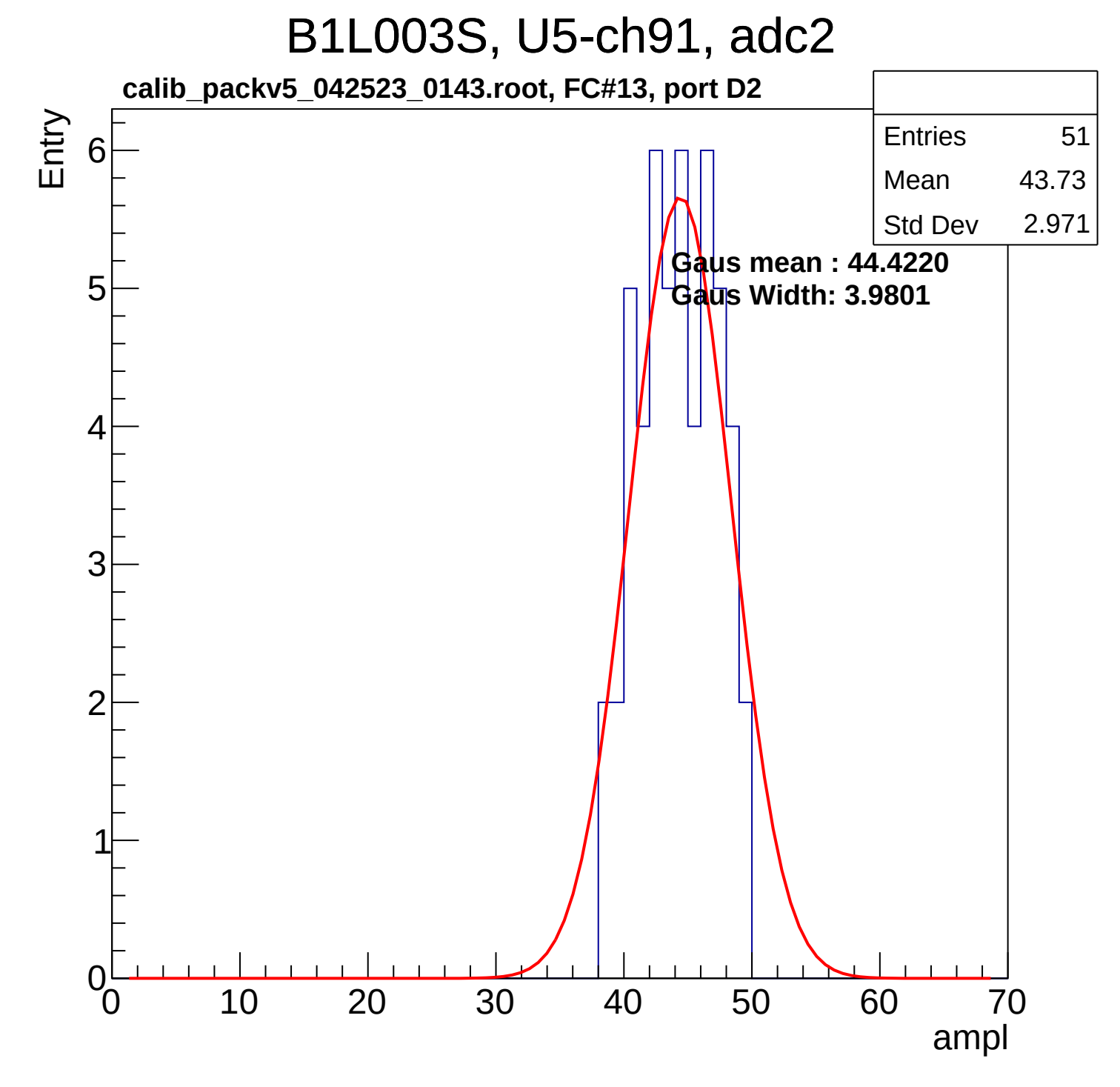
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.73
Std Dev 2.971
Gaus mean : 44.4220
Gaus Width: 3.9801
Entry
ampl
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3
4
5
6
0
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20
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40
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60
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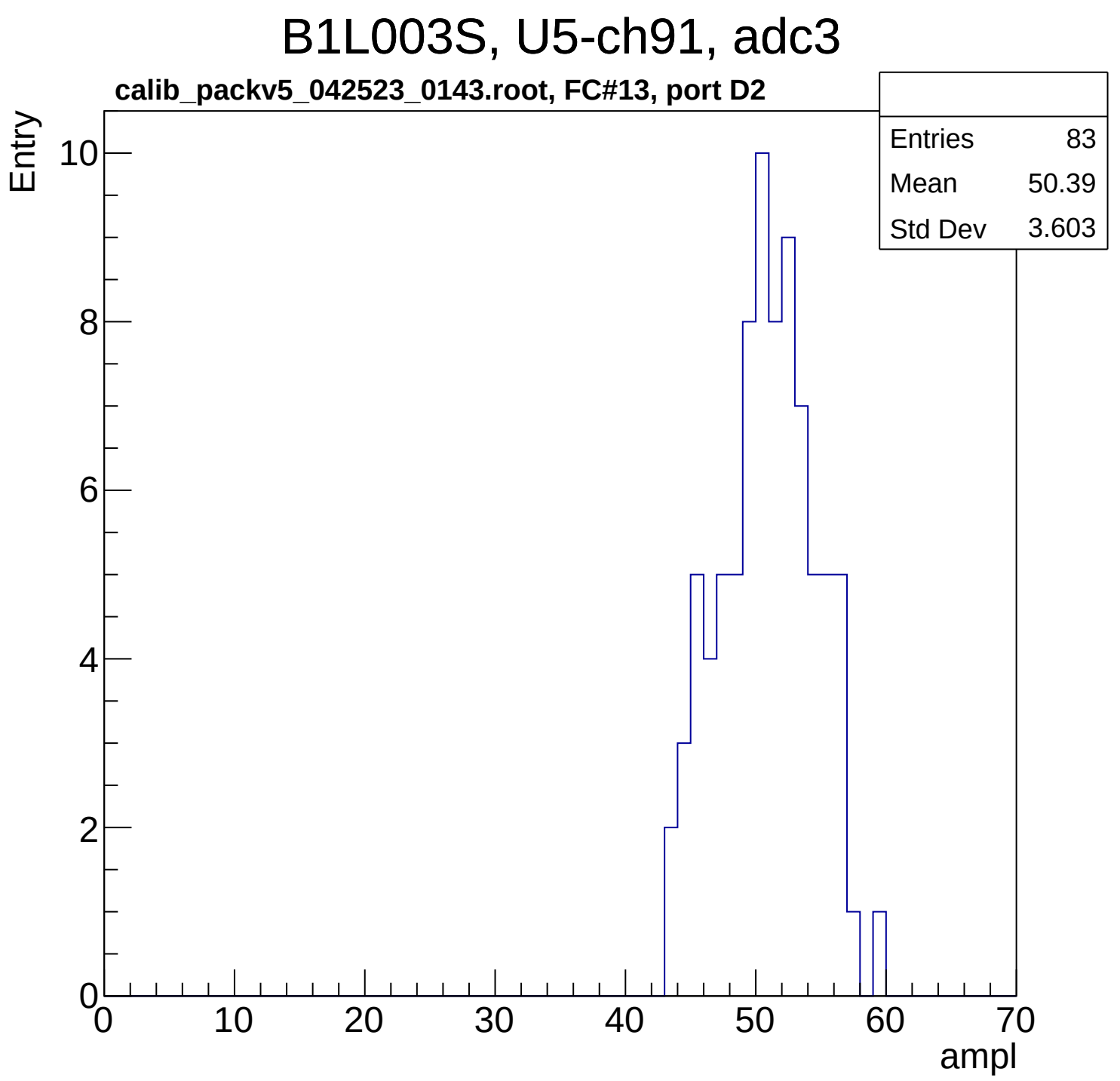
B1L003S, U5-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 50.39
Std Dev 3.603
Entry
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2
4
6
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10
0
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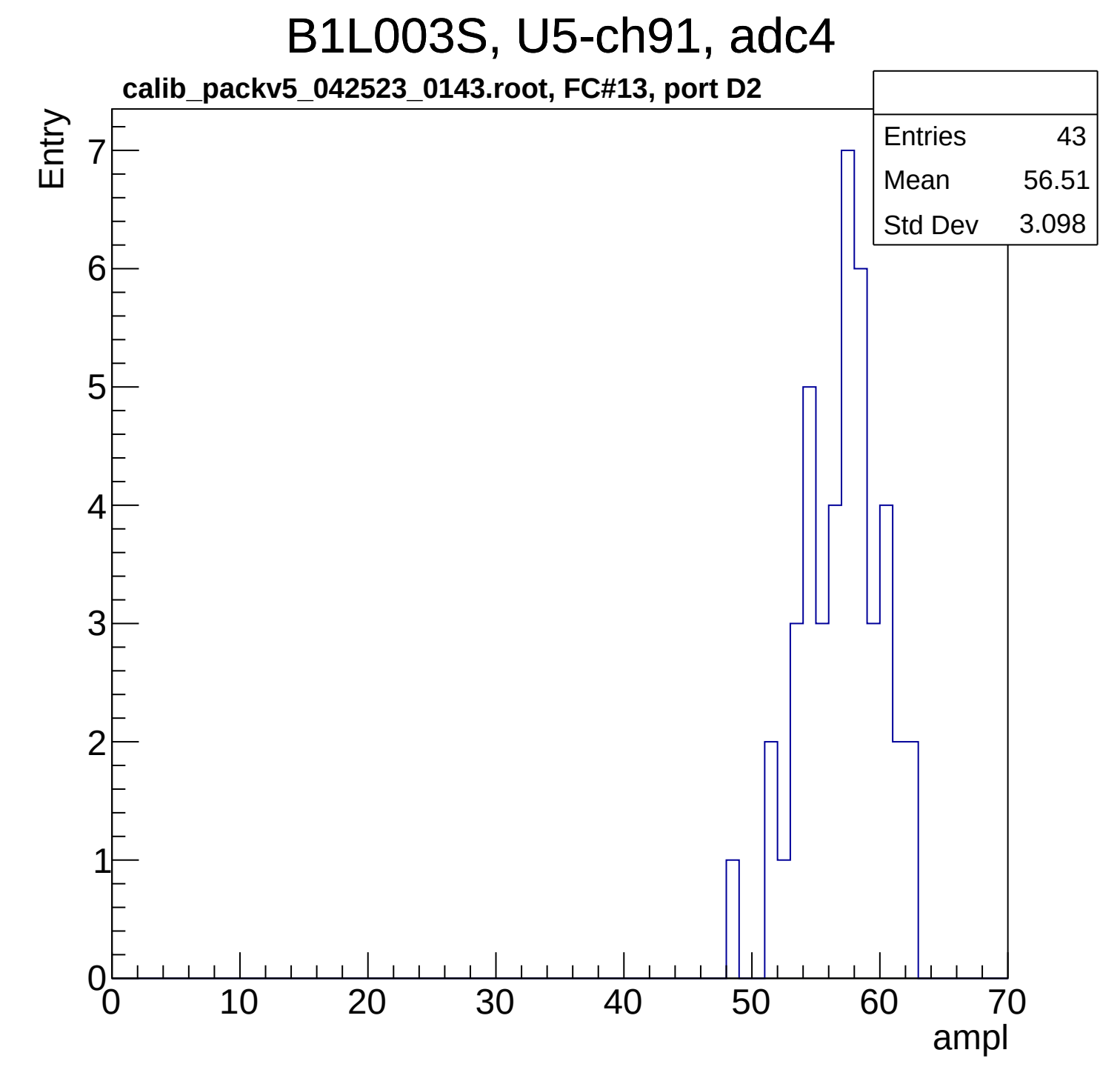
30
40
50
60
70
ampl


B1L003S, U5-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 56.51
Std Dev 3.098
Entry
ampl
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3
4
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6
7
0
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60
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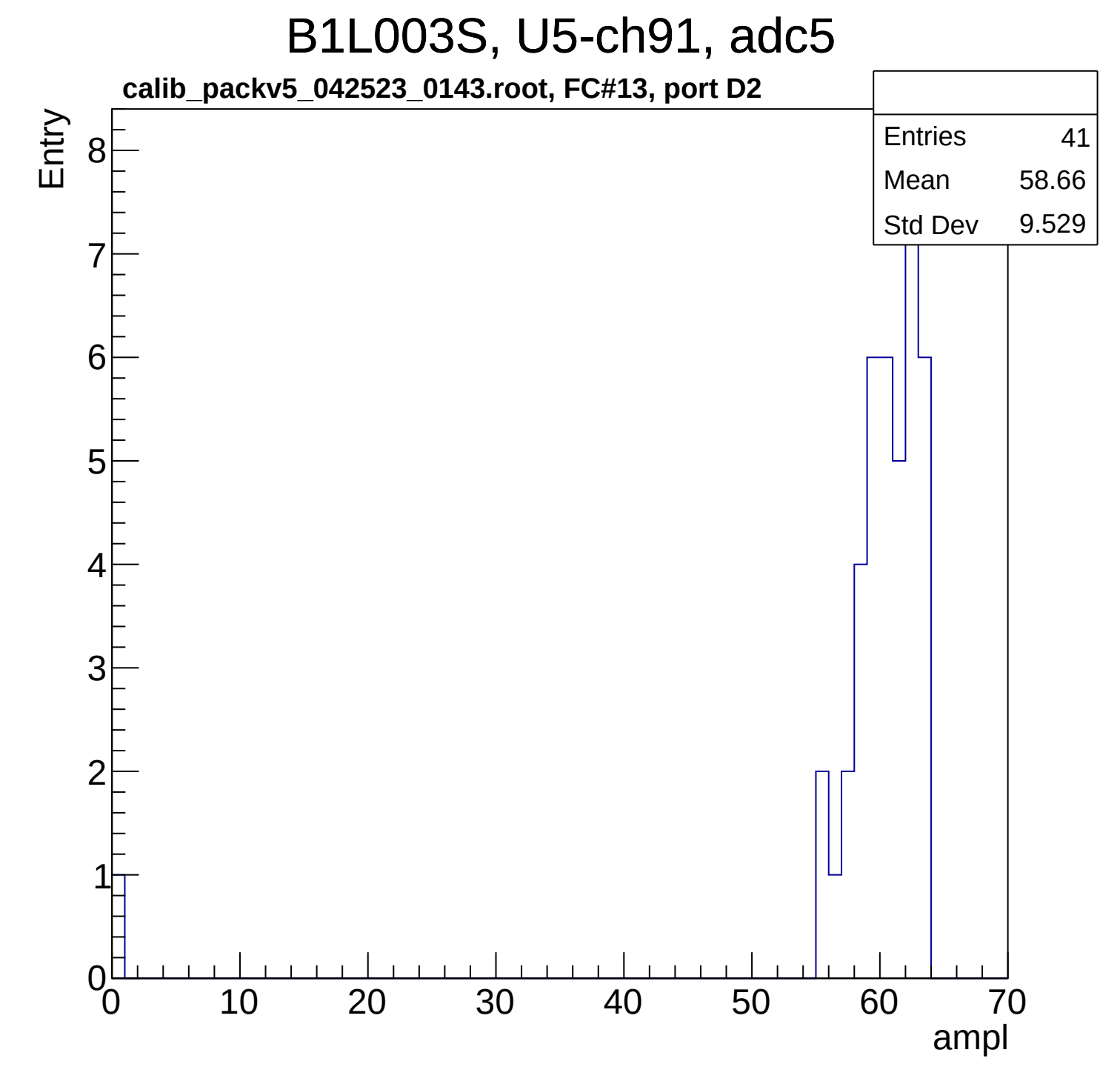
B1L003S, U5-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.66
Std Dev 9.529
Entry
ampl
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4
5
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7
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0
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60
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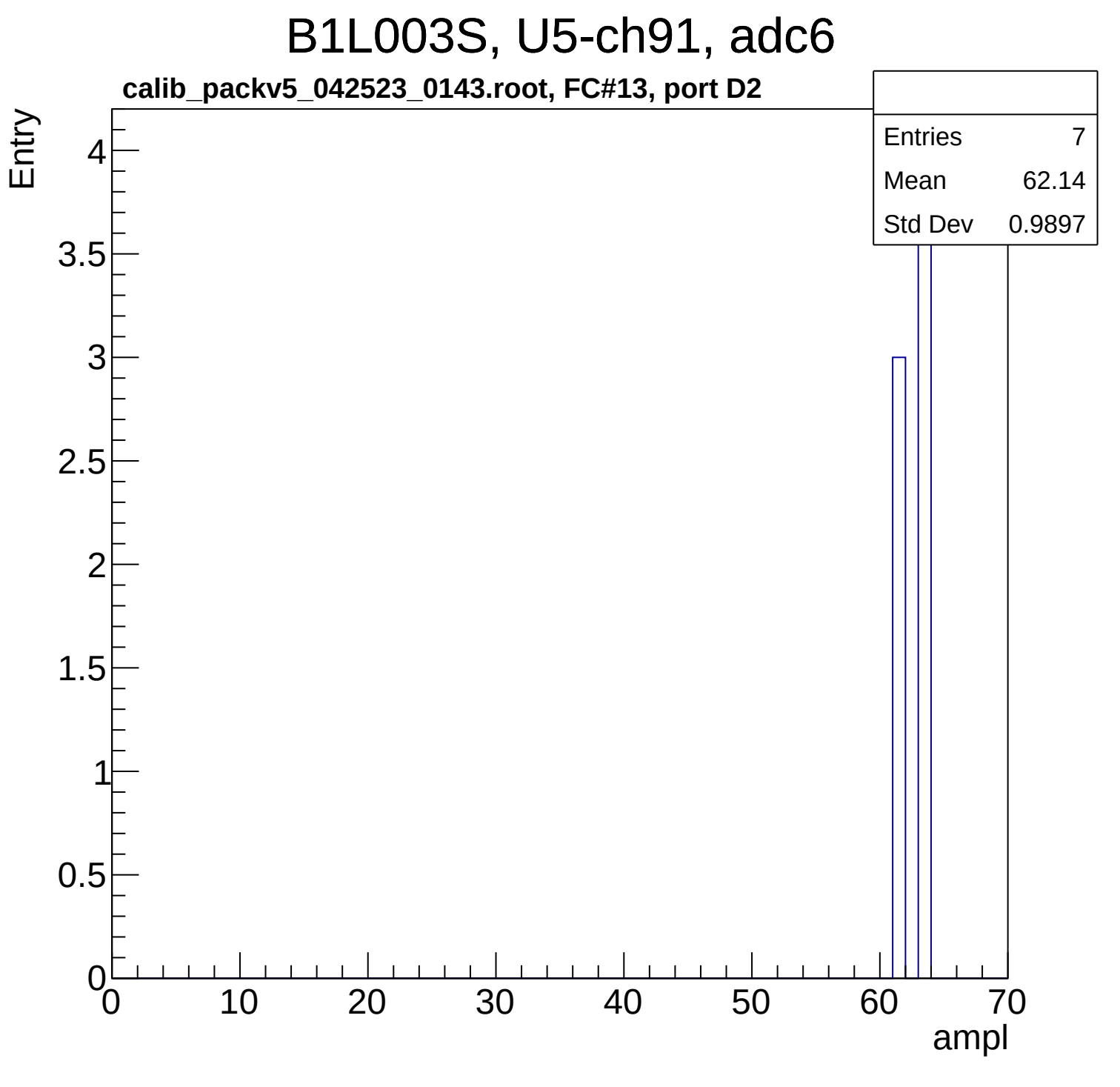
B1L003S, U5-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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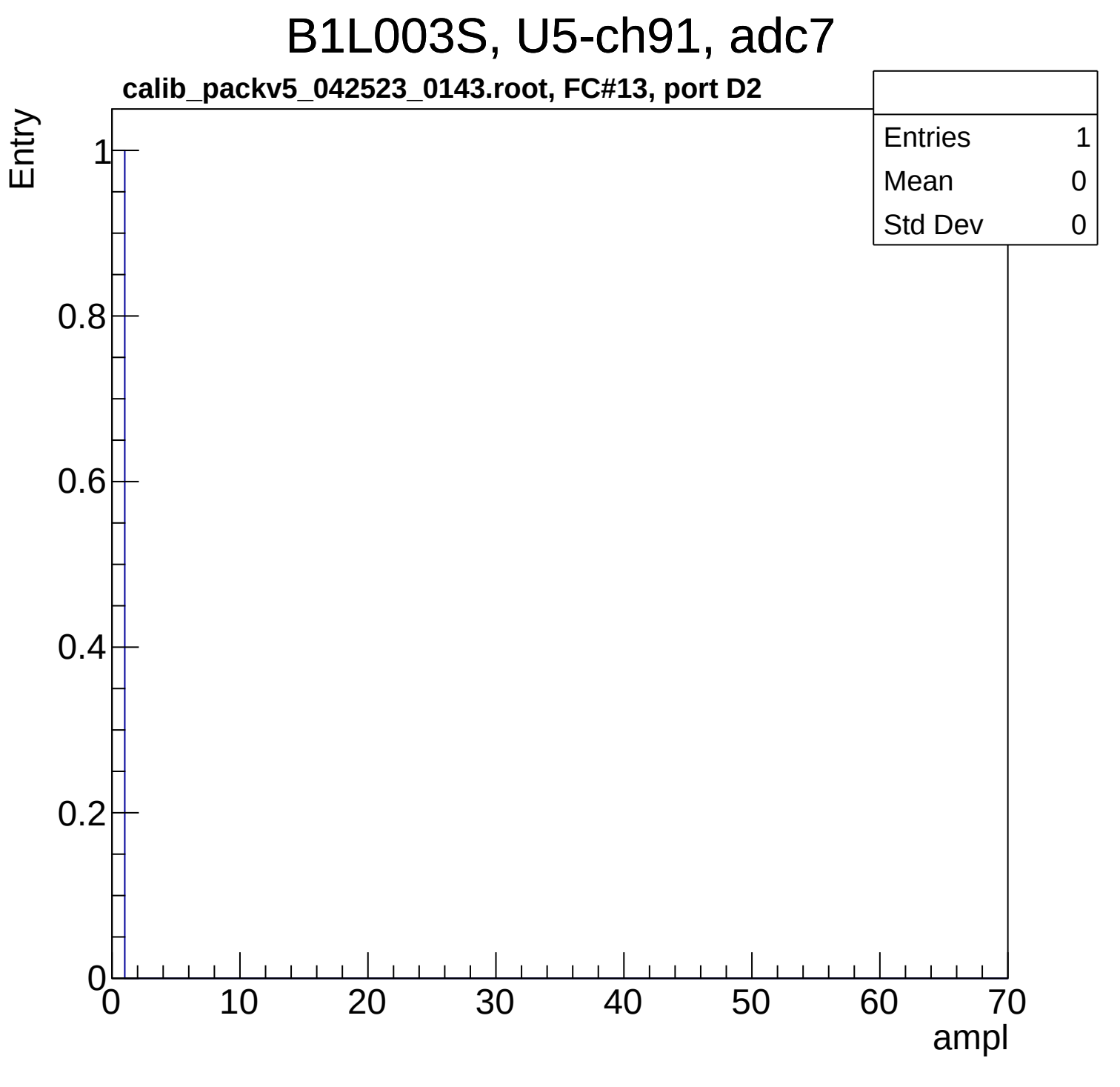
B1L003S, U5-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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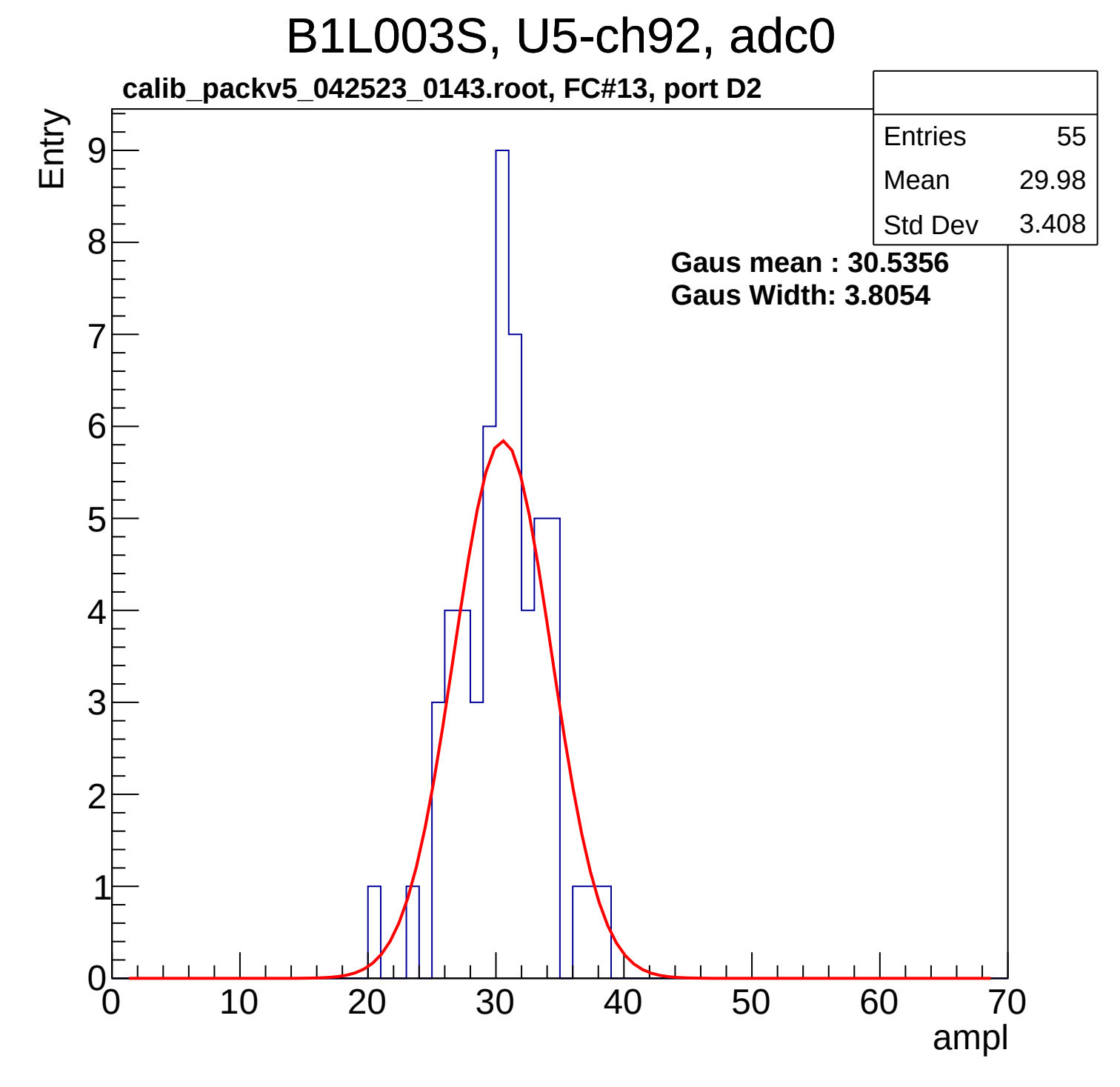
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.98
Std Dev 3.408
Gaus mean : 30.5356
Gaus Width: 3.8054
Entry
ampl
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20
30
40
50
60
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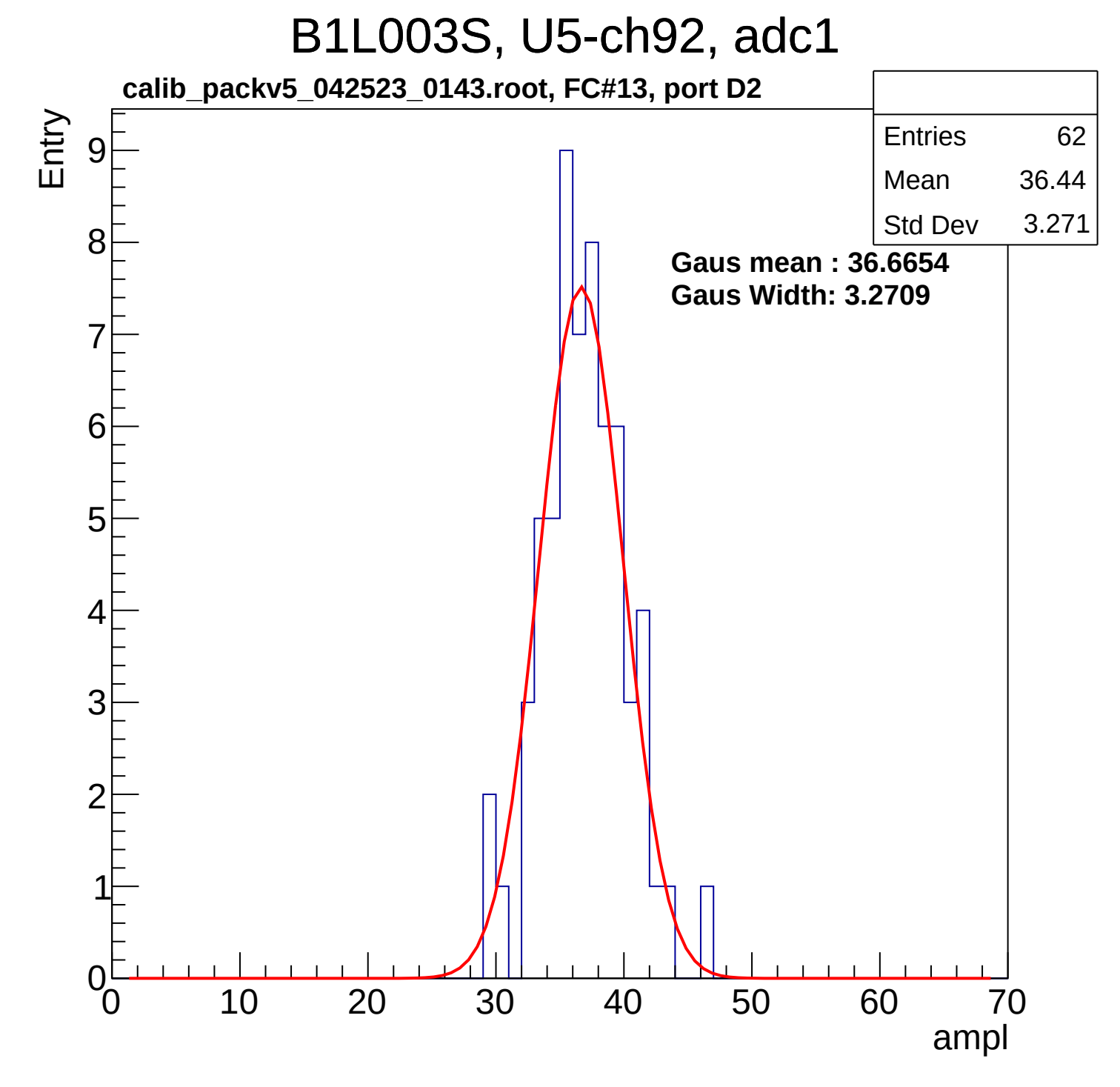
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.44
Std Dev 3.271
Gaus mean : 36.6654
Gaus Width: 3.2709
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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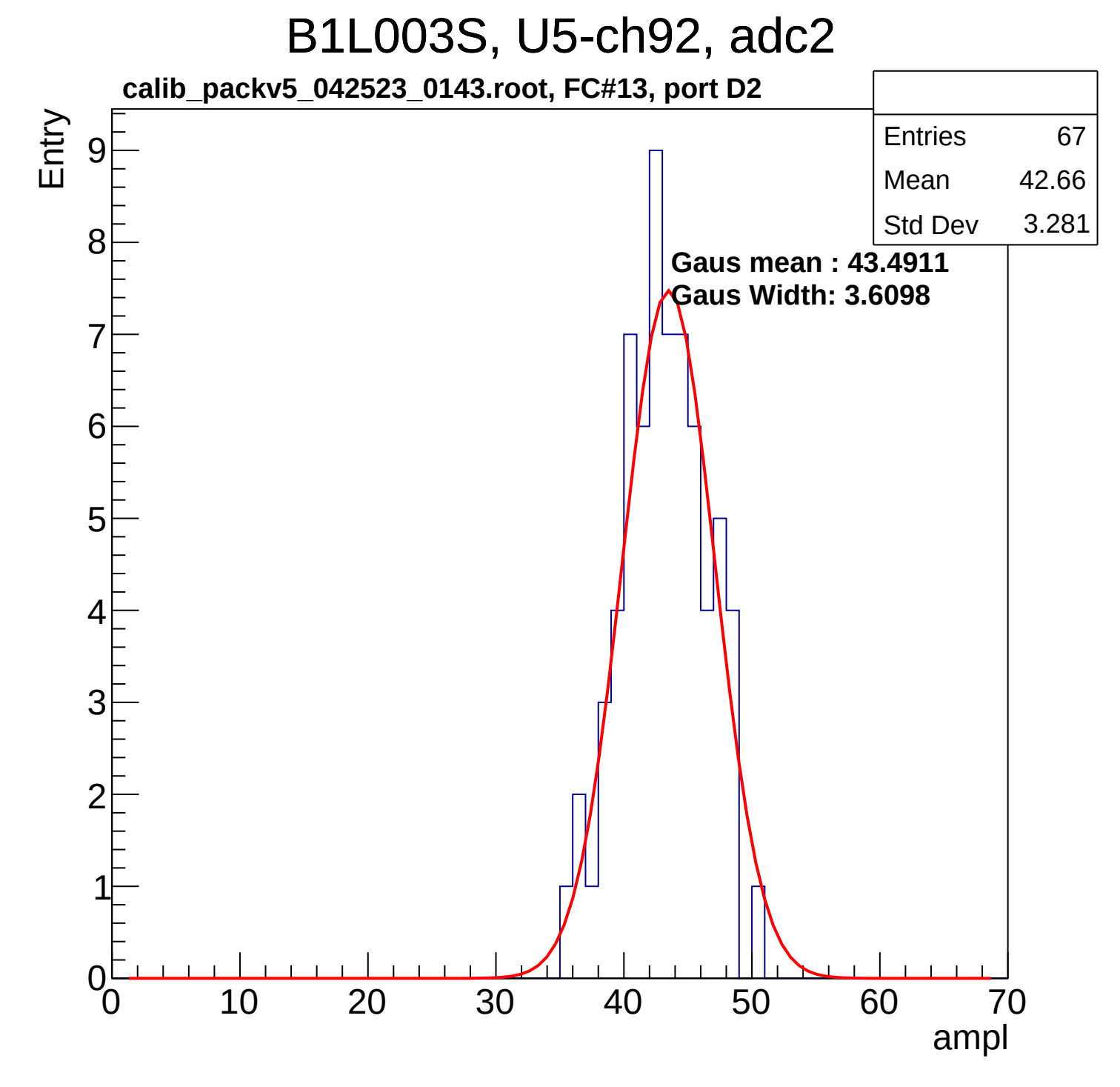
B1L003S, U5-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.66
Std Dev 3.281
Gaus mean : 43.4911
Gaus Width: 3.6098
Entry
ampl
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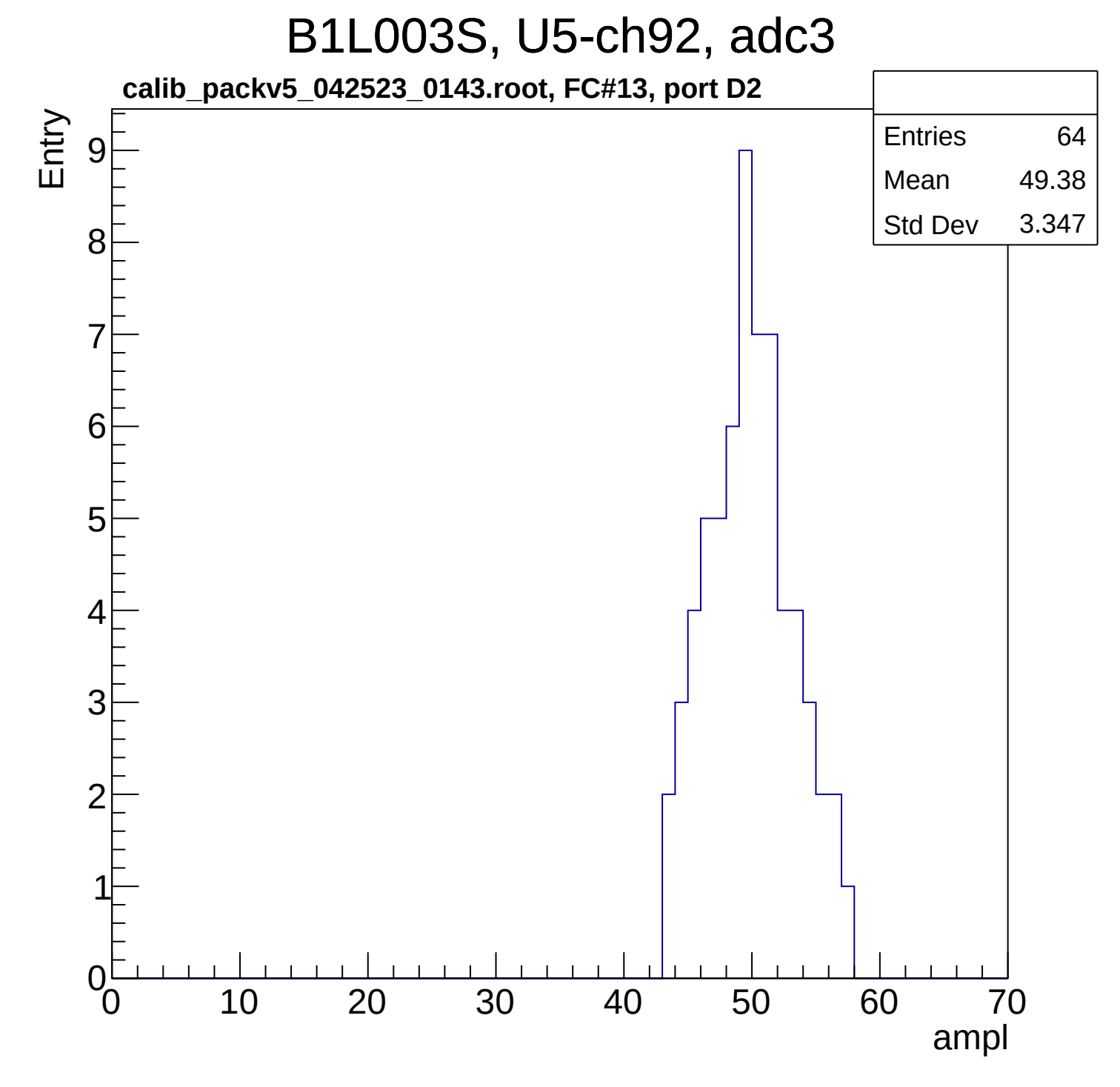
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.38
Std Dev 3.347
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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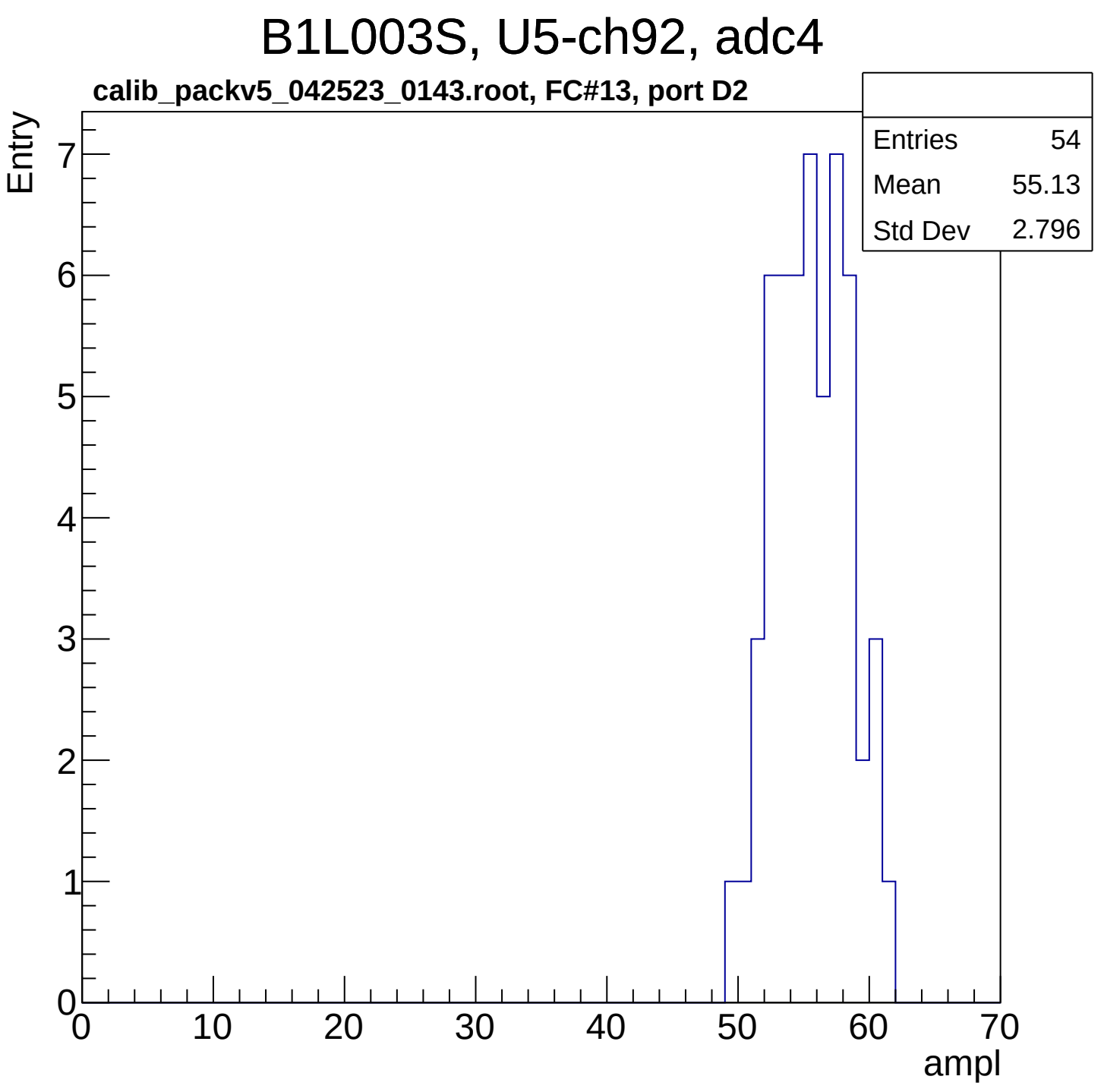
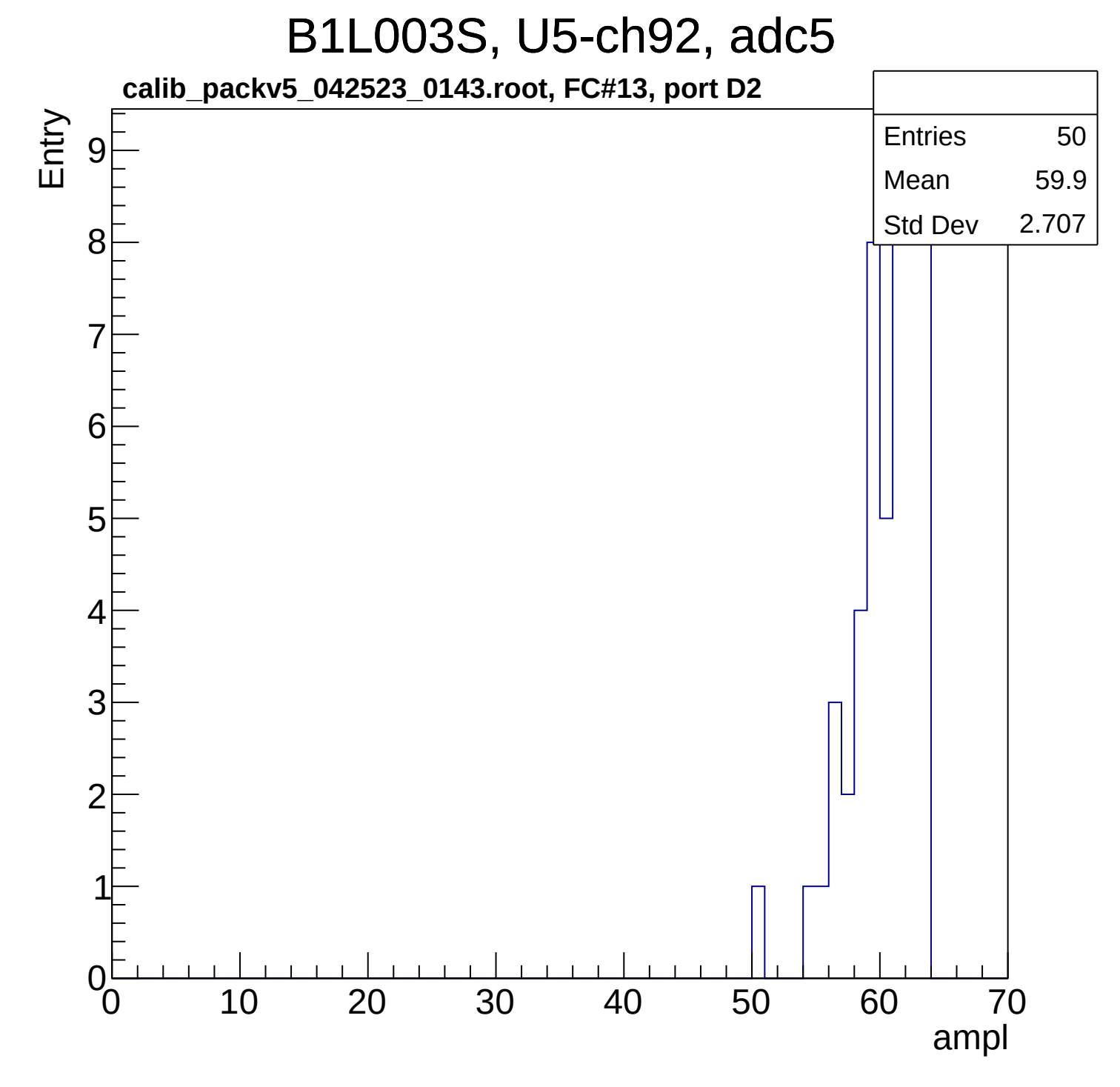
60
70


B1L003S, U5-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.13
Std Dev 2.796
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.9
Std Dev 2.707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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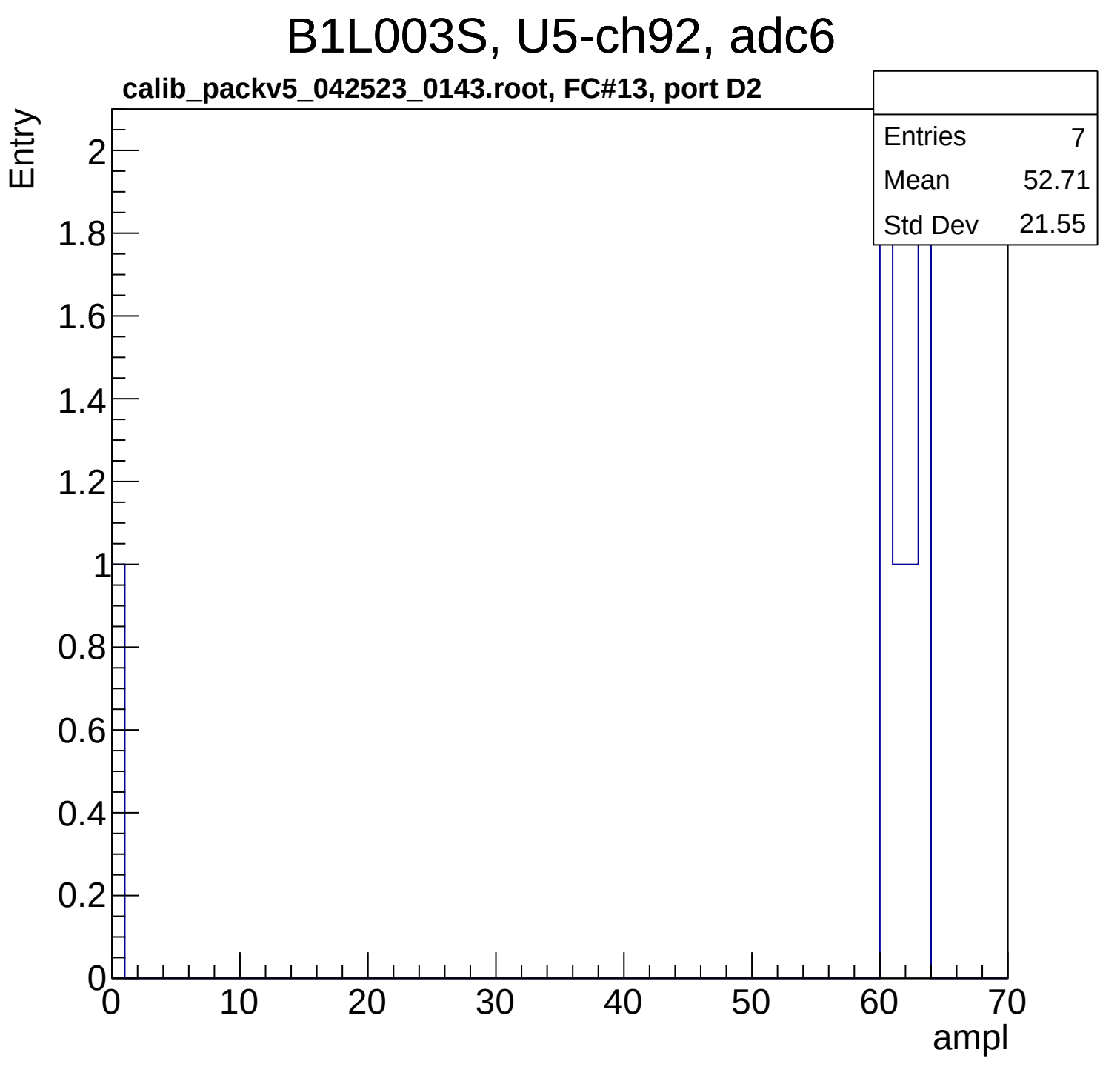
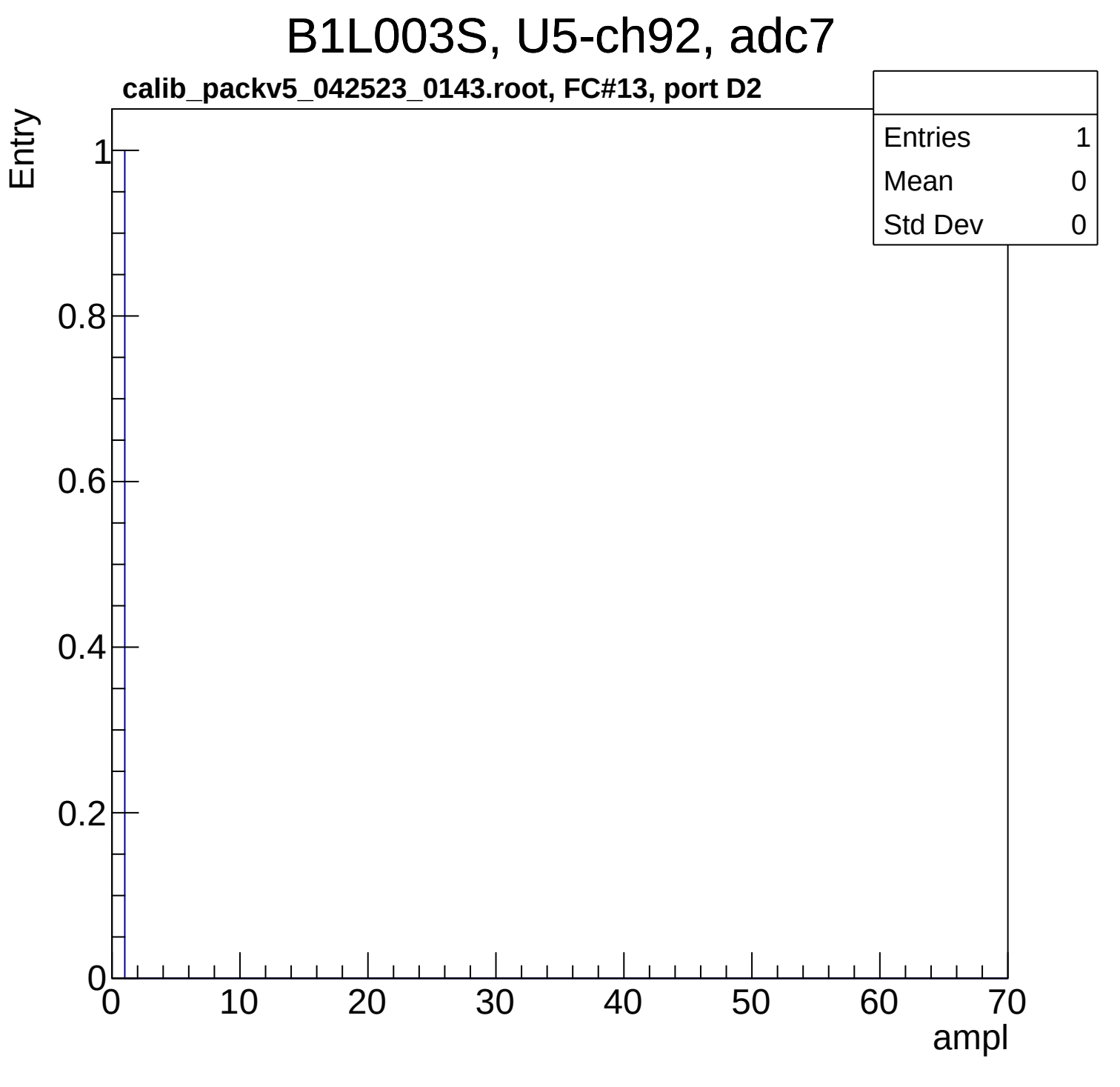
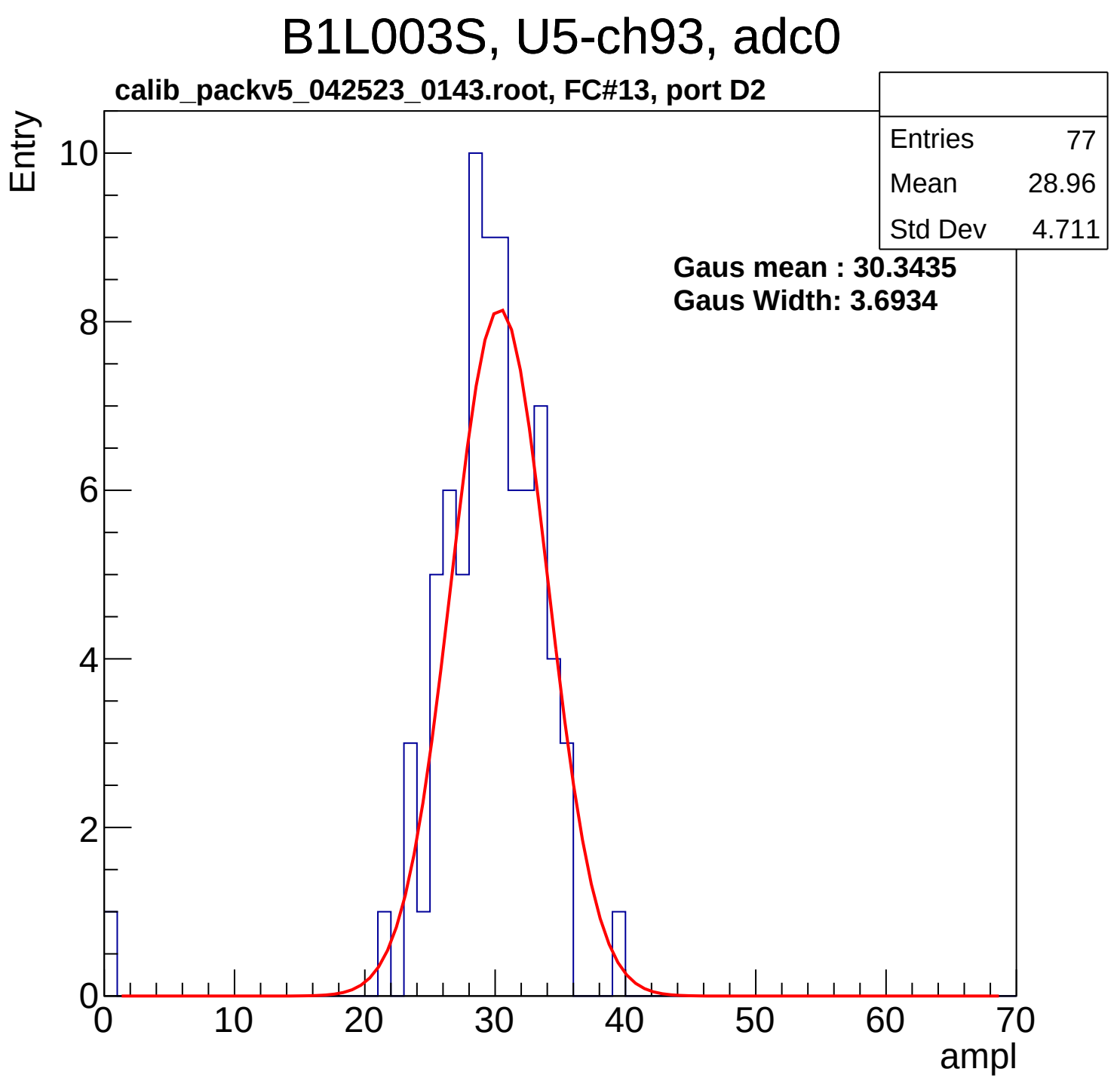
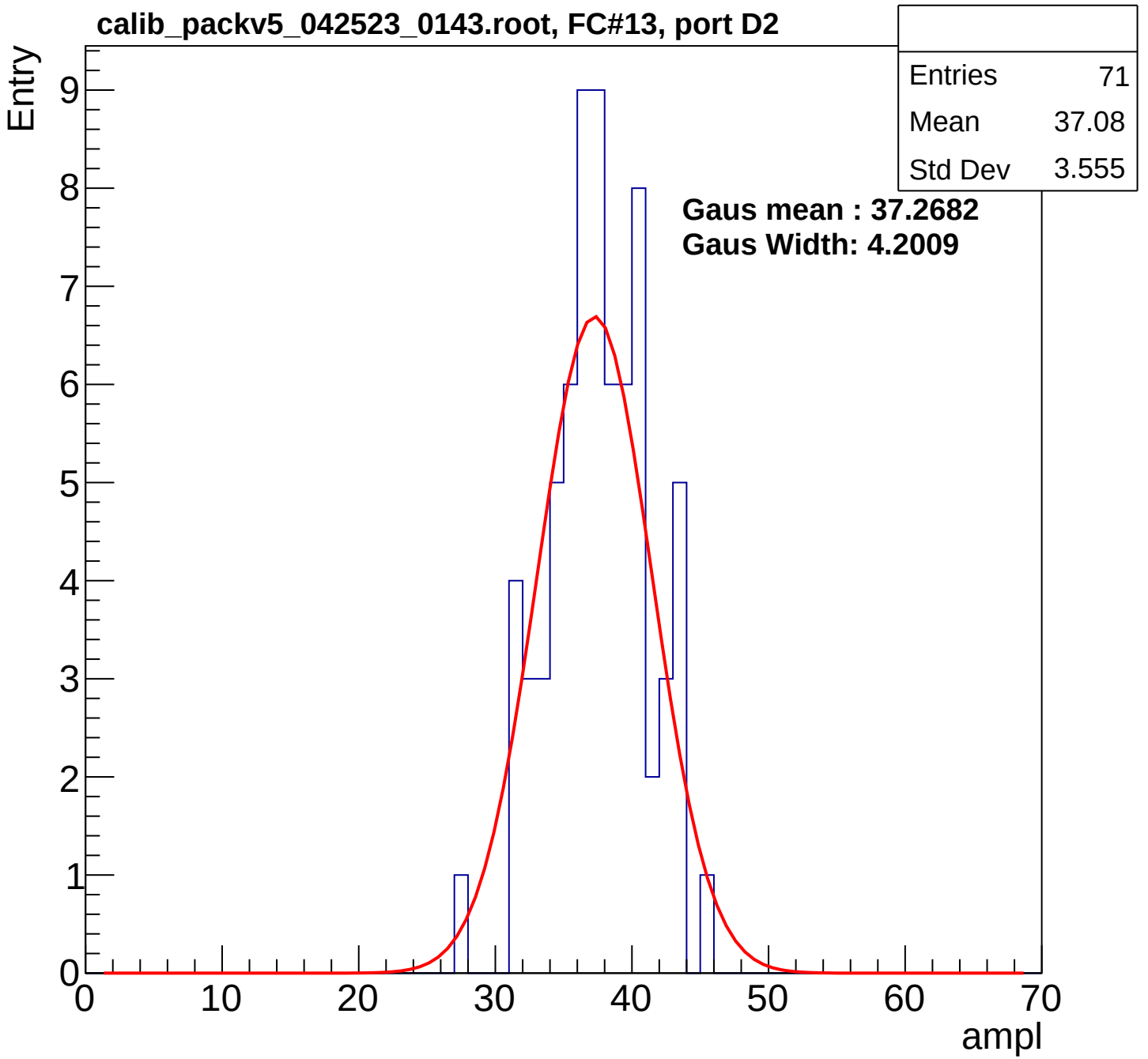
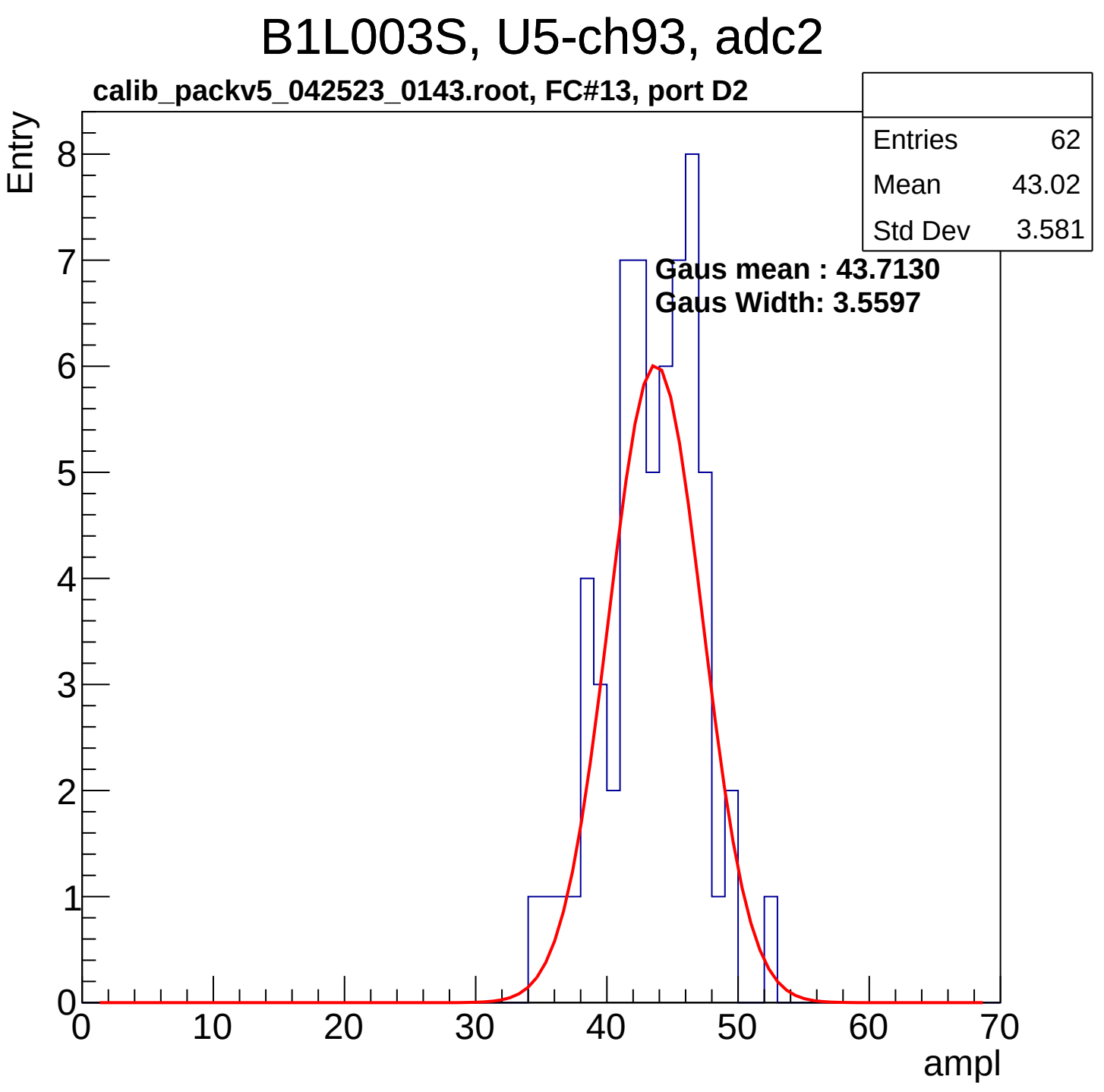
40
50
60
70

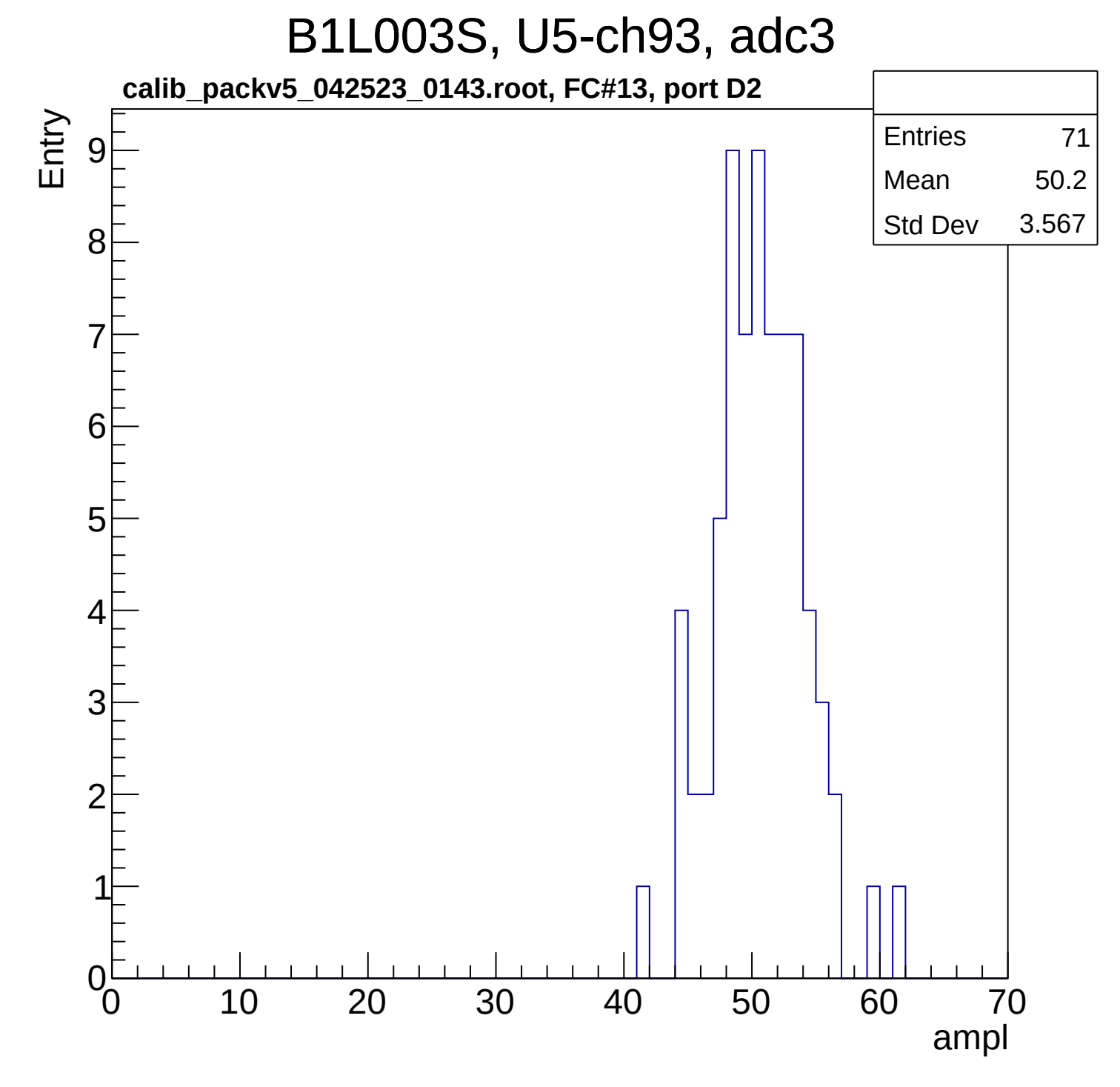

B1L003S, U5-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.71
Std Dev 21.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

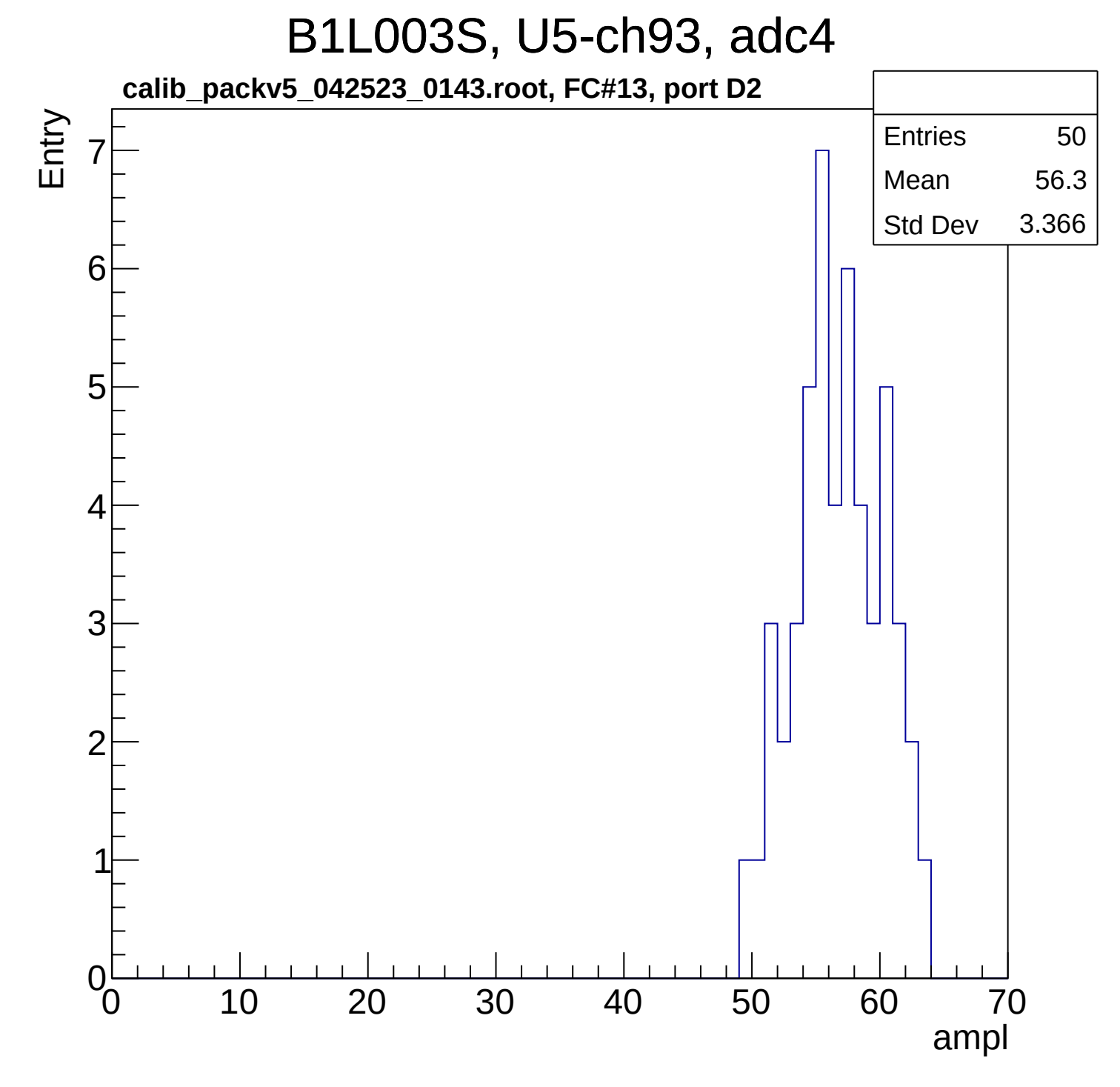

B1L003S, U5-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

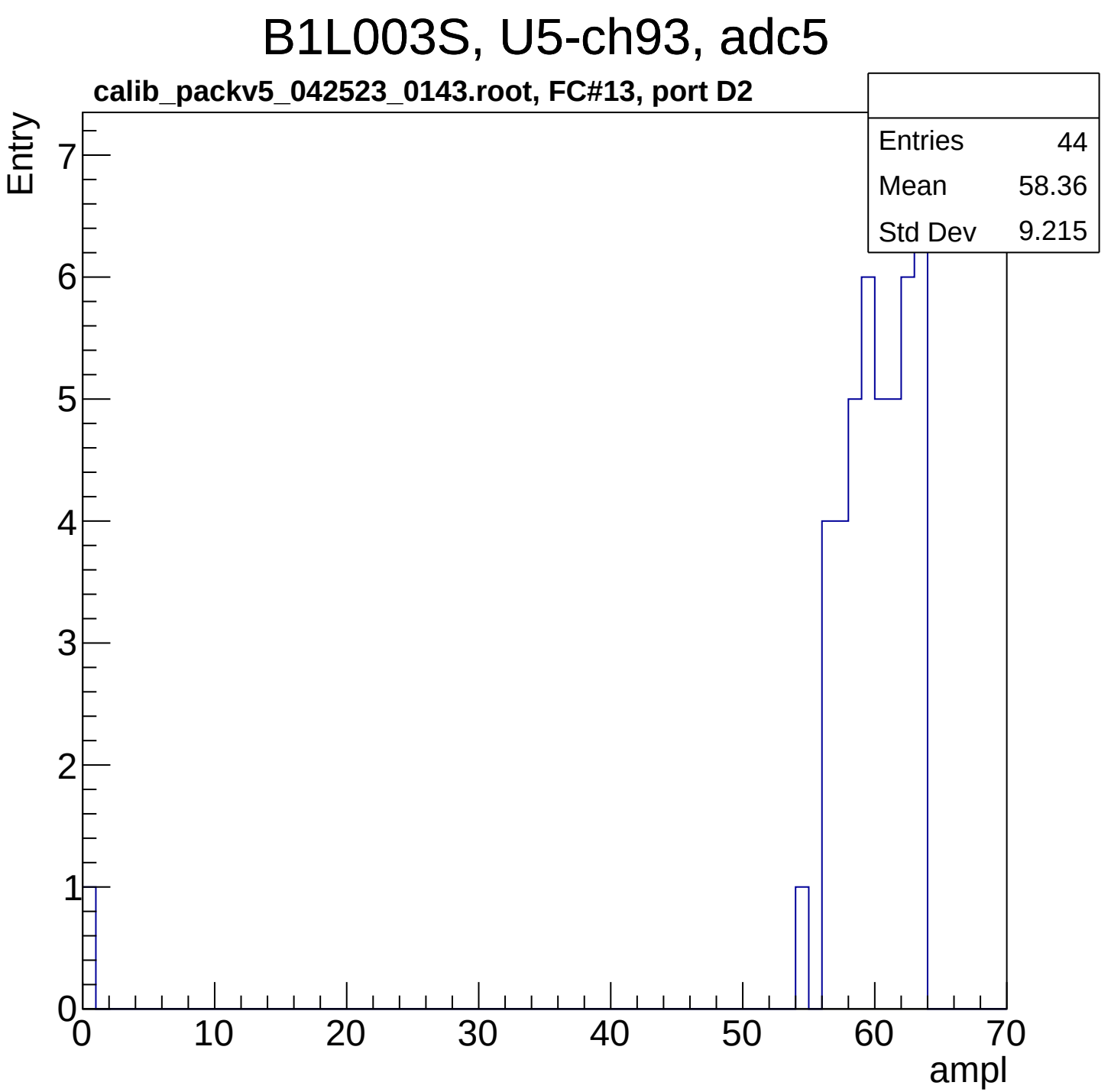

B1L003S, U5-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.96
Std Dev 4.711
Gaus mean : 30.3435
Gaus Width: 3.6934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

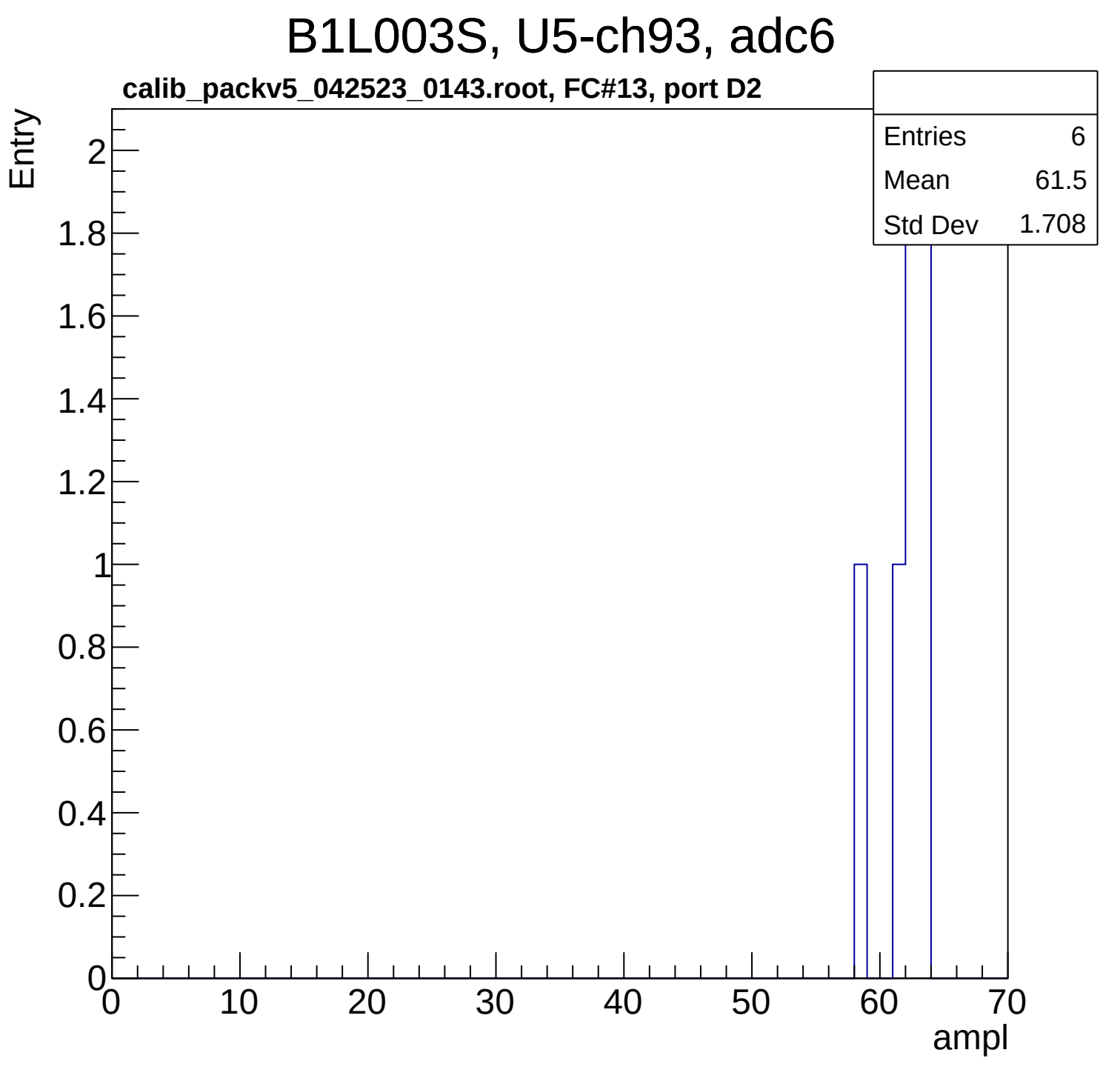

B1L003S, U5-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 37.08
Std Dev 3.555
Gaus mean : 37.2682
Gaus Width: 4.2009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

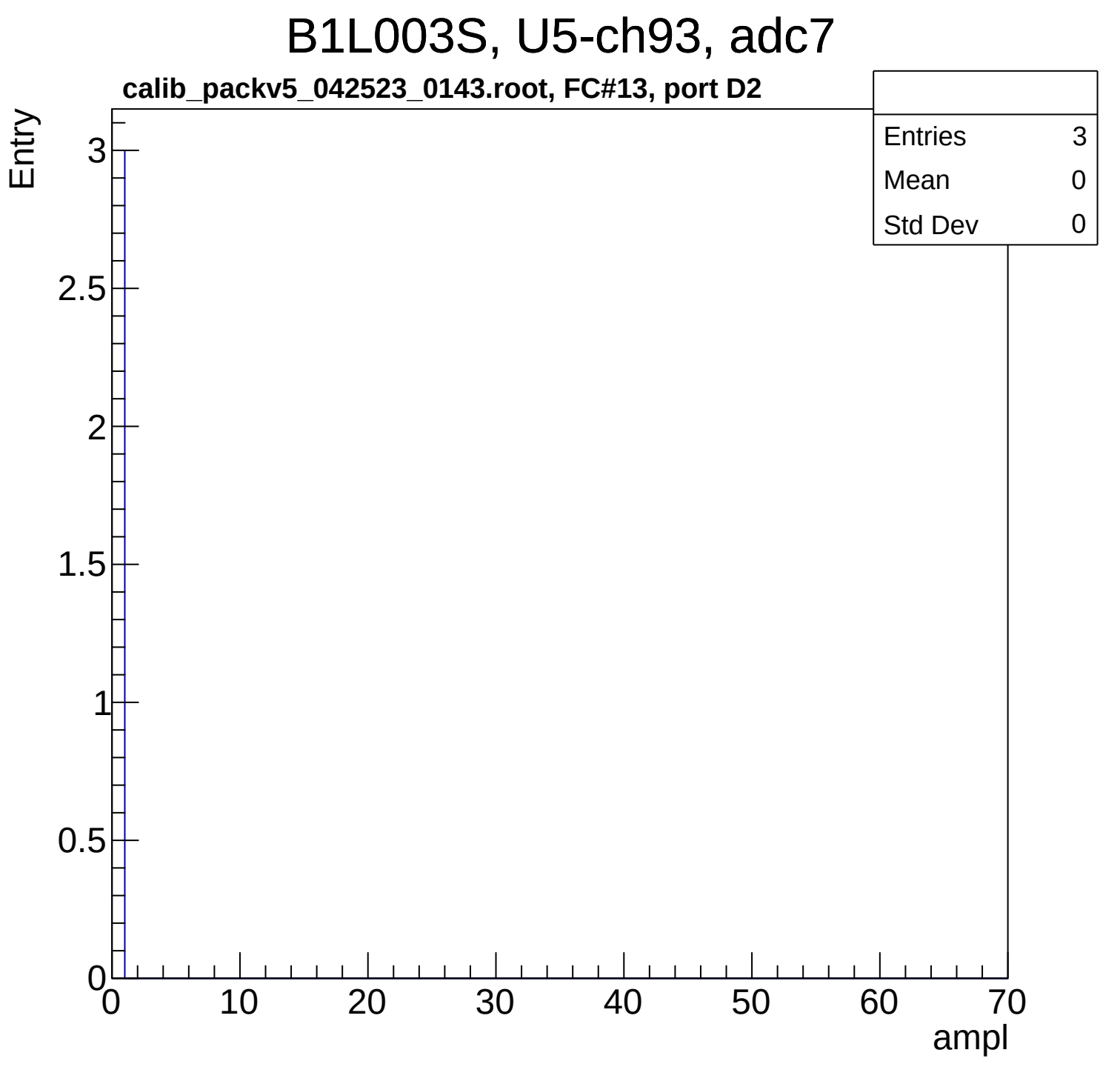

B1L003S, U5-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.02
Std Dev 3.581
Gaus mean : 43.7130
Gaus Width: 3.5597
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

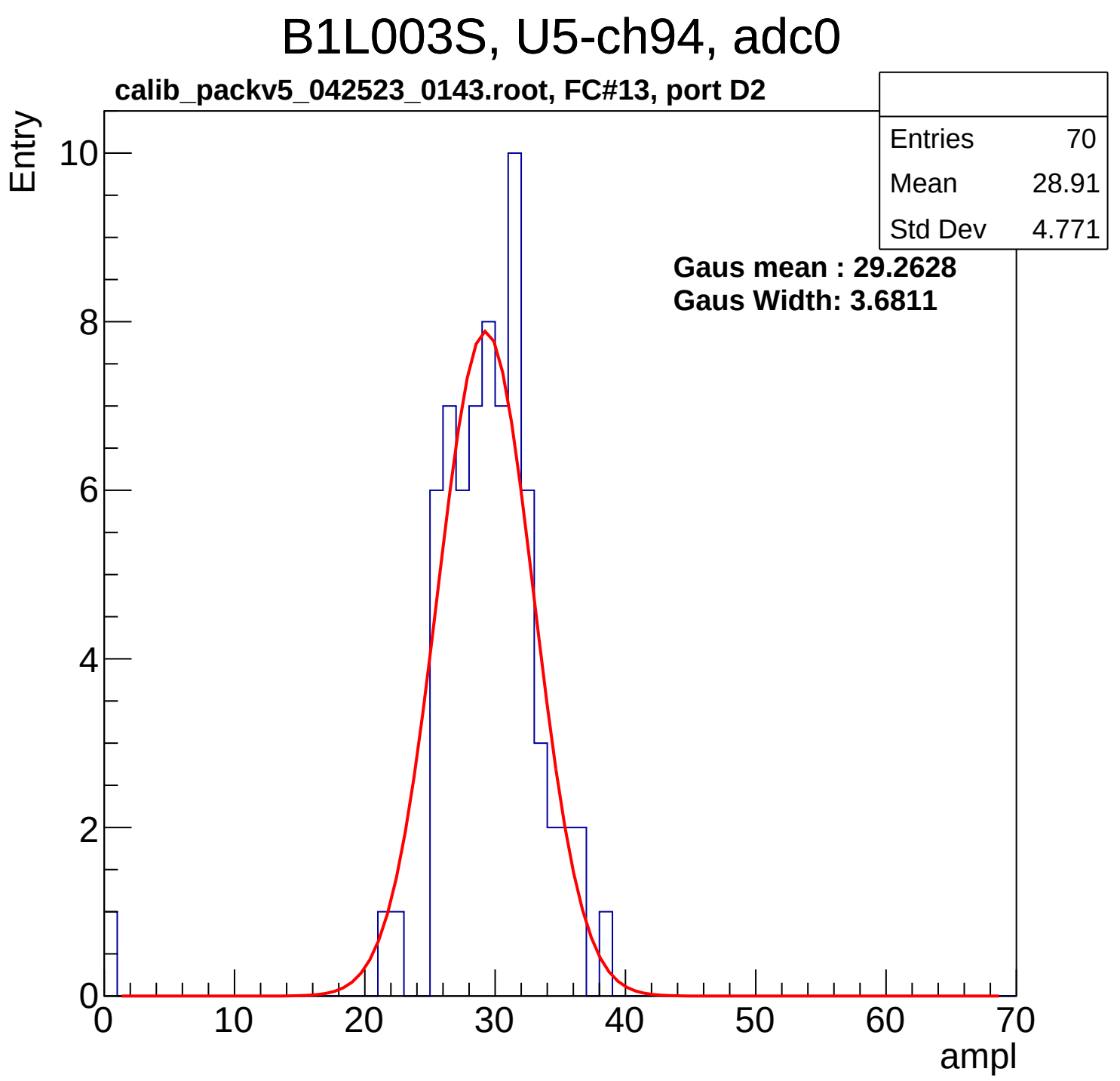

B1L003S, U5-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 50.2
Std Dev 3.567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

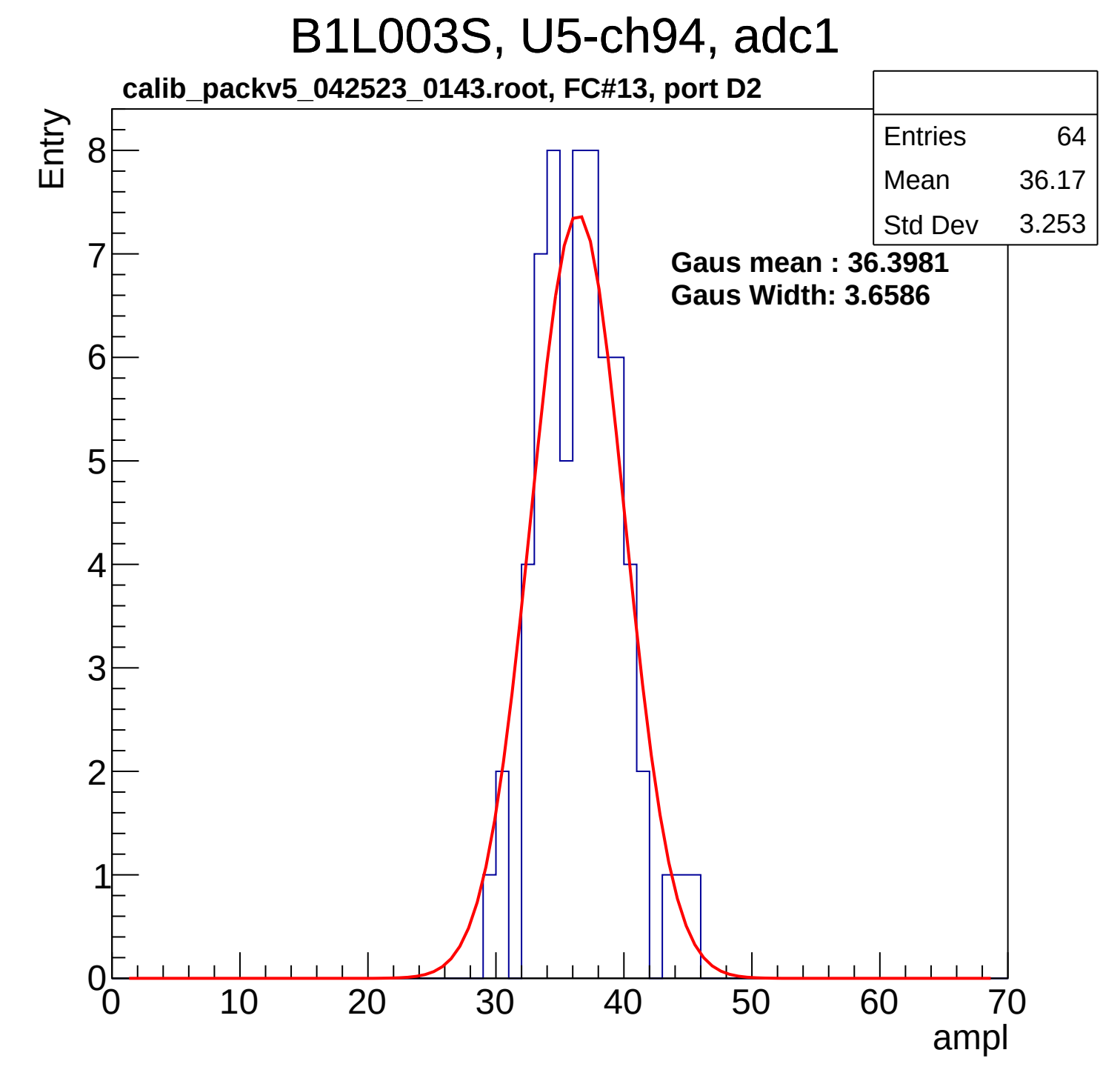

B1L003S, U5-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 56.3
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

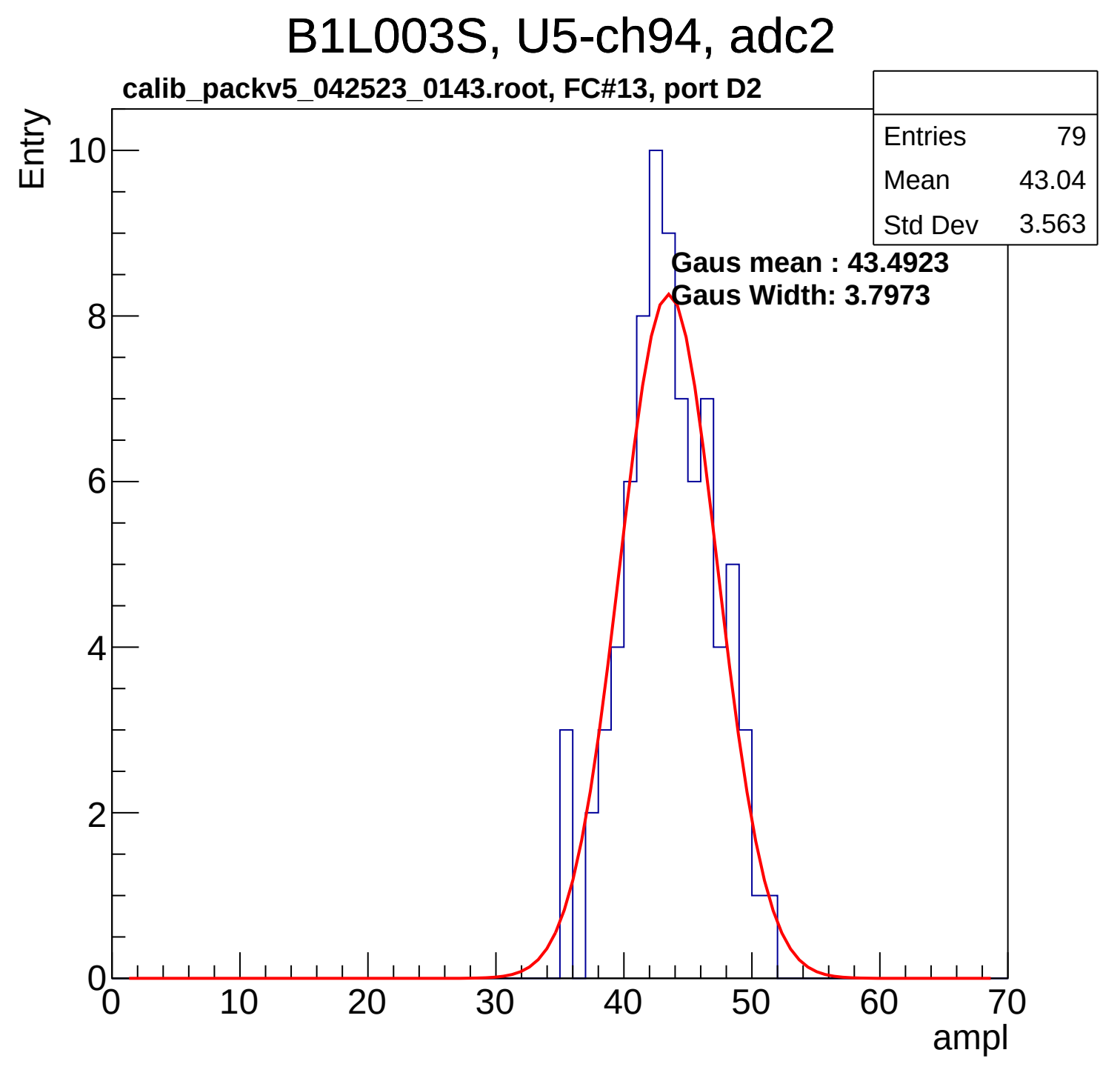

B1L003S, U5-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.36
Std Dev 9.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

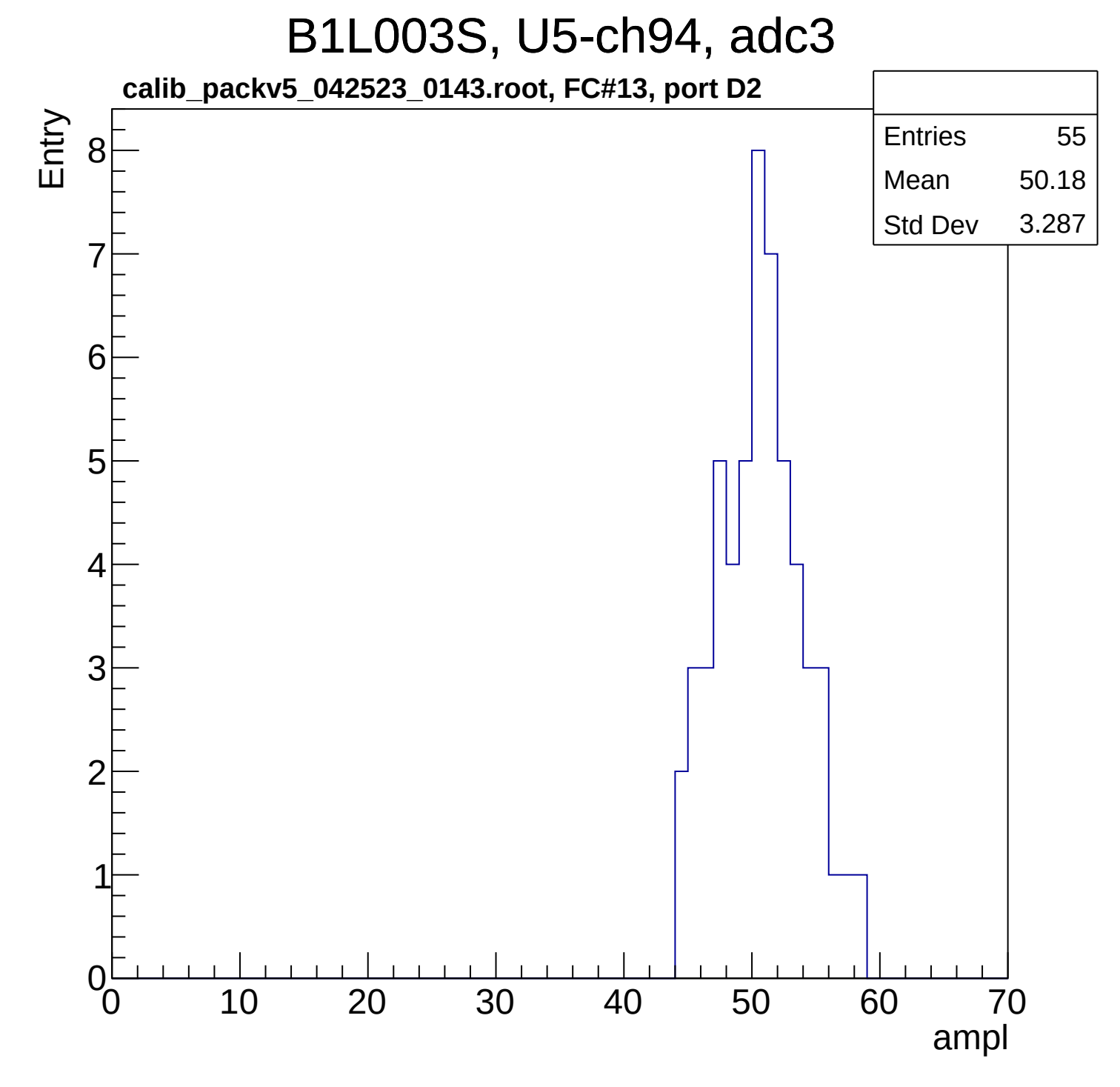

B1L003S, U5-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 1.708
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.91
Std Dev 4.771
Gaus mean : 29.2628
Gaus Width: 3.6811
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.17
Std Dev 3.253
Gaus mean : 36.3981
Gaus Width: 3.6586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 43.04
Std Dev 3.563
Gaus mean : 43.4923
Gaus Width: 3.7973
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 50.18
Std Dev 3.287
Entry
ampl
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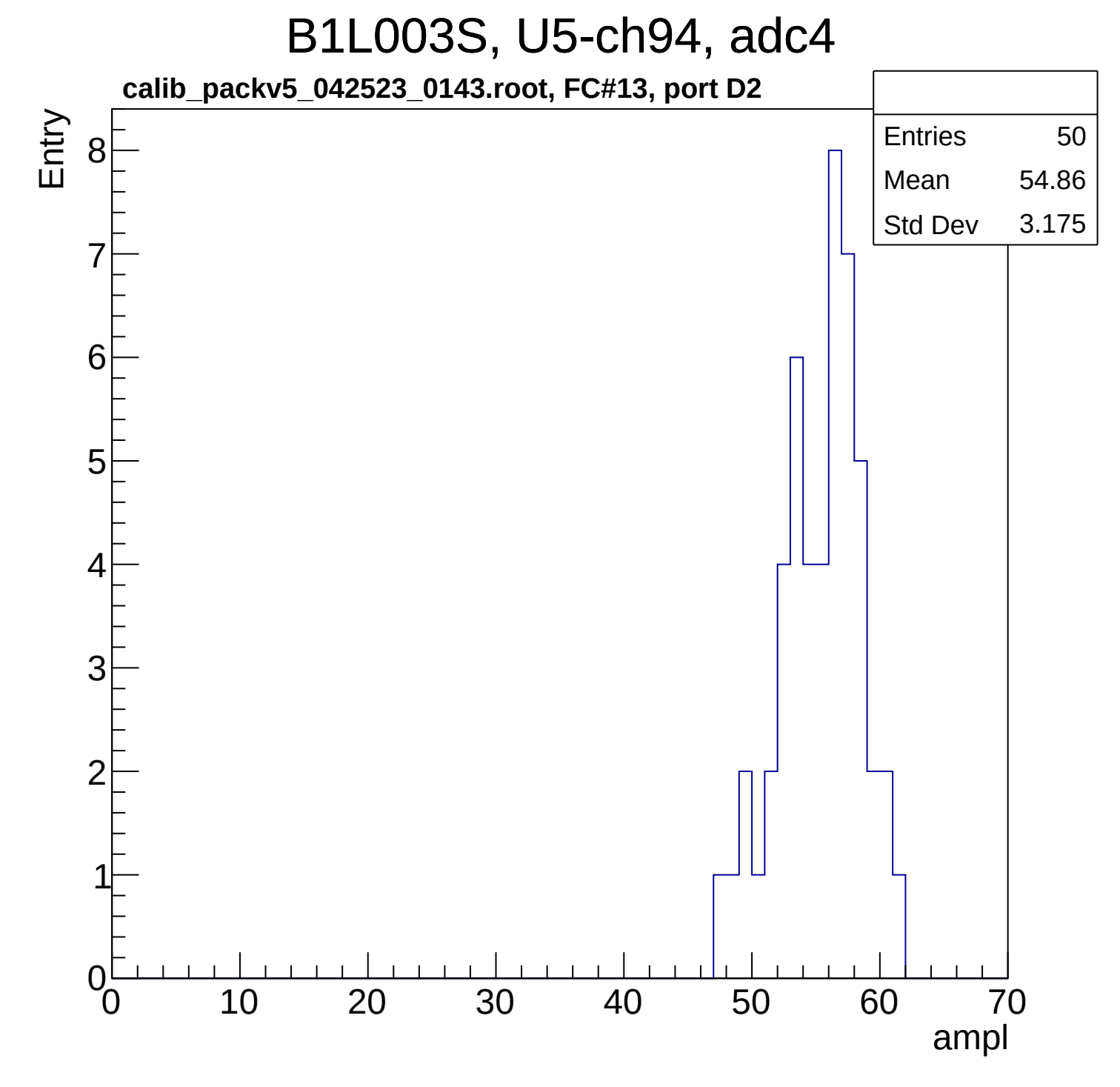
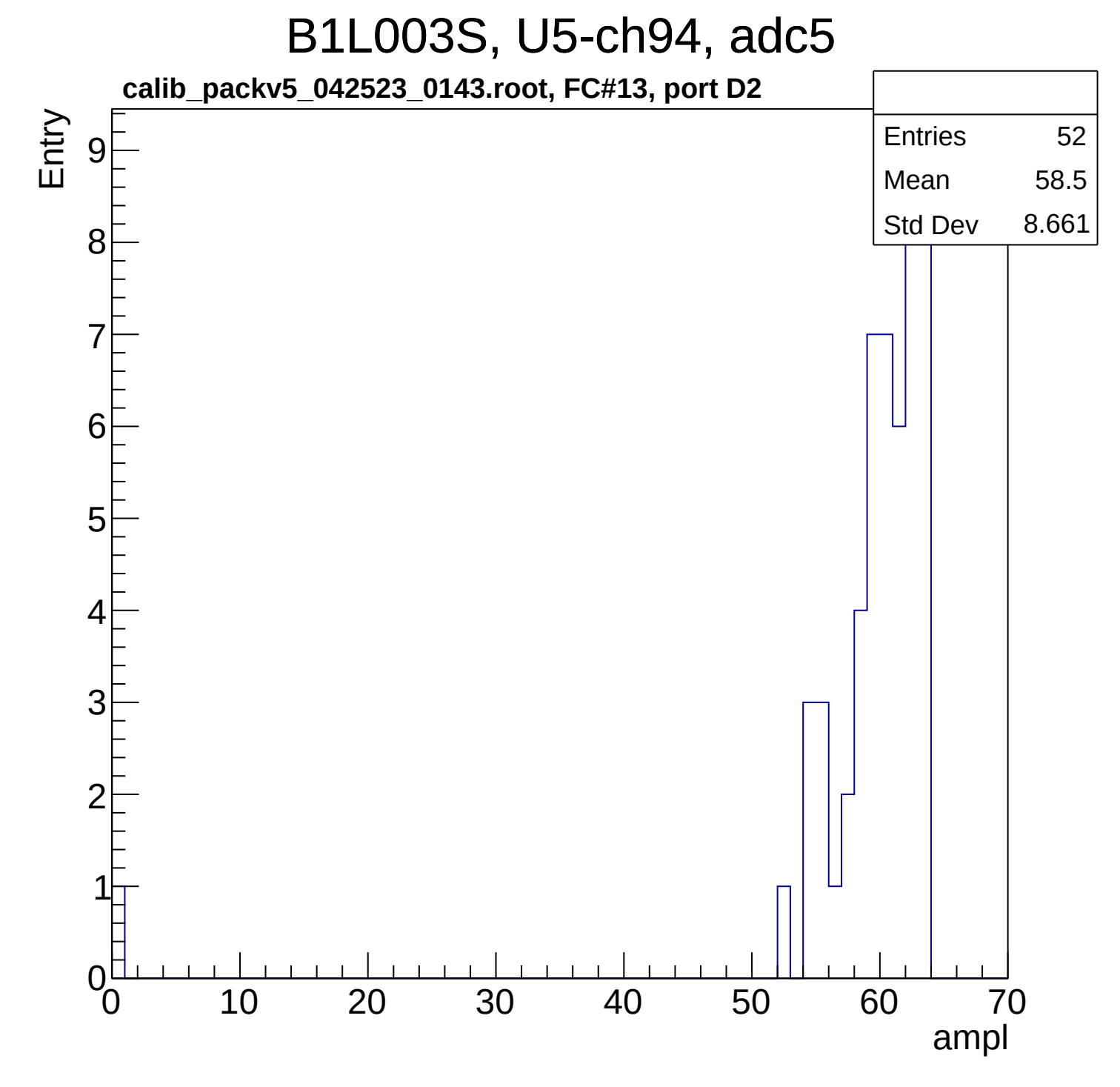
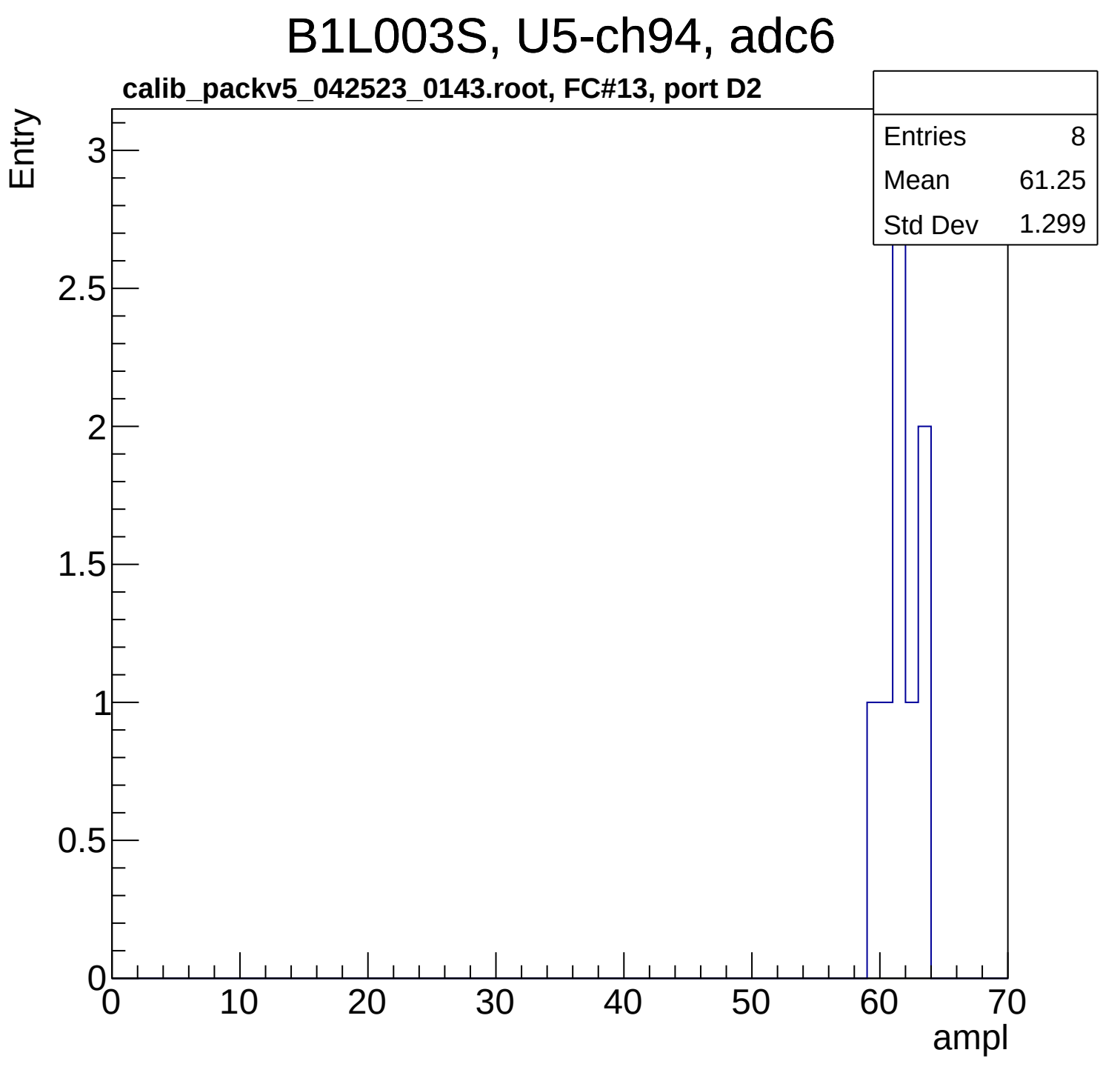
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.86
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.5
Std Dev 8.661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.25
Std Dev 1.299
Entry
ampl
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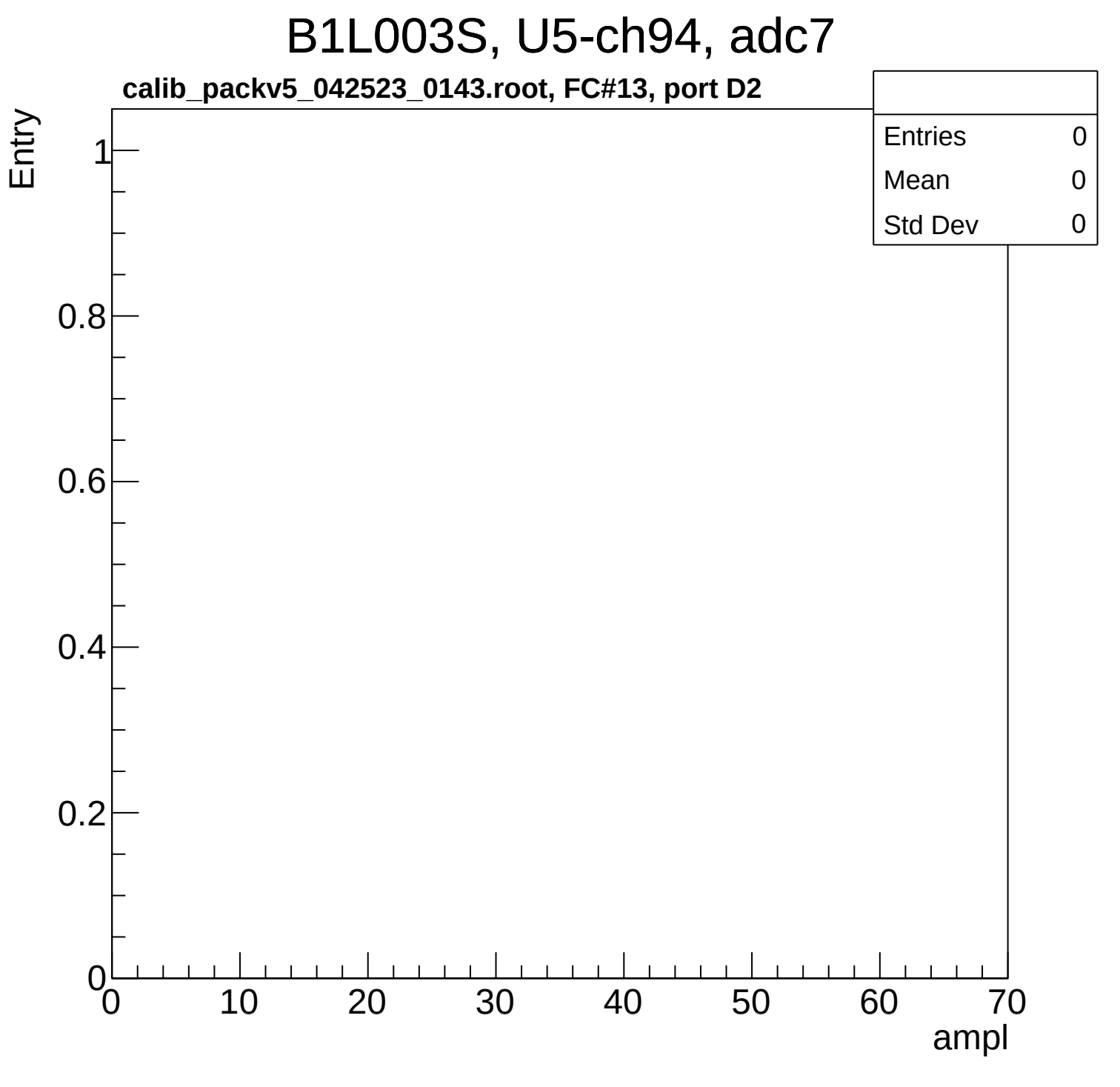
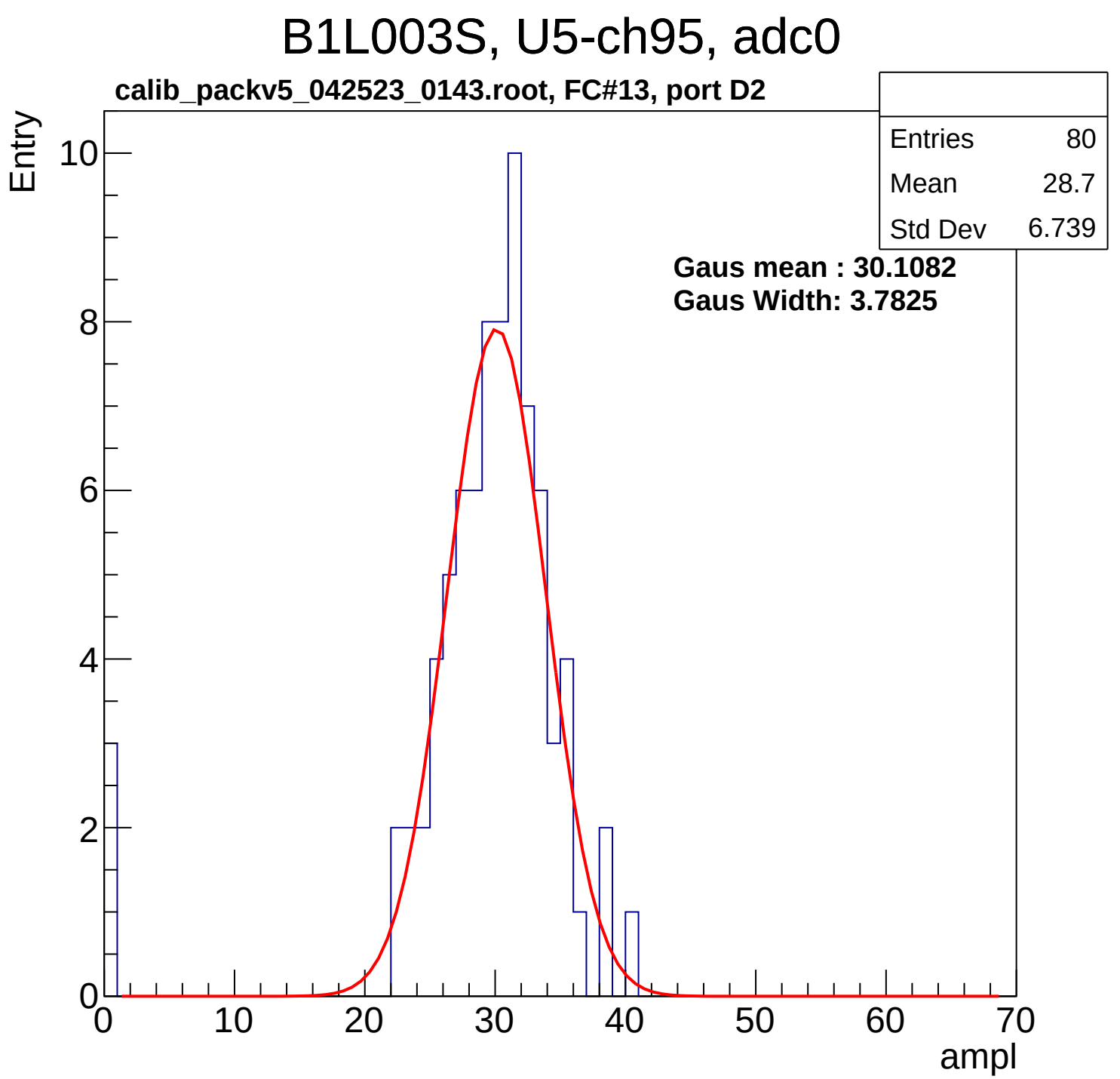
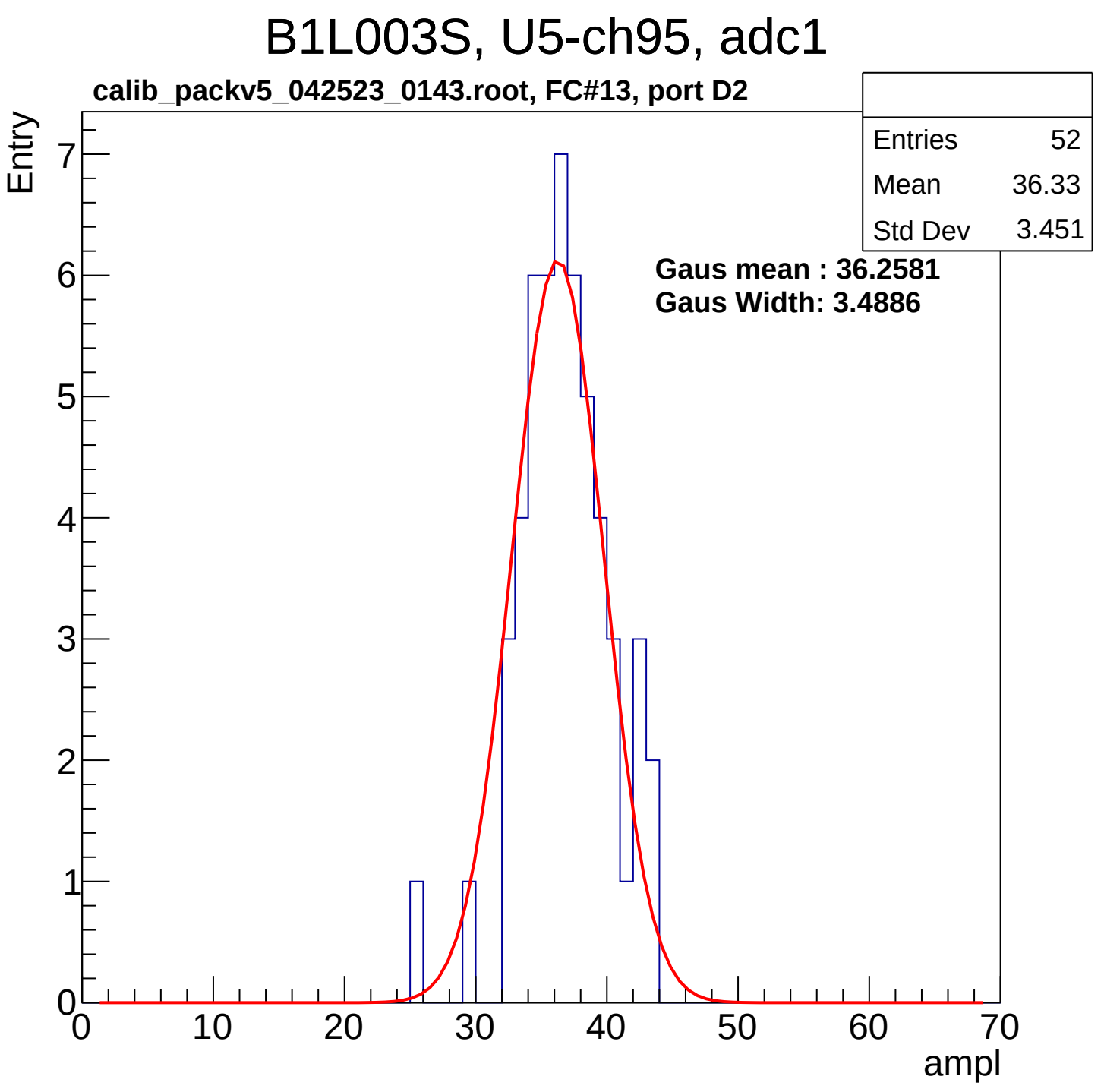
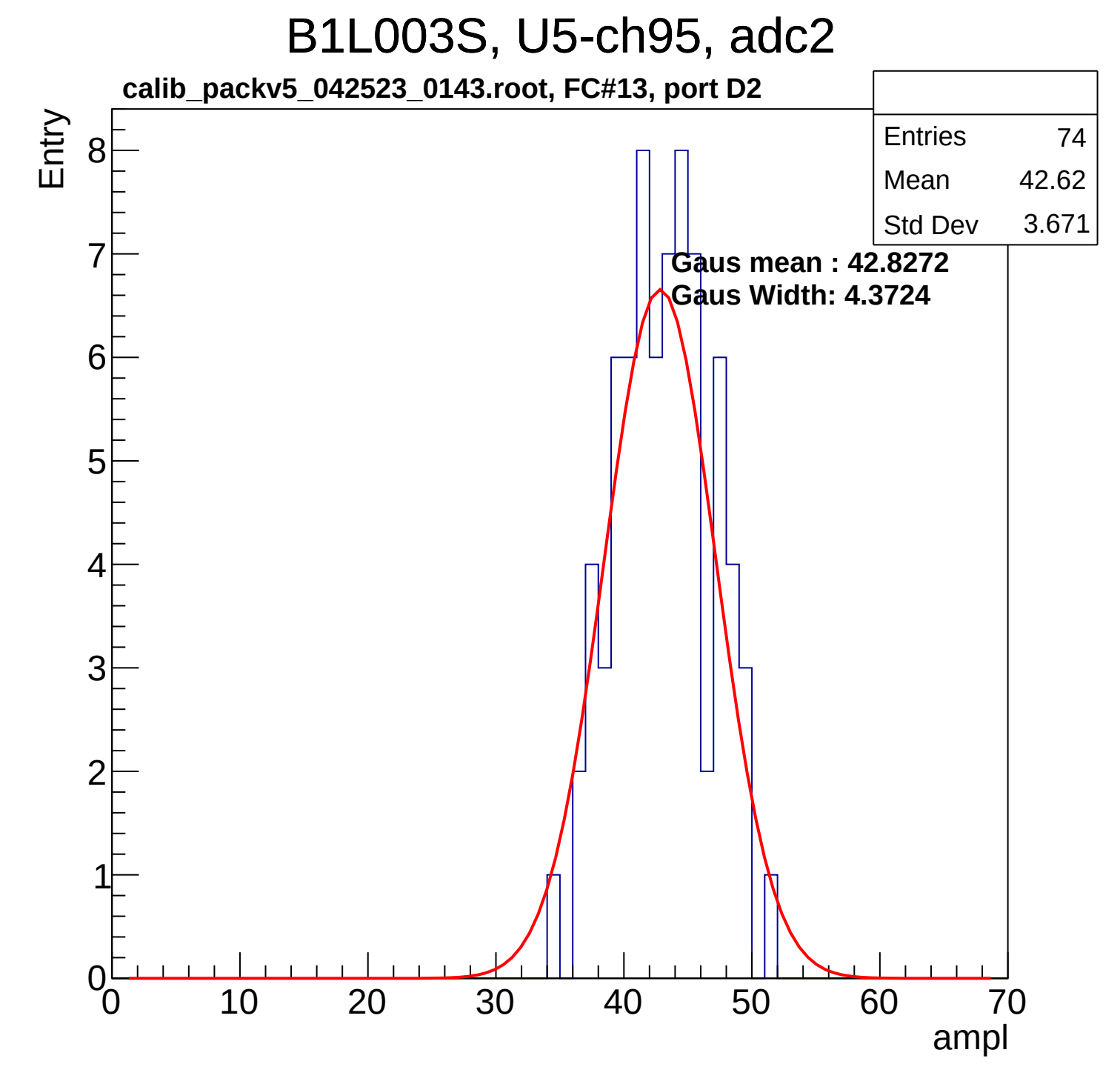
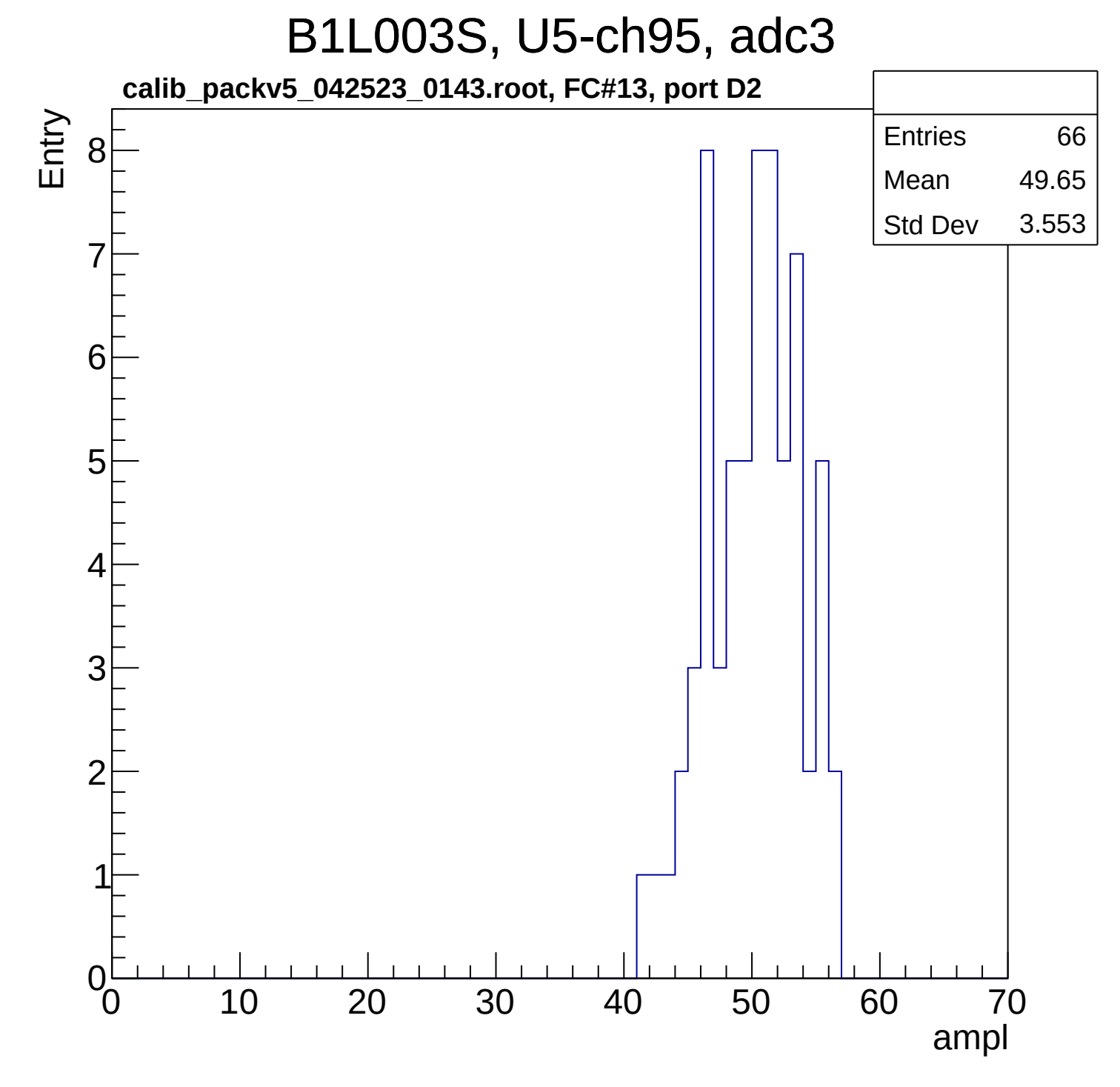
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

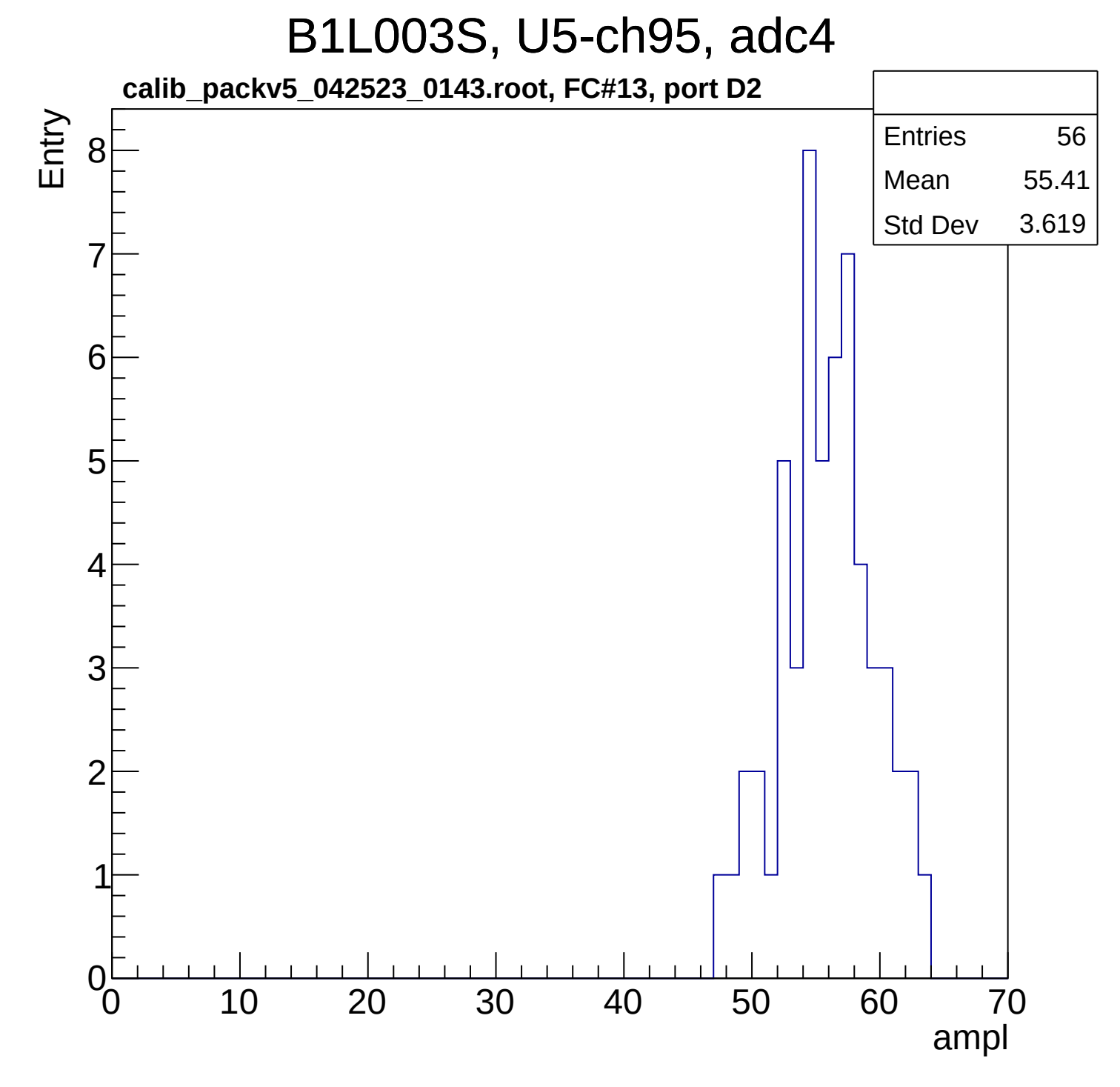

B1L003S, U5-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 28.7
Std Dev 6.739
Gaus mean : 30.1082
Gaus Width: 3.7825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 36.33
Std Dev 3.451
Gaus mean : 36.2581
Gaus Width: 3.4886
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.62
Std Dev 3.671
Gaus mean : 42.8272
Gaus Width: 4.3724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.65
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.41
Std Dev 3.619
Entry
ampl
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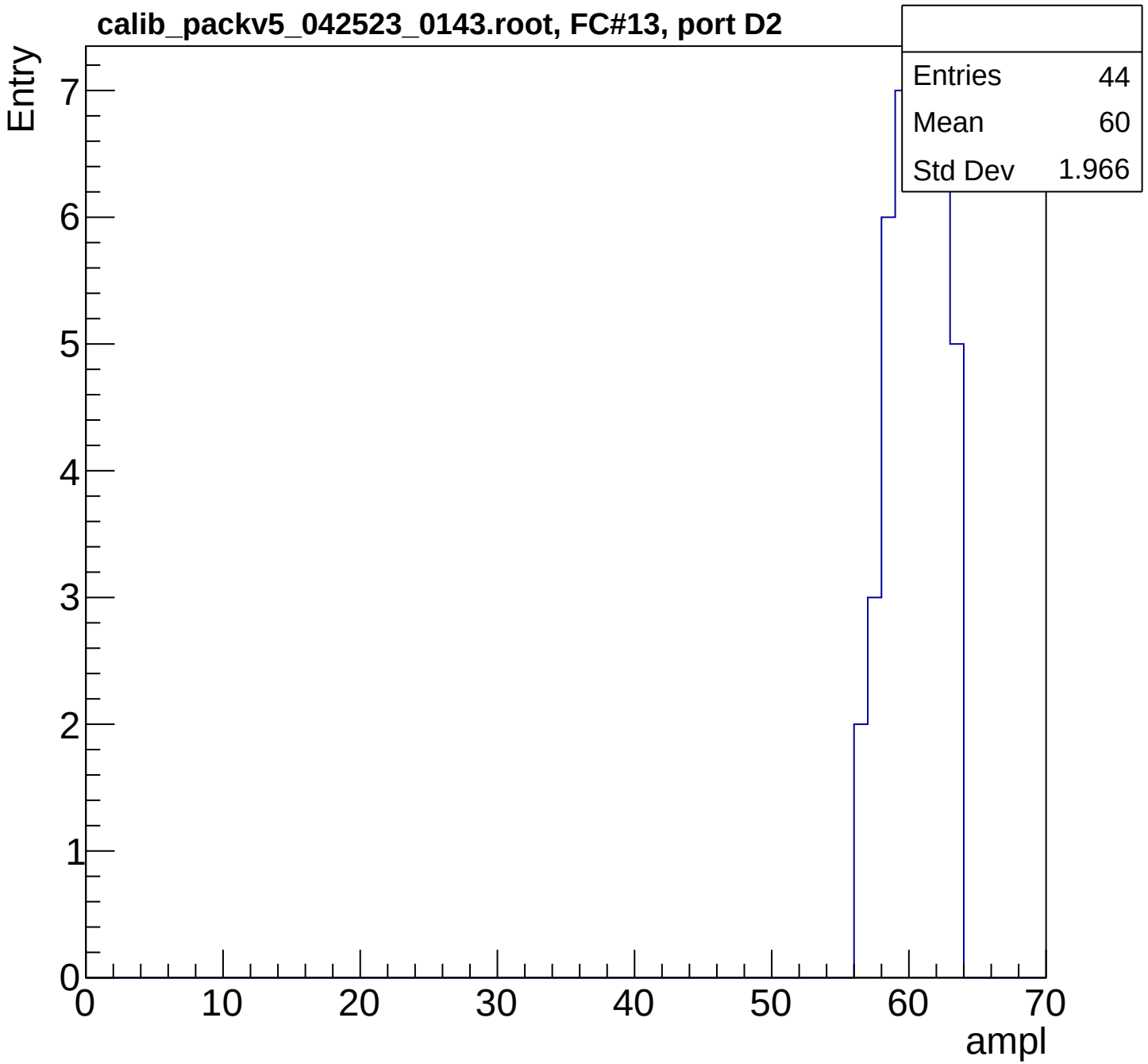
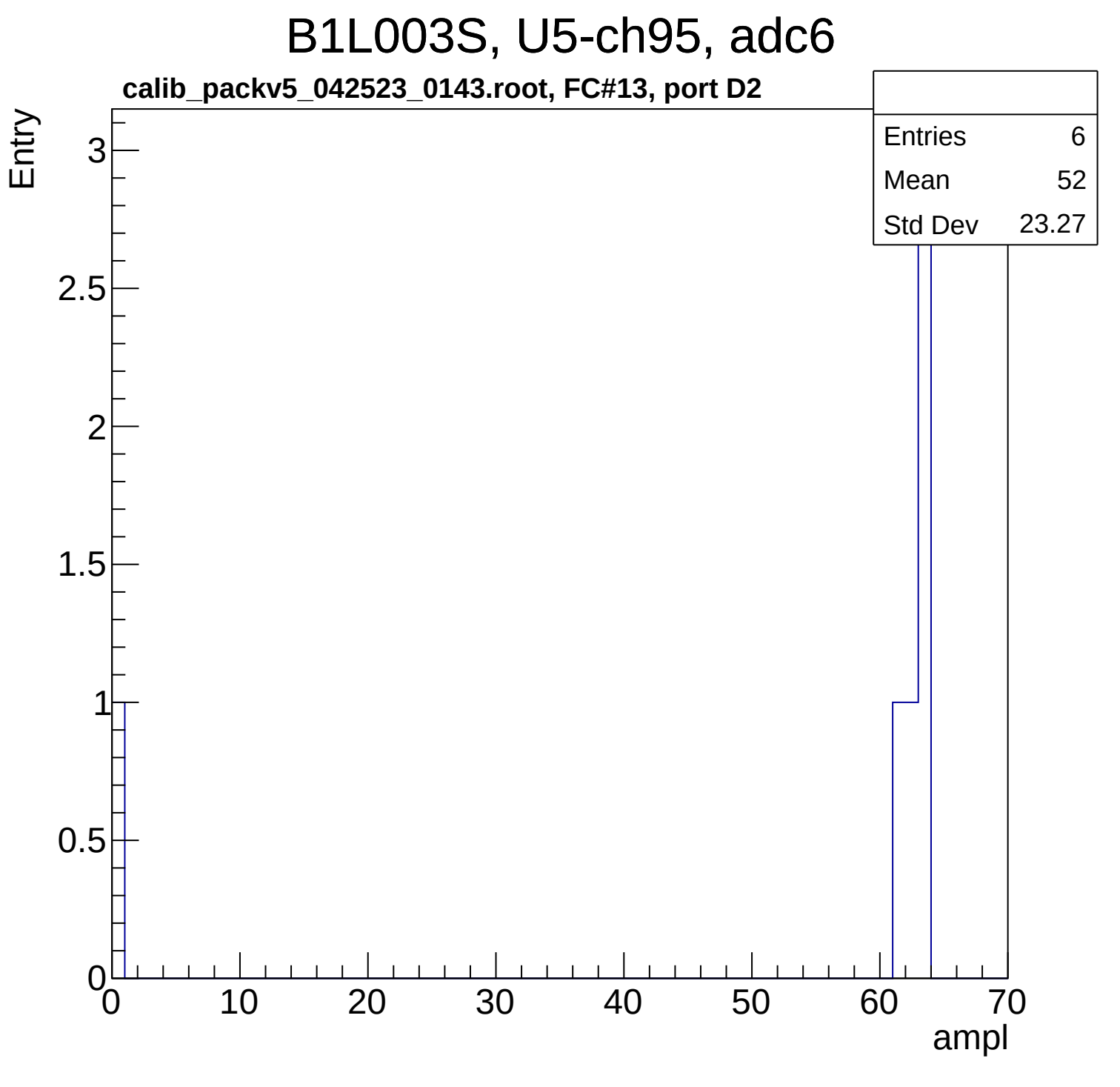
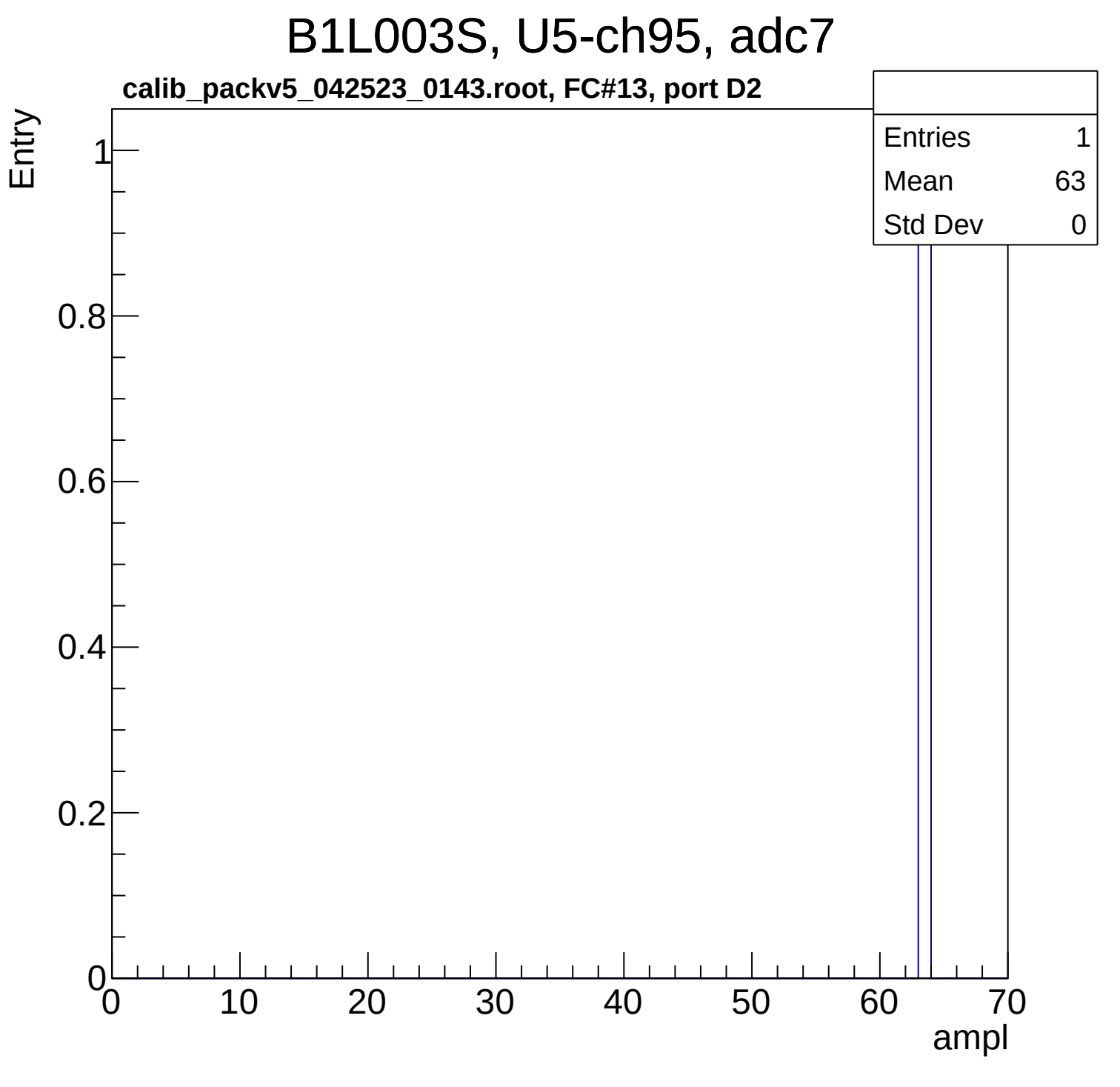
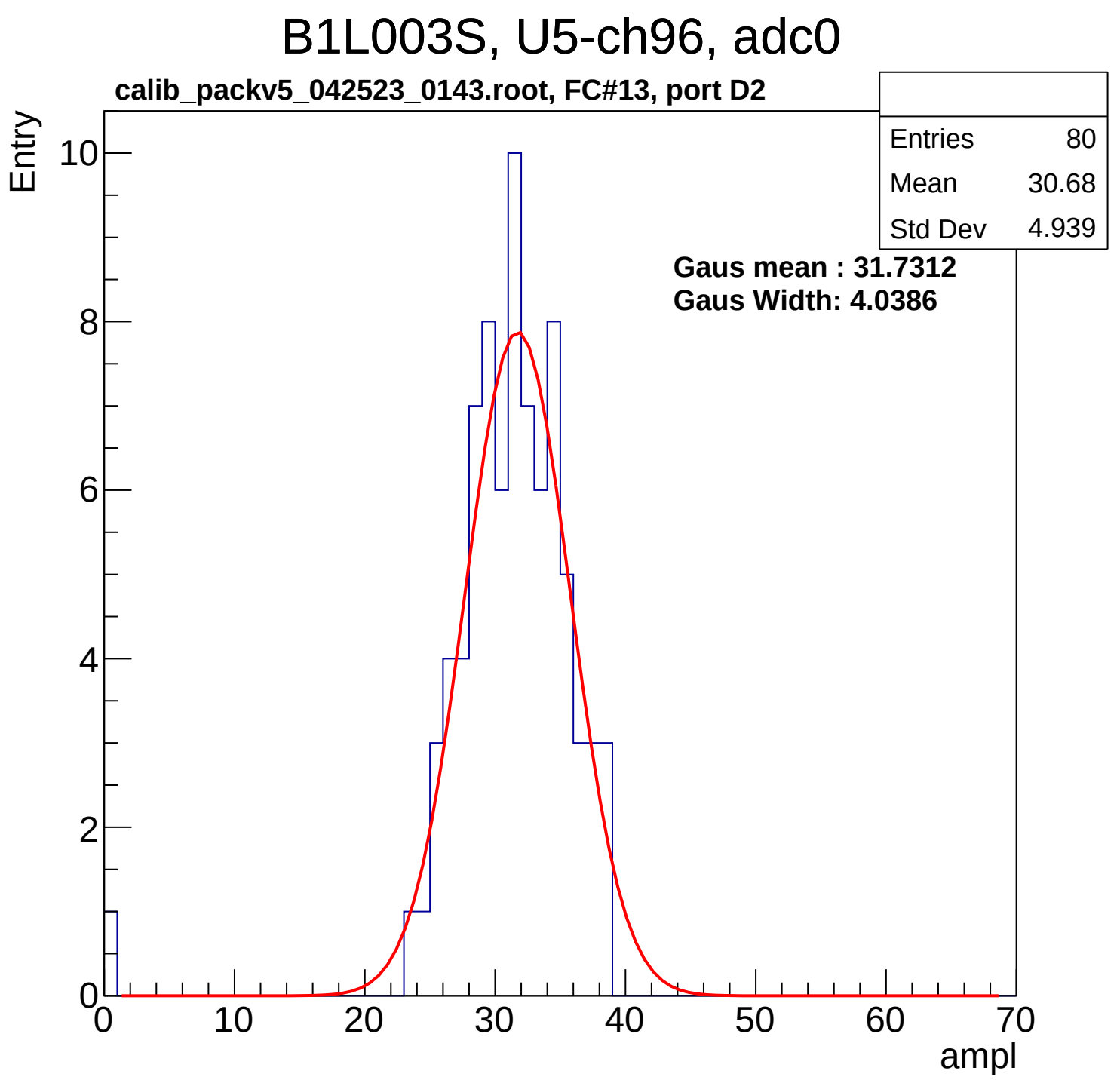
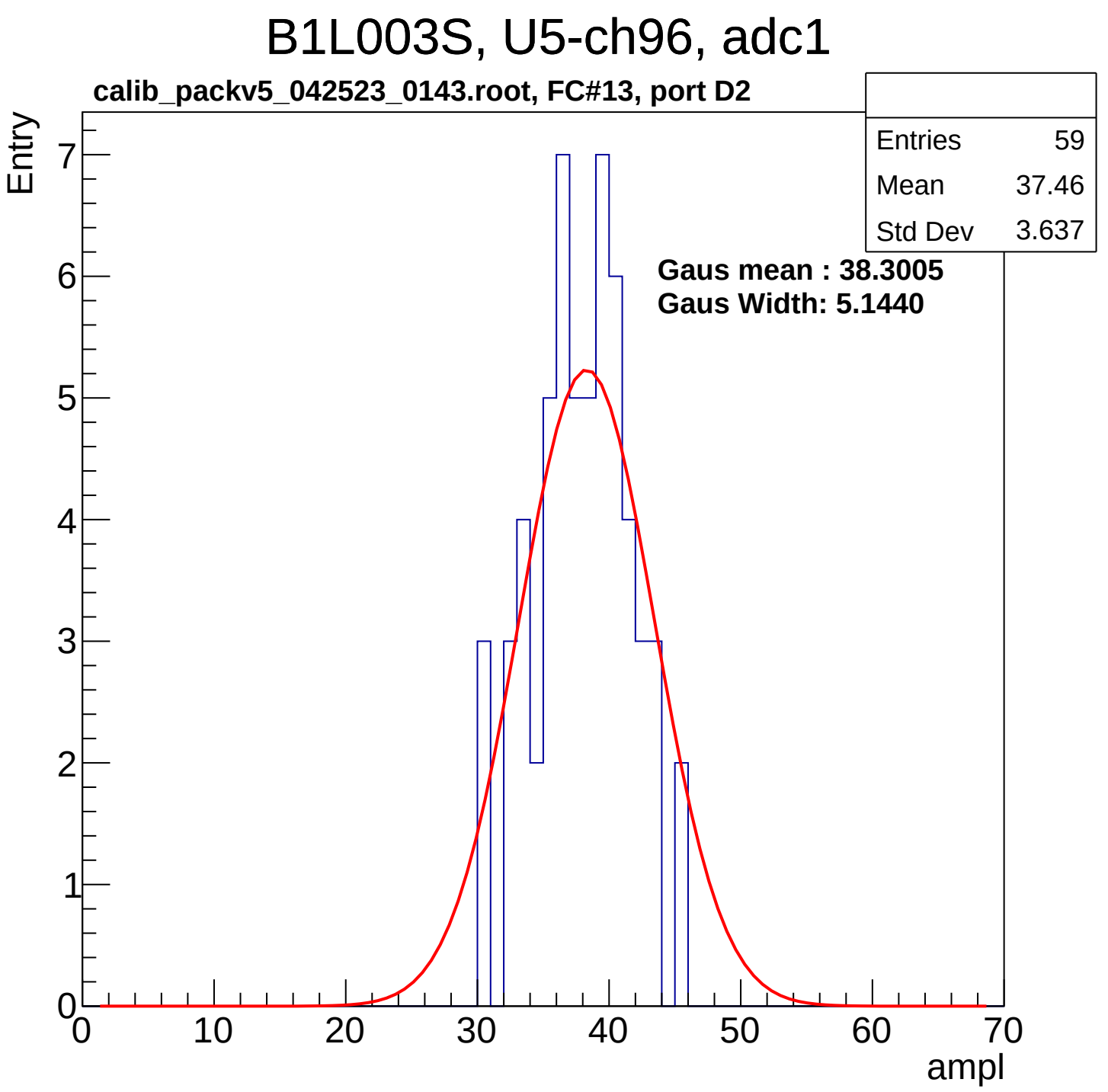
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

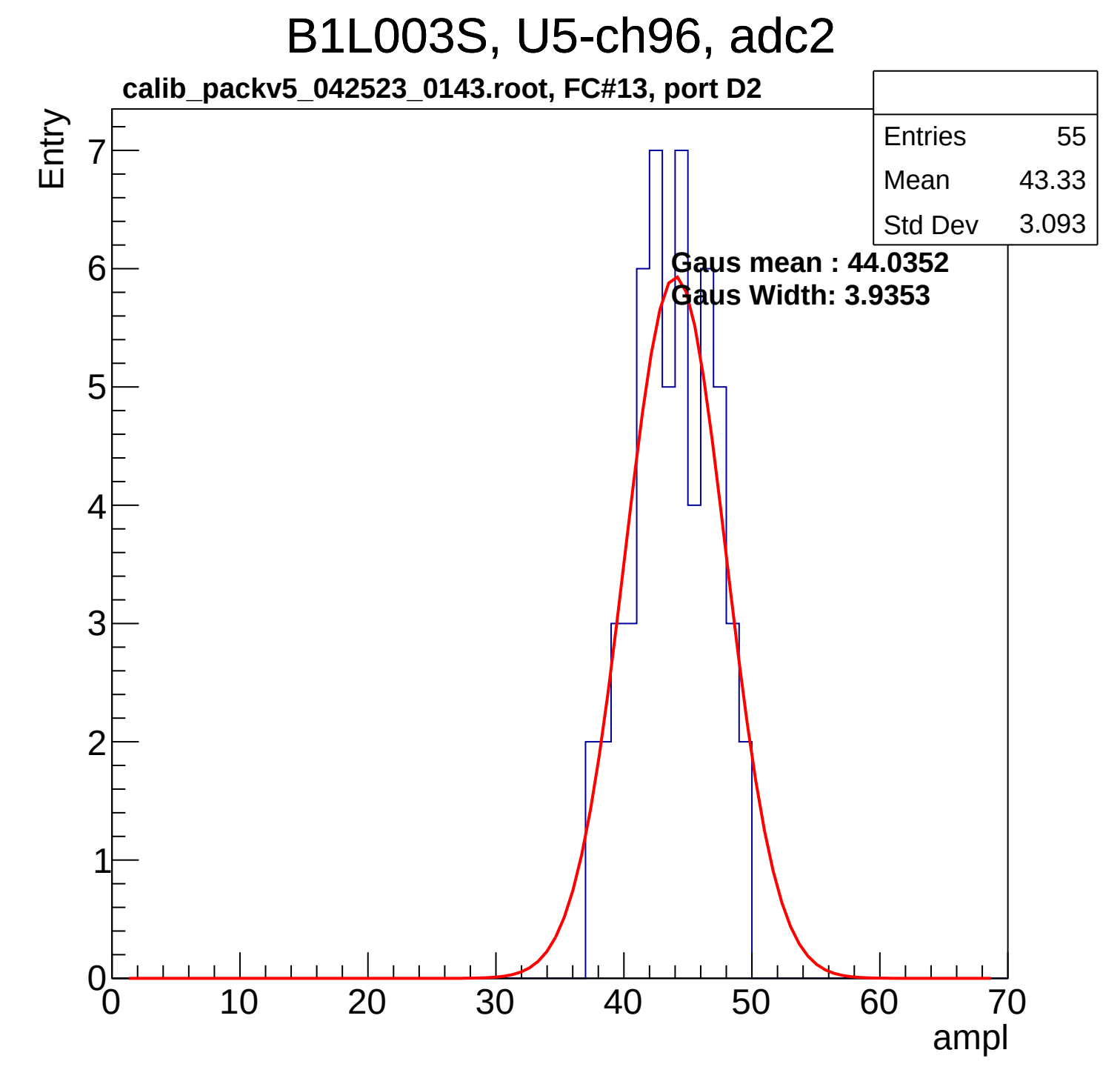

B1L003S, U5-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 60
Std Dev 1.966
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

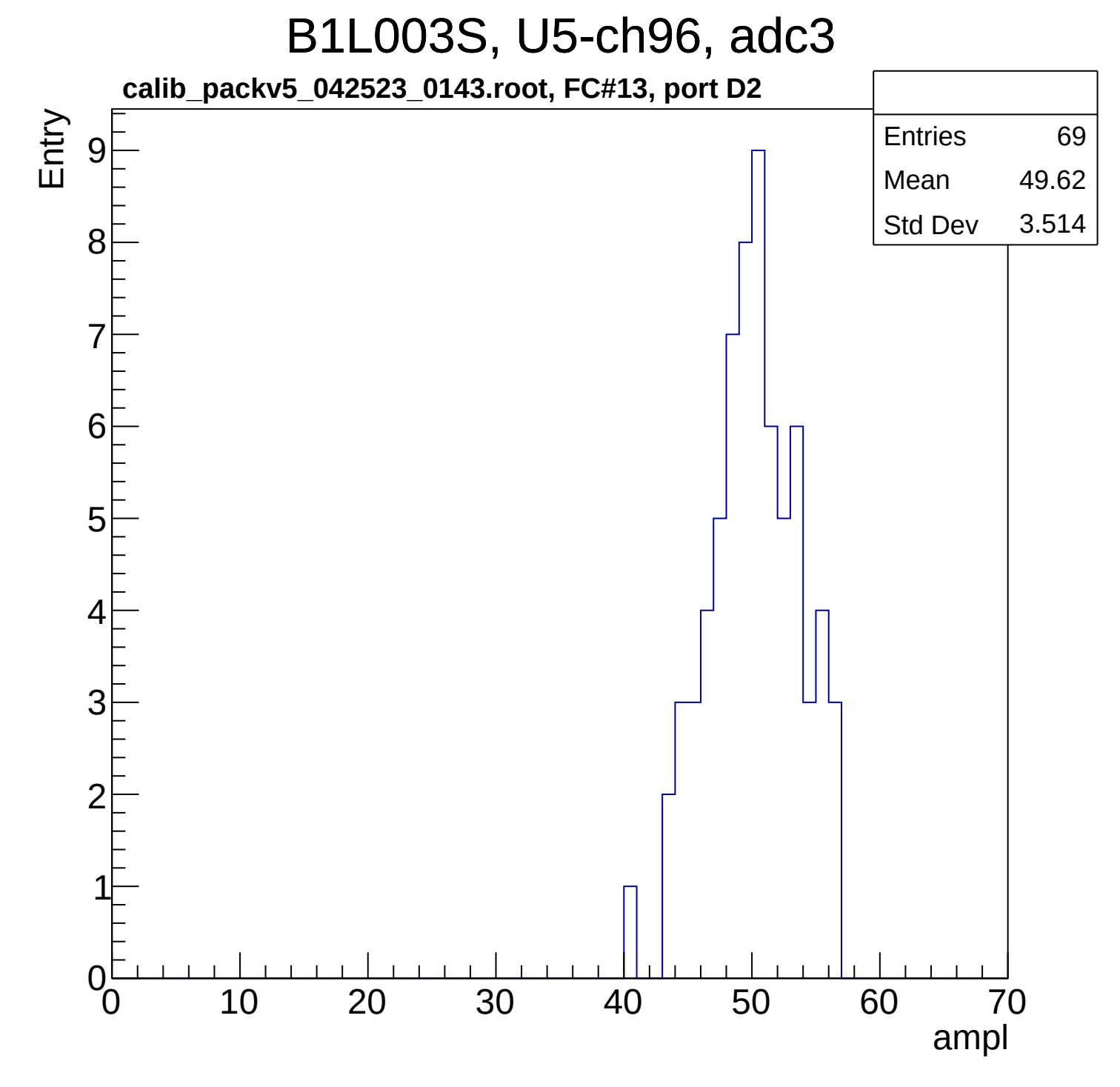

B1L003S, U5-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52
Std Dev 23.27
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 30.68
Std Dev 4.939
Gaus mean : 31.7312
Gaus Width: 4.0386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.46
Std Dev 3.637
Gaus mean : 38.3005
Gaus Width: 5.1440
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.33
Std Dev 3.093
Gaus mean : 44.0352
Gaus Width: 3.9353
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.62
Std Dev 3.514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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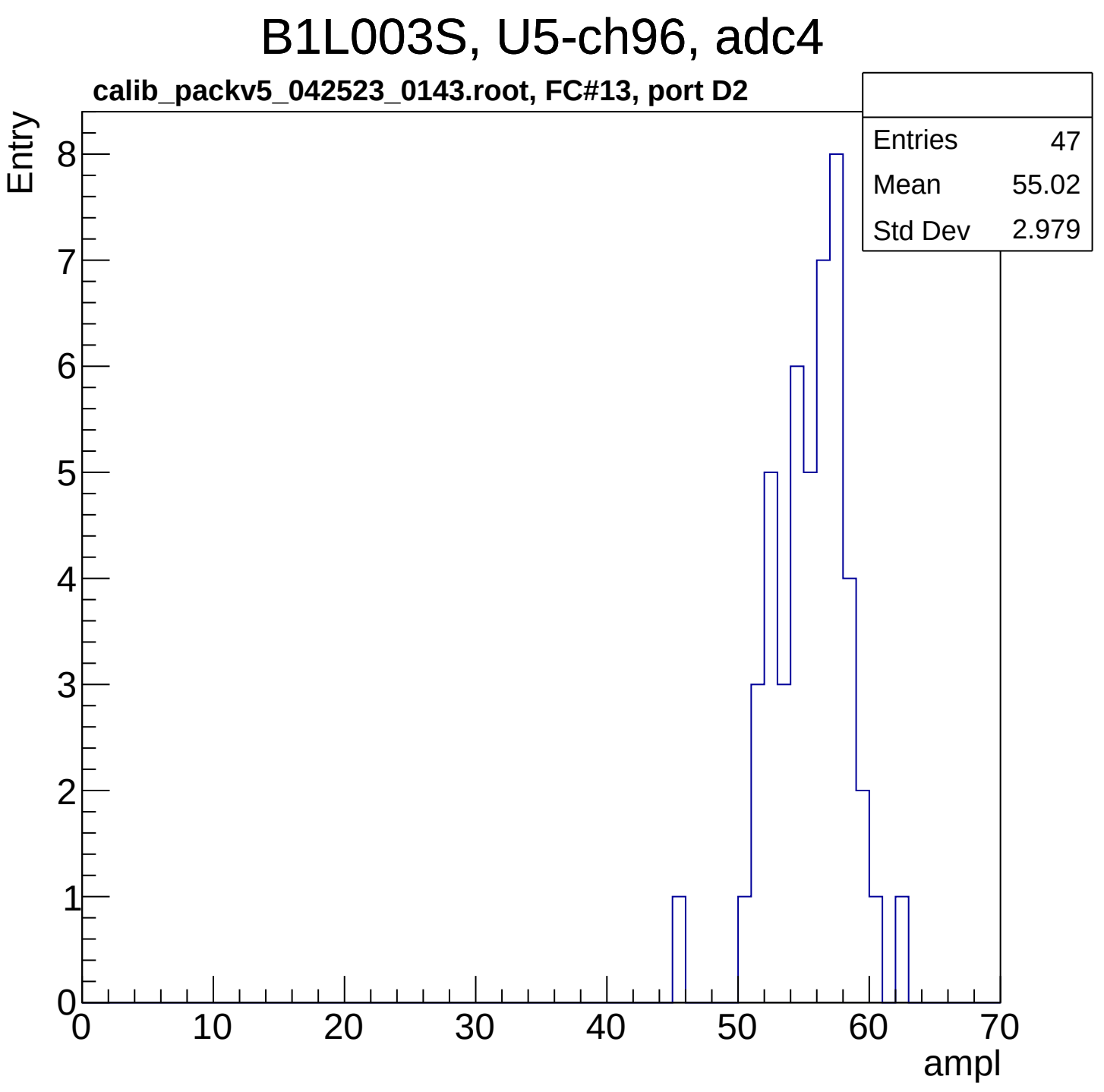
B1L003S, U5-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.02
Std Dev 2.979
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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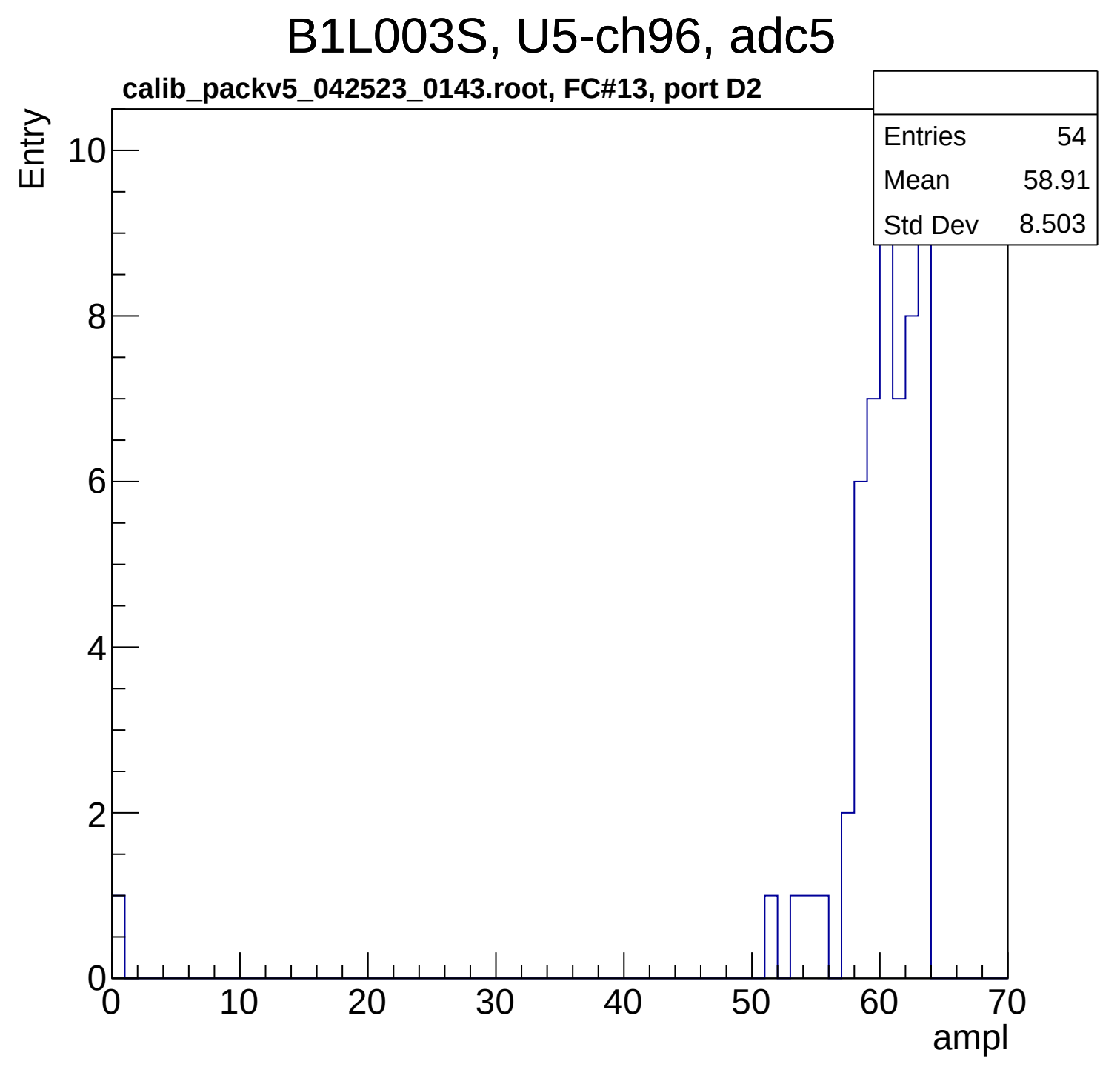
B1L003S, U5-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.91
Std Dev 8.503
Entry
ampl
0
2
4
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0
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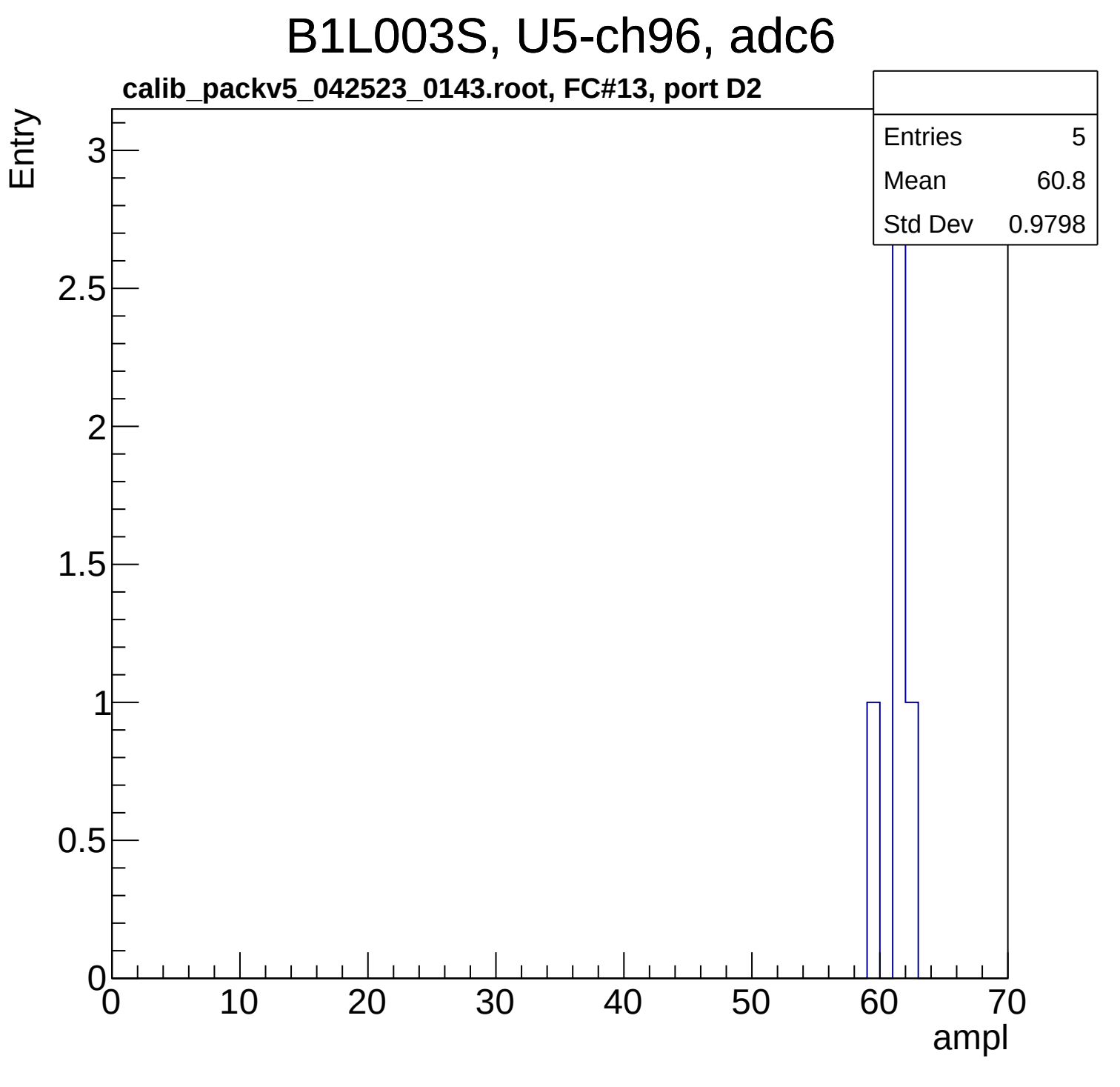
B1L003S, U5-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 60.8
Std Dev 0.9798
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
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40
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60
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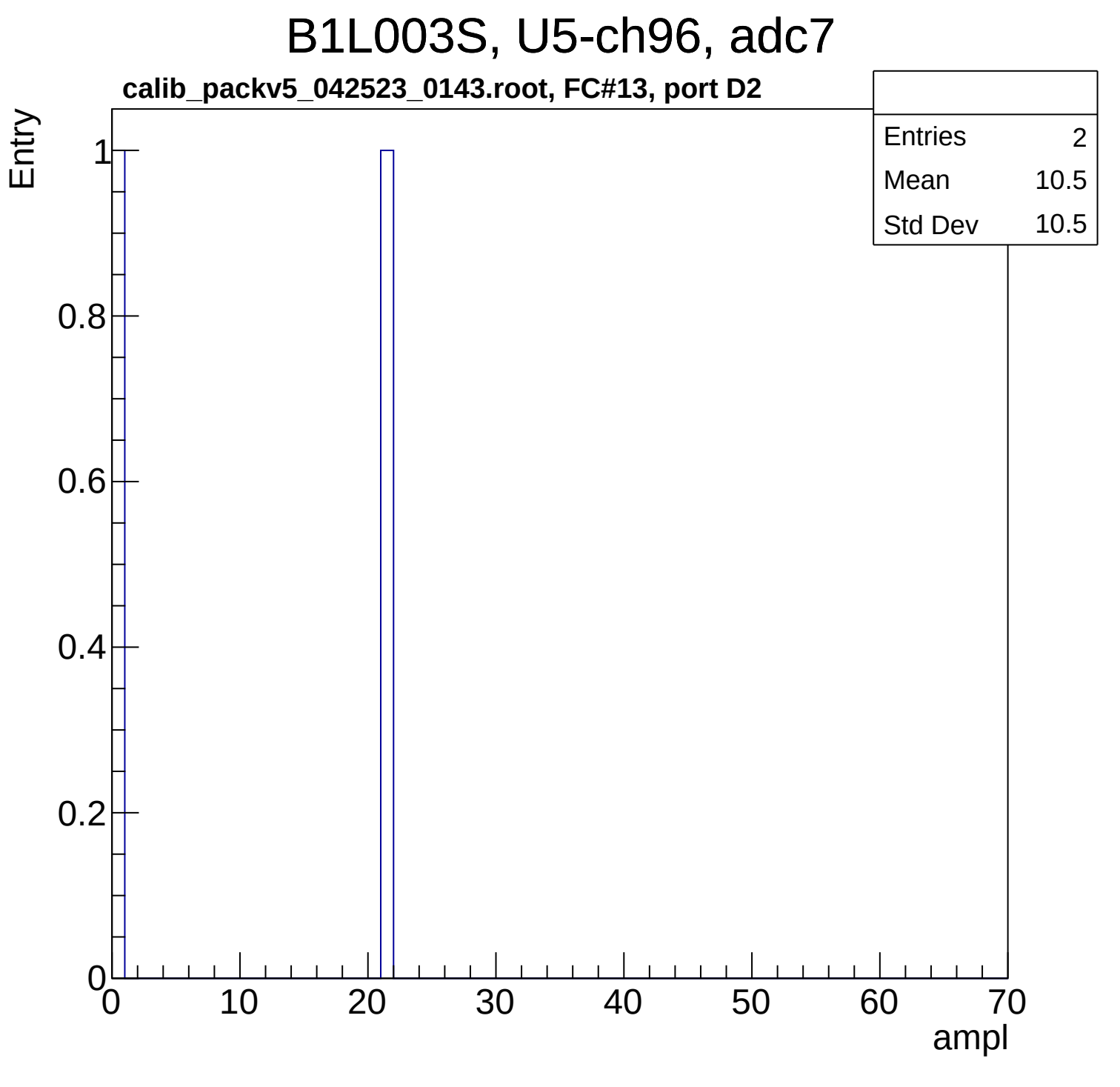
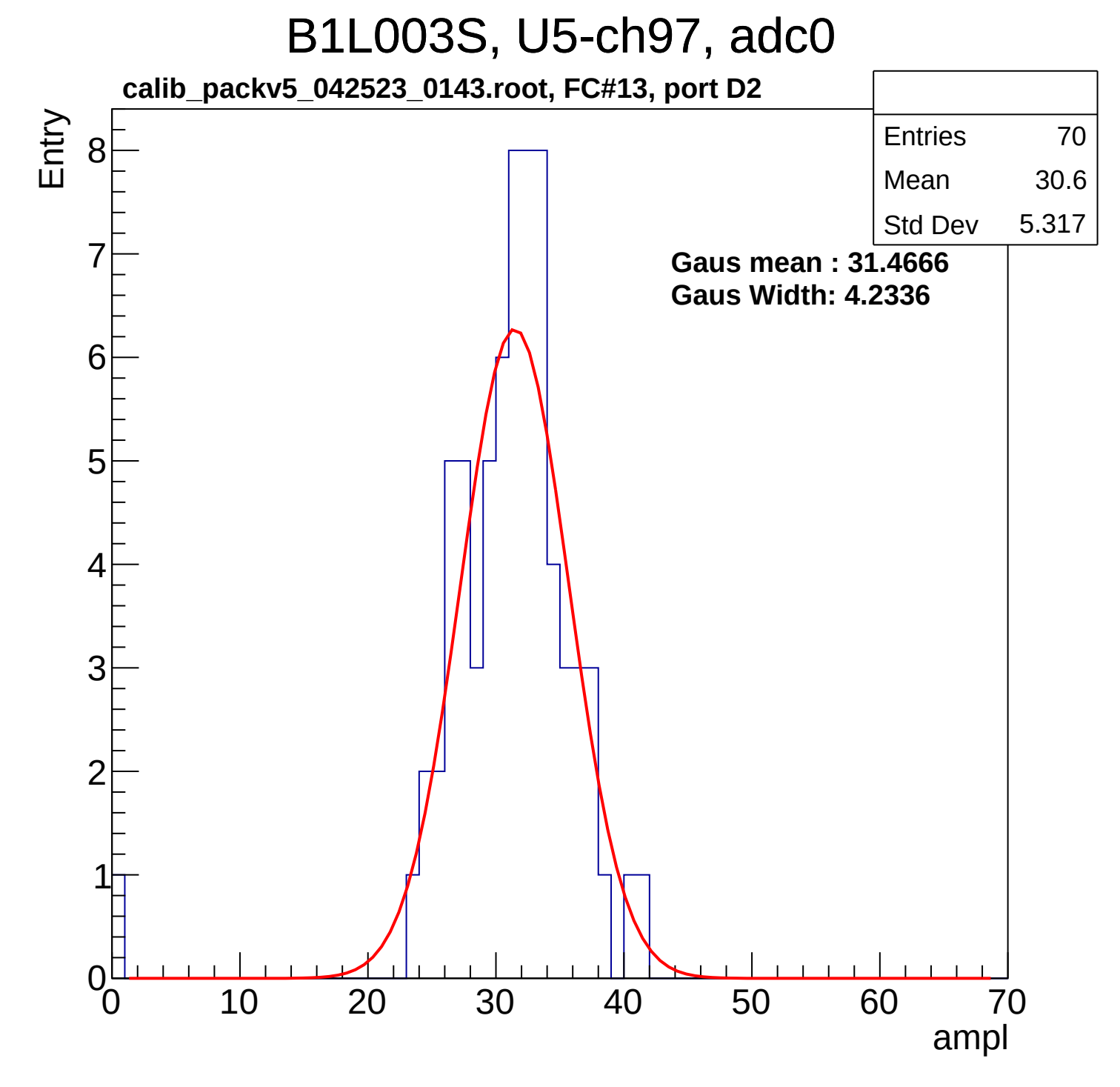

B1L003S, U5-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 30.6
Std Dev 5.317
Gaus mean : 31.4666
Gaus Width: 4.2336
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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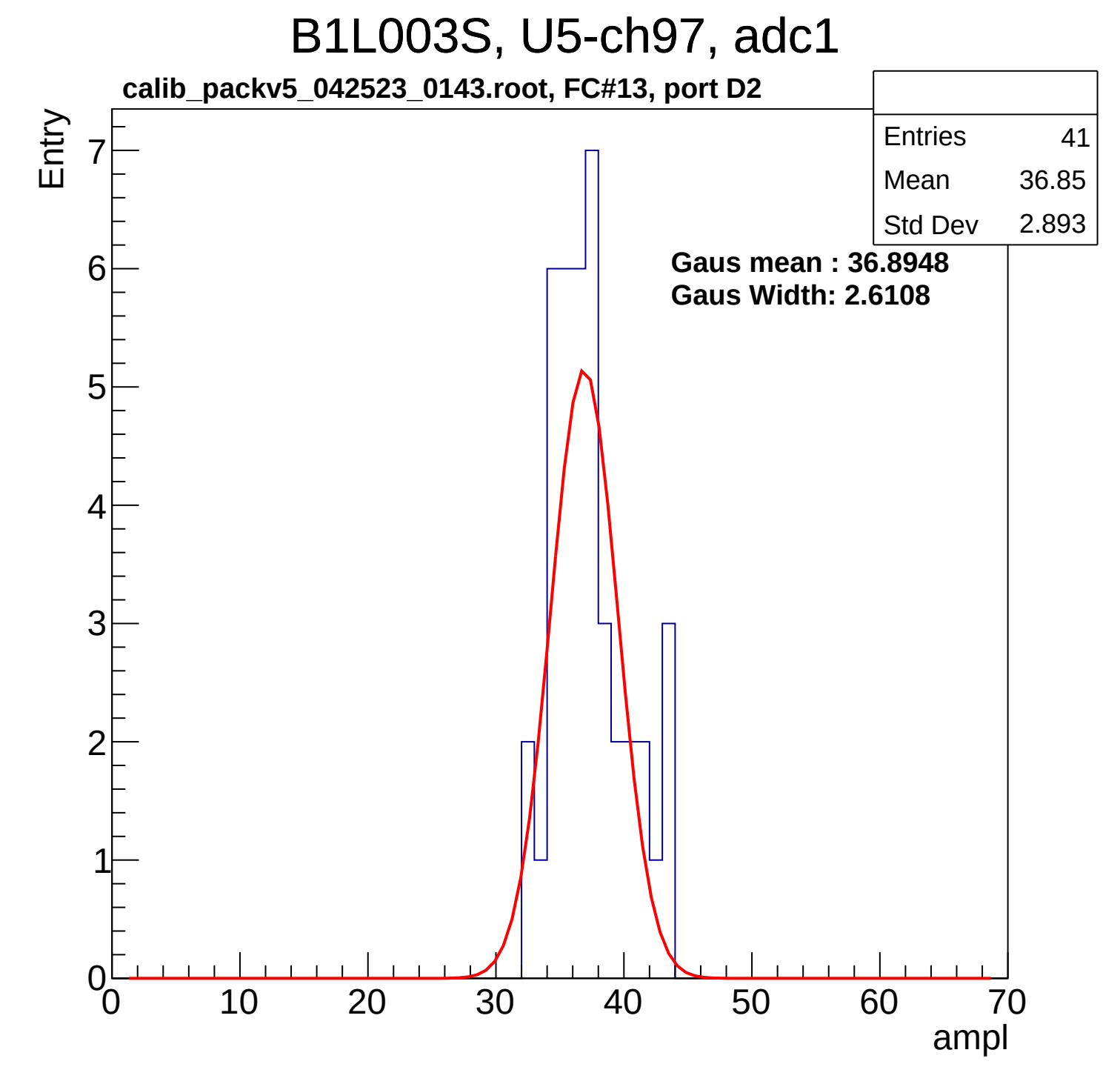
B1L003S, U5-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 36.85
Std Dev 2.893
Gaus mean : 36.8948
Gaus Width: 2.6108
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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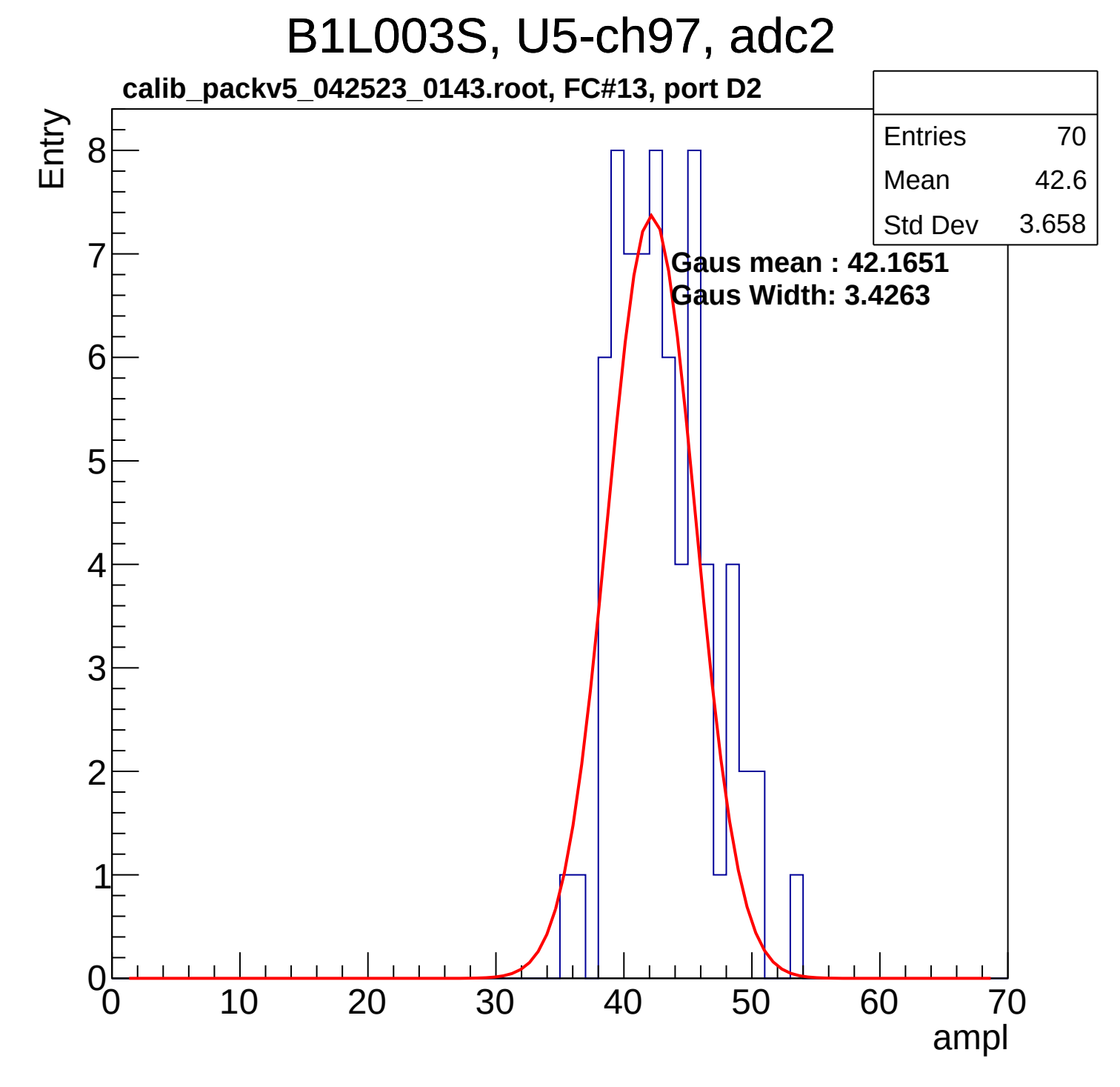
B1L003S, U5-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.6
Std Dev 3.658
Gaus mean : 42.1651
Gaus Width: 3.4263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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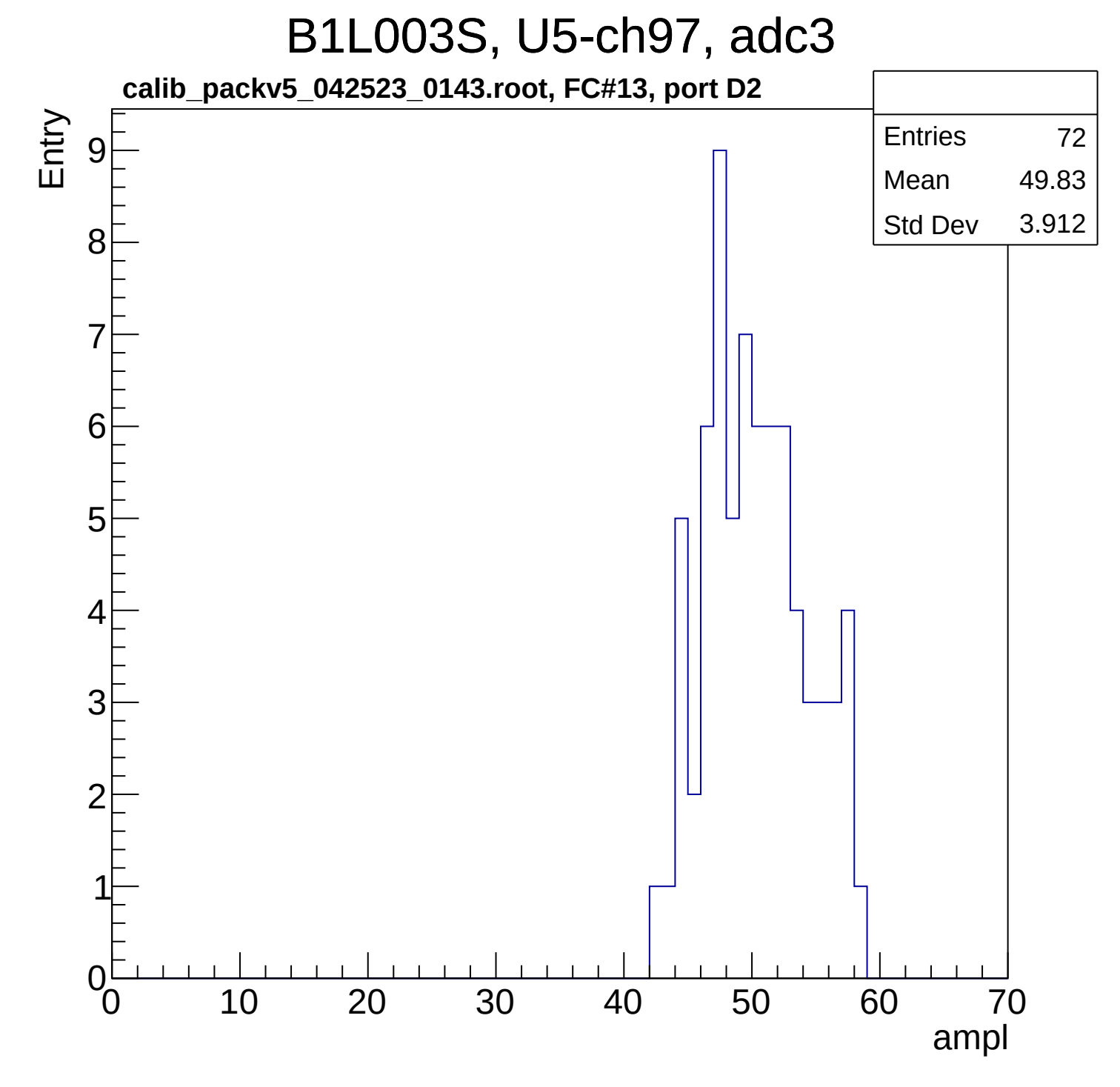
B1L003S, U5-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.83
Std Dev 3.912
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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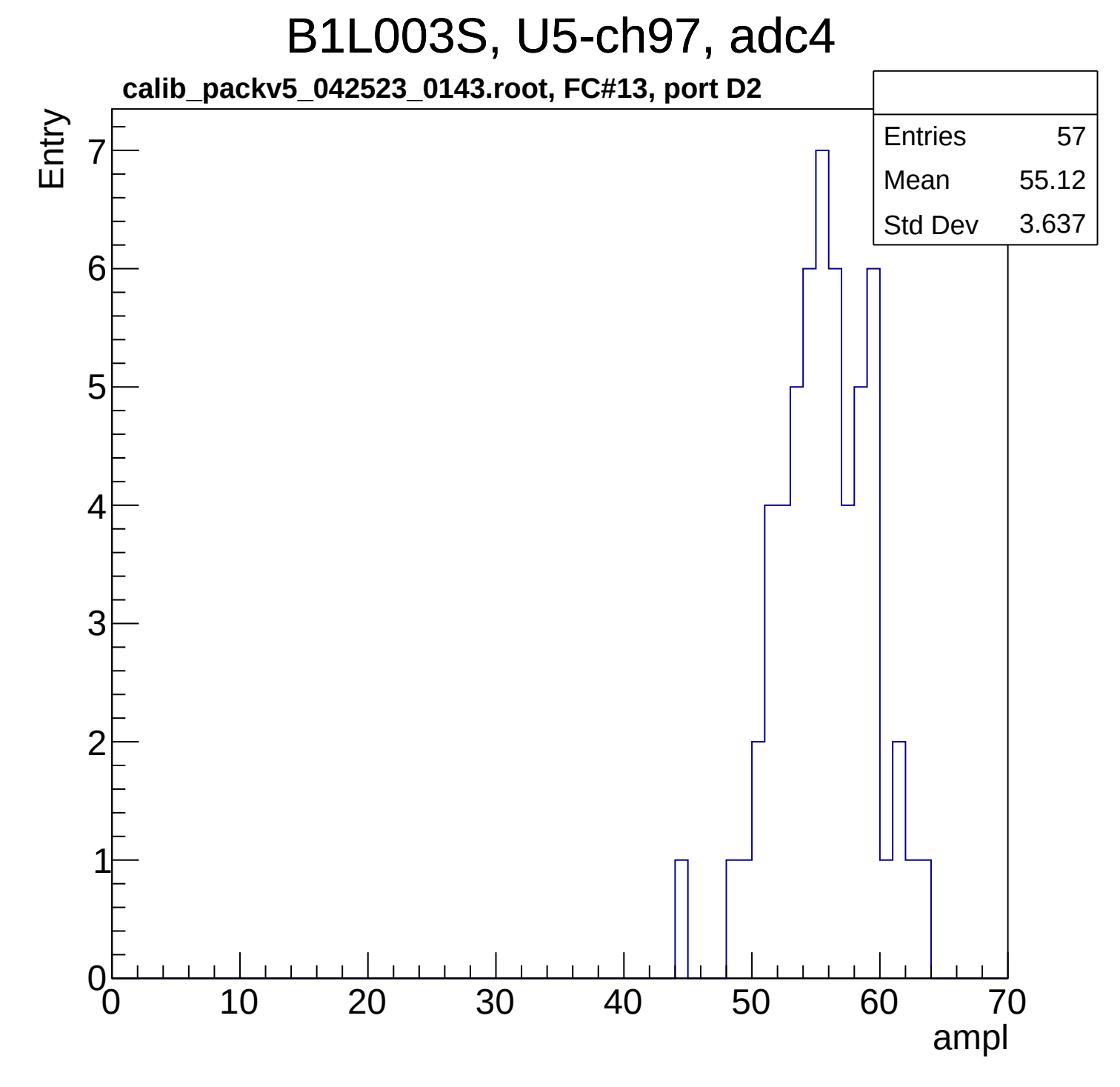
B1L003S, U5-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.12
Std Dev 3.637
Entry
ampl
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2
3
4
5
6
7
0
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20
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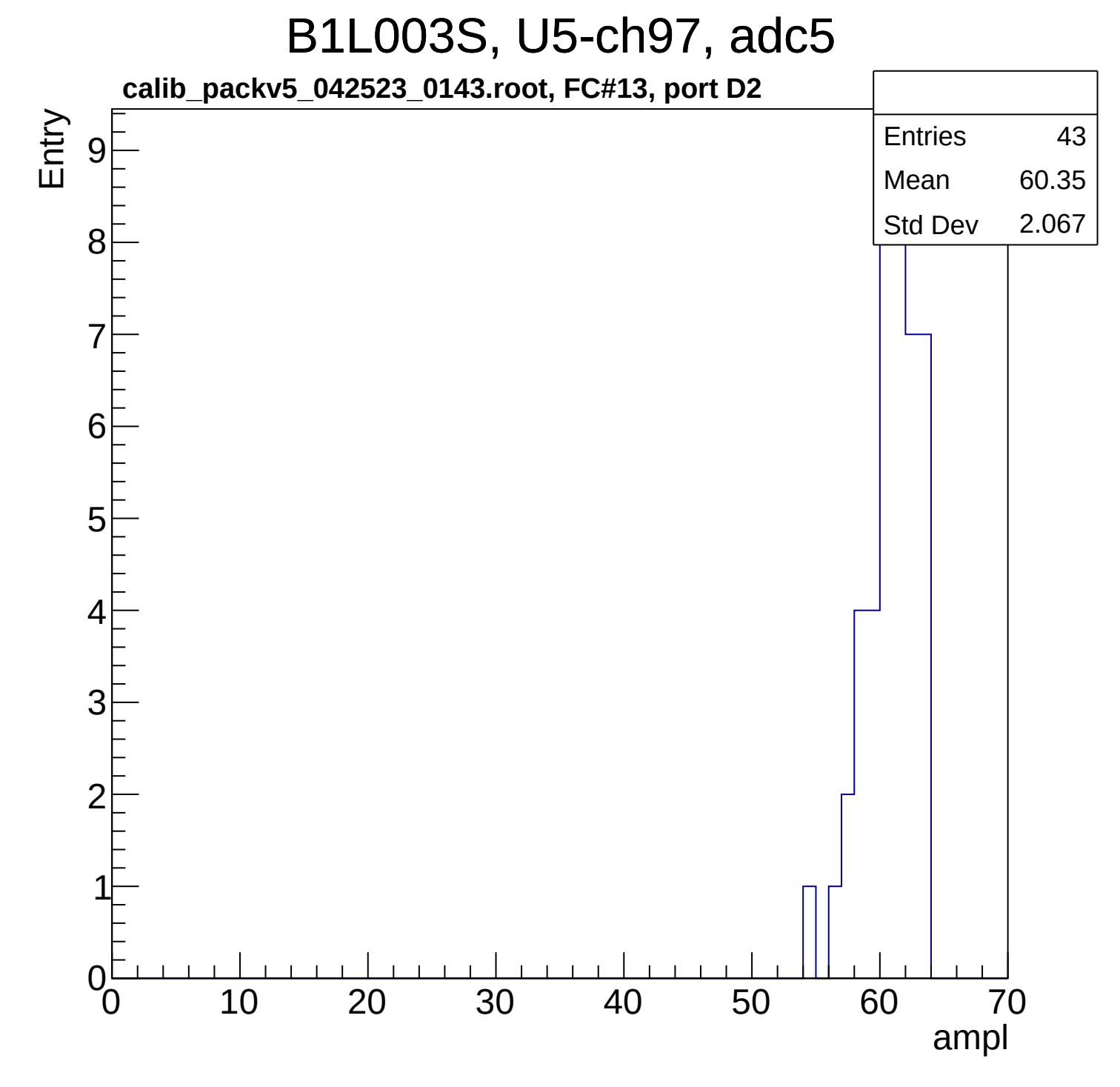
40
50
60
70


B1L003S, U5-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 60.35
Std Dev 2.067
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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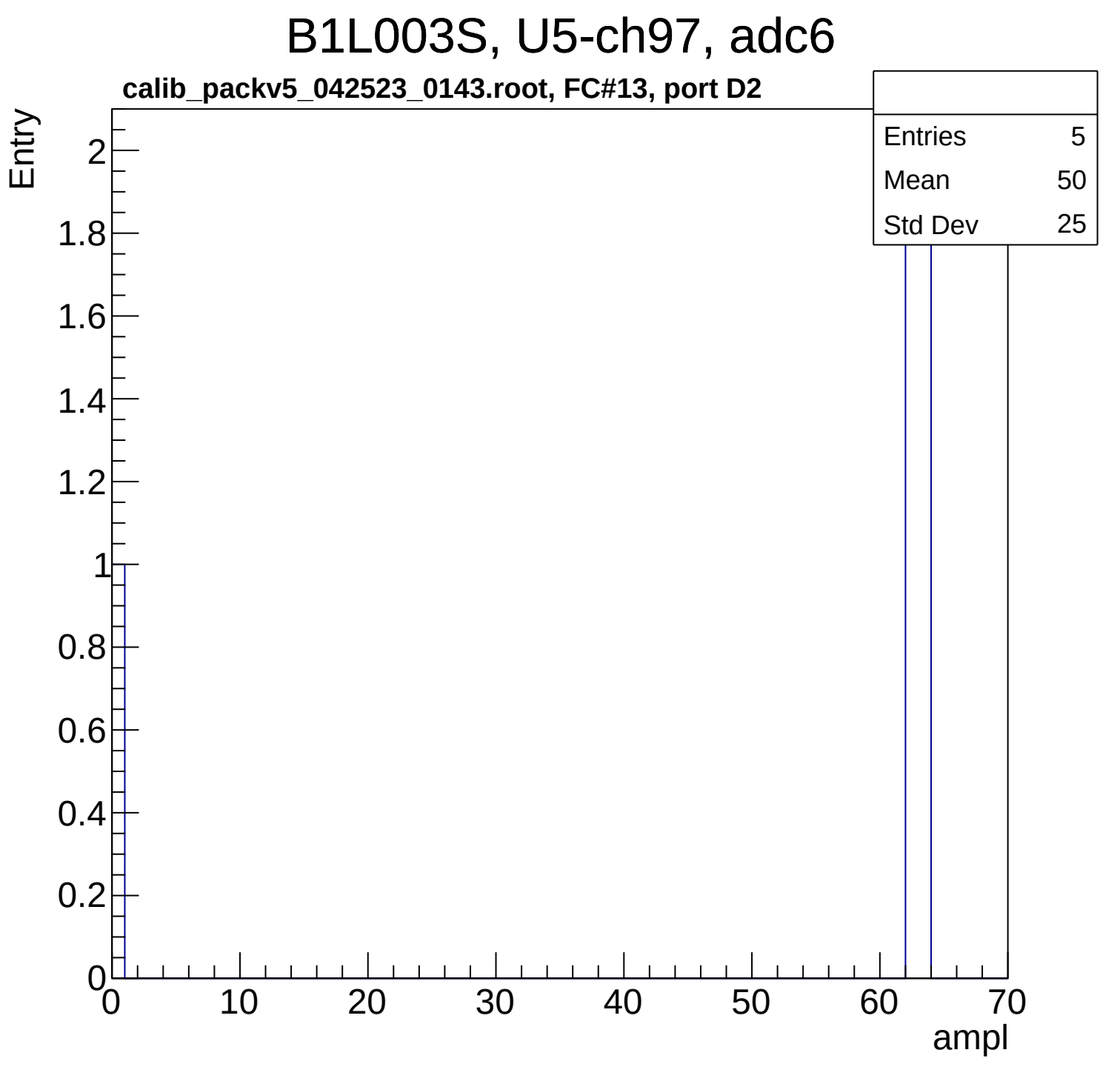
B1L003S, U5-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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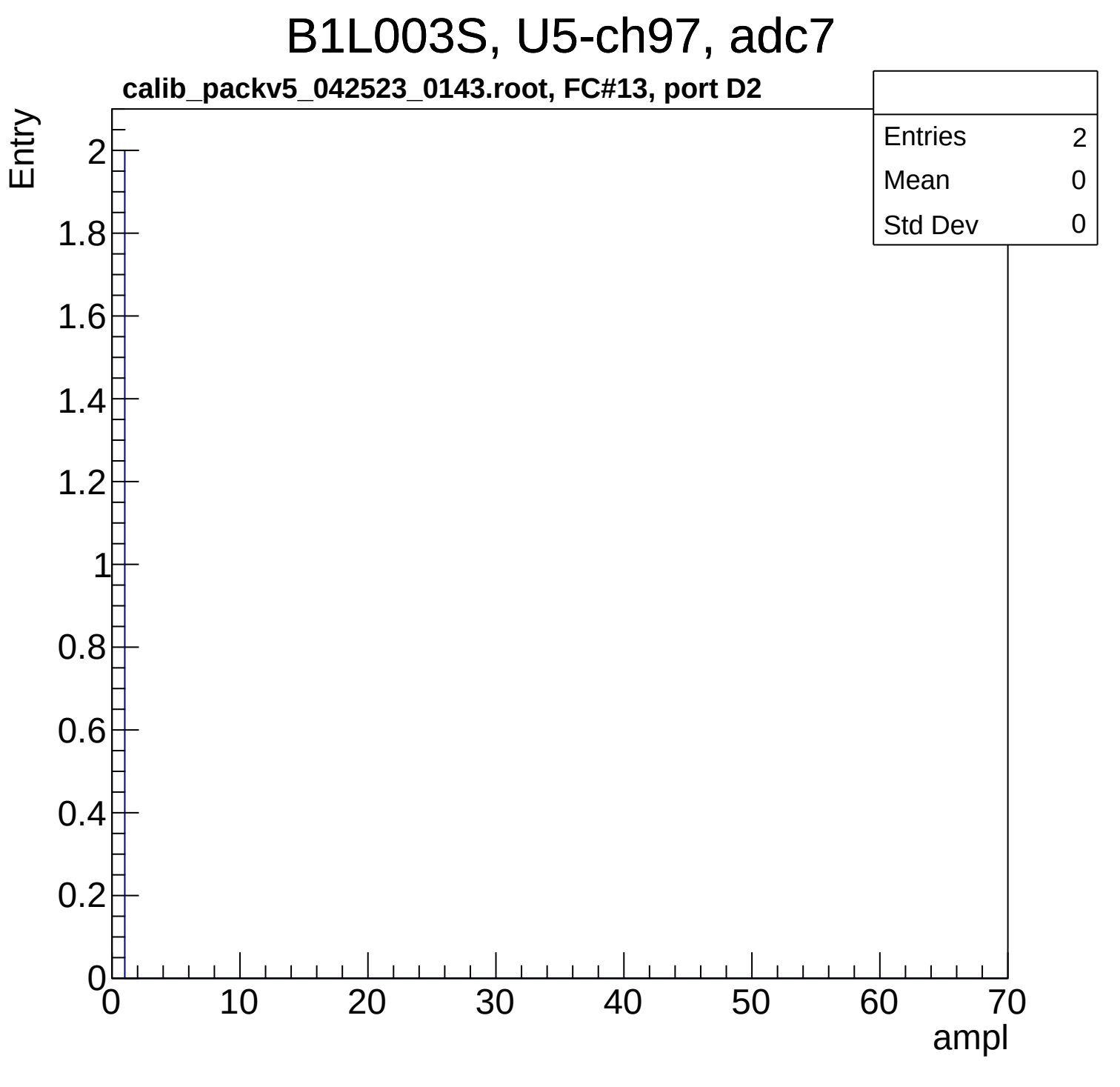
B1L003S, U5-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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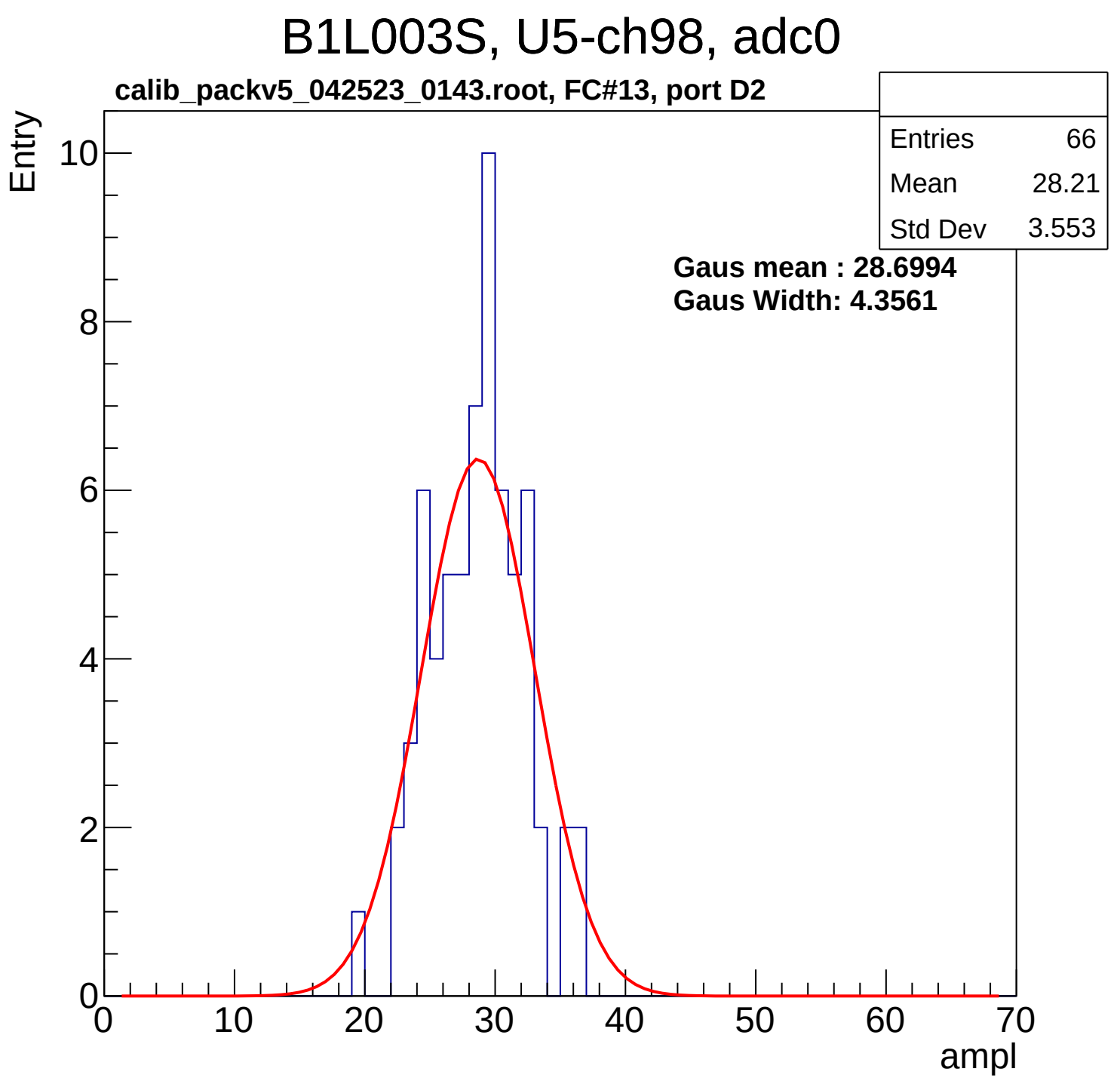
B1L003S, U5-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.21
Std Dev 3.553
Gaus mean : 28.6994
Gaus Width: 4.3561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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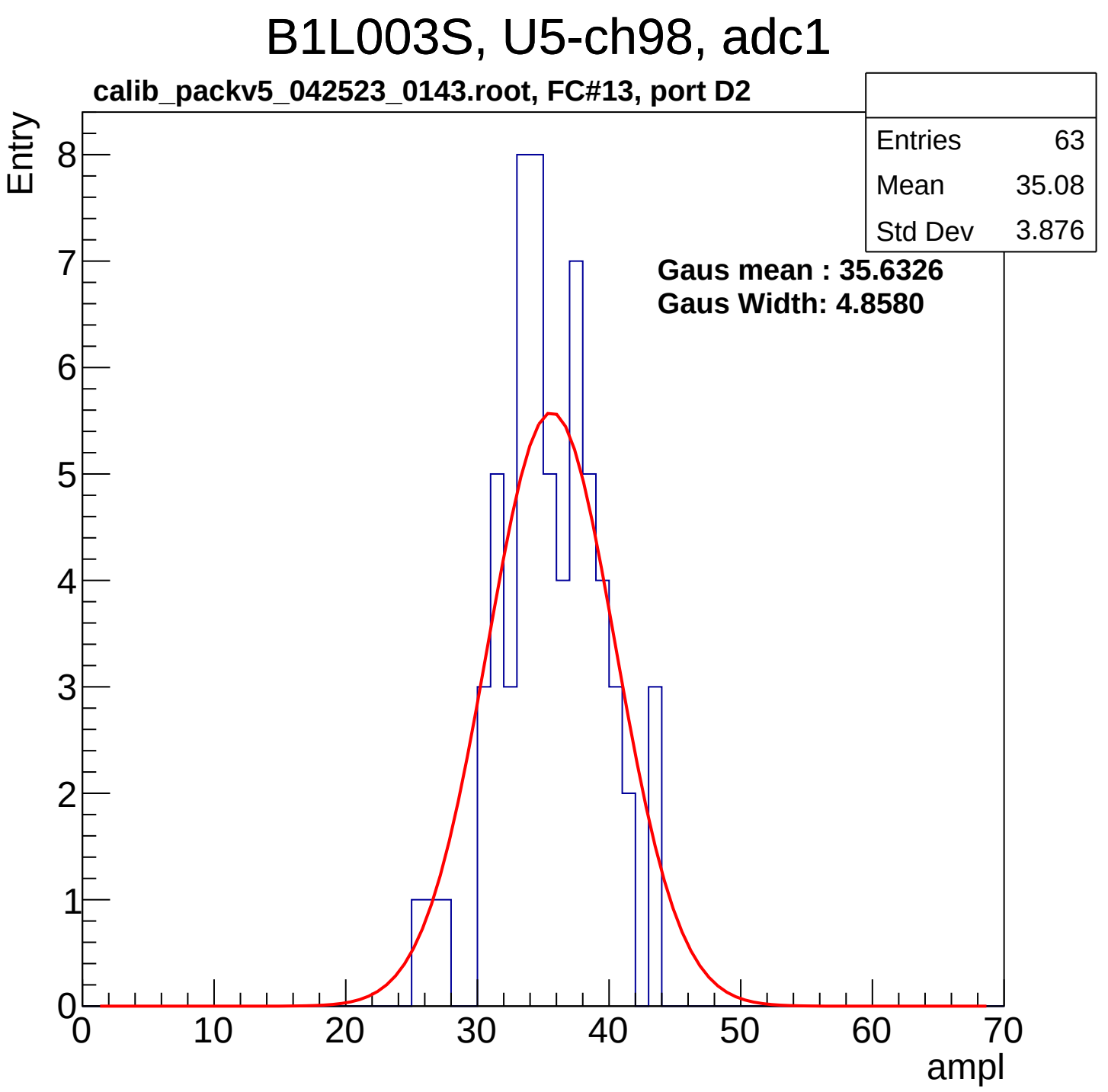
B1L003S, U5-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.08
Std Dev 3.876
Gaus mean : 35.6326
Gaus Width: 4.8580
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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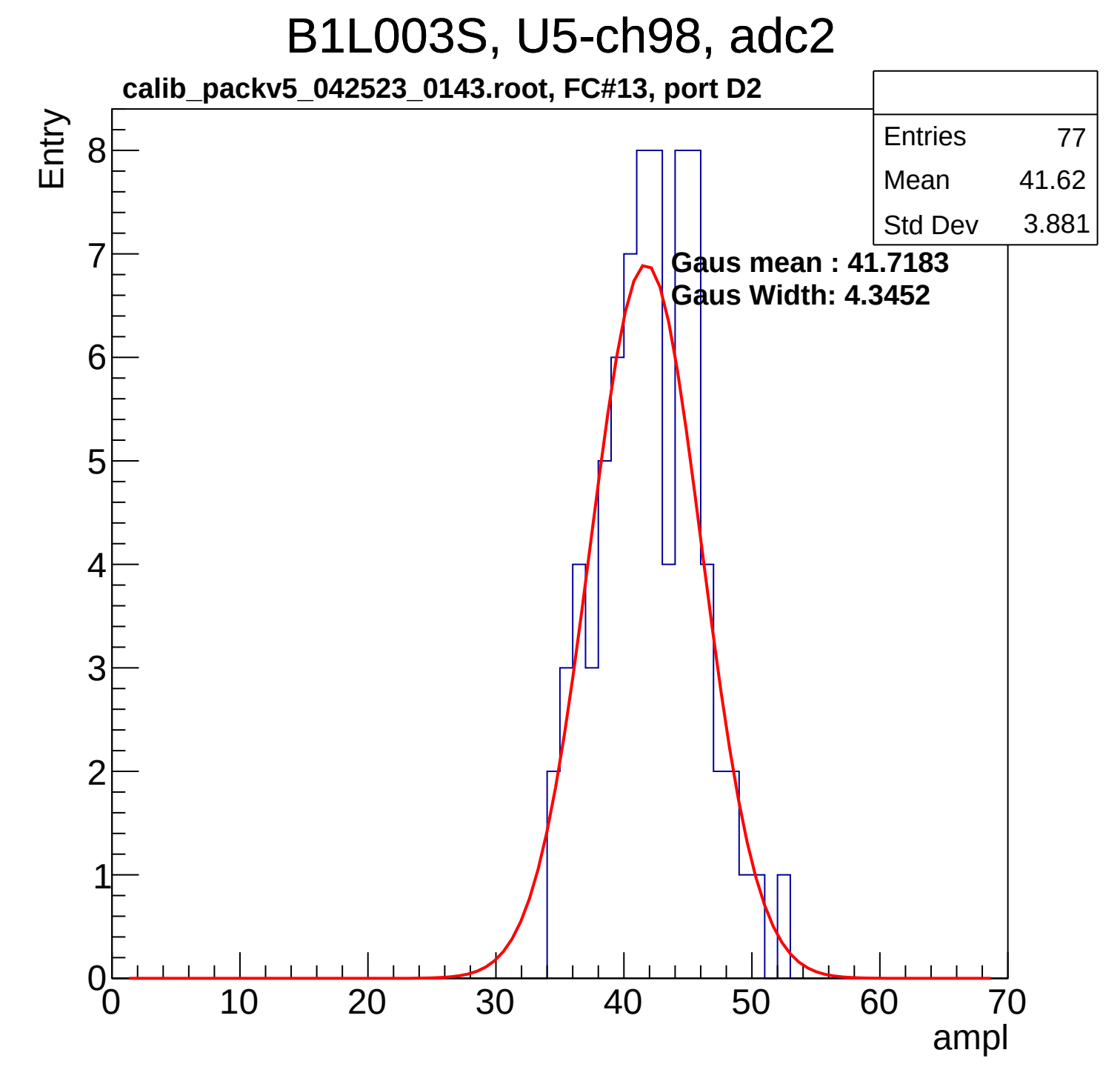
B1L003S, U5-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 41.62
Std Dev 3.881
Gaus mean : 41.7183
Gaus Width: 4.3452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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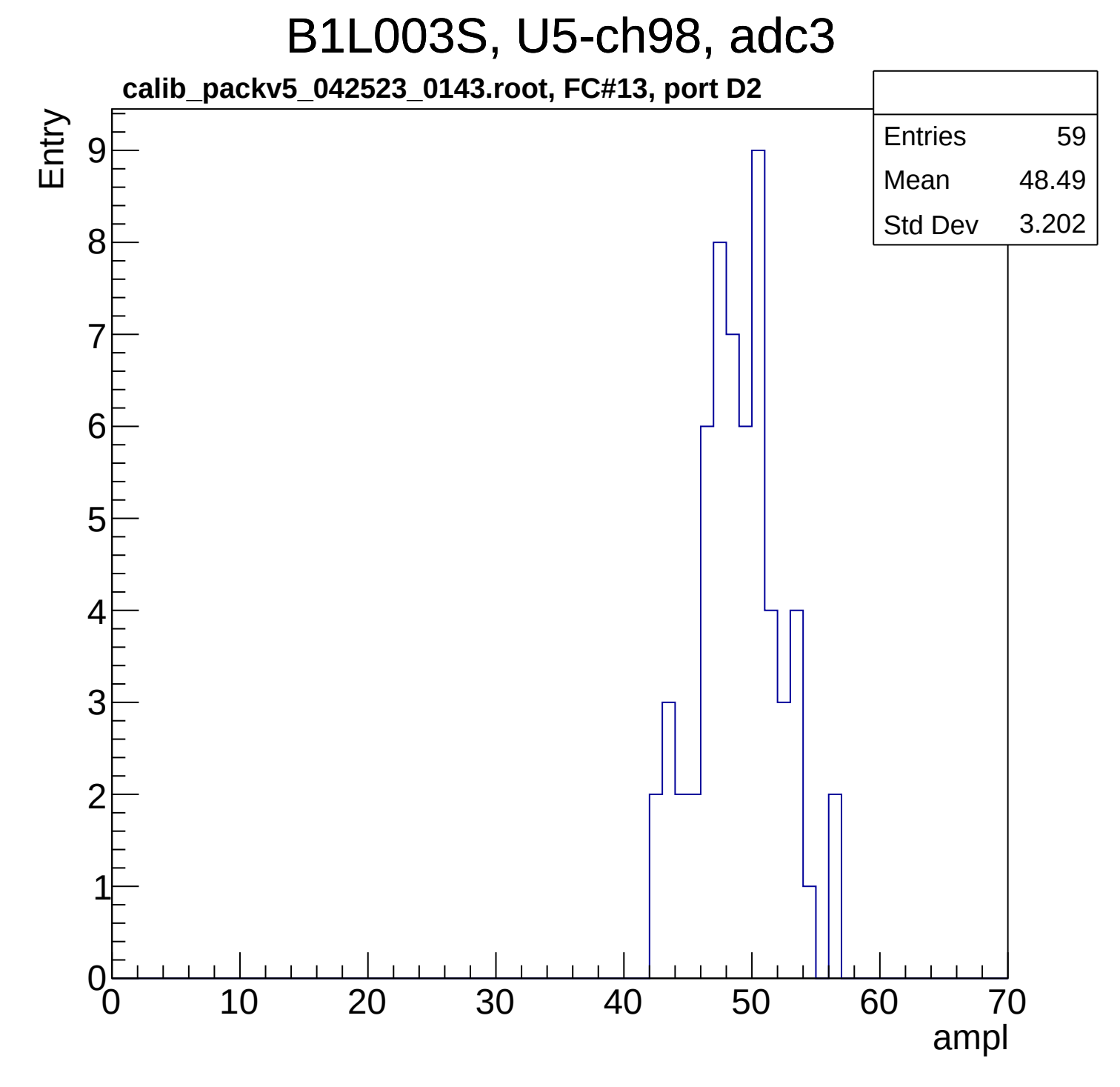
B1L003S, U5-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.49
Std Dev 3.202
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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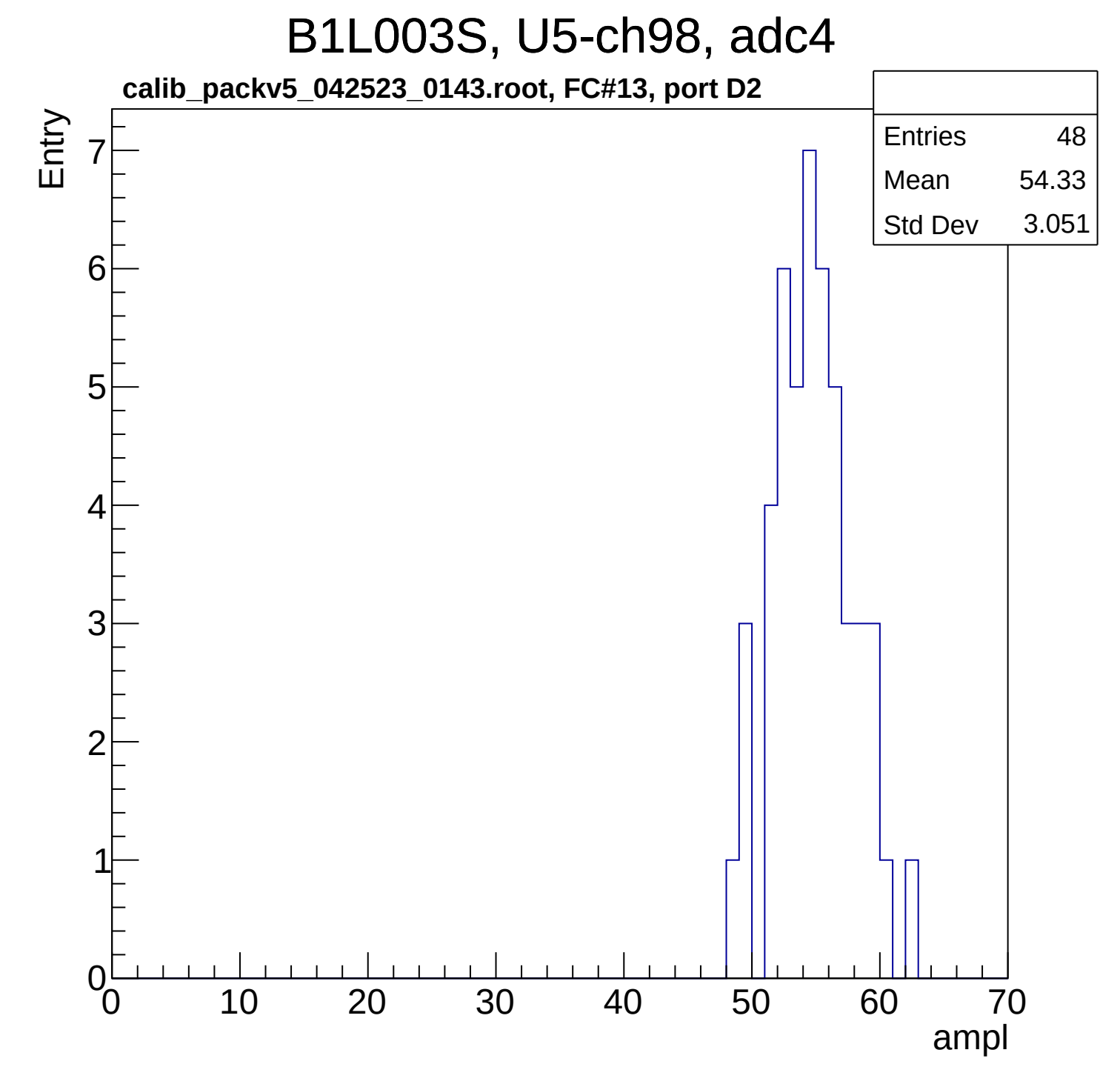
B1L003S, U5-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.33
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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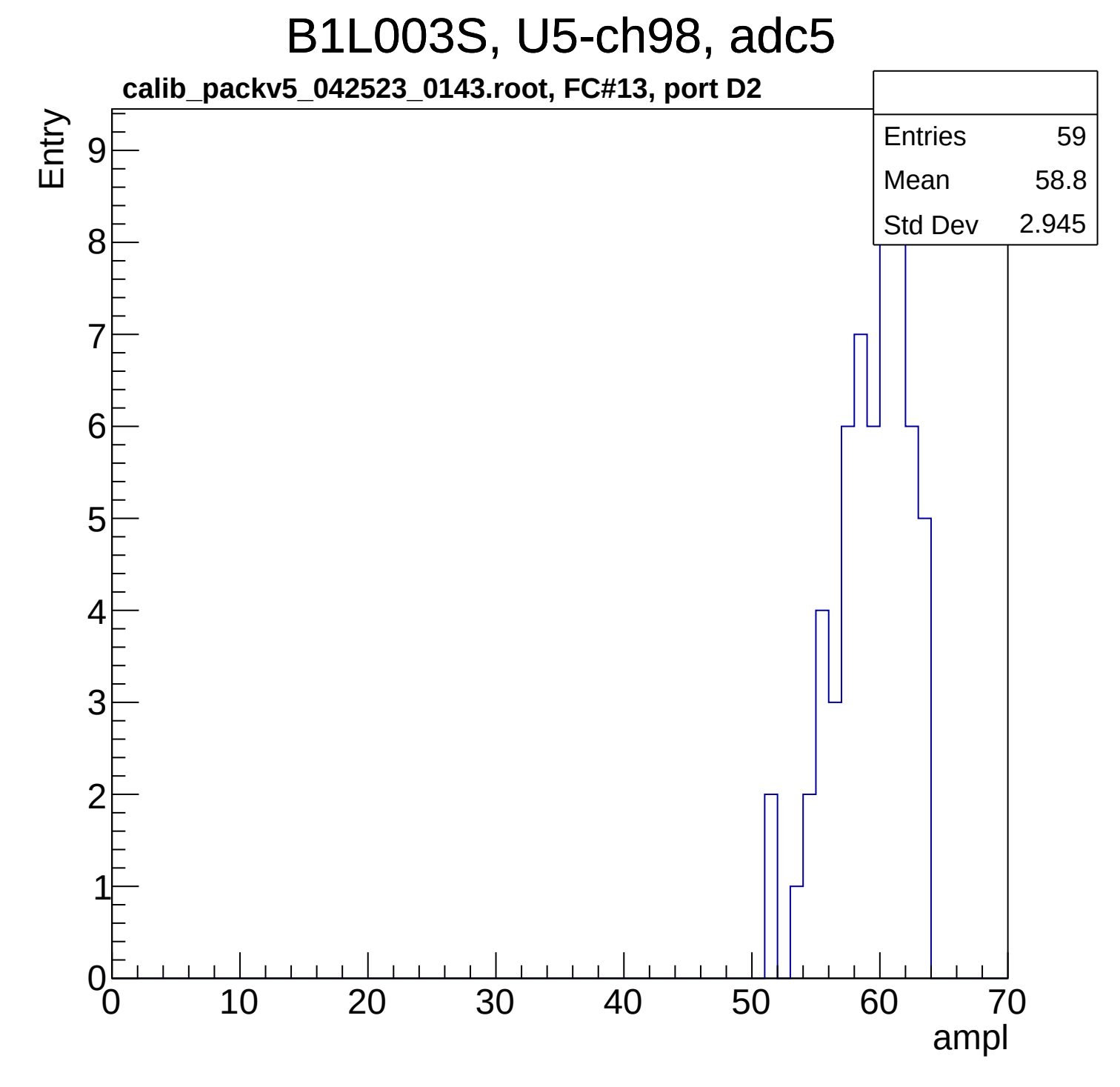
B1L003S, U5-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.8
Std Dev 2.945
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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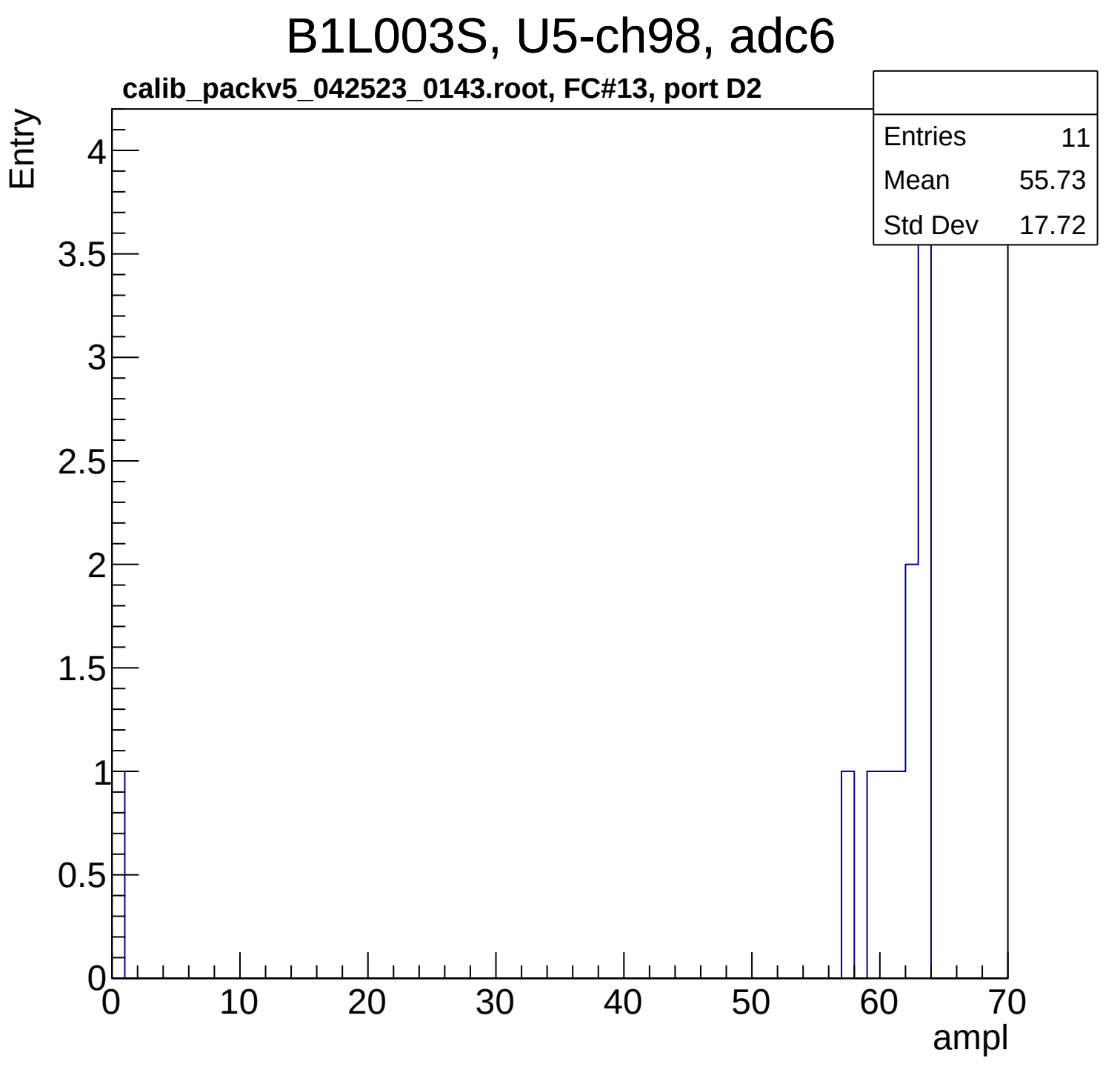
B1L003S, U5-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.73
Std Dev 17.72
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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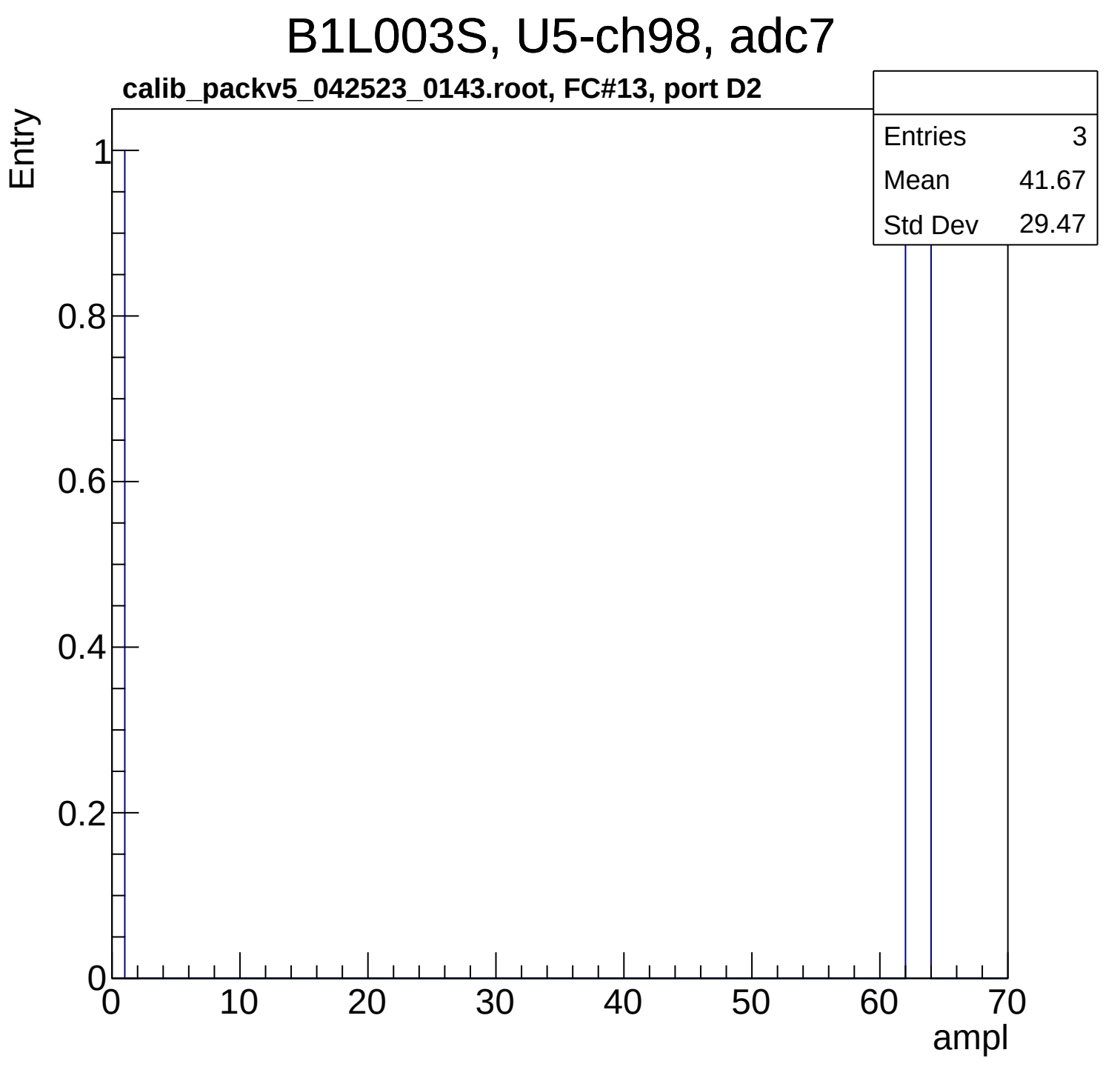
B1L003S, U5-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
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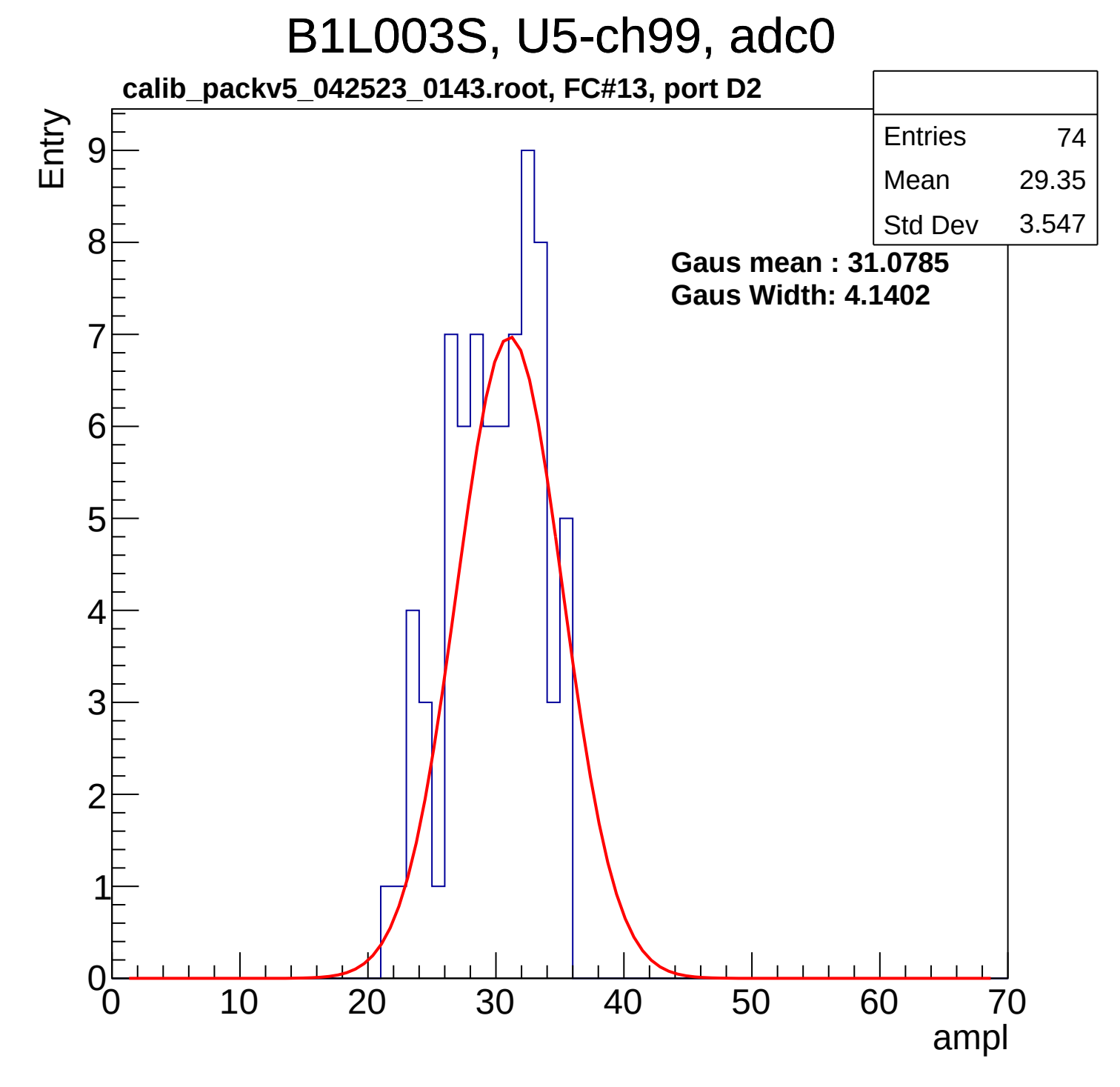
50
60
70
ampl


B1L003S, U5-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.35
Std Dev 3.547
Gaus mean : 31.0785
Gaus Width: 4.1402
Entry
ampl
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2
3
4
5
6
7
8
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0
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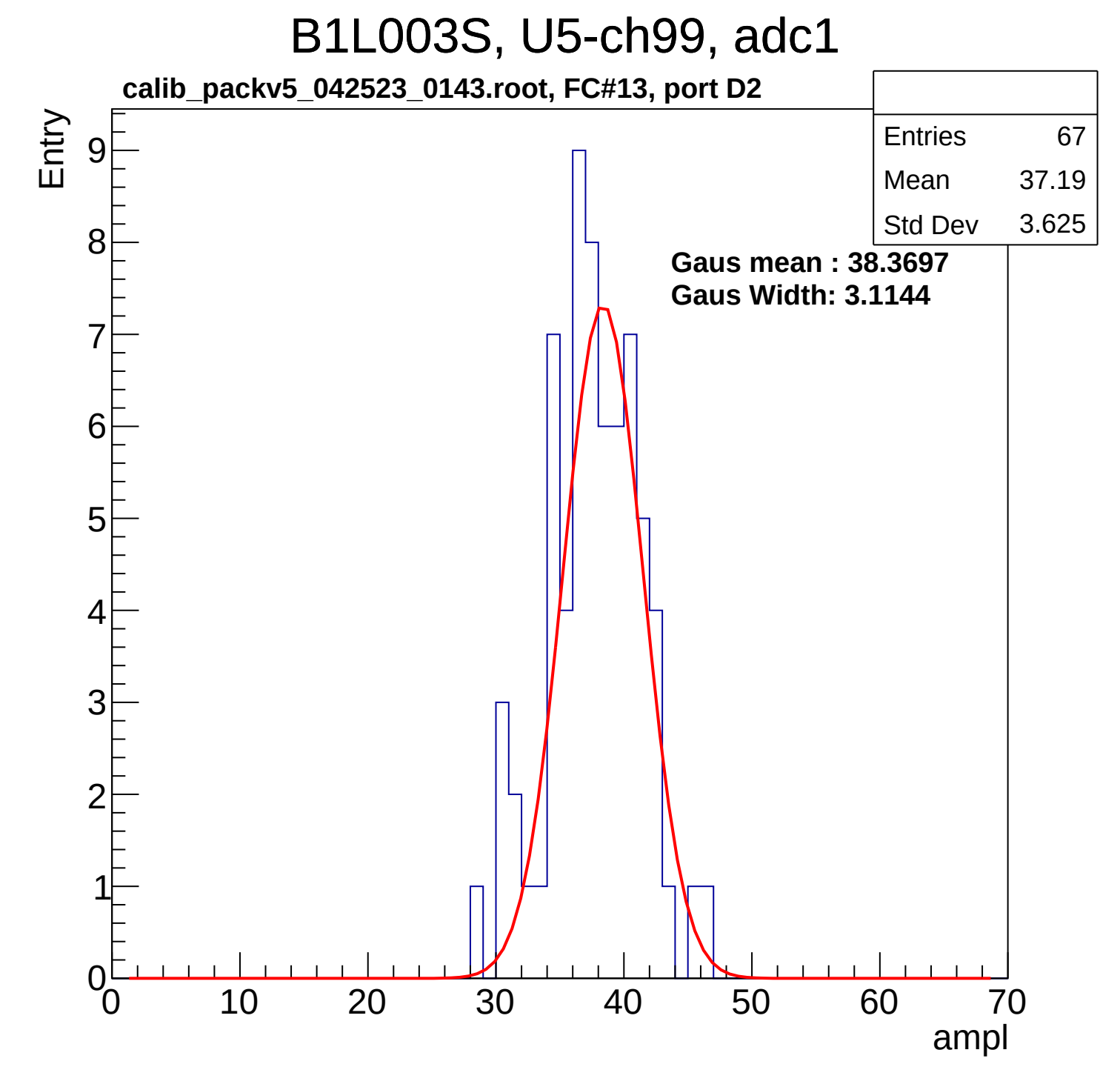
40
50
60
70


B1L003S, U5-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.19
Std Dev 3.625
Gaus mean : 38.3697
Gaus Width: 3.1144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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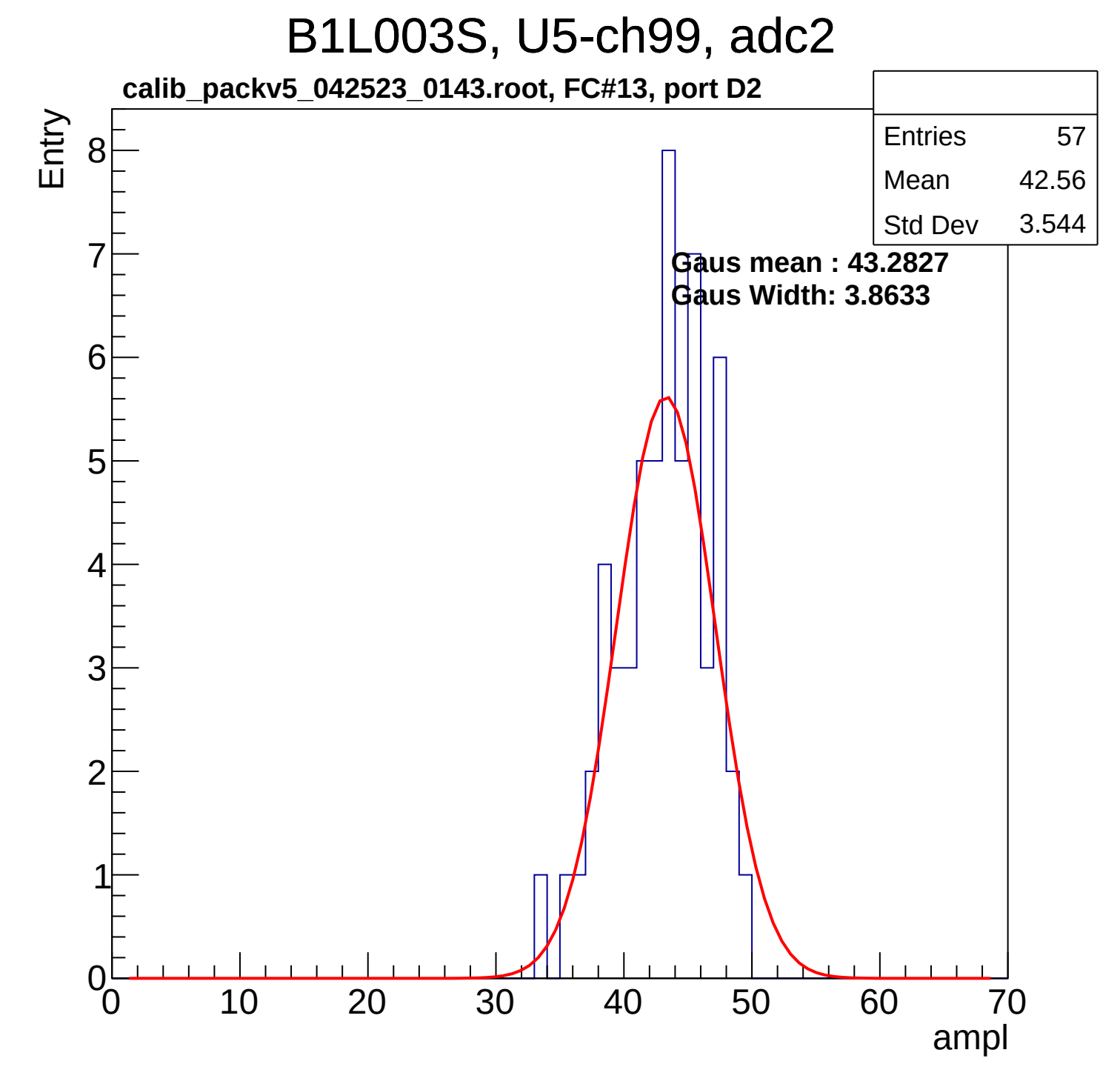
B1L003S, U5-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.56
Std Dev 3.544
Gaus mean : 43.2827
Gaus Width: 3.8633
Entry
ampl
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1
2
3
4
5
6
7
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0
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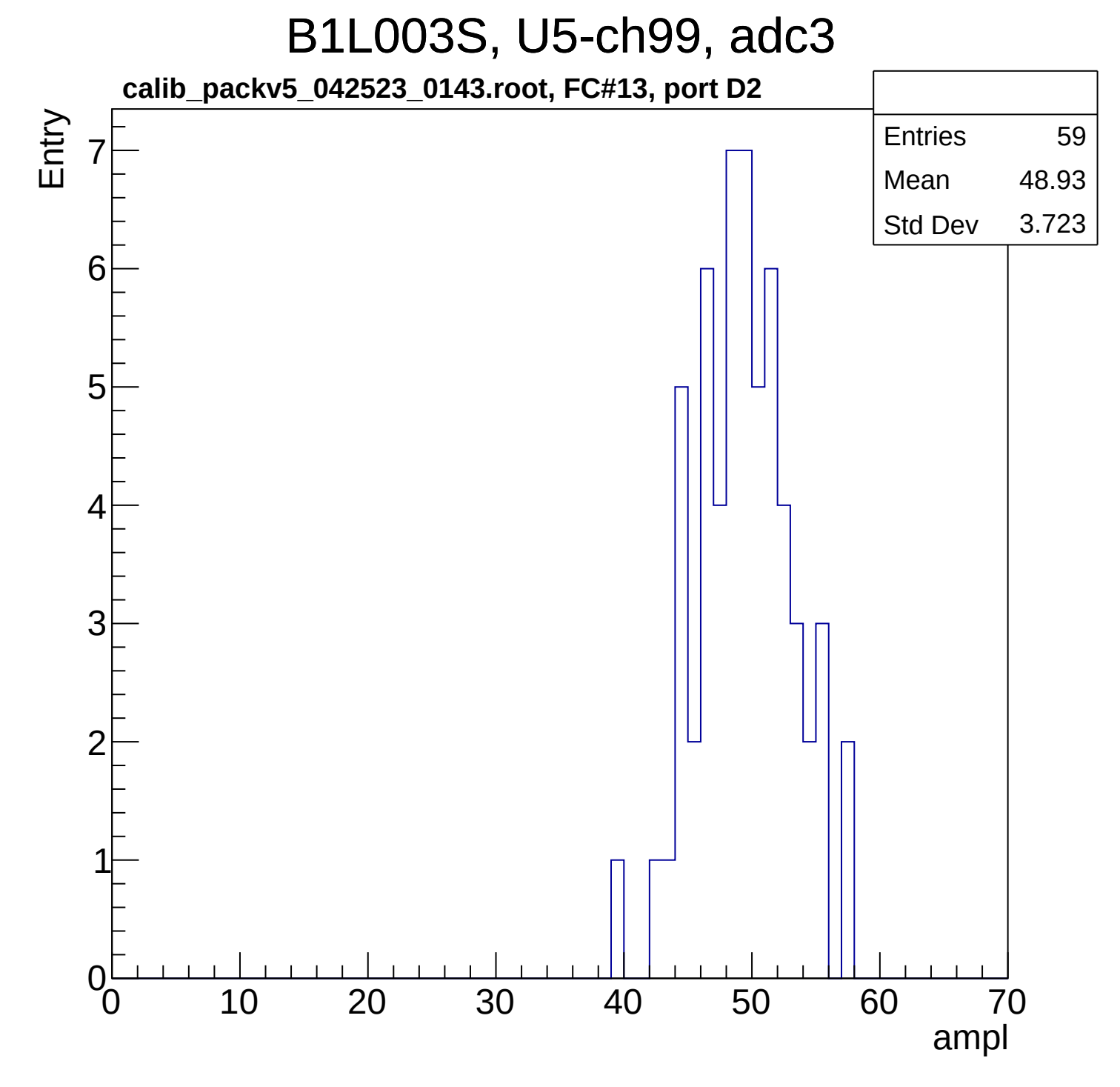
40
50
60
70


B1L003S, U5-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.93
Std Dev 3.723
Entry
ampl
0
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4
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0
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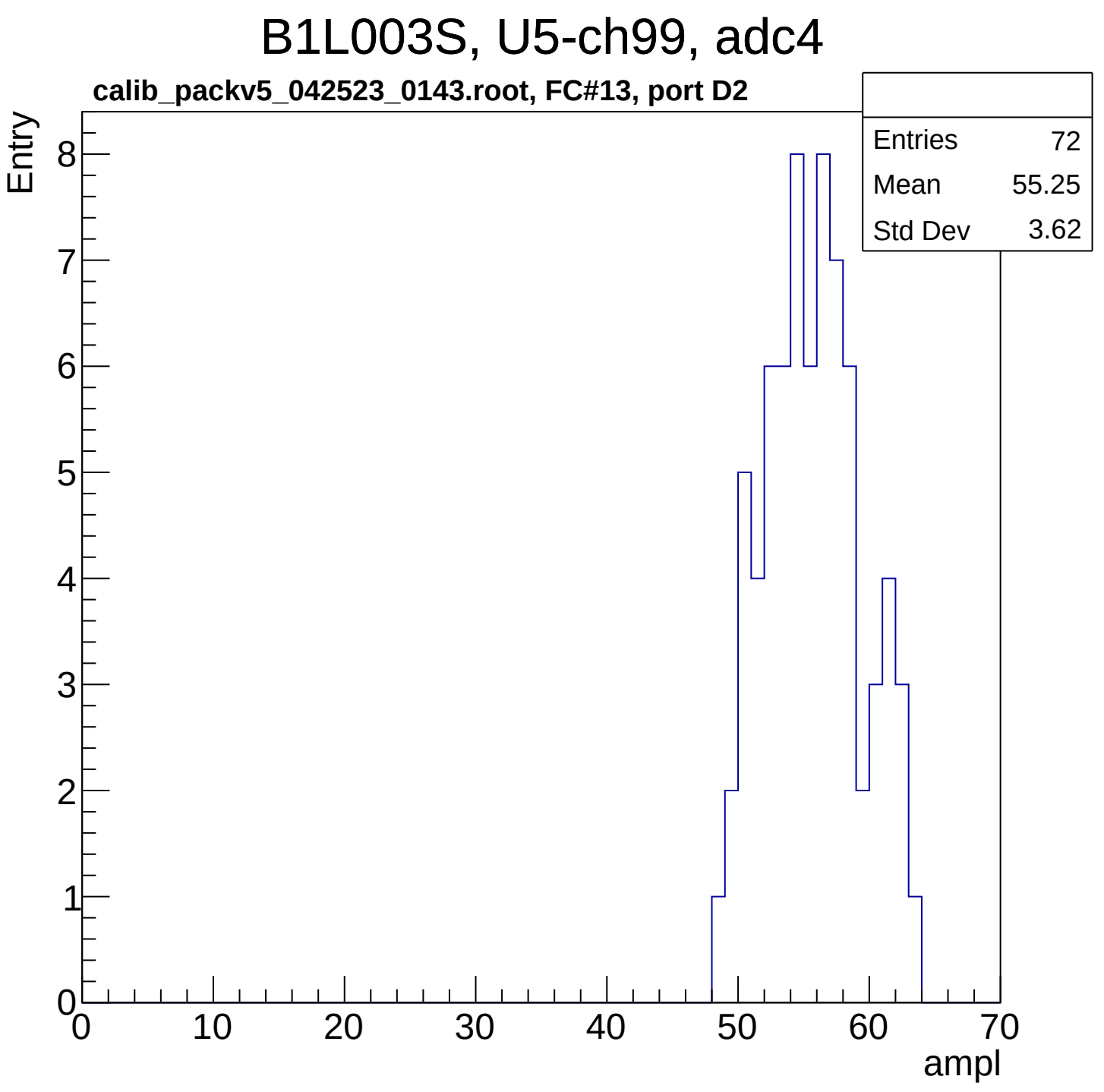
B1L003S, U5-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 55.25
Std Dev 3.62
Entry
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2
3
4
5
6
7
8
0
10
20
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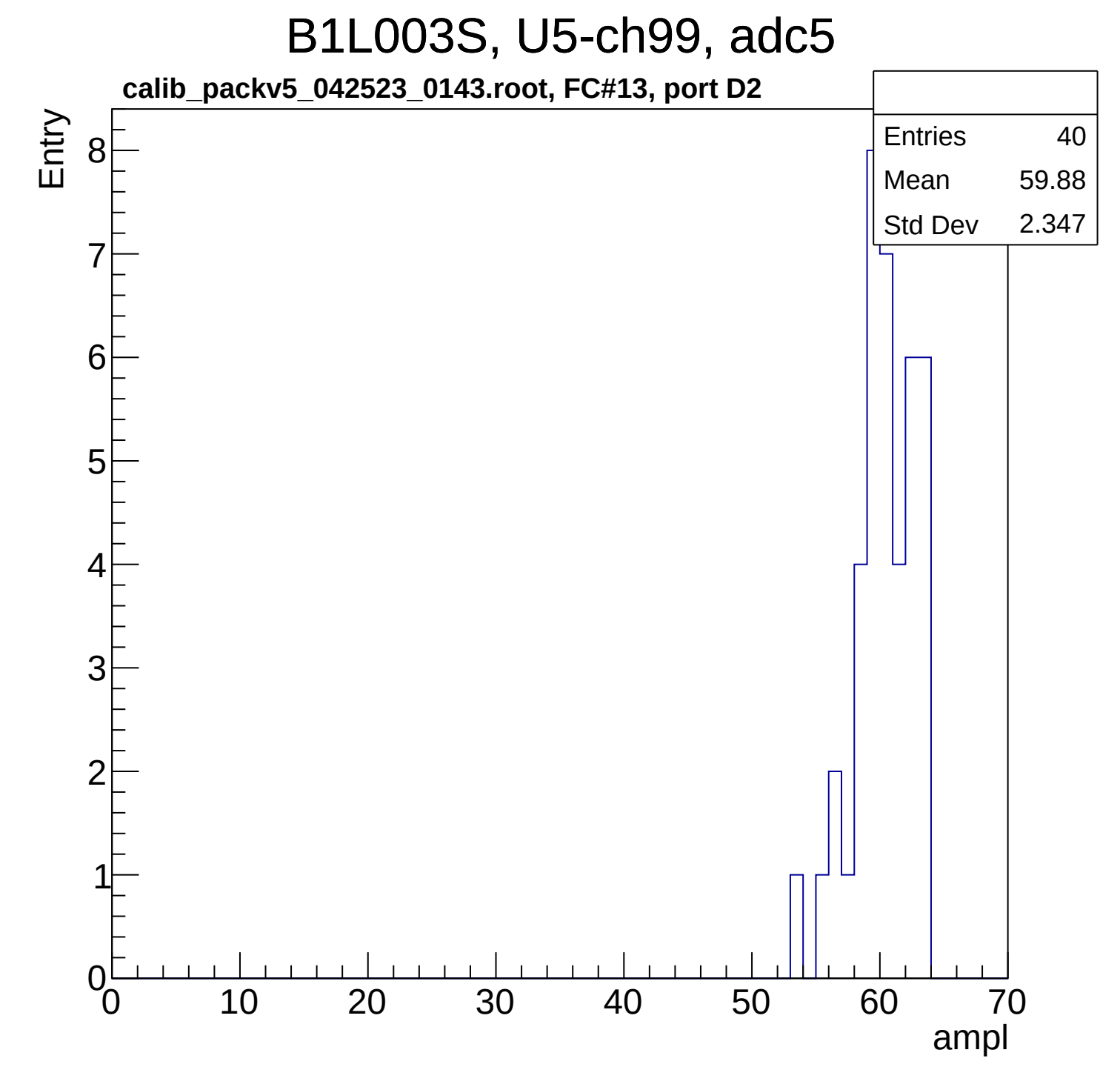
40
50
60
70
ampl


B1L003S, U5-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.88
Std Dev 2.347
Entry
ampl
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1
2
3
4
5
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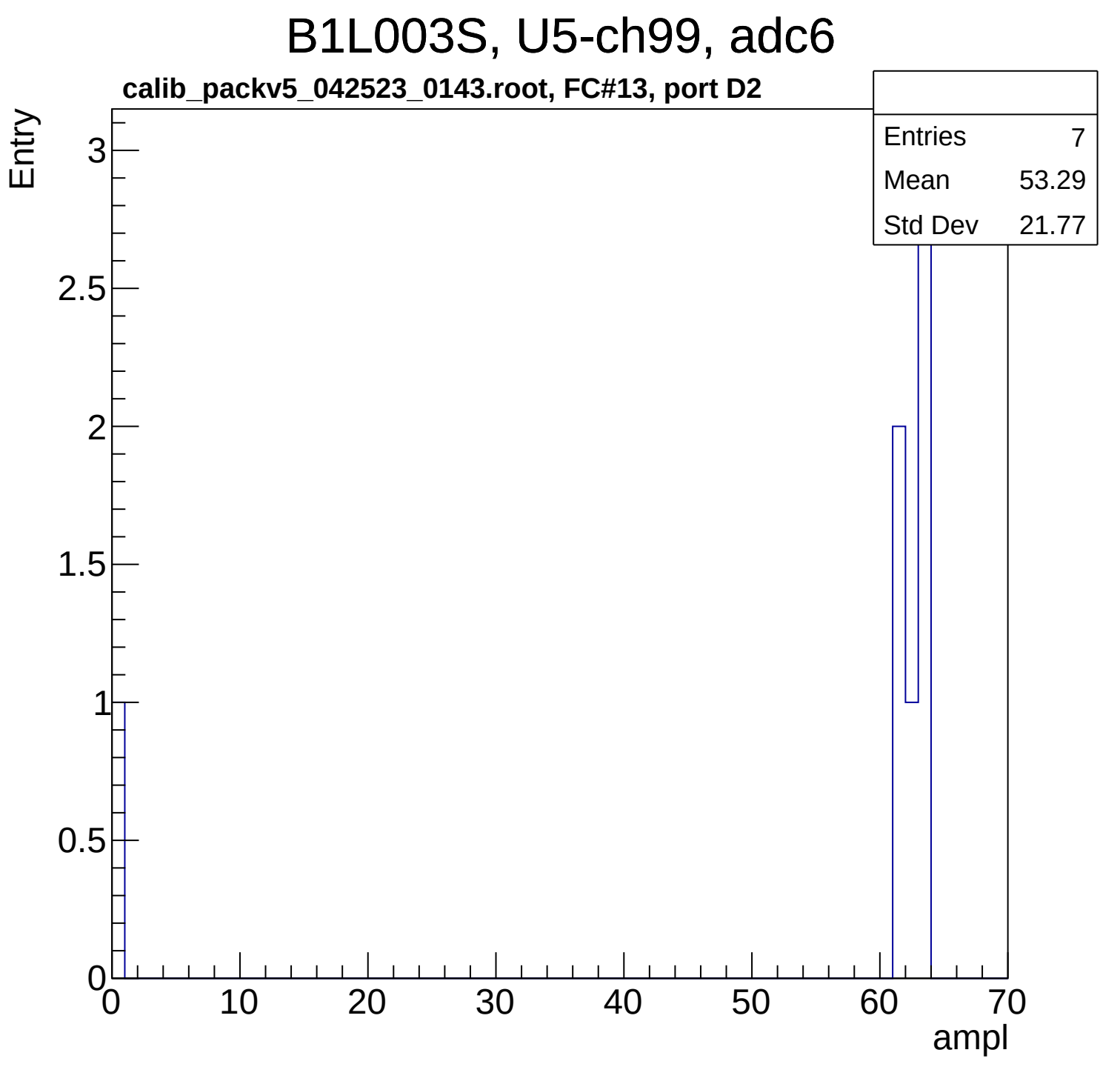
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.29
Std Dev 21.77
Entry
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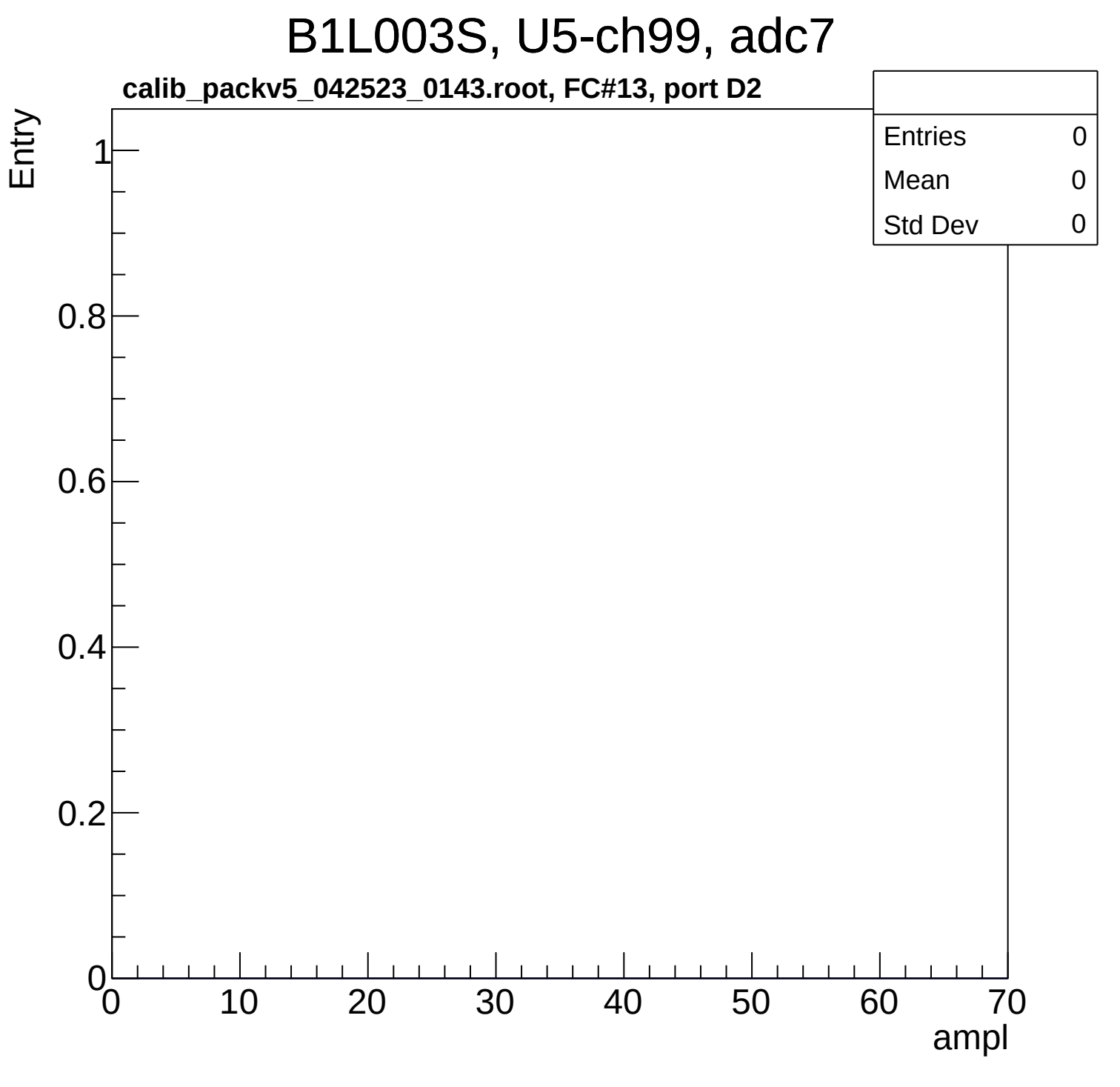
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
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10
20
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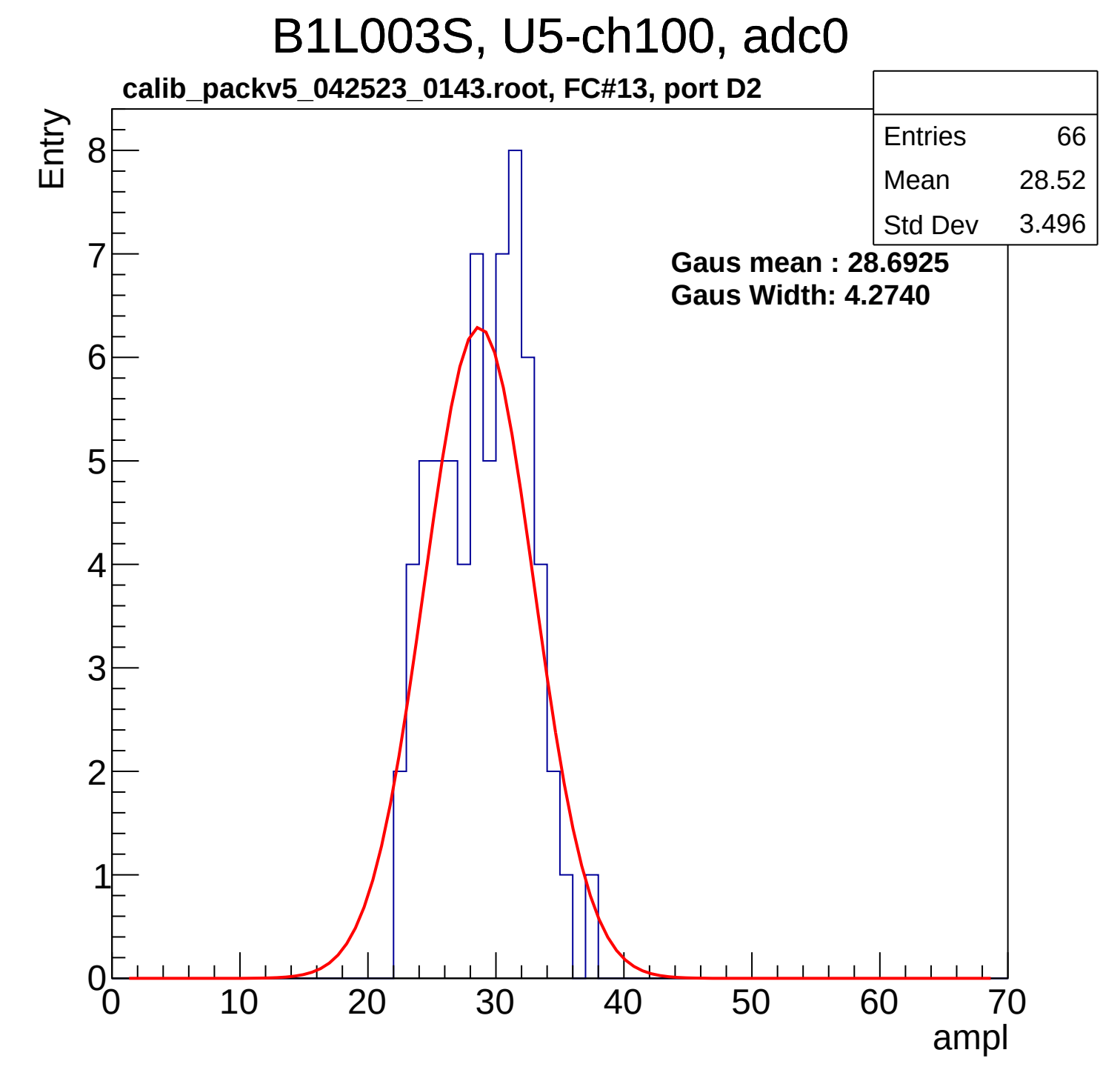
50
60
70
ampl


B1L003S, U5-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.52
Std Dev 3.496
Gaus mean : 28.6925
Gaus Width: 4.2740
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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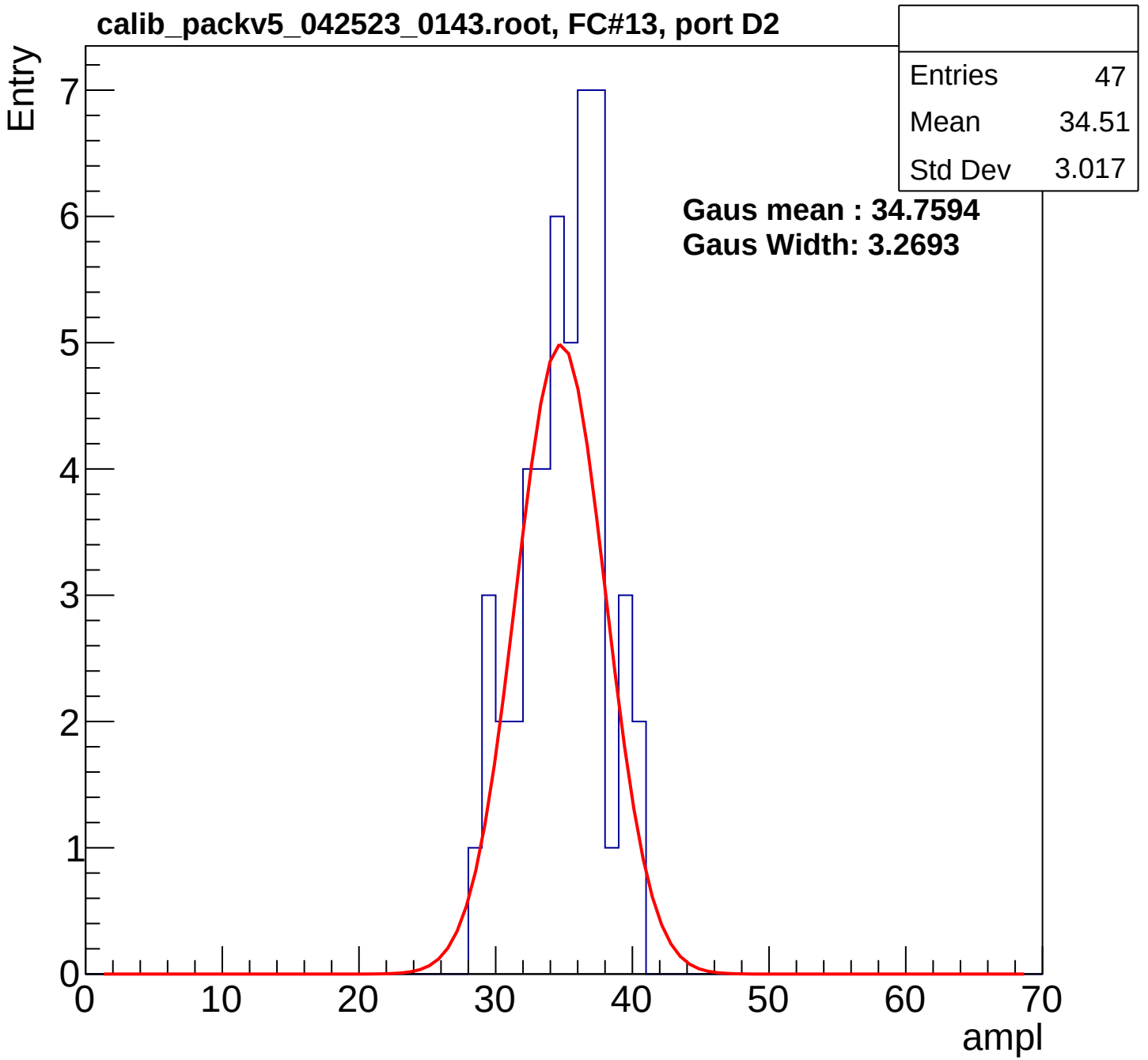
B1L003S, U5-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 34.51
Std Dev 3.017
Gaus mean : 34.7594
Gaus Width: 3.2693
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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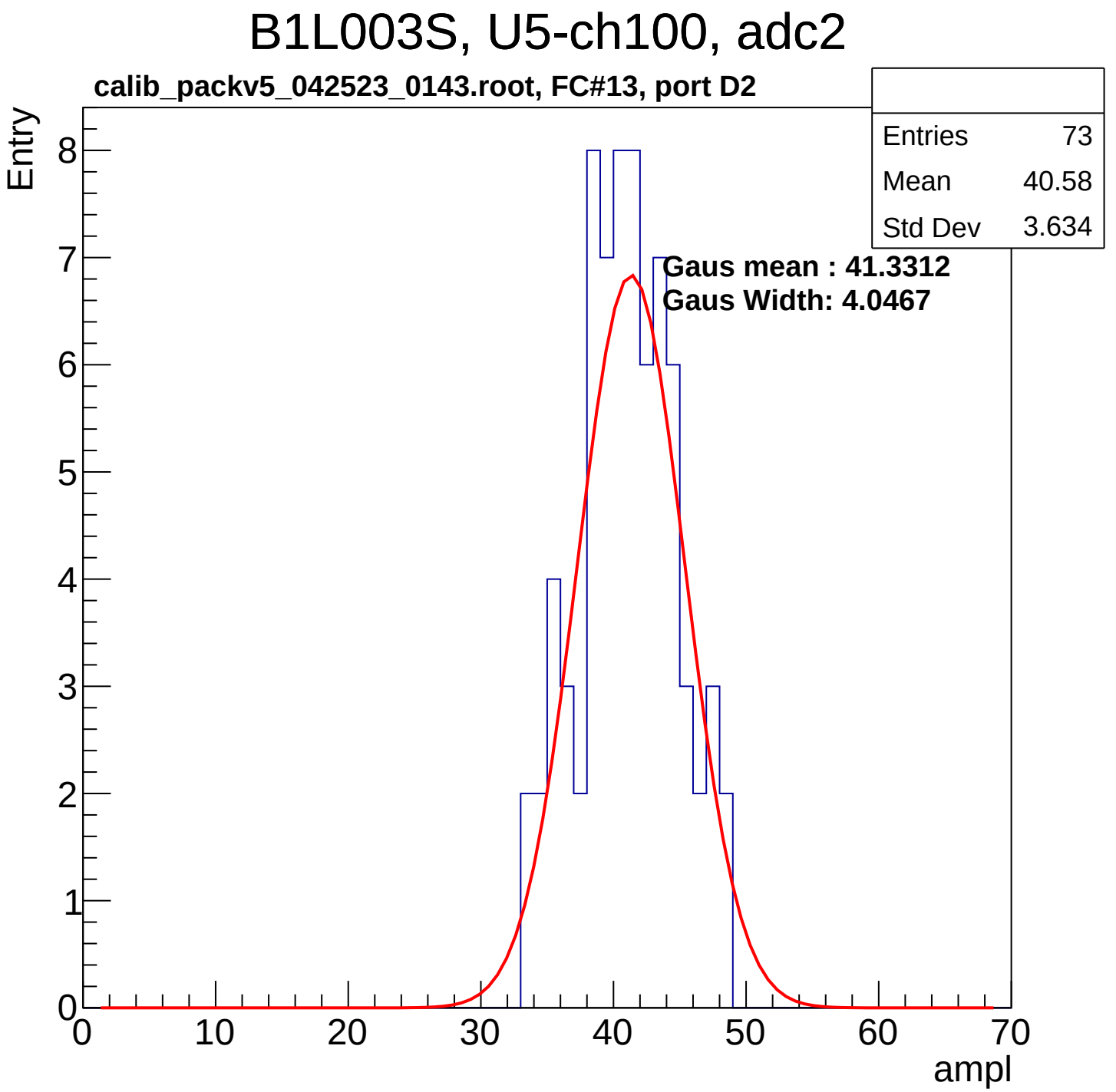
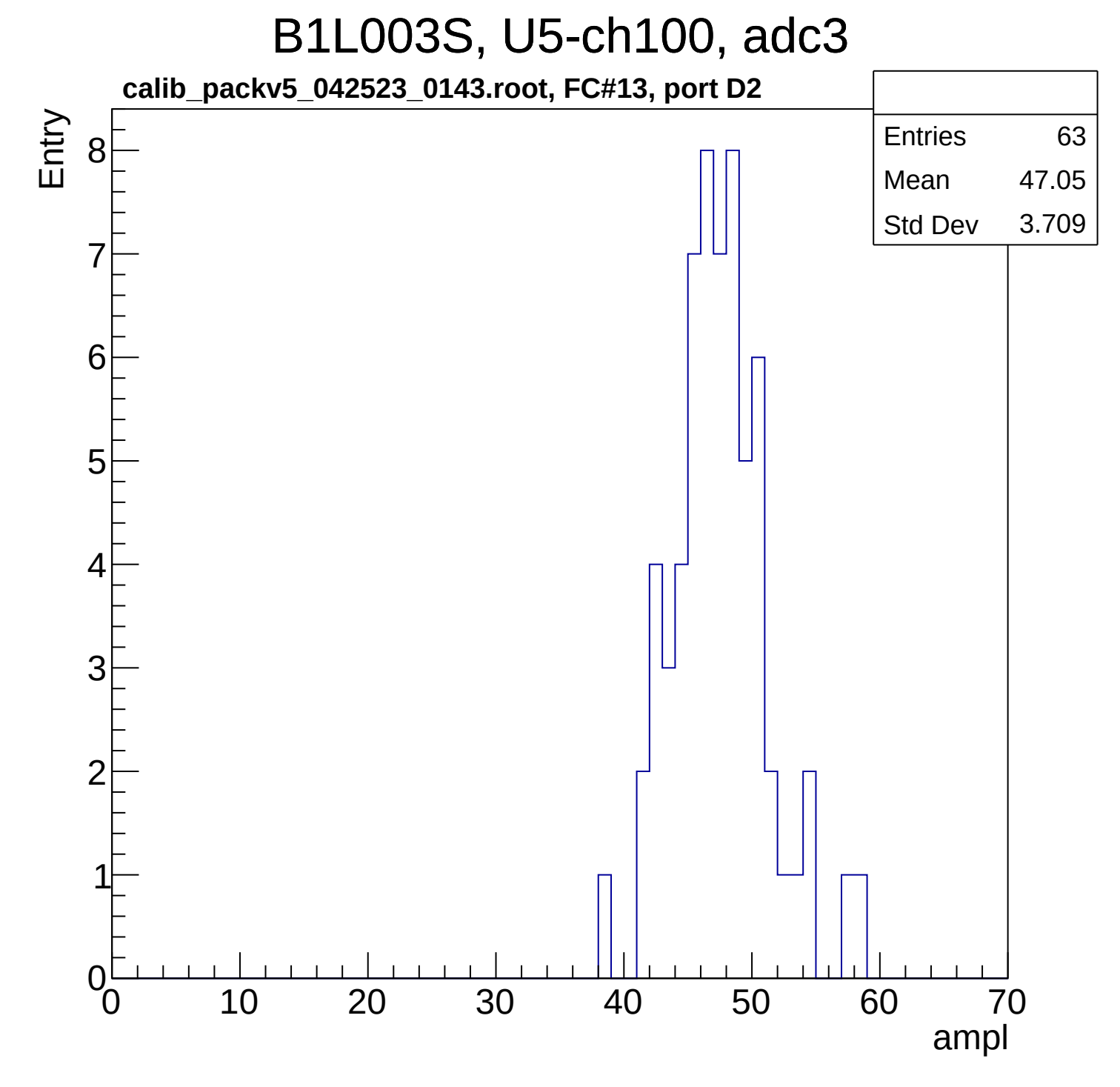

B1L003S, U5-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 40.58
Std Dev 3.634
Gaus mean : 41.3312
Gaus Width: 4.0467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 47.05
Std Dev 3.709
Entry
ampl
0
1
2
3
4
5
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7
8
0
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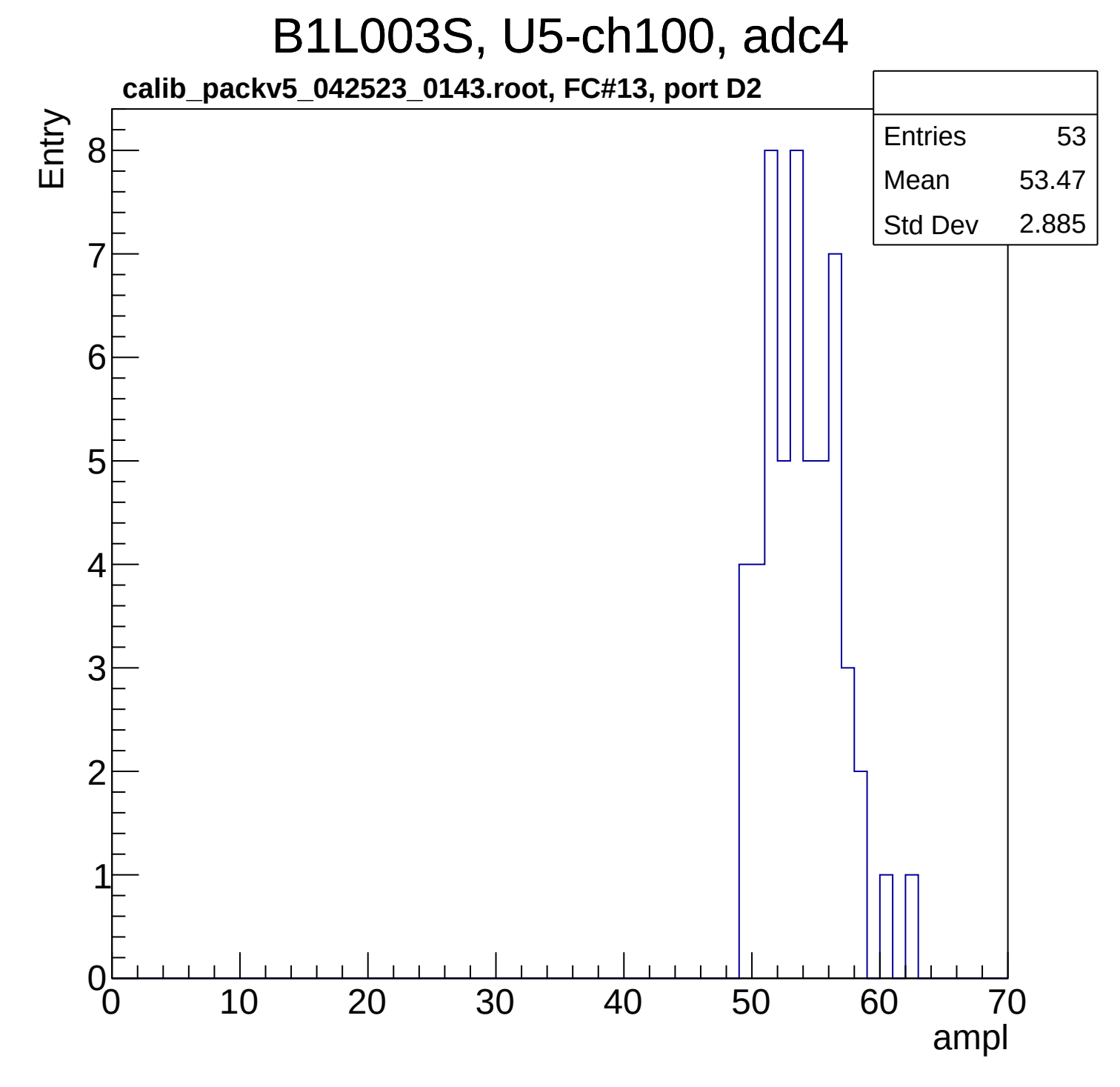
B1L003S, U5-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 53.47
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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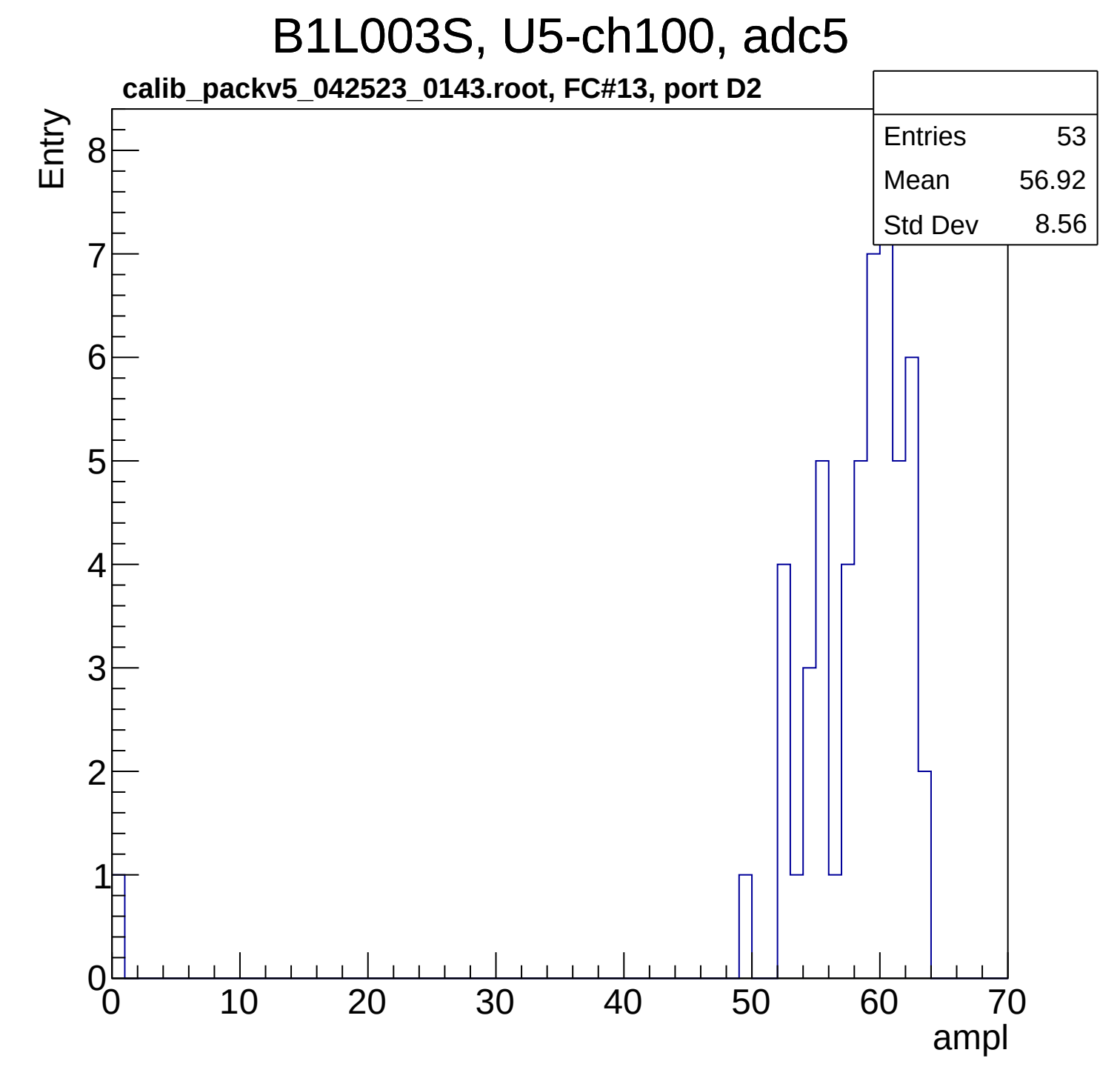
B1L003S, U5-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.92
Std Dev 8.56
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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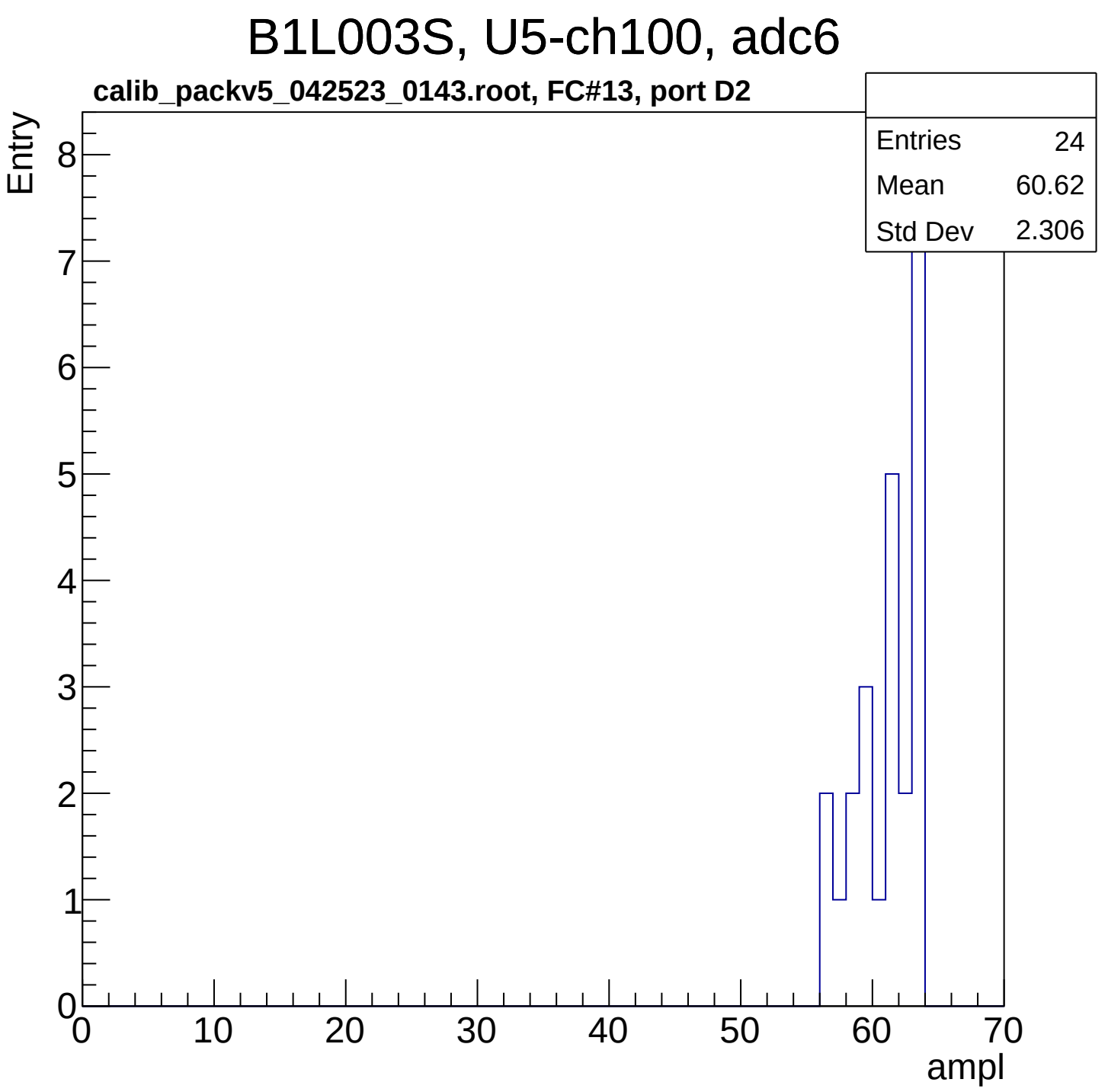
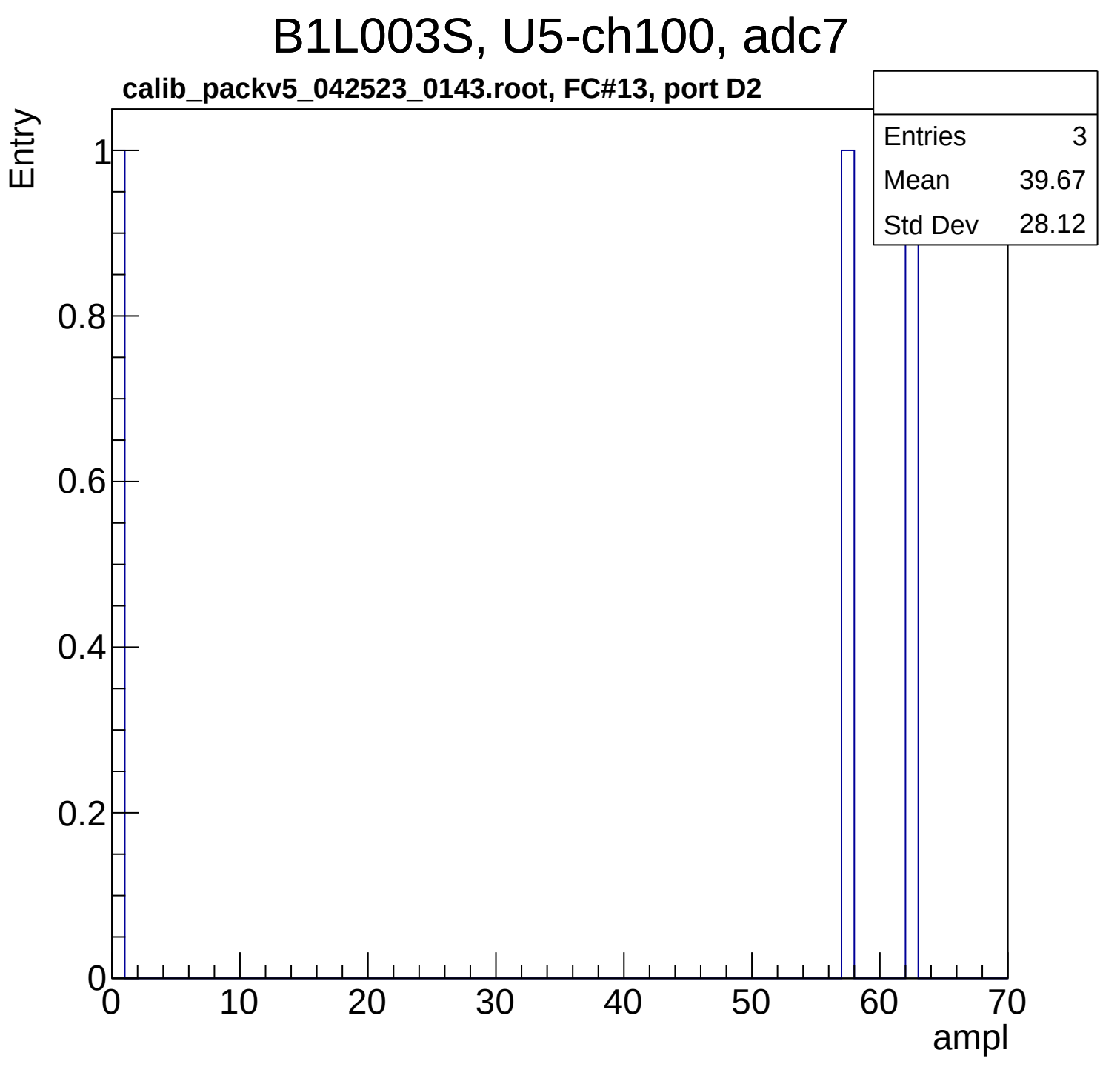
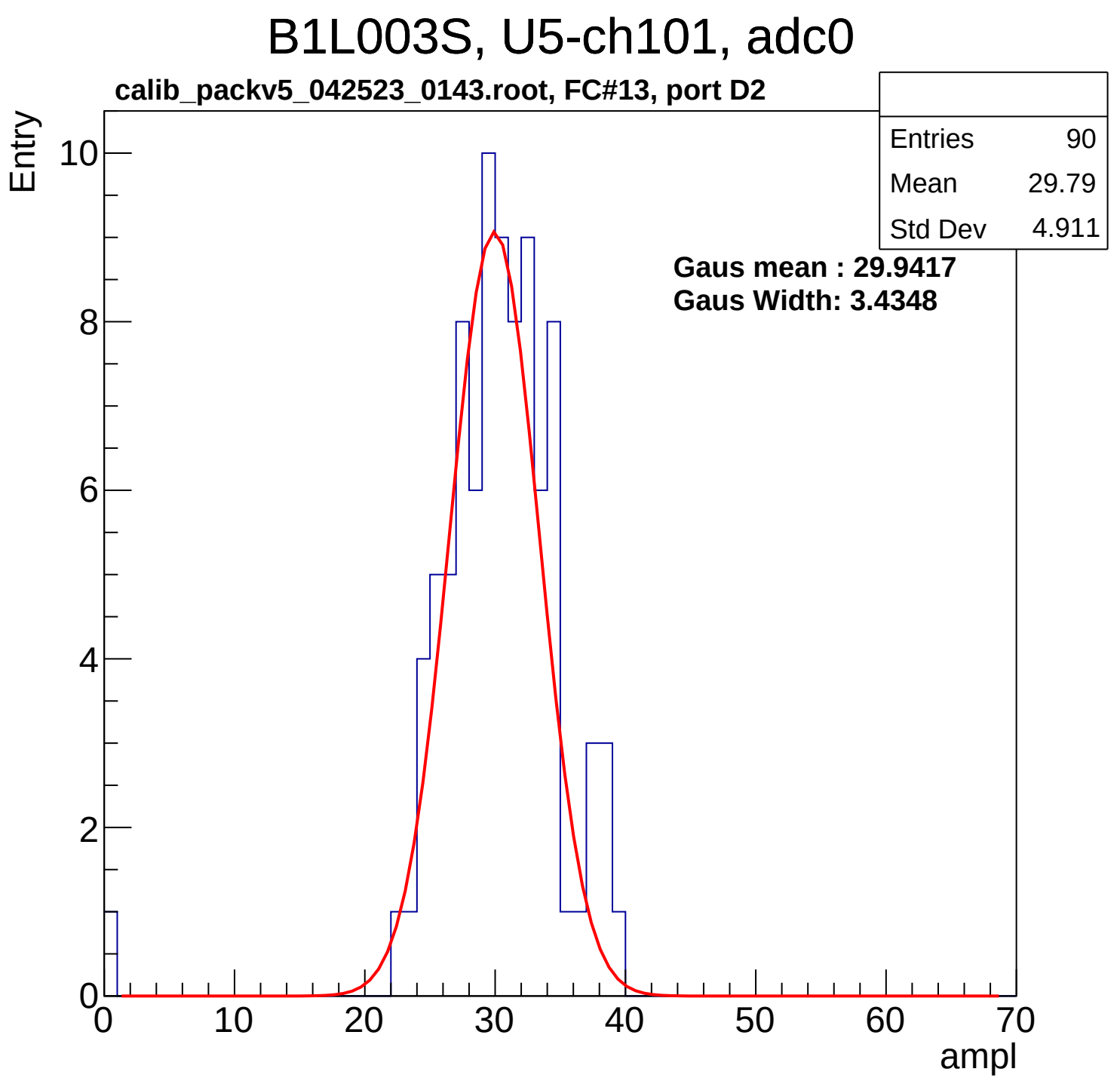
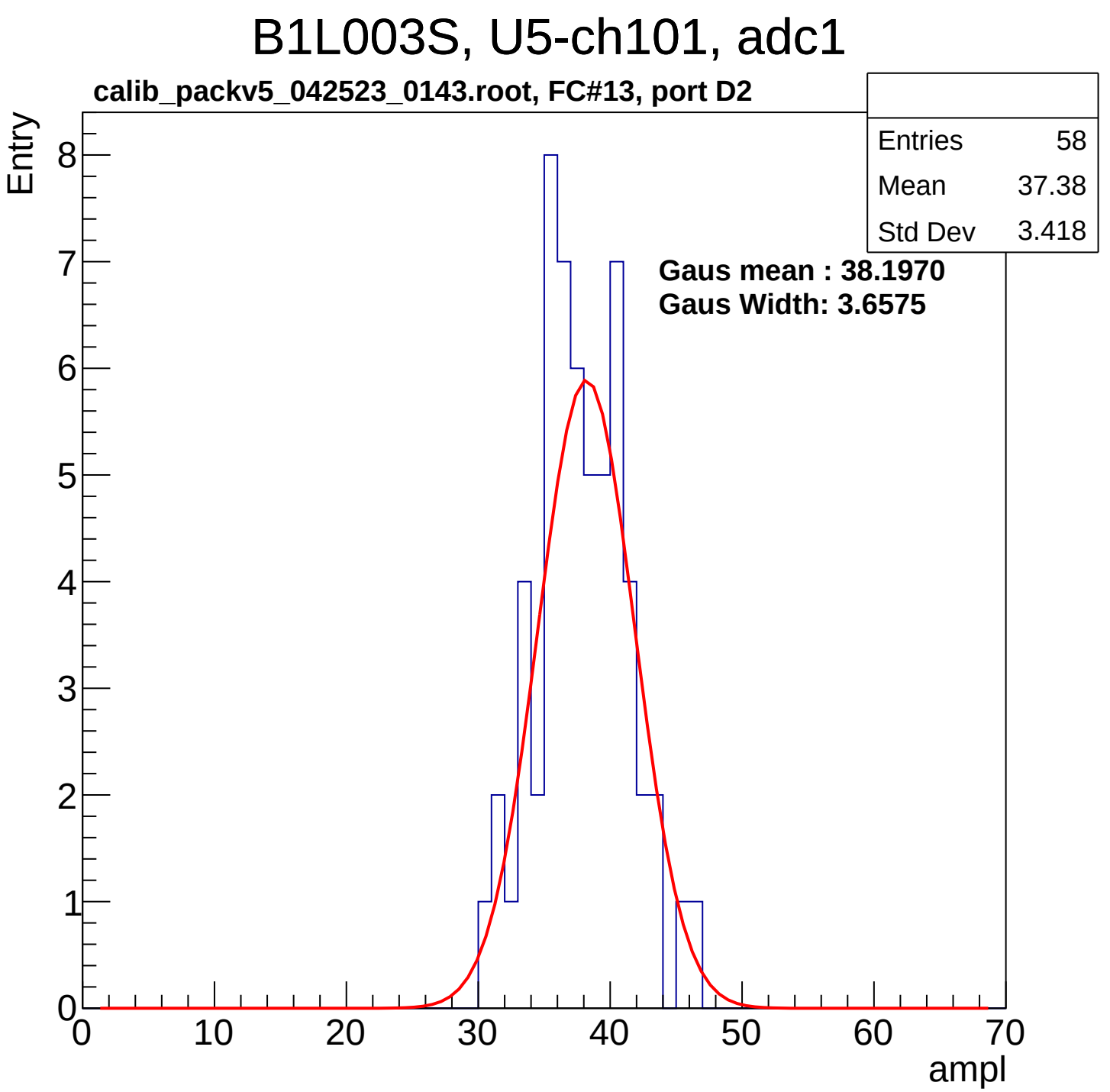

B1L003S, U5-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 60.62
Std Dev 2.306
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 39.67
Std Dev 28.12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 90
Mean 29.79
Std Dev 4.911
Gaus mean : 29.9417
Gaus Width: 3.4348
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 37.38
Std Dev 3.418
Gaus mean : 38.1970
Gaus Width: 3.6575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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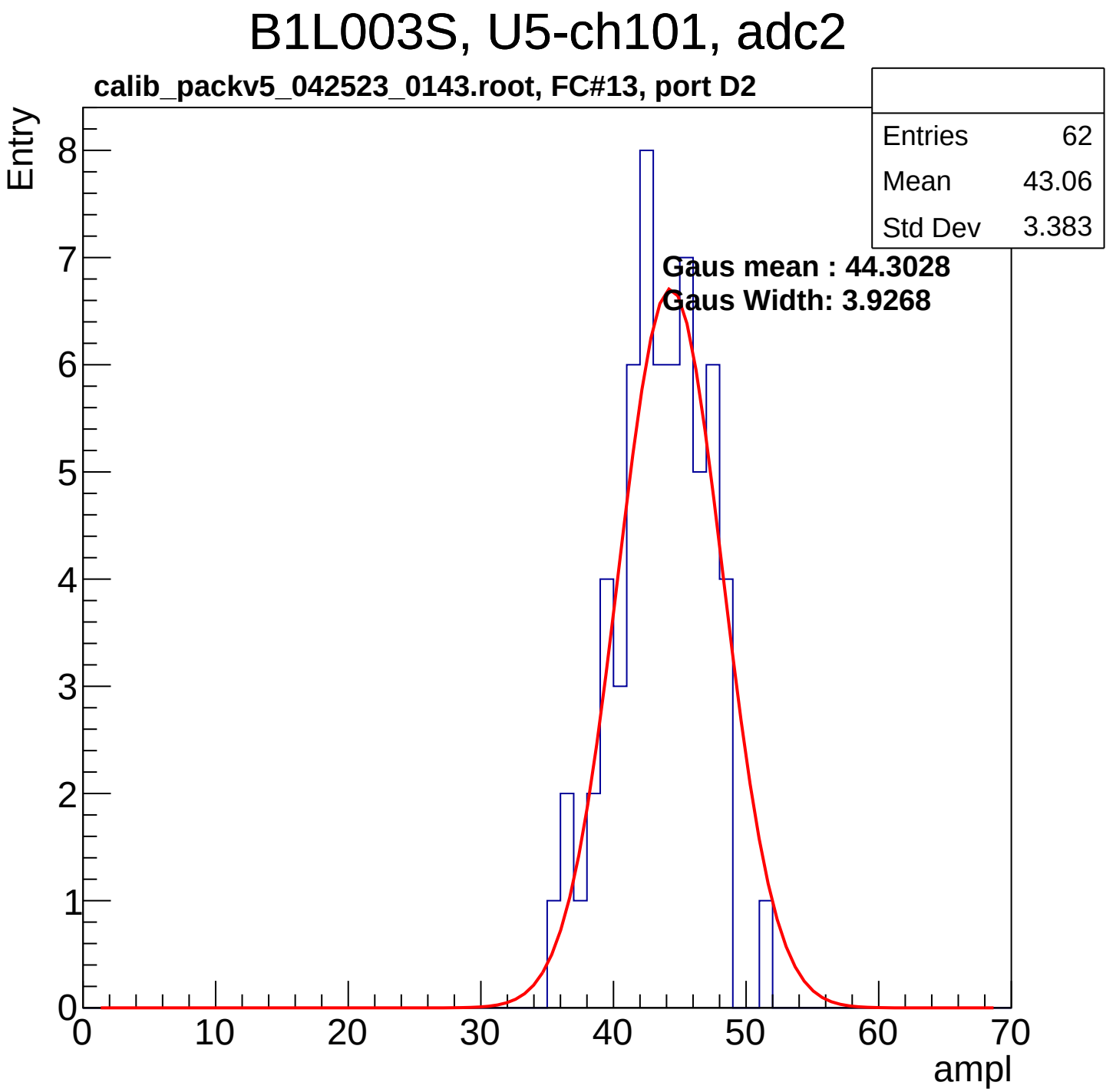
B1L003S, U5-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.06
Std Dev 3.383
Gaus mean : 44.3028
Gaus Width: 3.9268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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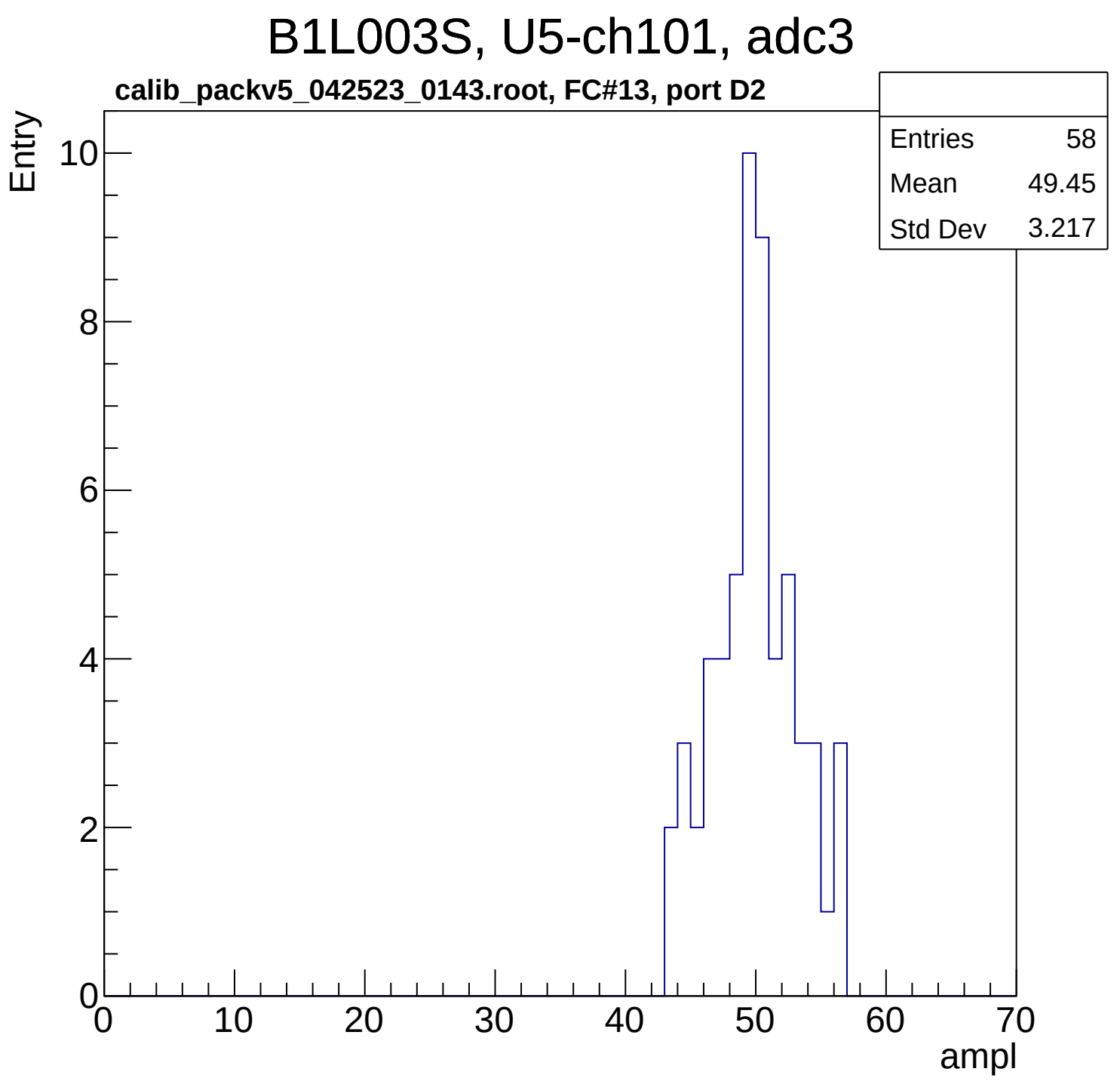
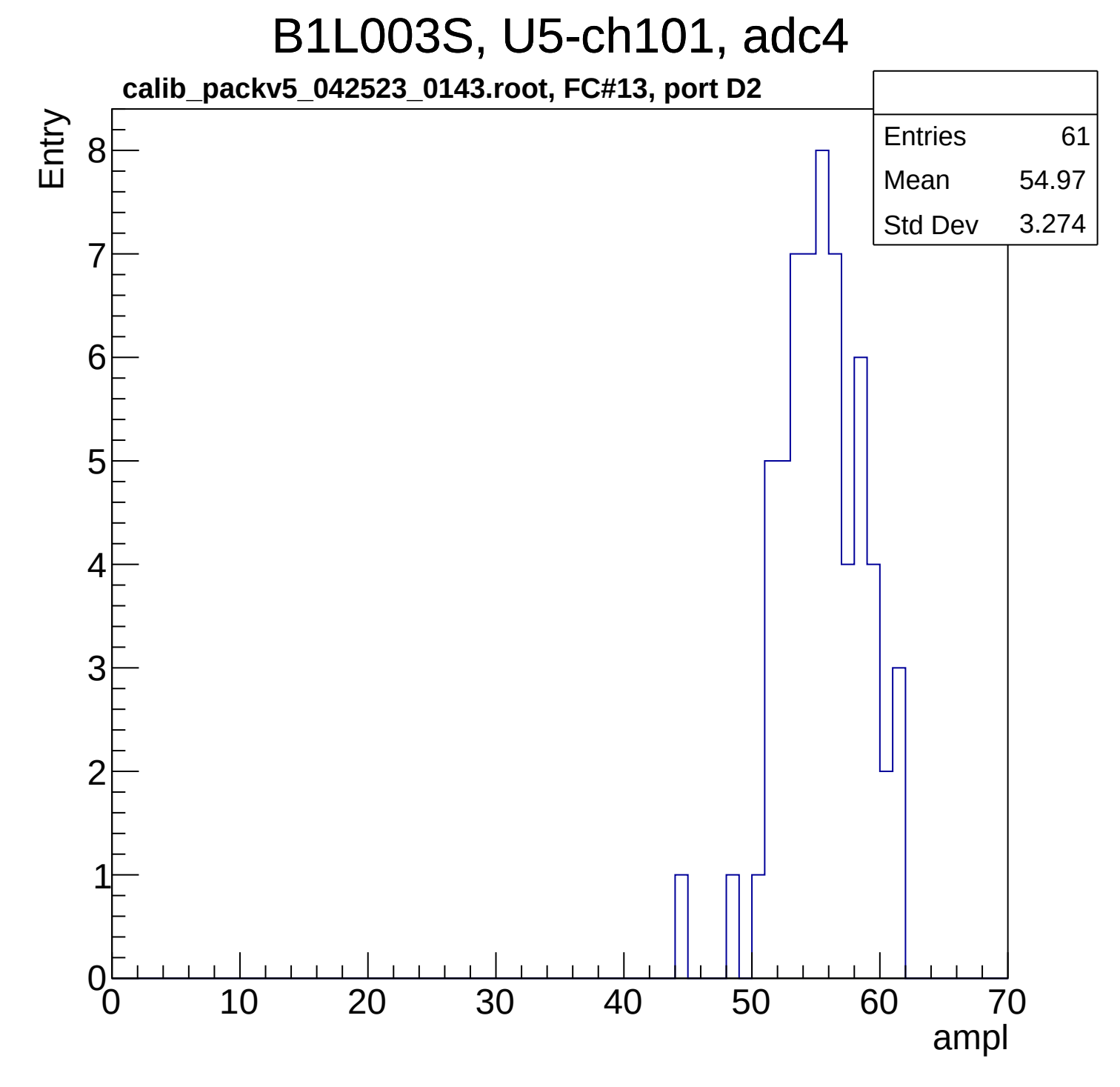
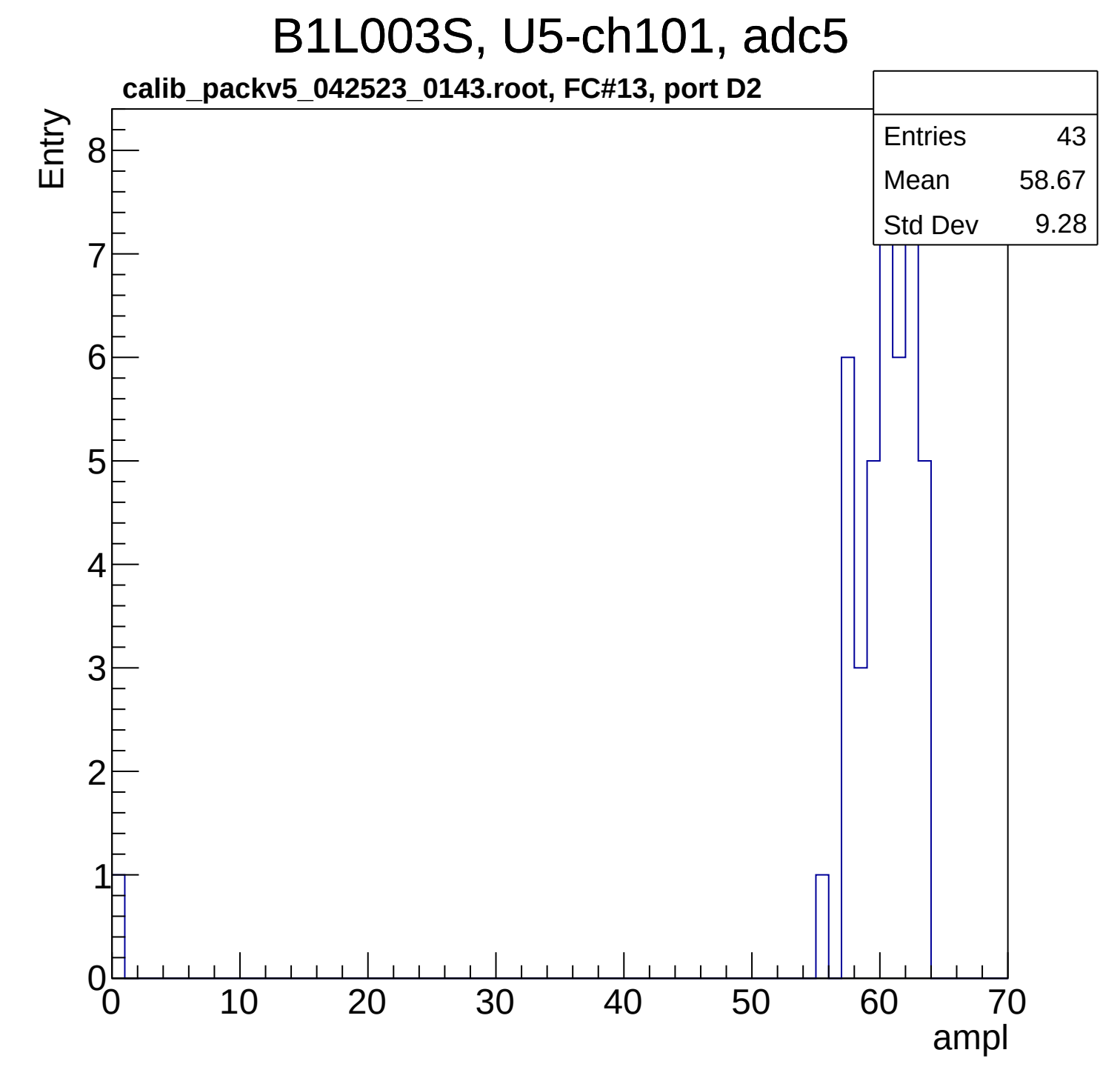
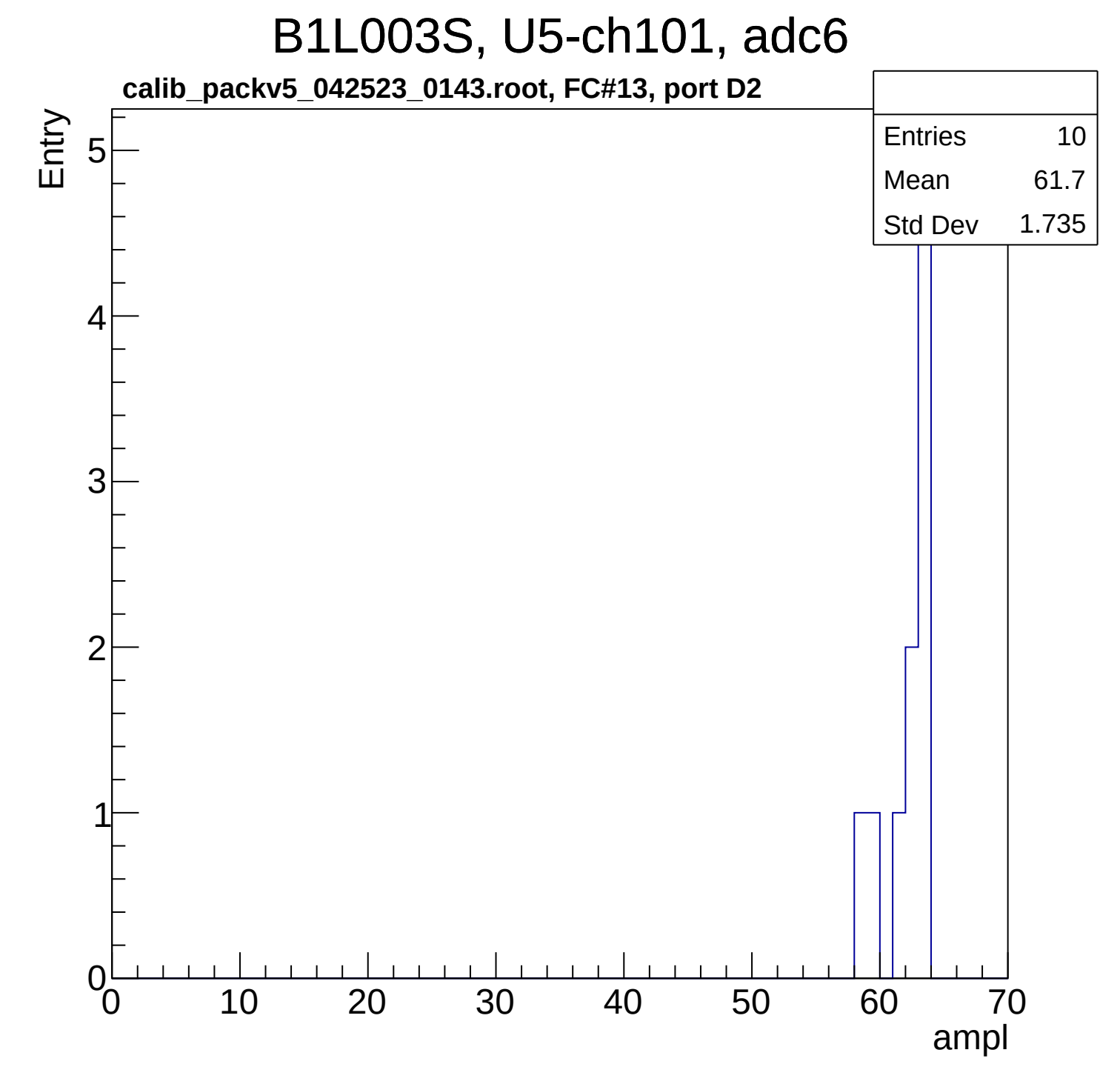
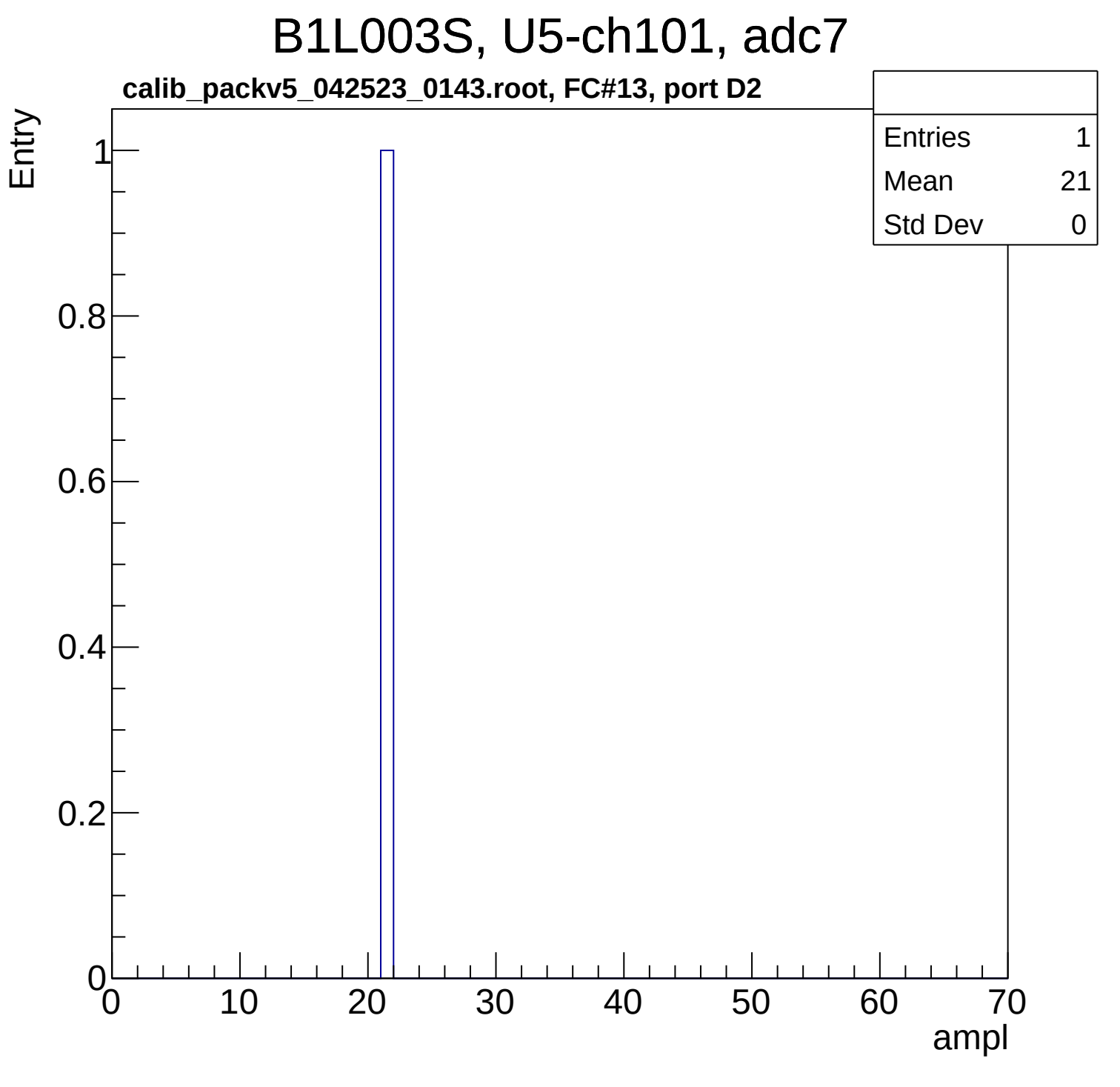
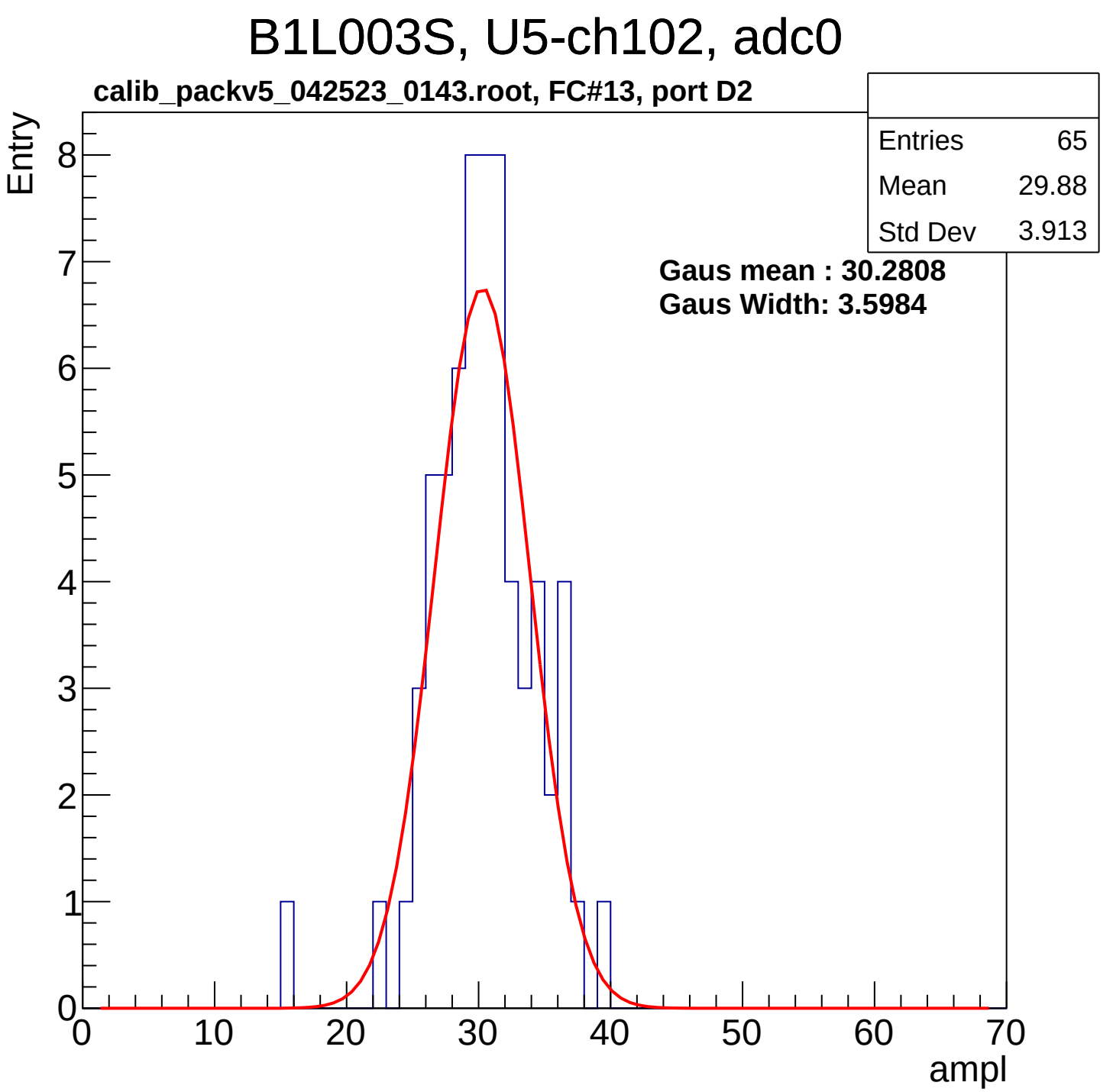

B1L003S, U5-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.45
Std Dev 3.217
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.97
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.67
Std Dev 9.28
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.7
Std Dev 1.735
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U5-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.88
Std Dev 3.913
Gaus mean : 30.2808
Gaus Width: 3.5984
Entry
ampl
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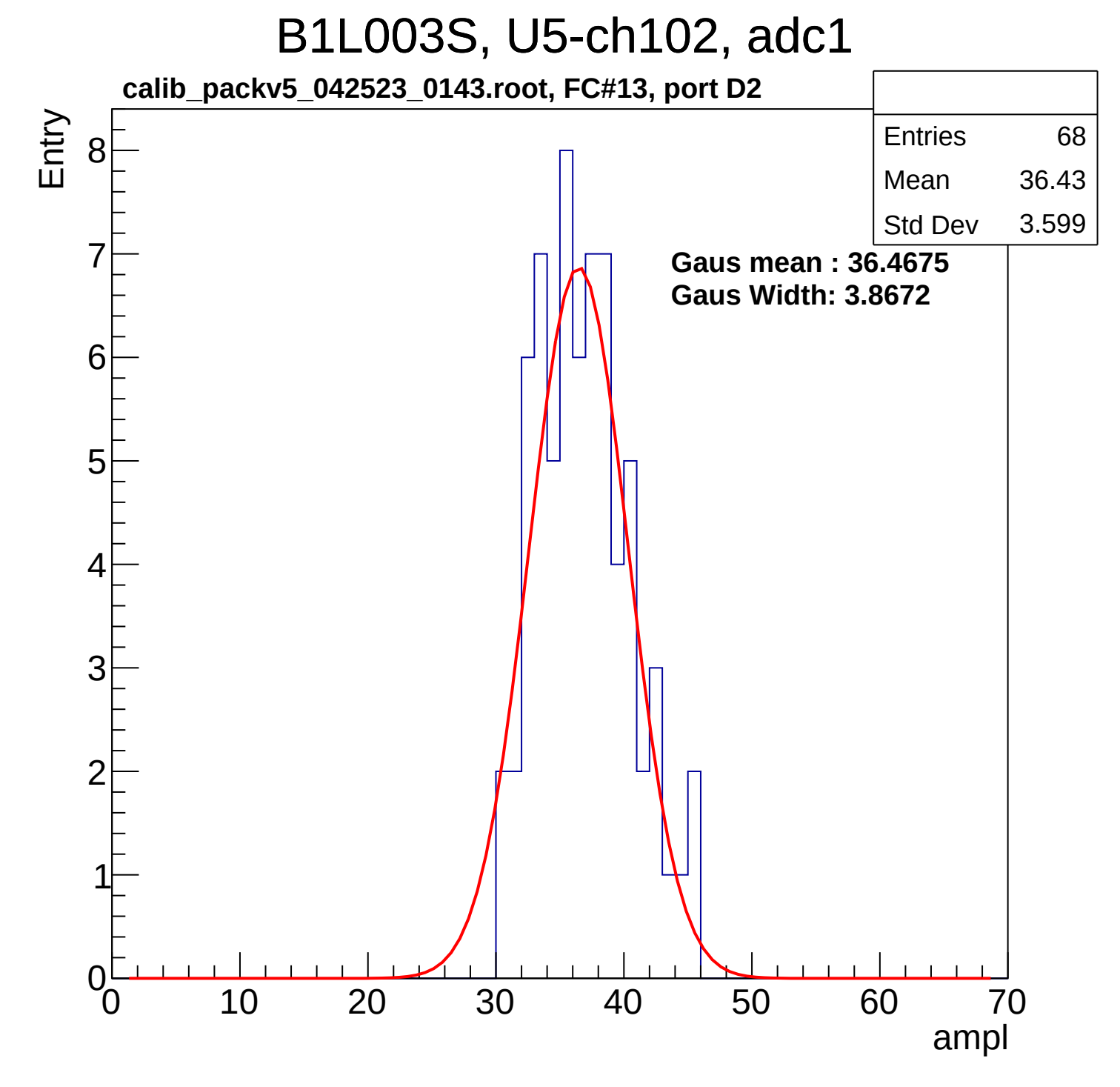
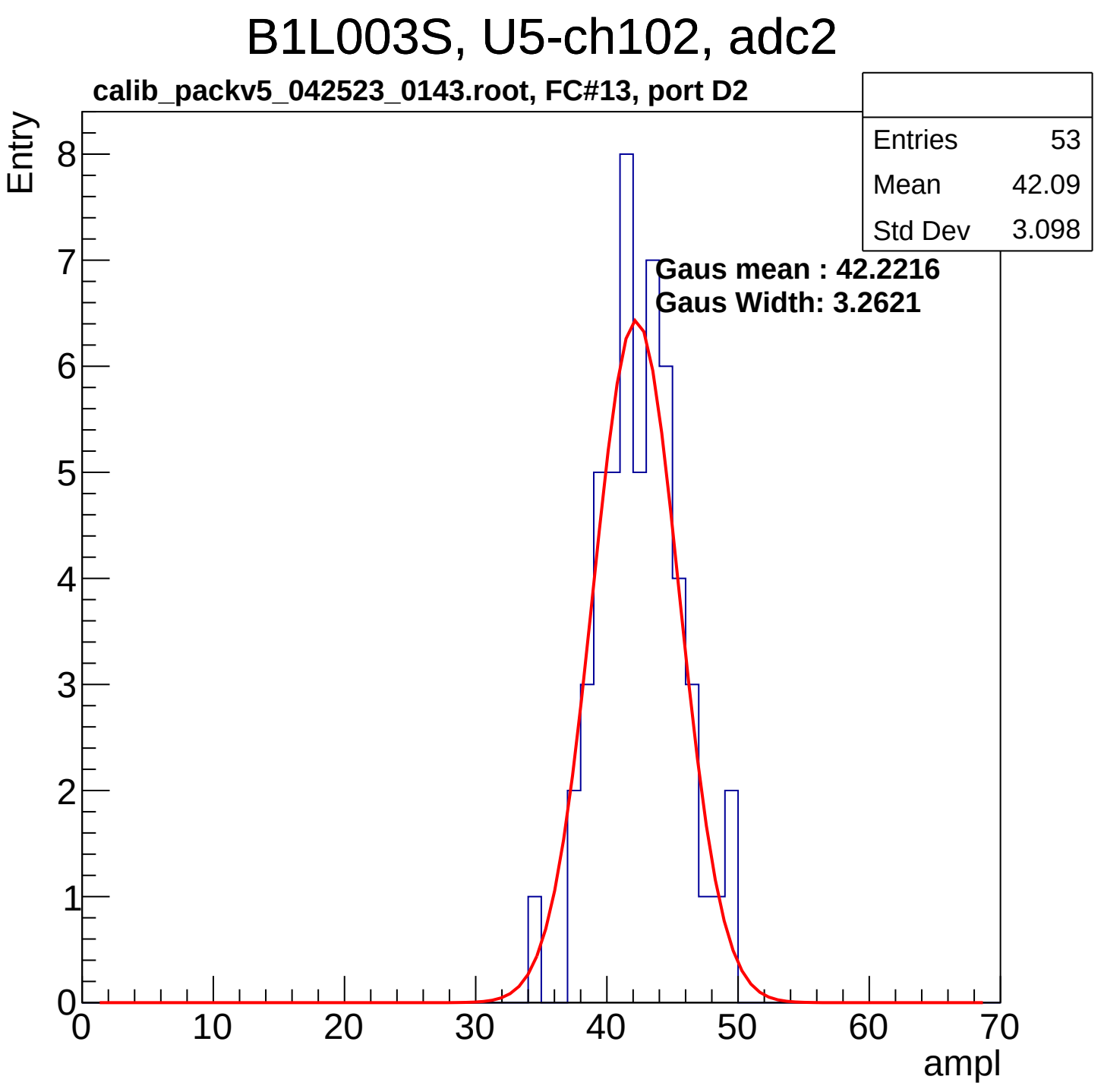
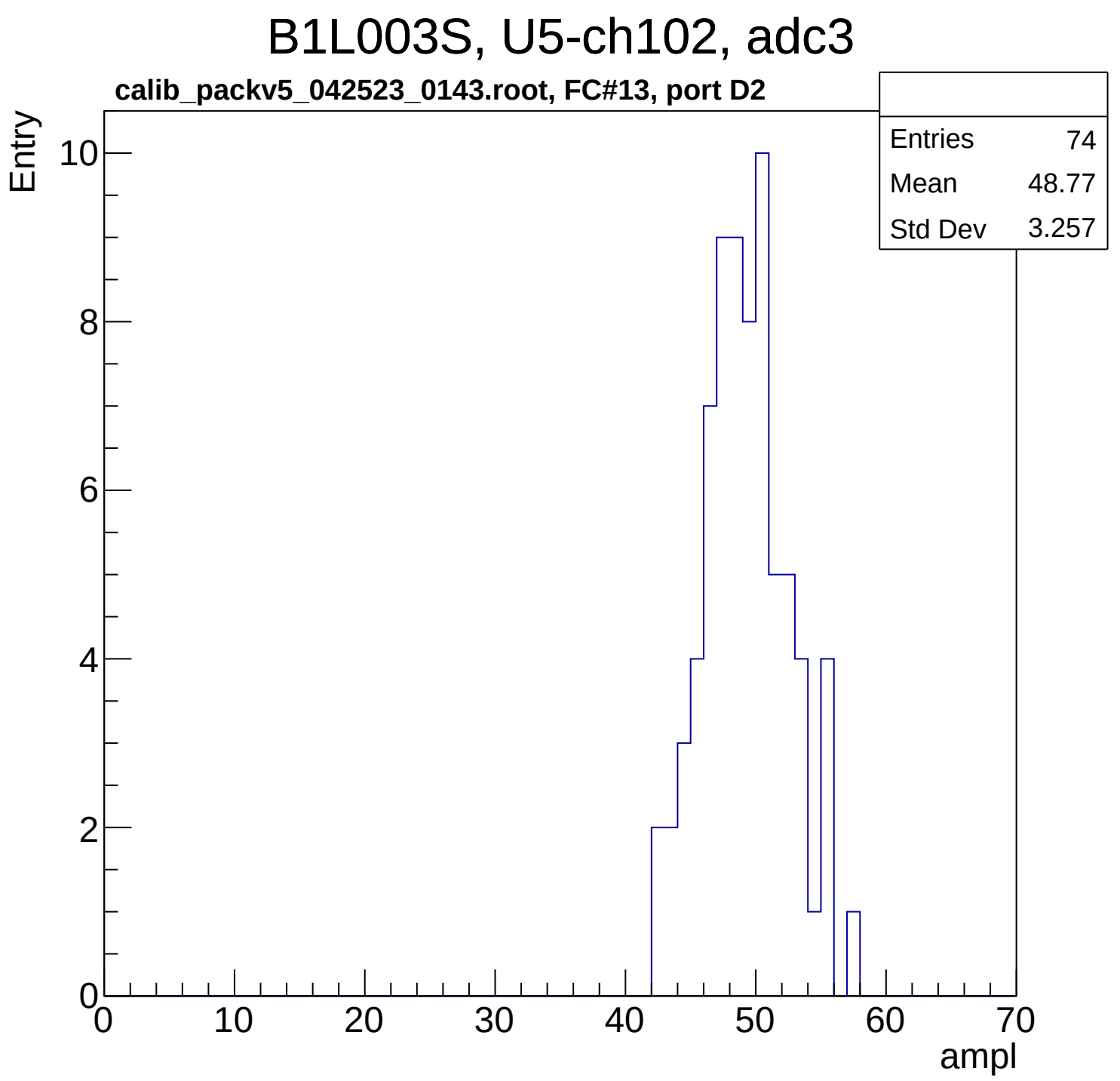
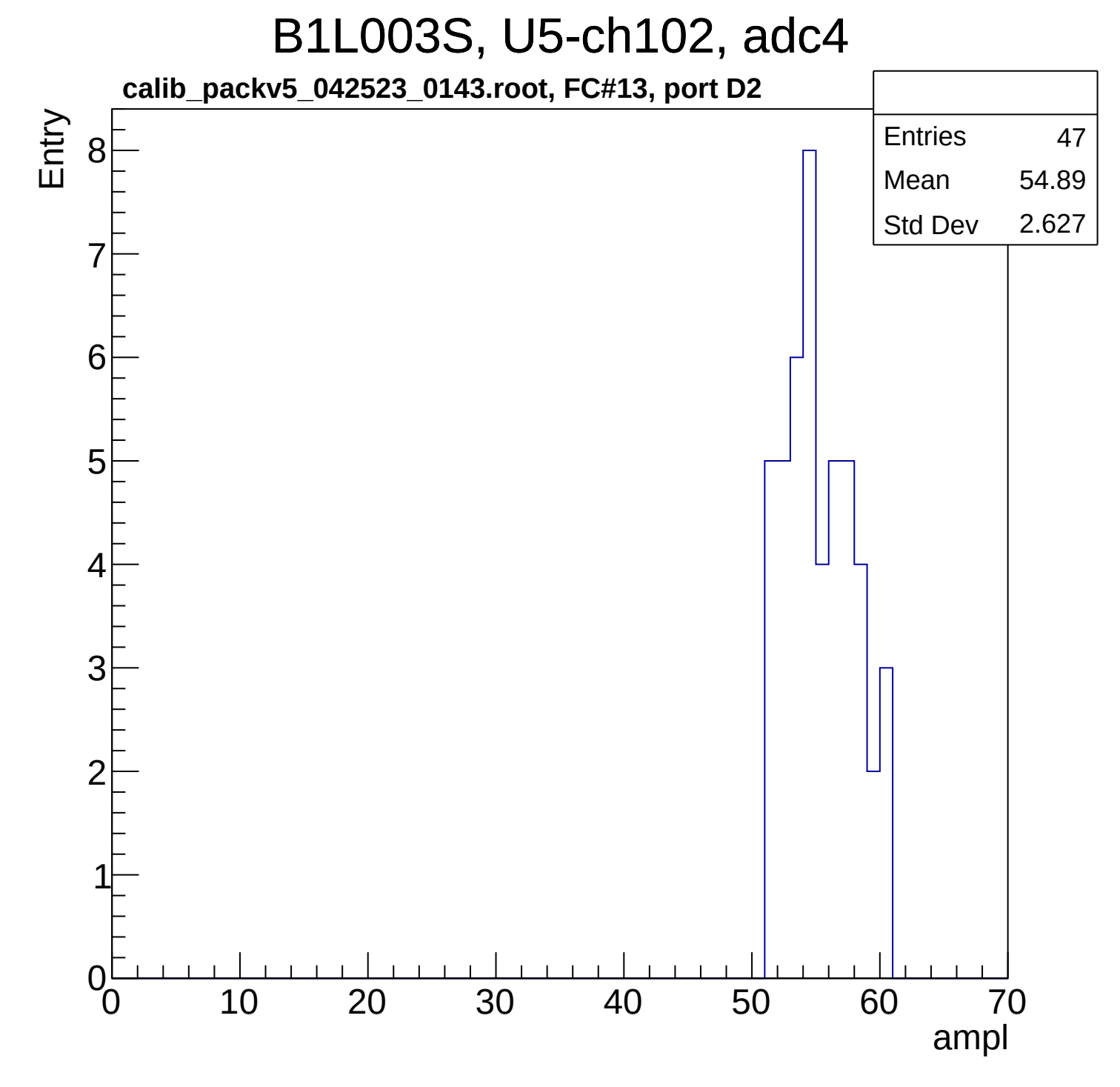
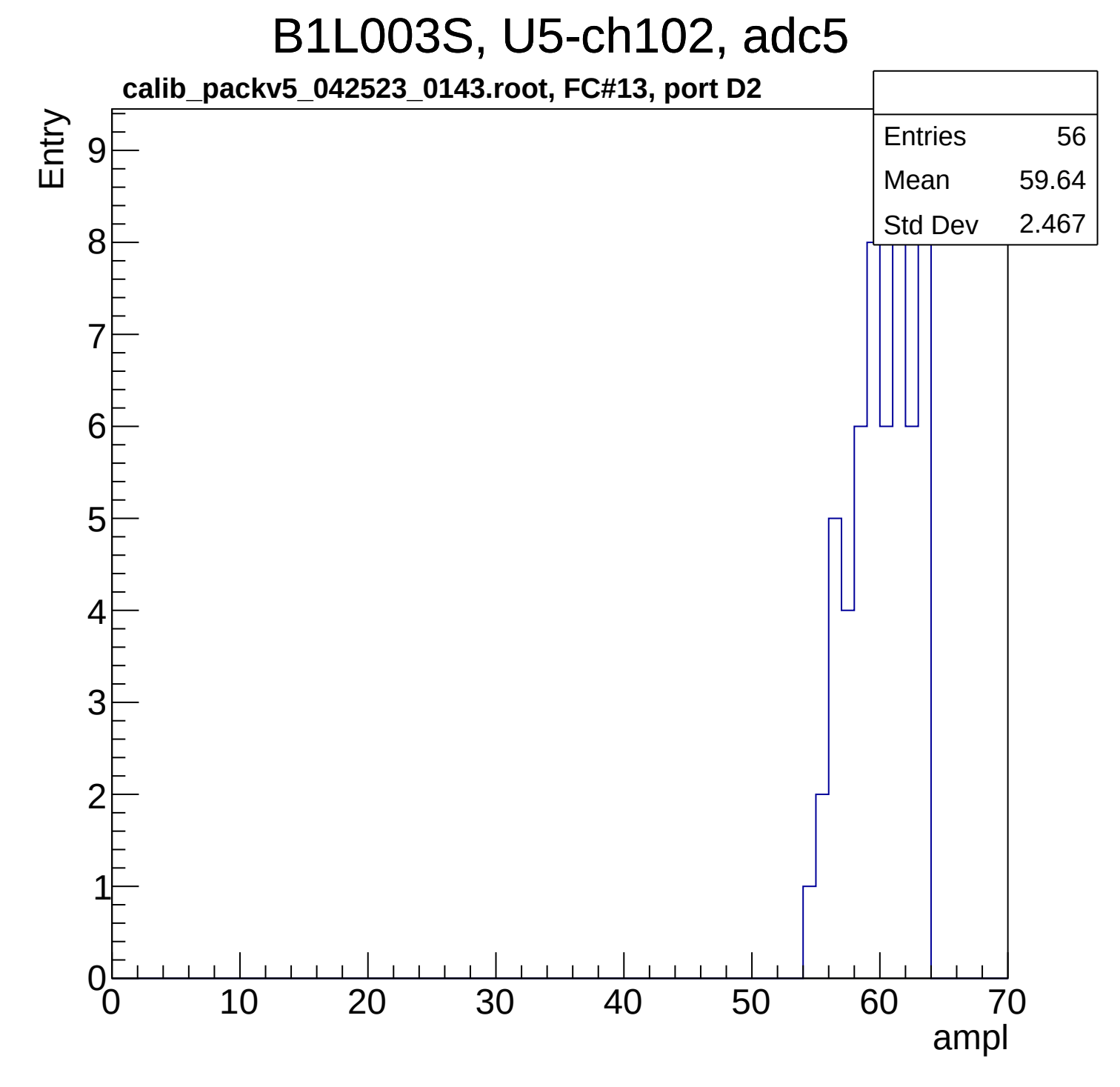
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.43
Std Dev 3.599
Gaus mean : 36.4675
Gaus Width: 3.8672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.09
Std Dev 3.098
Gaus mean : 42.2216
Gaus Width: 3.2621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.77
Std Dev 3.257
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.89
Std Dev 2.627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.64
Std Dev 2.467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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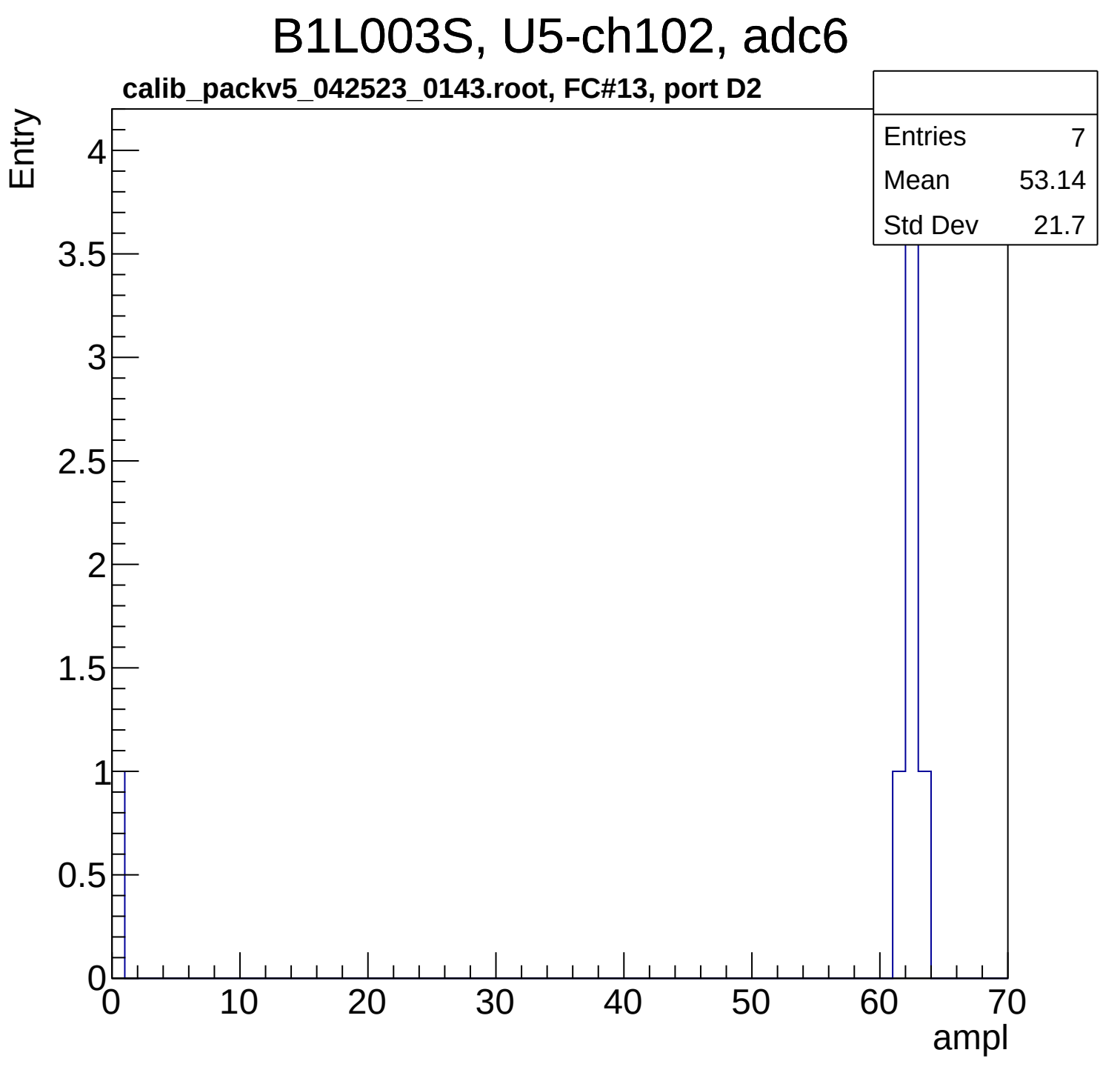
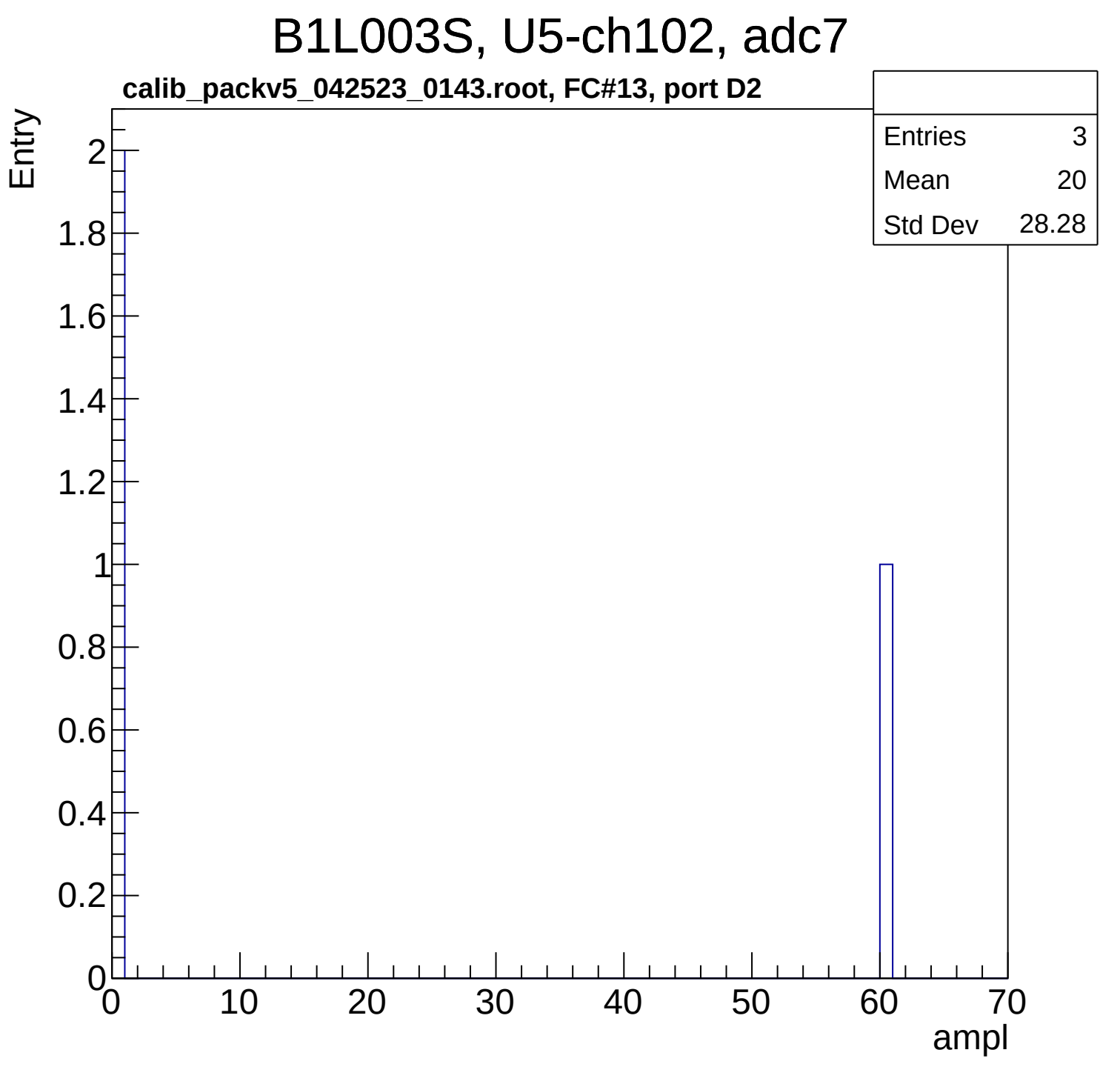

B1L003S, U5-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.14
Std Dev 21.7
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20
Std Dev 28.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
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60
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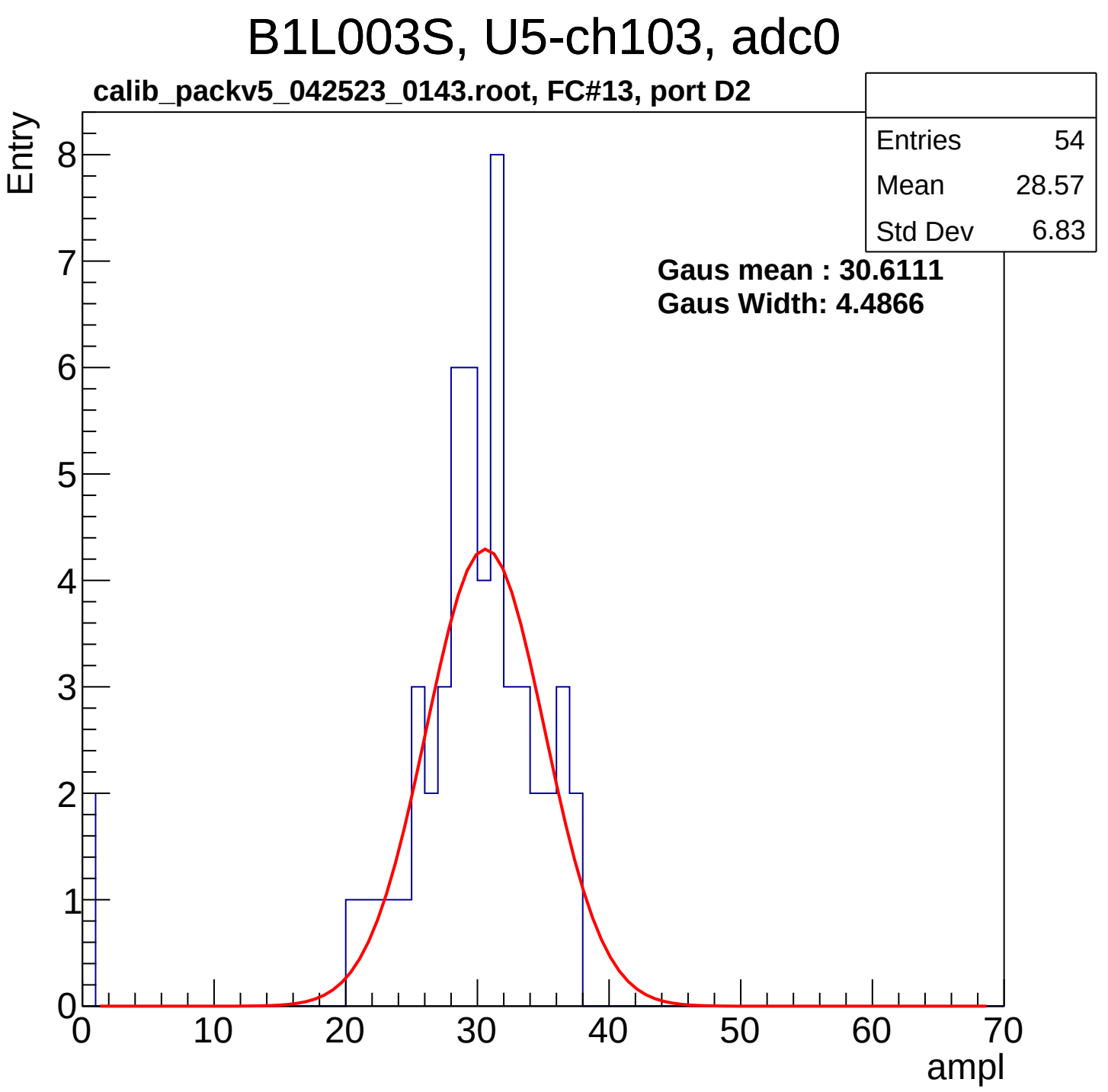
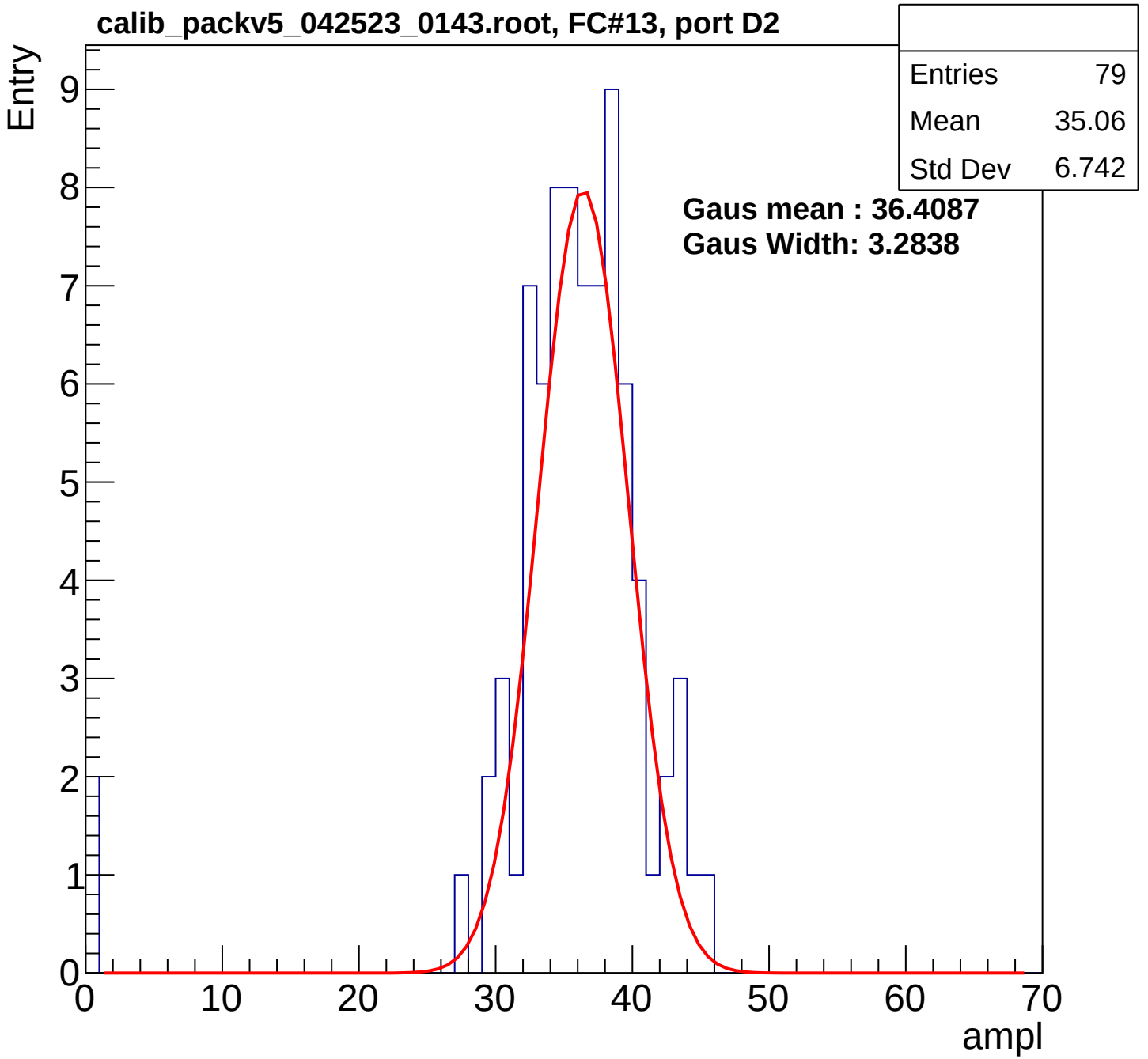
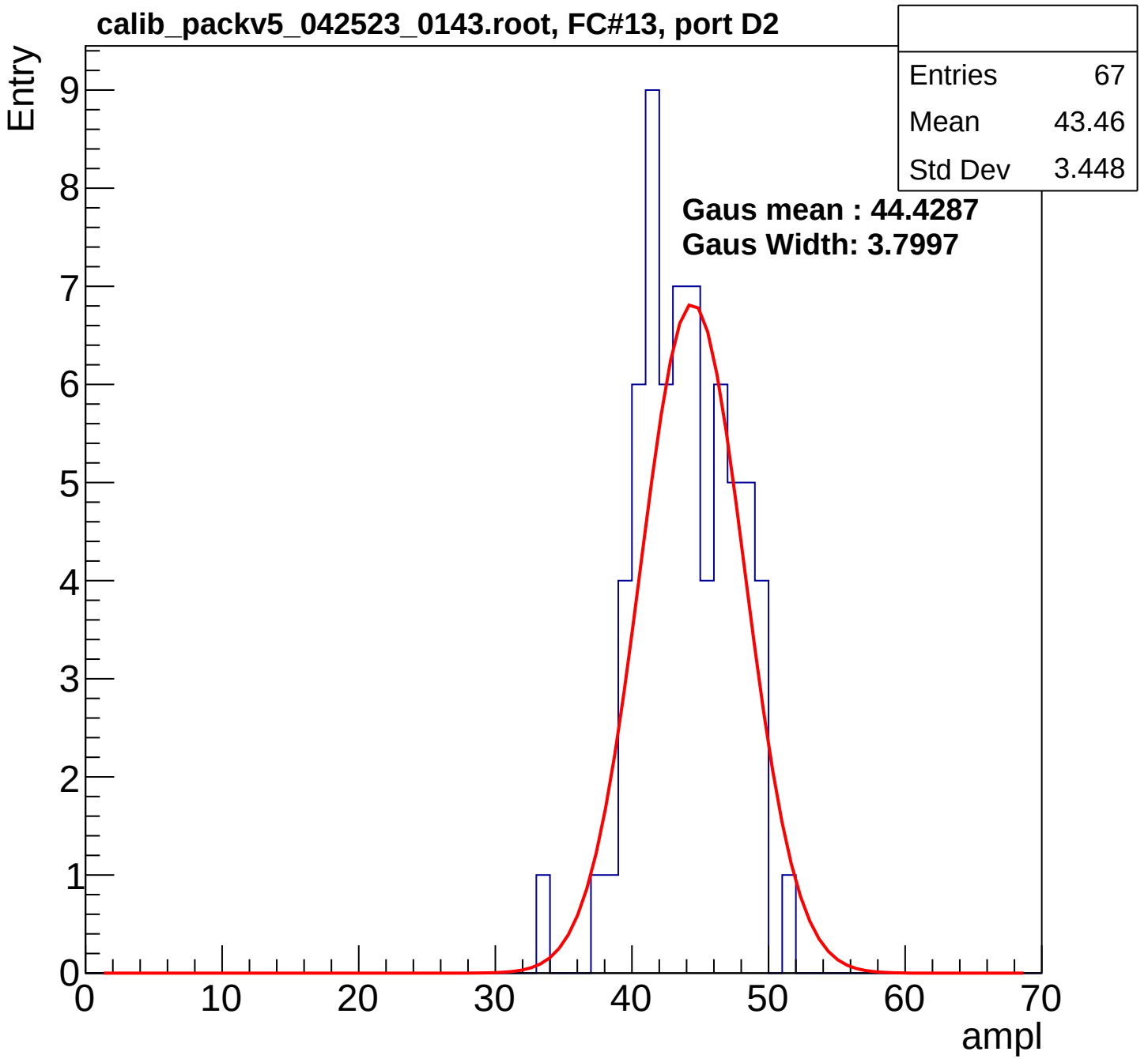
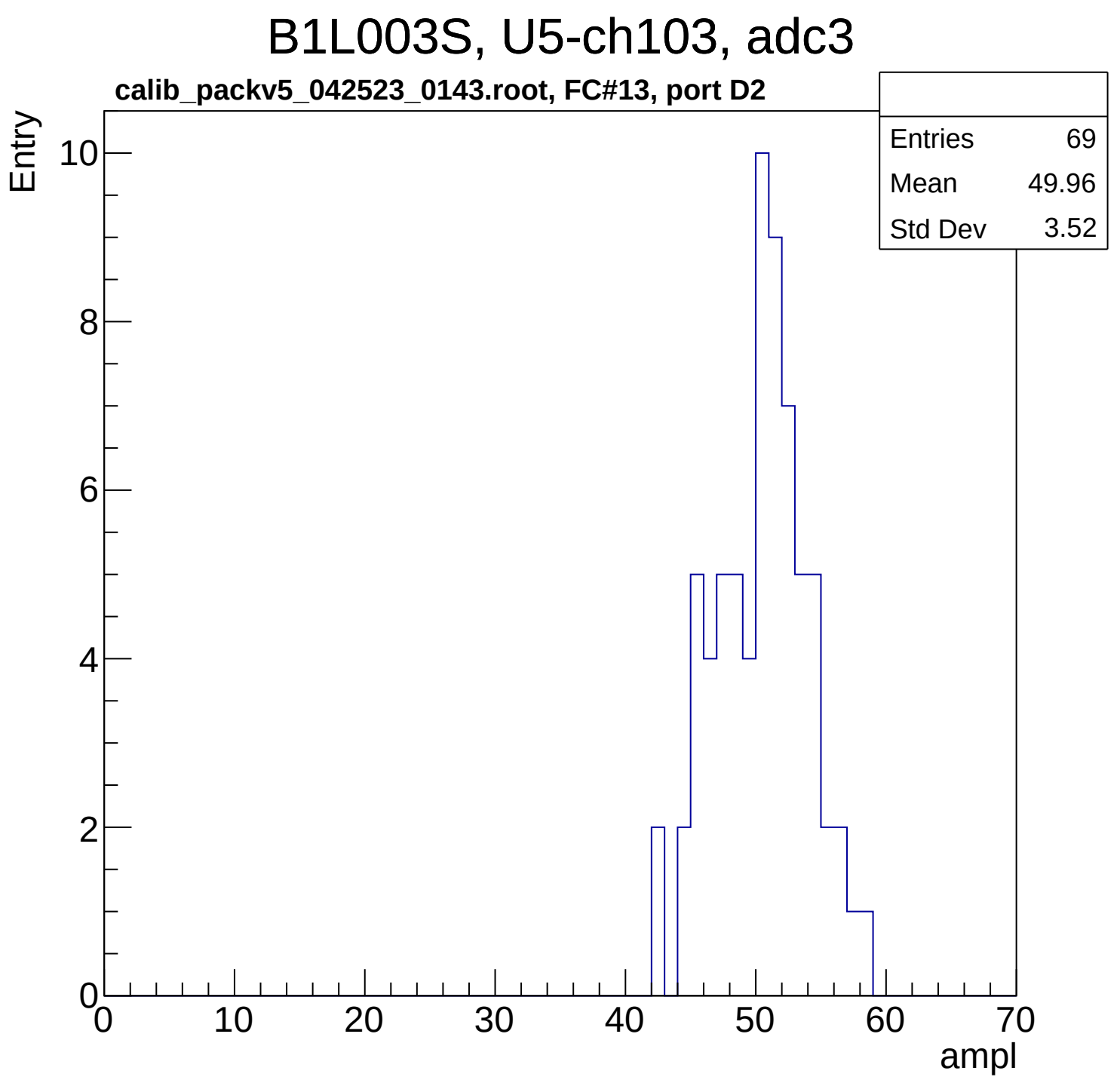
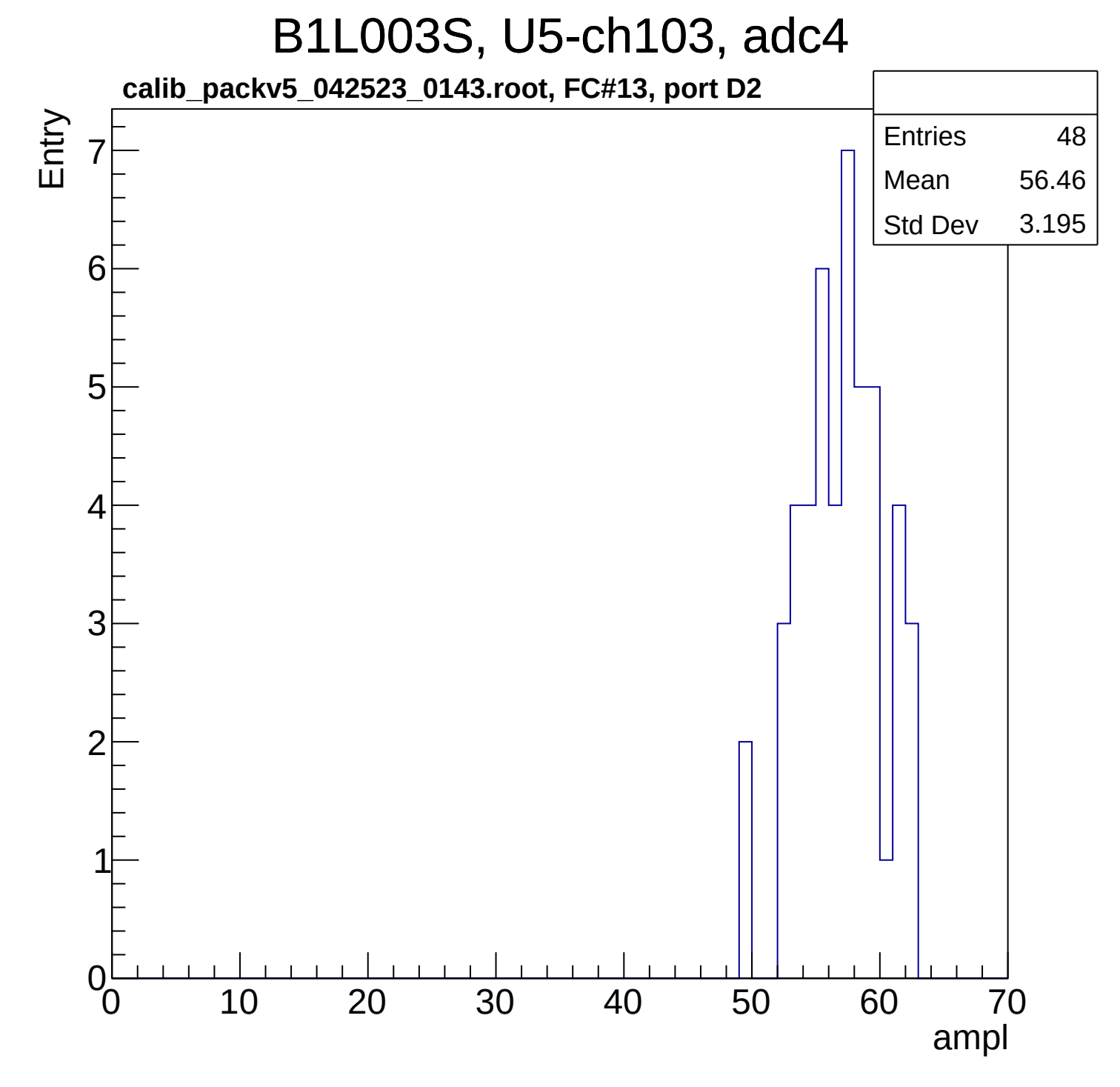

B1L003S, U5-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 28.57
Std Dev 6.83
Gaus mean : 30.6111
Gaus Width: 4.4866
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 35.06
Std Dev 6.742
Gaus mean : 36.4087
Gaus Width: 3.2838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.46
Std Dev 3.448
Gaus mean : 44.4287
Gaus Width: 3.7997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.96
Std Dev 3.52
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 56.46
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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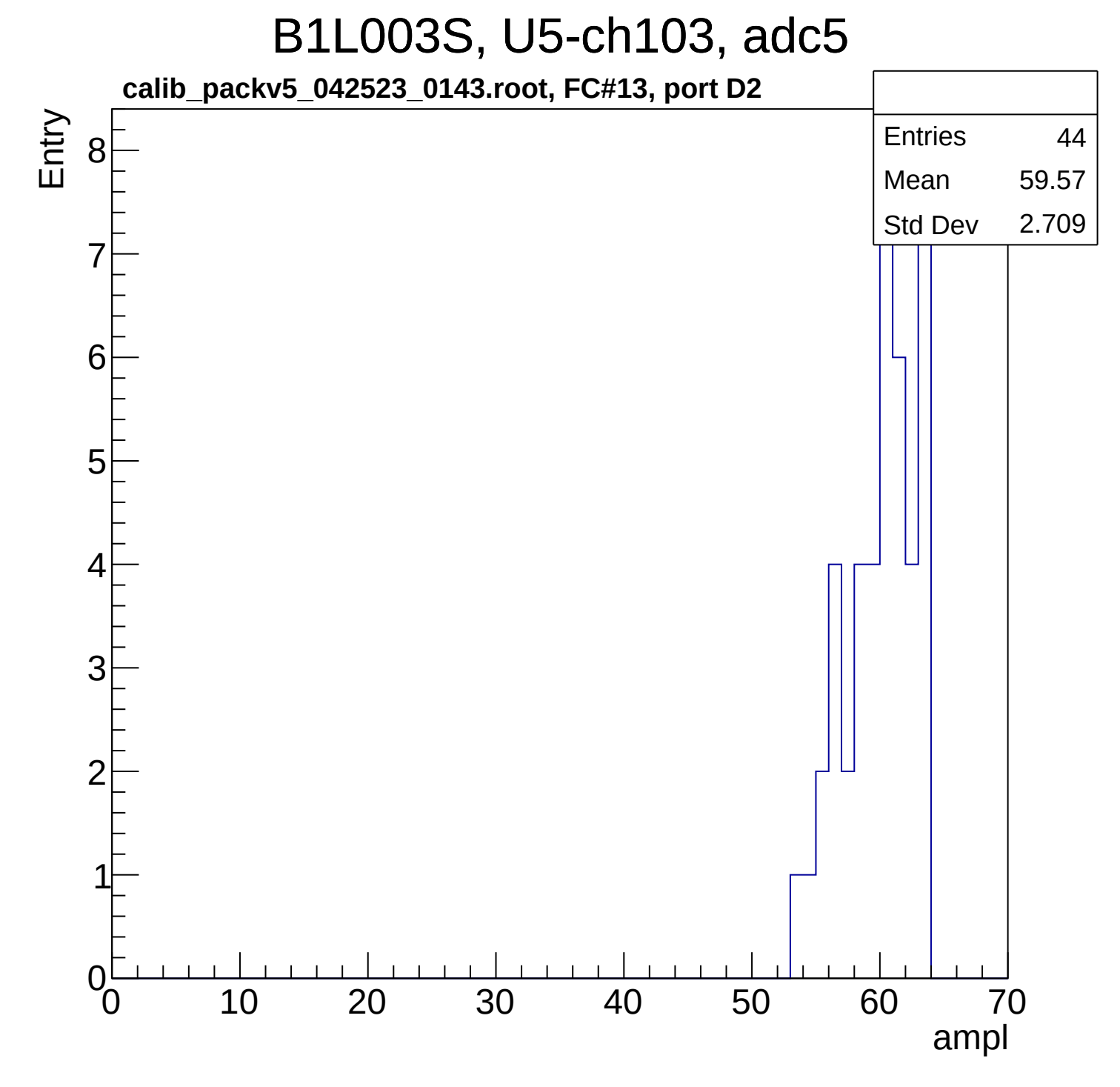
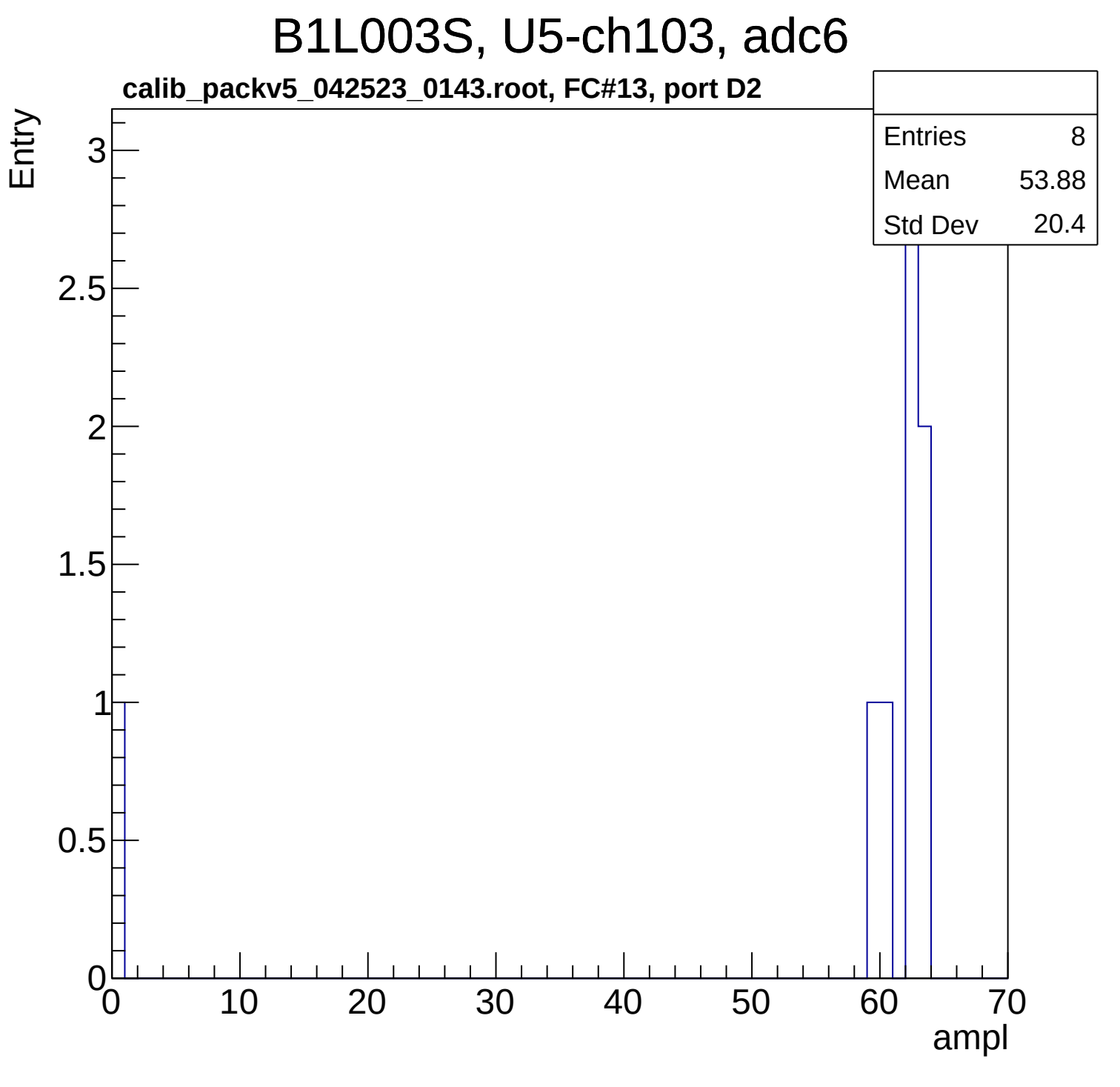
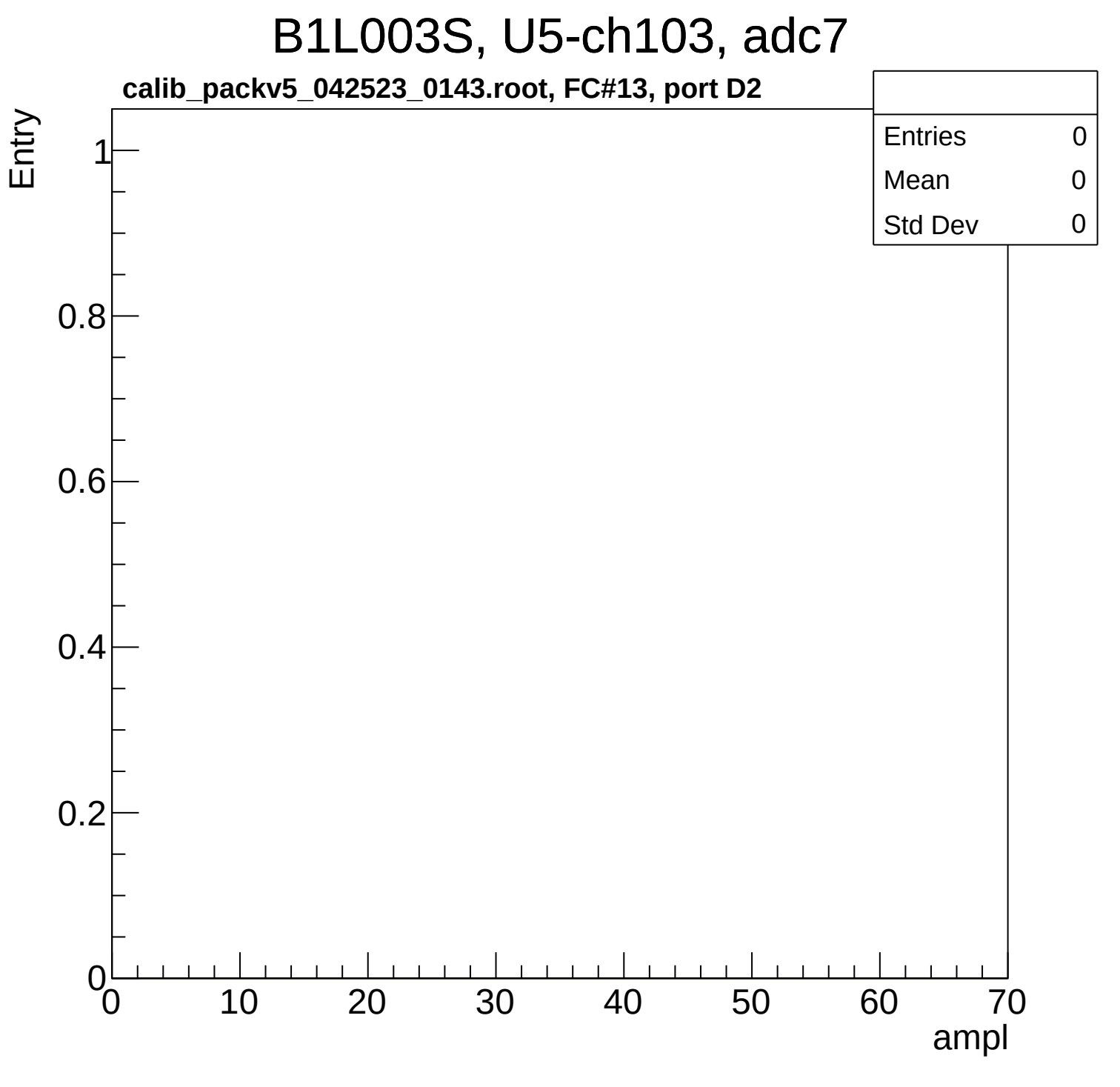

B1L003S, U5-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.57
Std Dev 2.709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 53.88
Std Dev 20.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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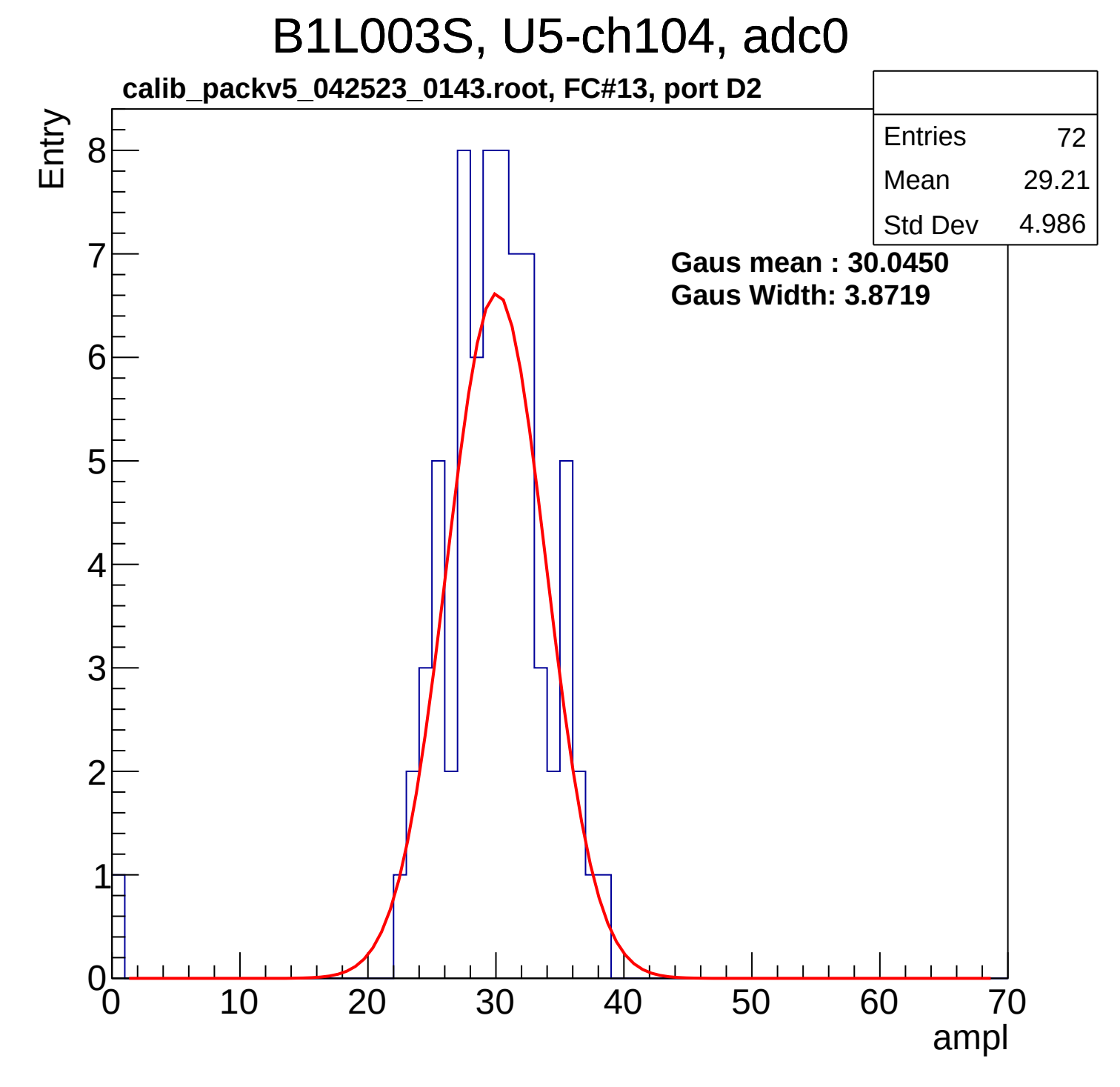
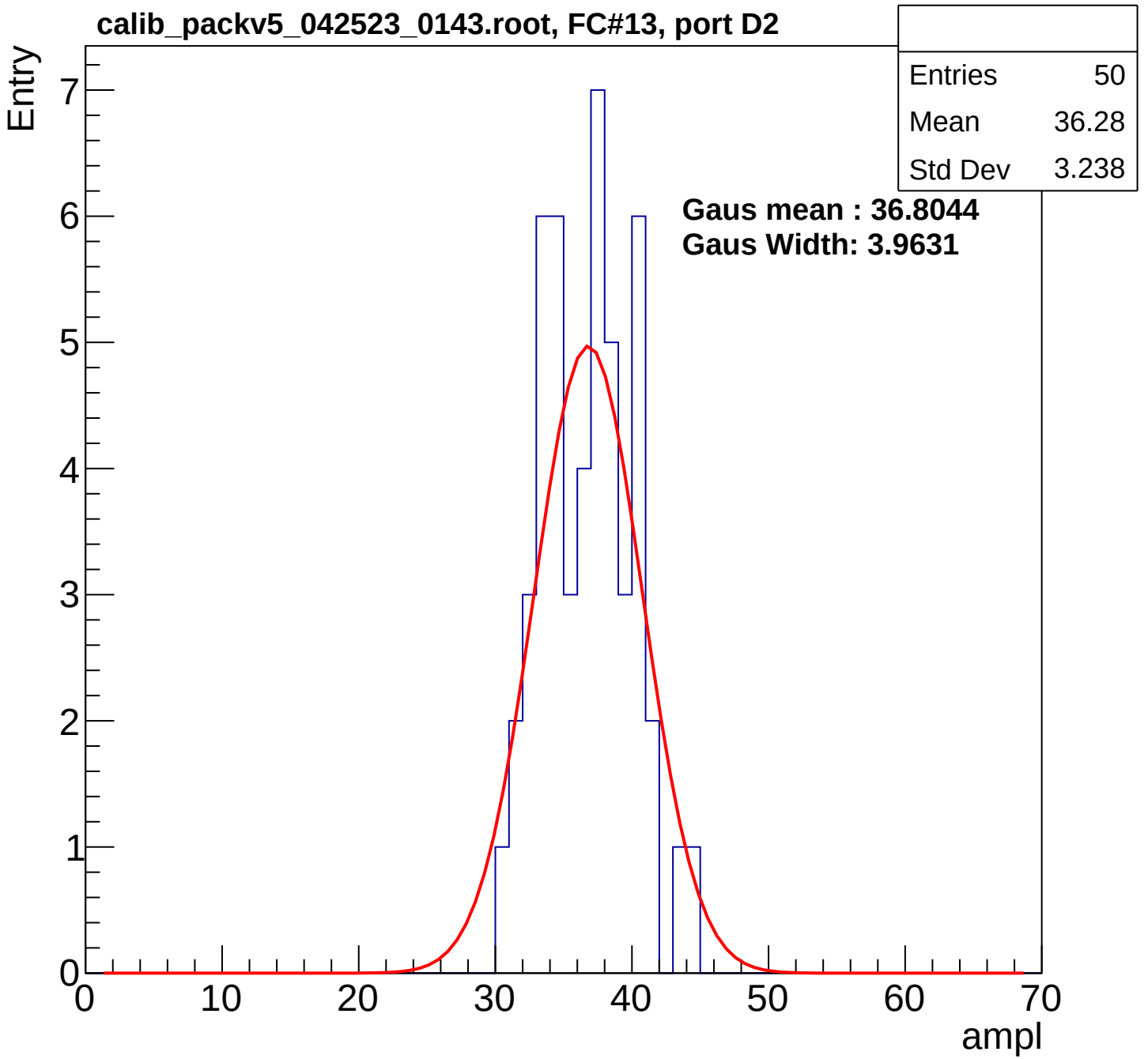
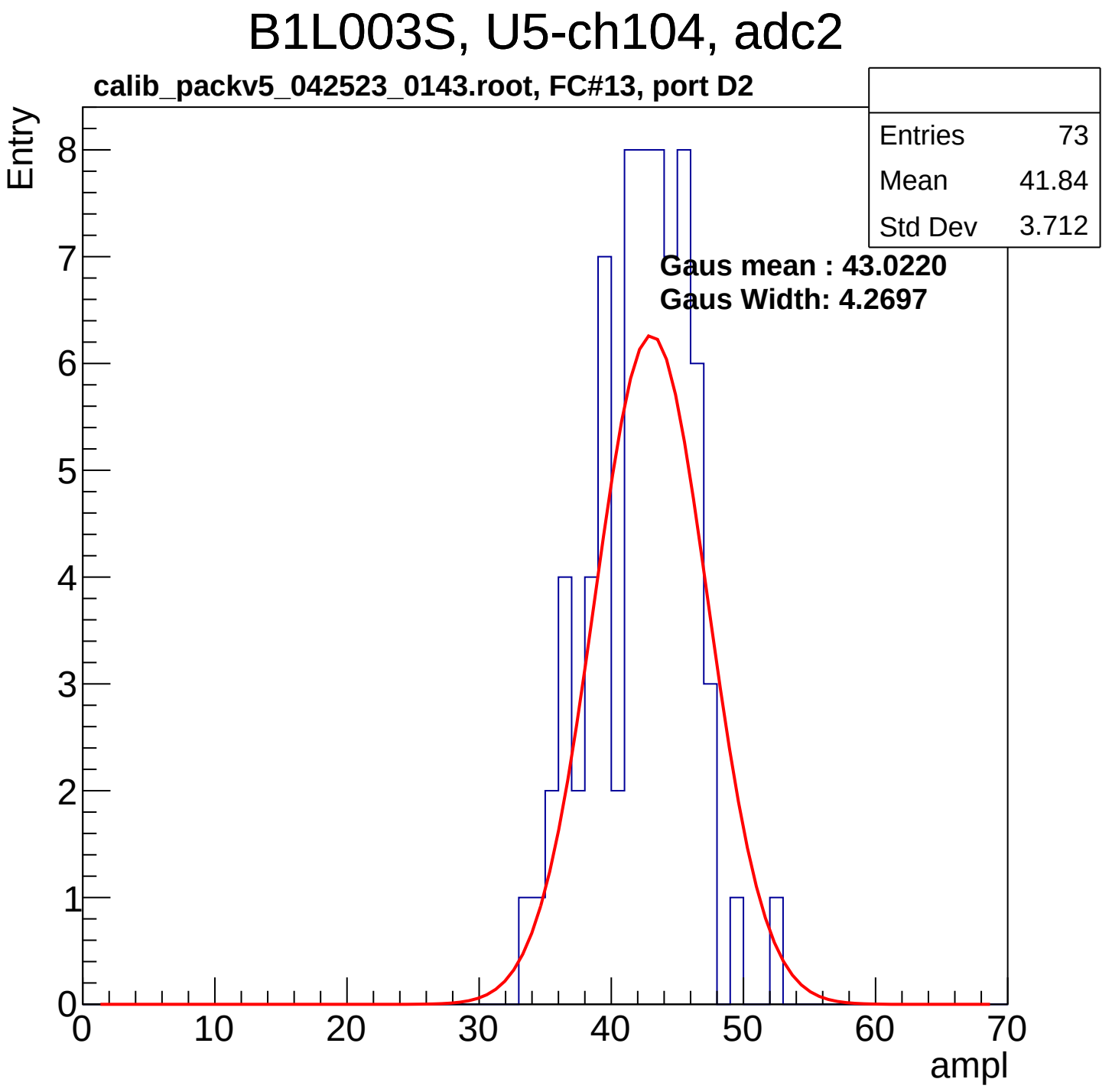
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.21
Std Dev 4.986
Gaus mean : 30.0450
Gaus Width: 3.8719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 36.28
Std Dev 3.238
Gaus mean : 36.8044
Gaus Width: 3.9631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.84
Std Dev 3.712
Gaus mean : 43.0220
Gaus Width: 4.2697
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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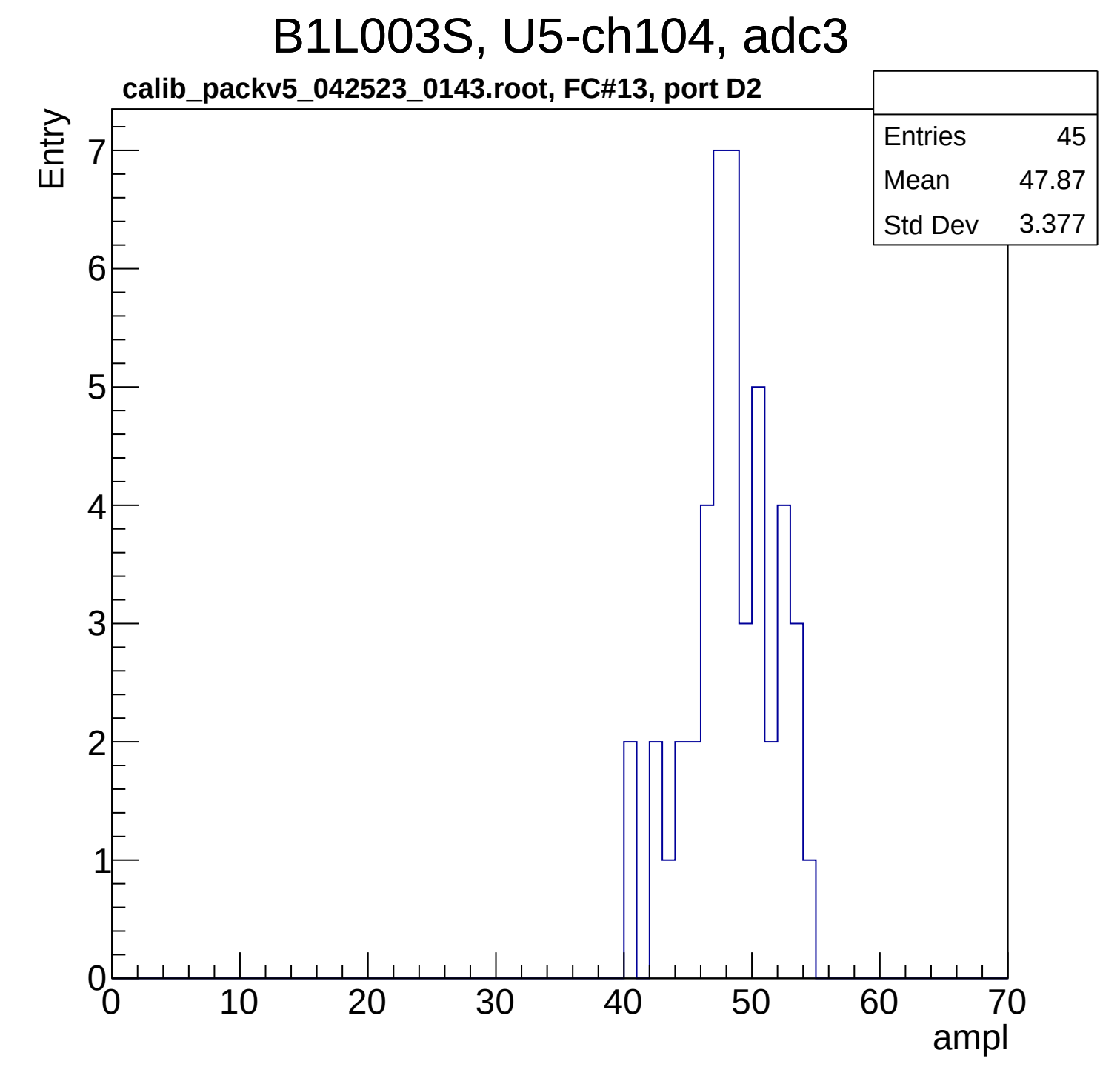
B1L003S, U5-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 47.87
Std Dev 3.377
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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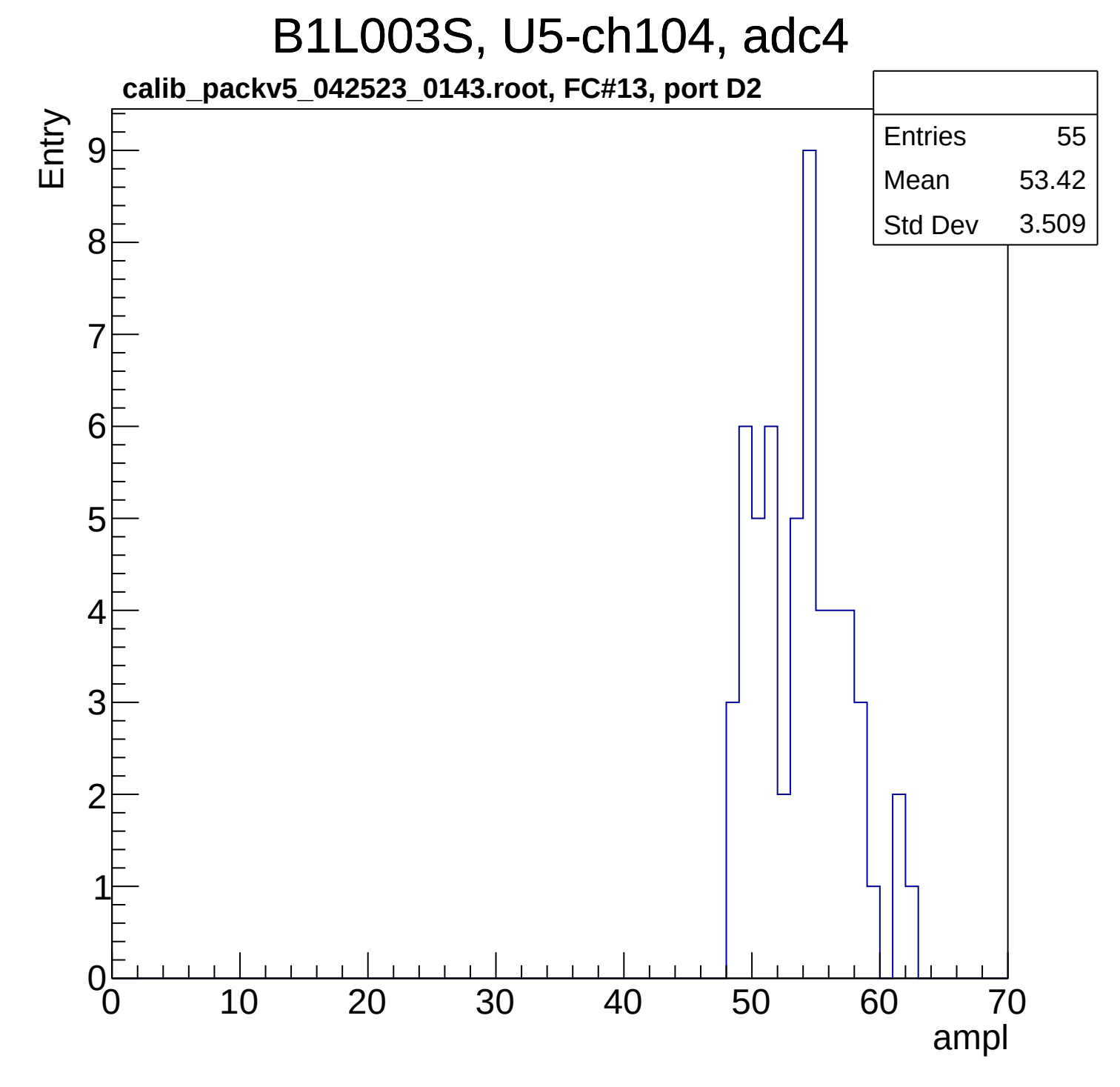
B1L003S, U5-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.42
Std Dev 3.509
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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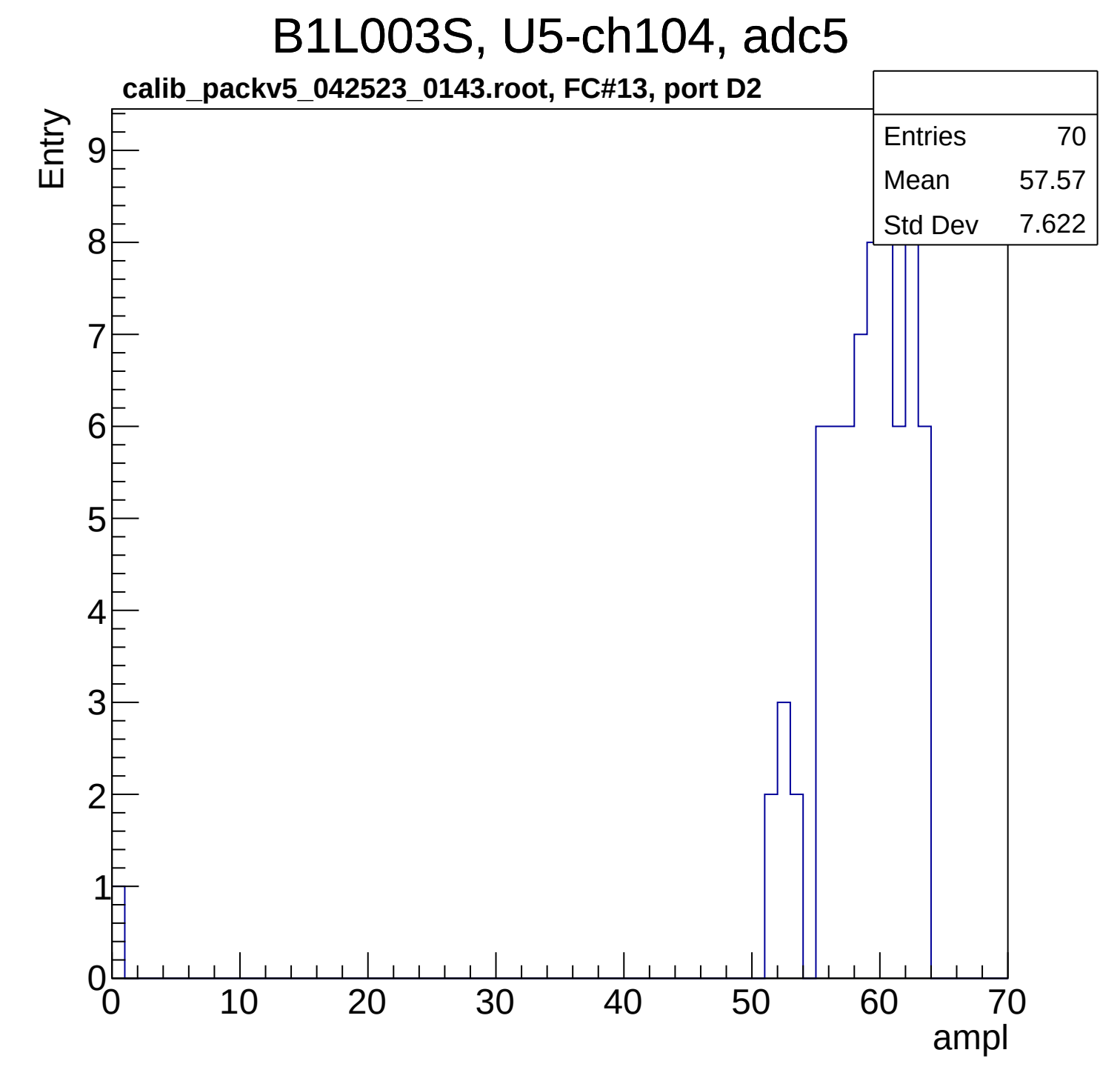
B1L003S, U5-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 57.57
Std Dev 7.622
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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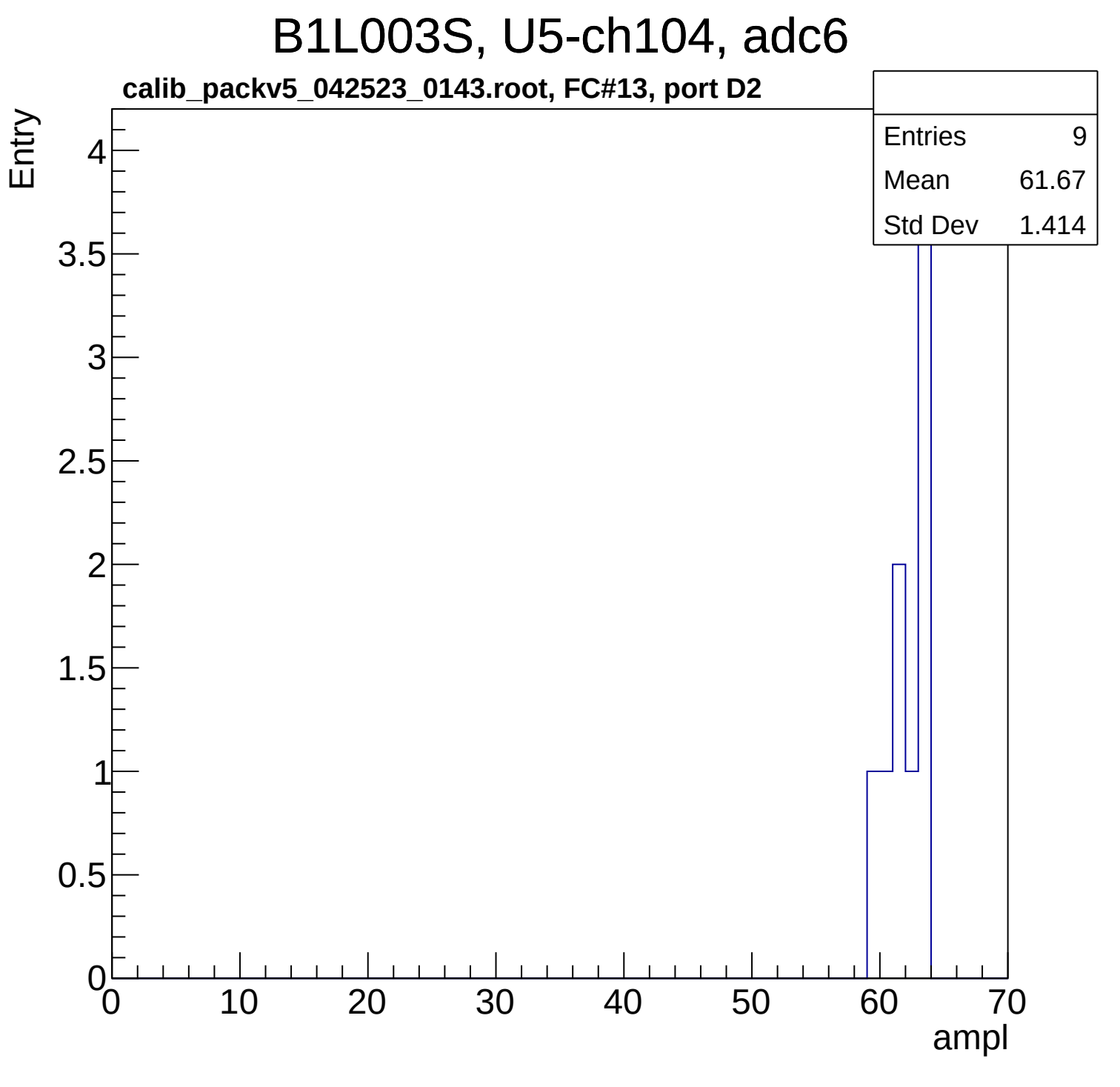
B1L003S, U5-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.414
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
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30
40
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60
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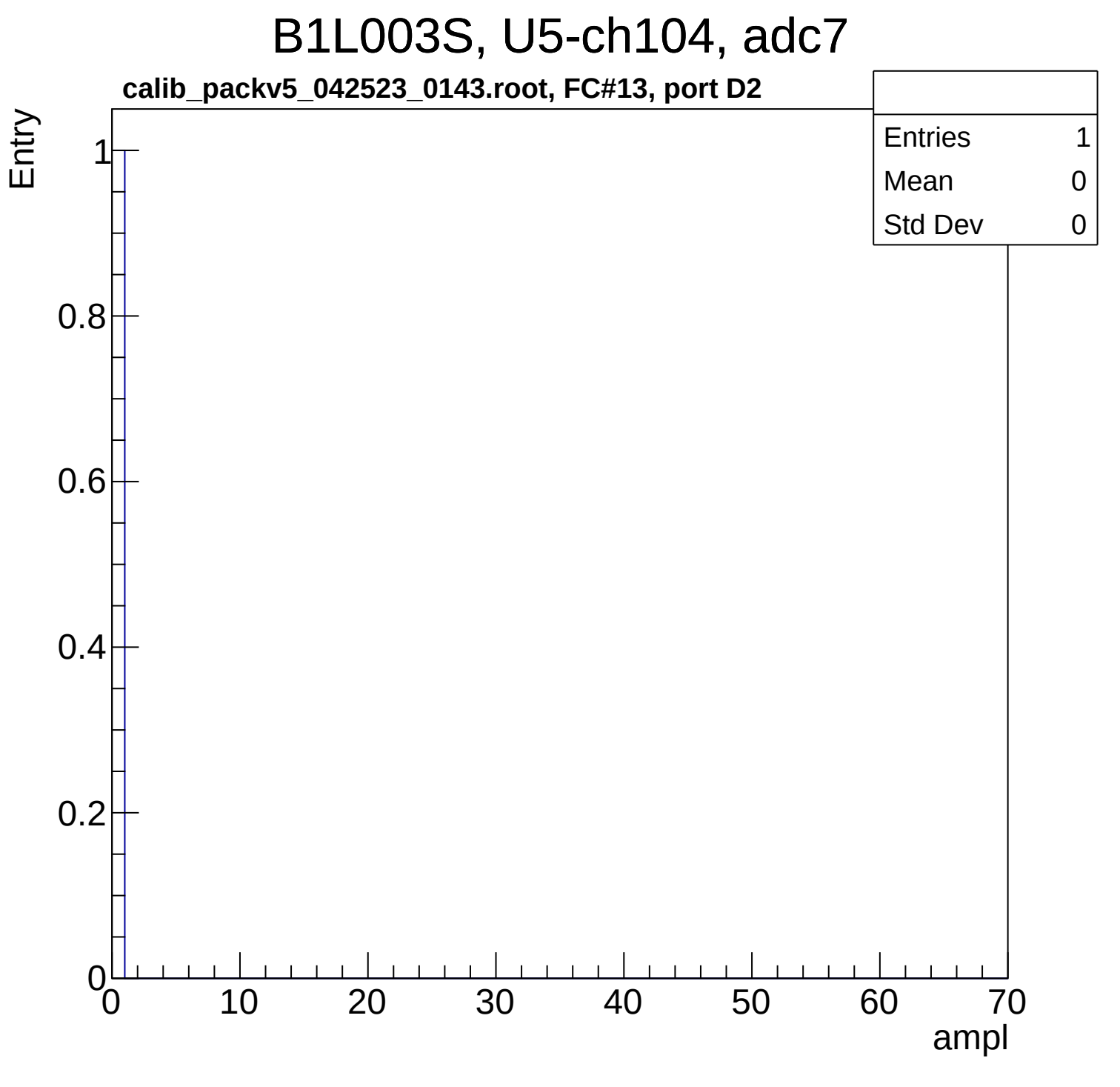
B1L003S, U5-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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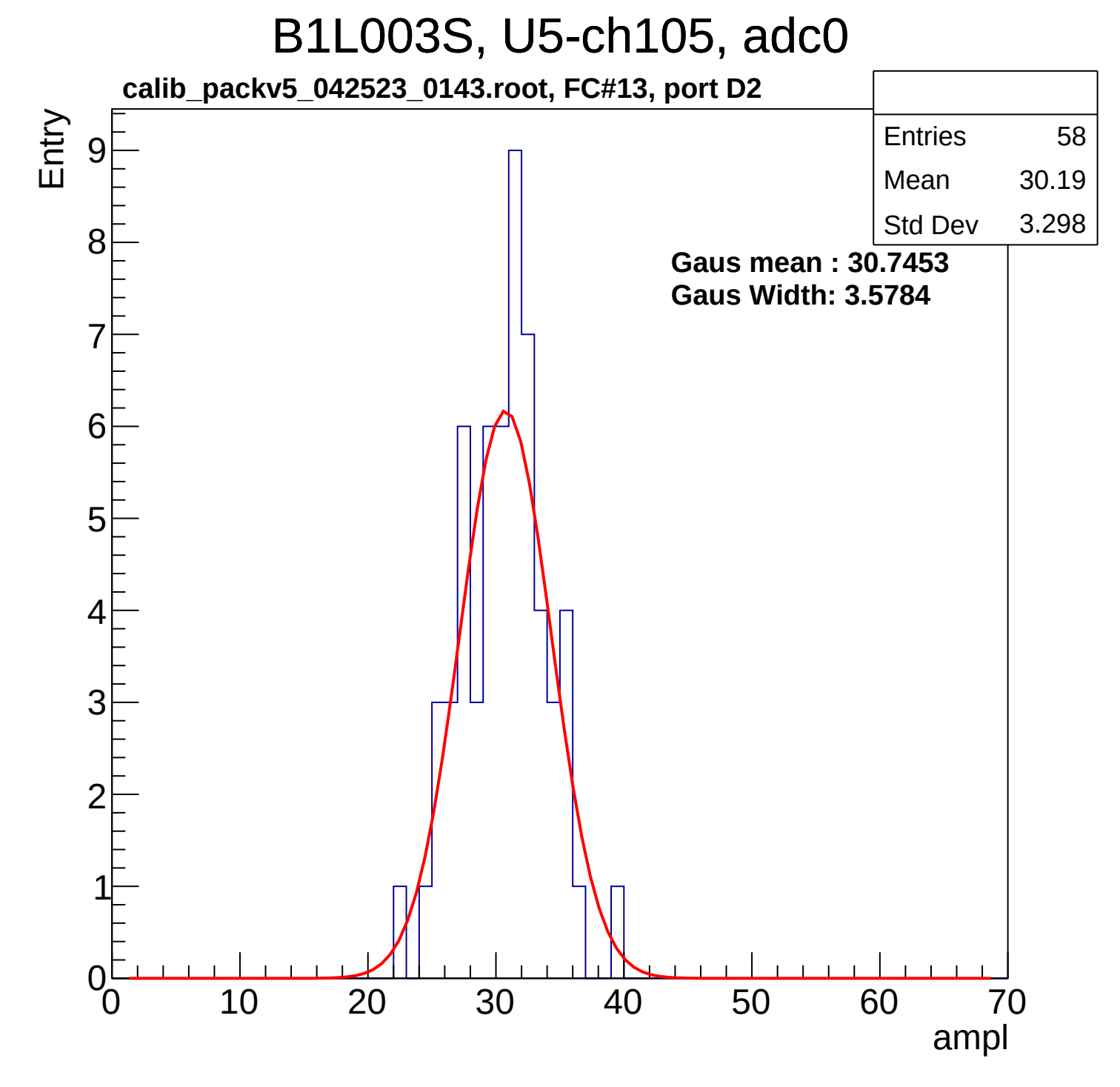
40
50
60
70
ampl


B1L003S, U5-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 30.19
Std Dev 3.298
Gaus mean : 30.7453
Gaus Width: 3.5784
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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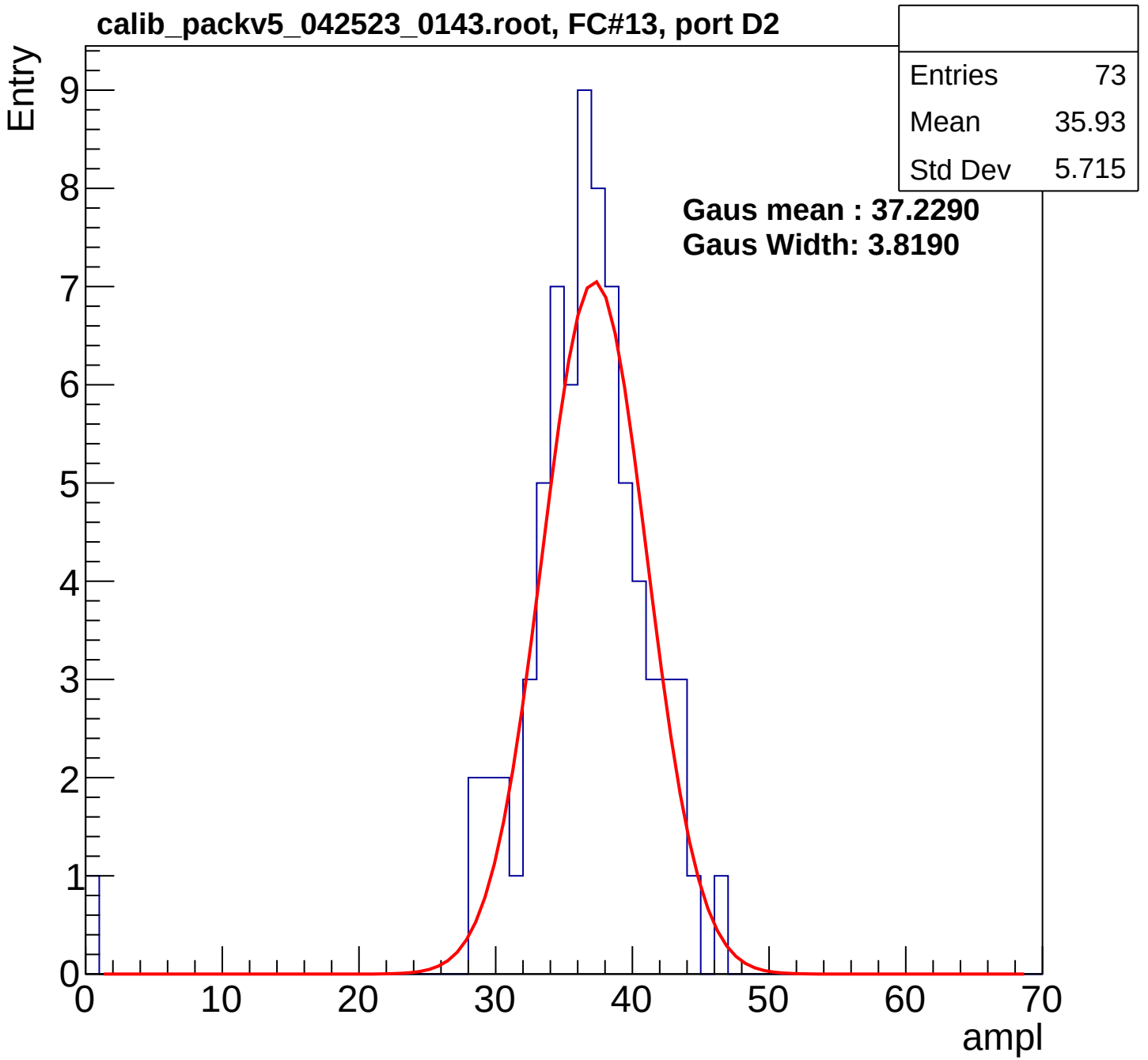
B1L003S, U5-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.93
Std Dev 5.715
Gaus mean : 37.2290
Gaus Width: 3.8190
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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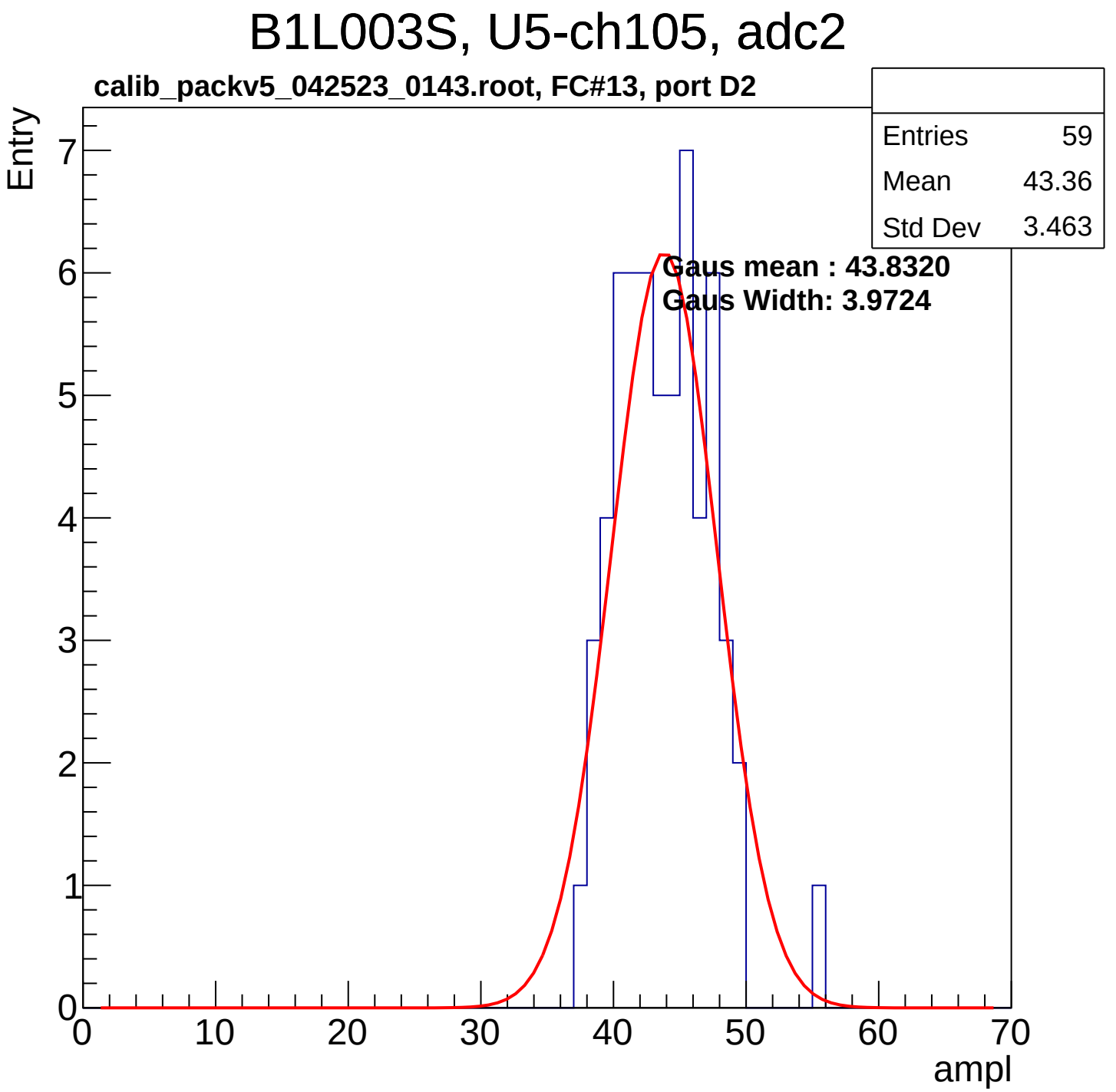
B1L003S, U5-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.36
Std Dev 3.463
Gaus mean : 43.8320
Gaus Width: 3.9724
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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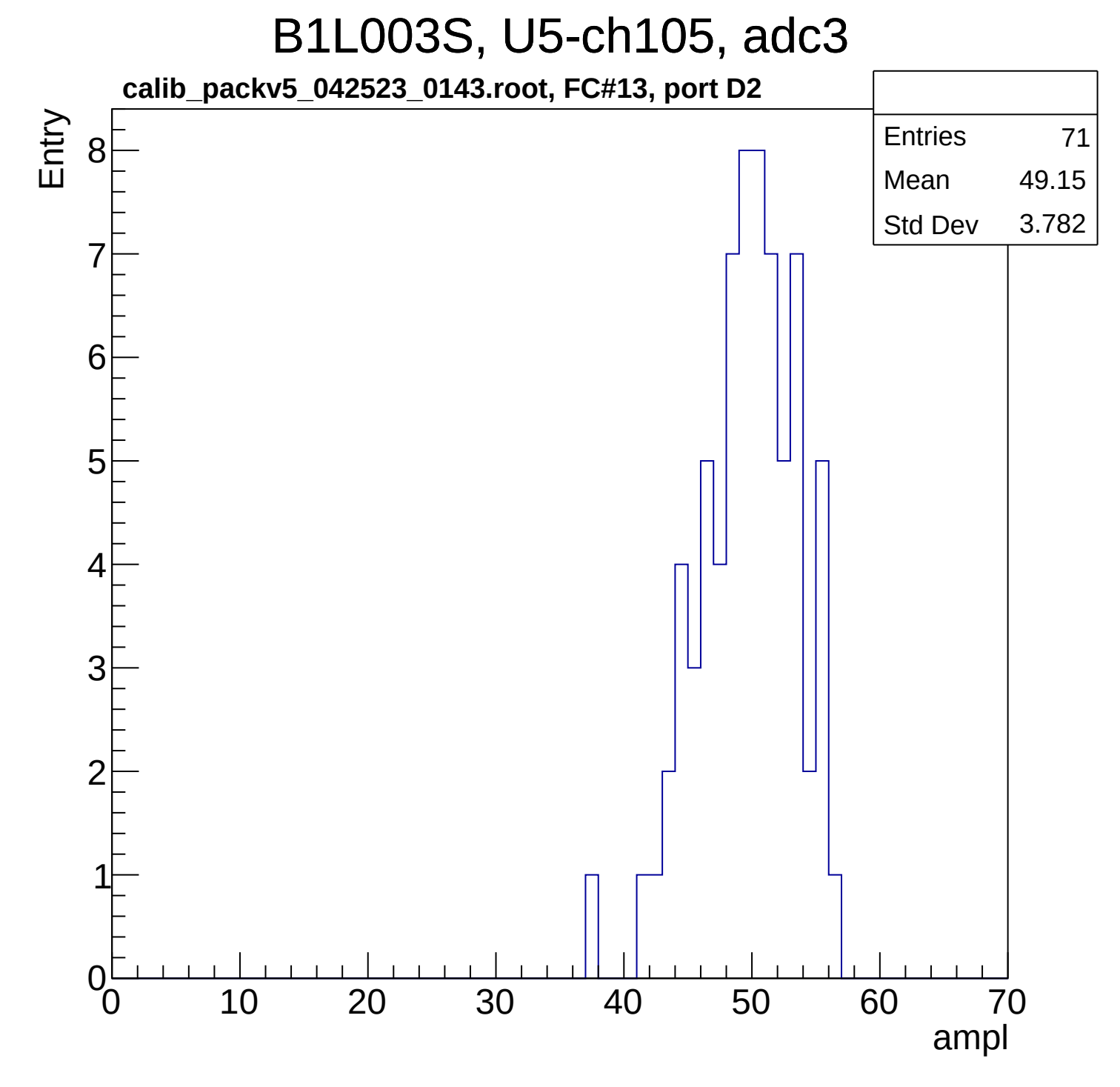
B1L003S, U5-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.15
Std Dev 3.782
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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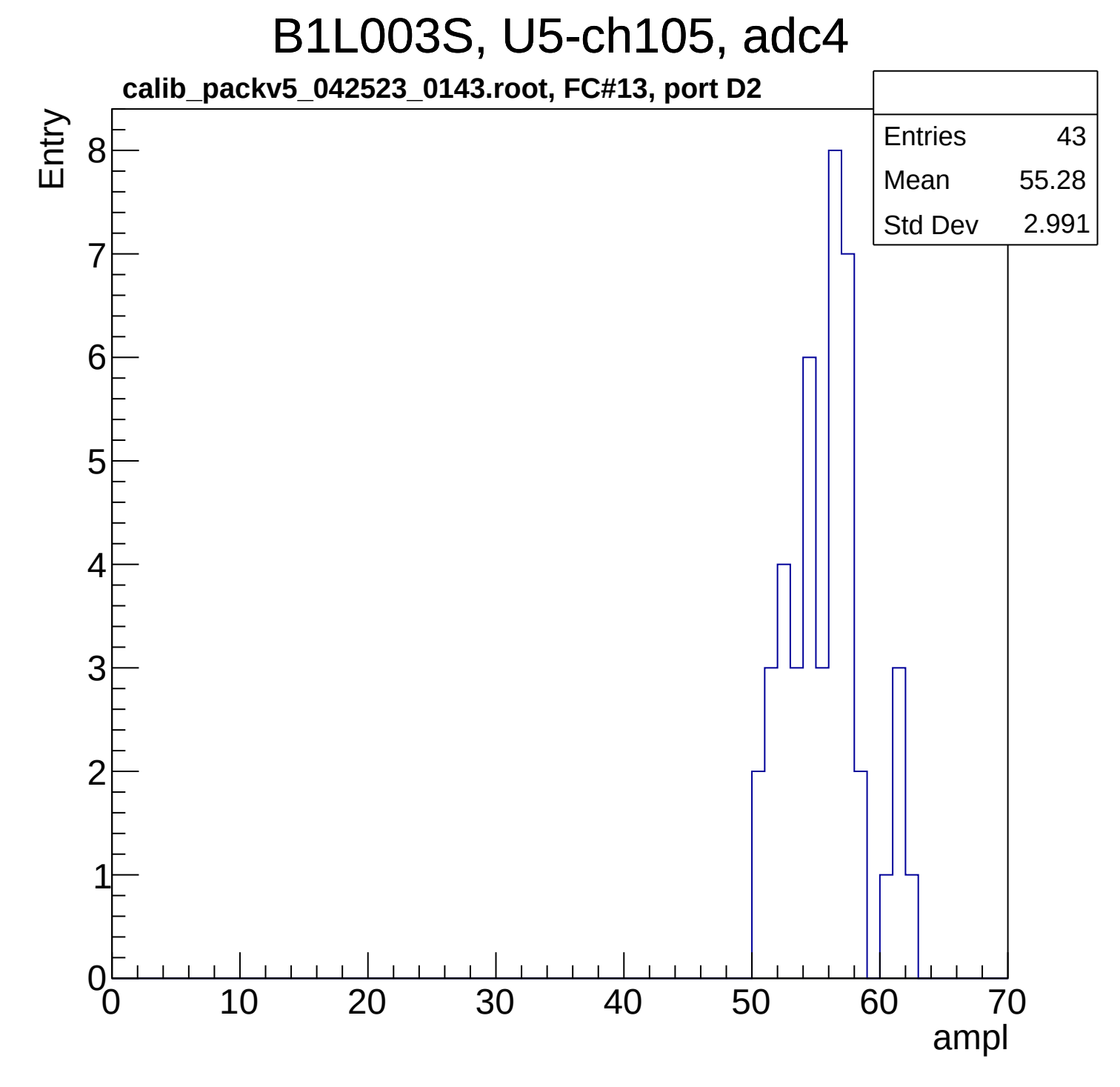
B1L003S, U5-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 55.28
Std Dev 2.991
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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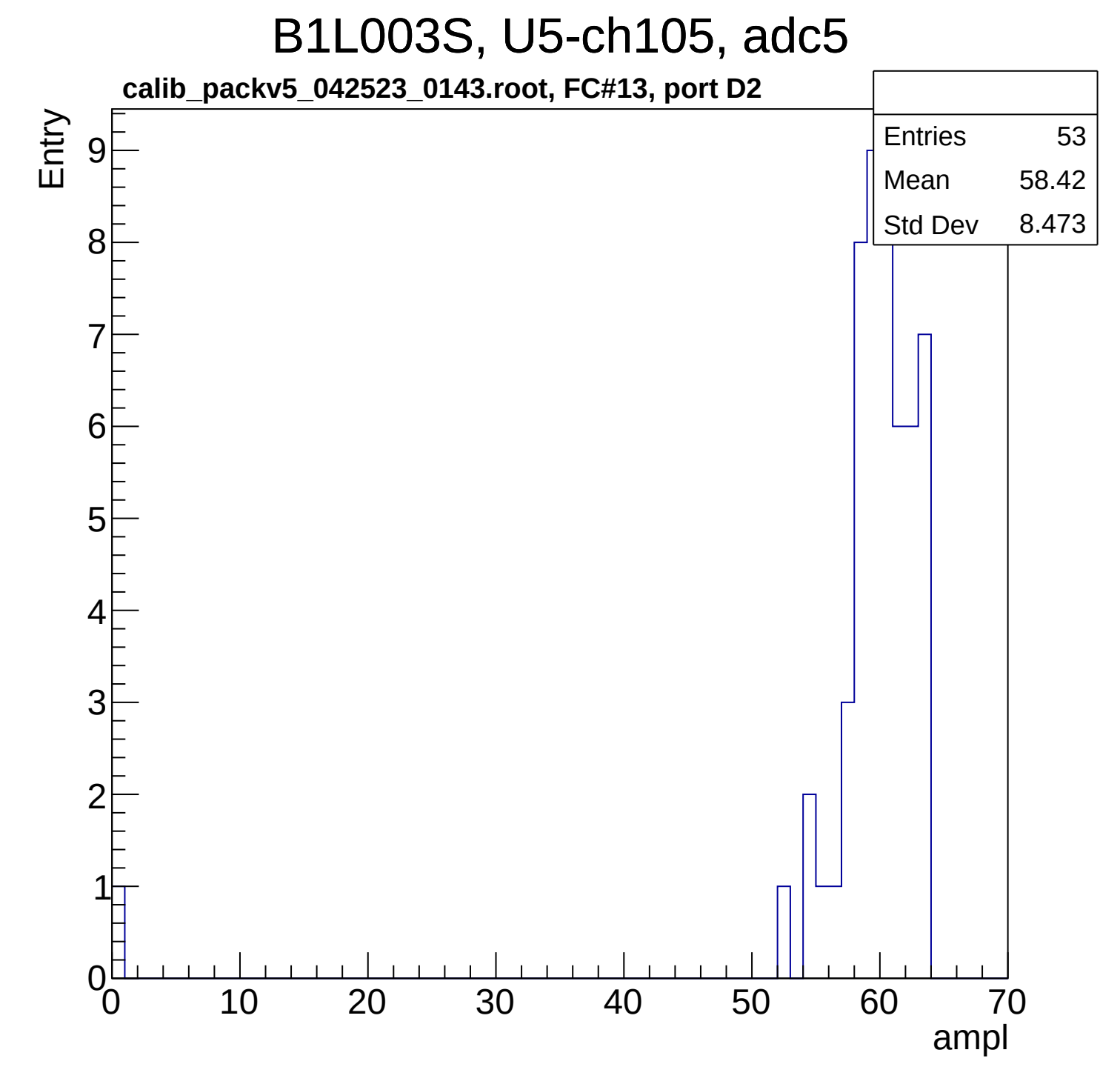
B1L003S, U5-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.42
Std Dev 8.473
Entry
ampl
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2
3
4
5
6
7
8
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0
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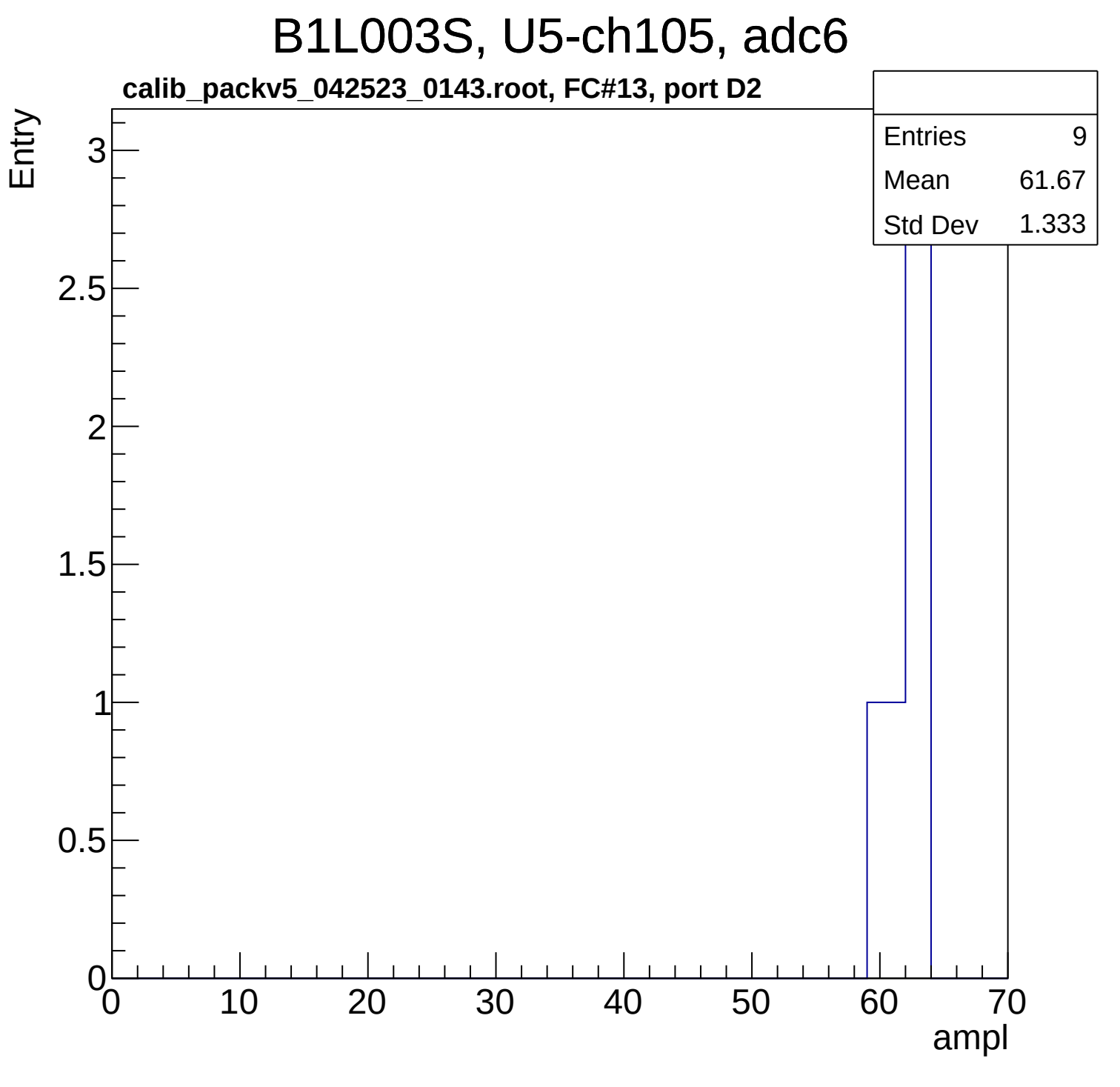
B1L003S, U5-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.333
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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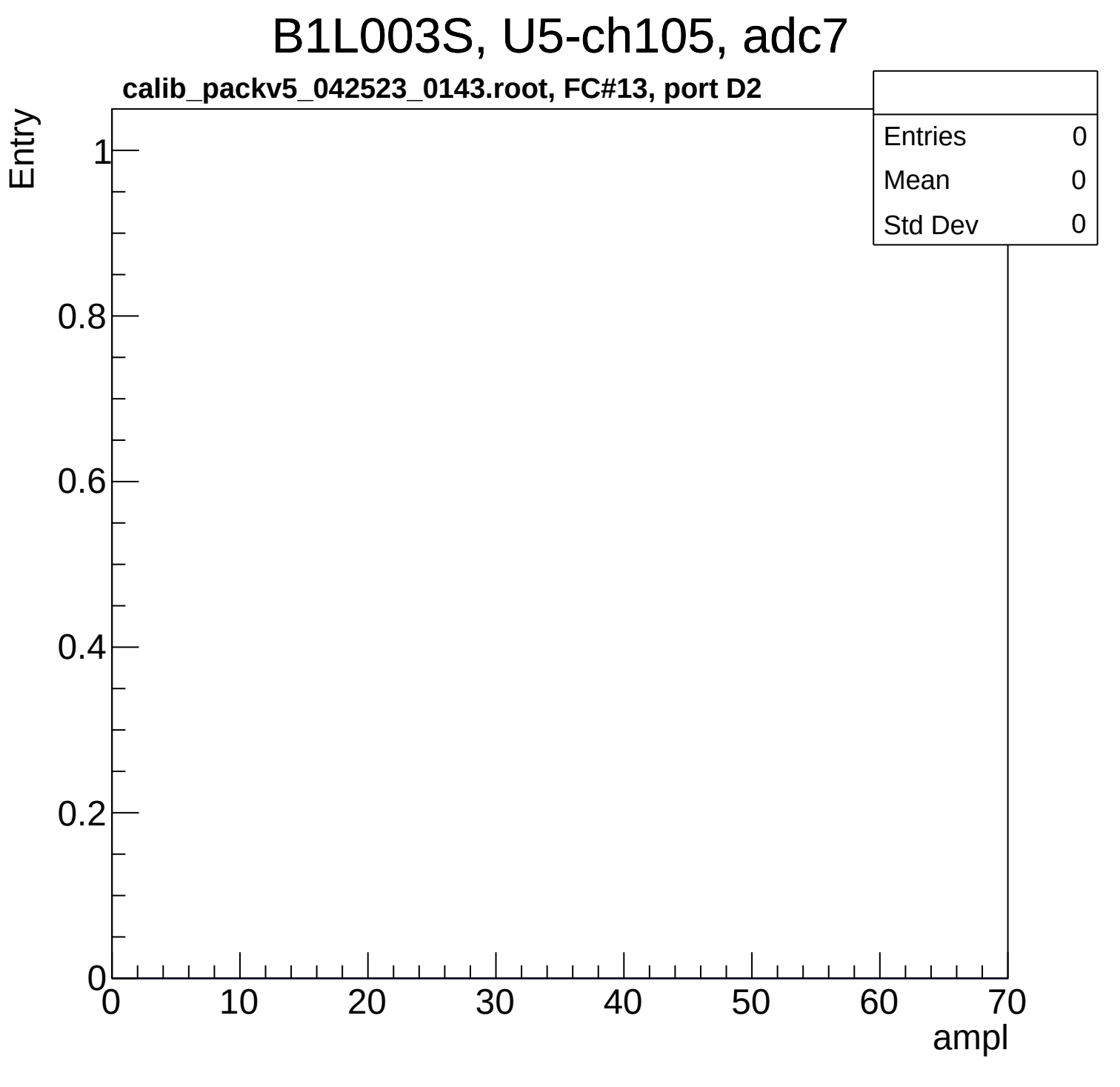
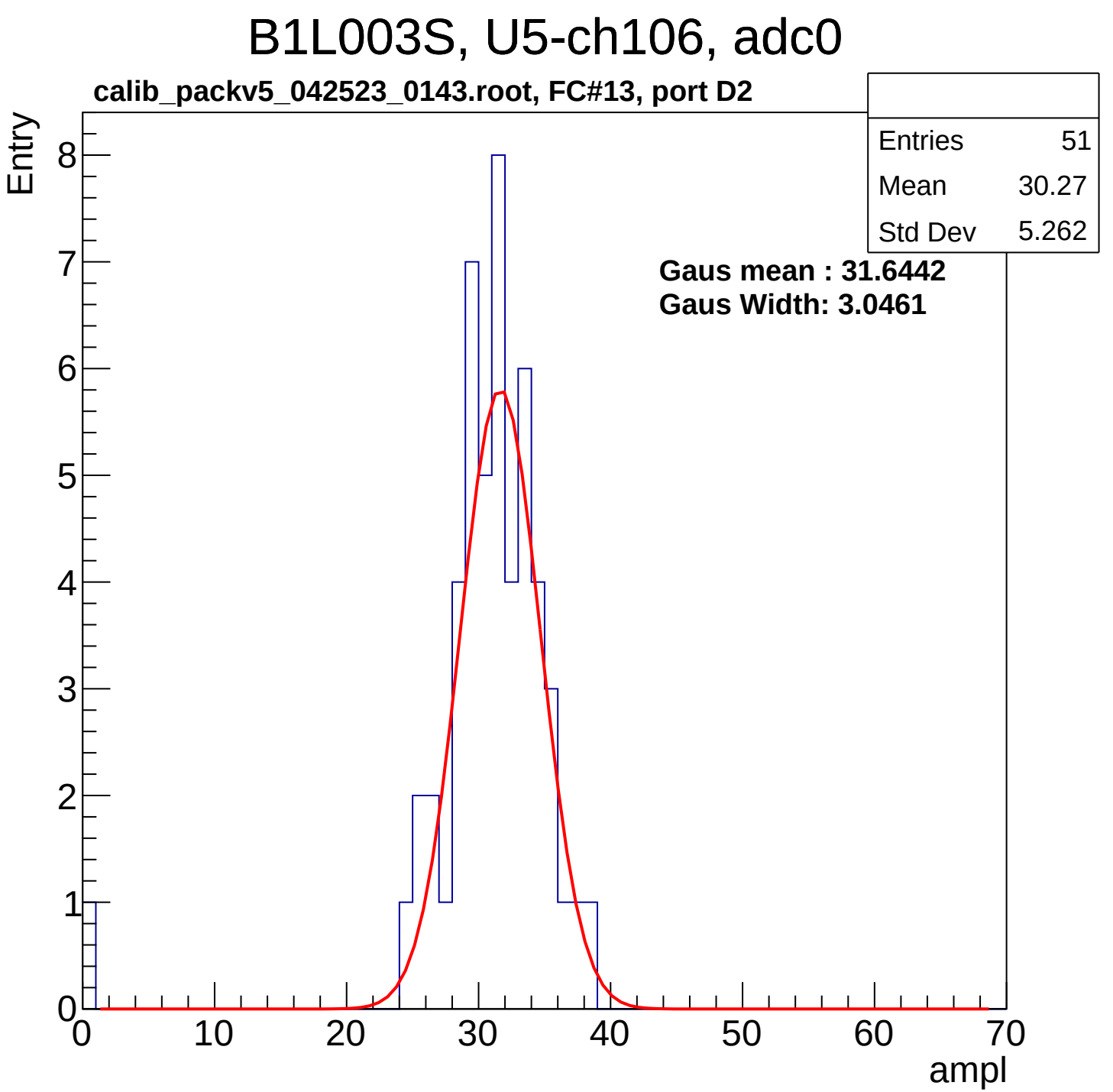

B1L003S, U5-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 30.27
Std Dev 5.262
Gaus mean : 31.6442
Gaus Width: 3.0461
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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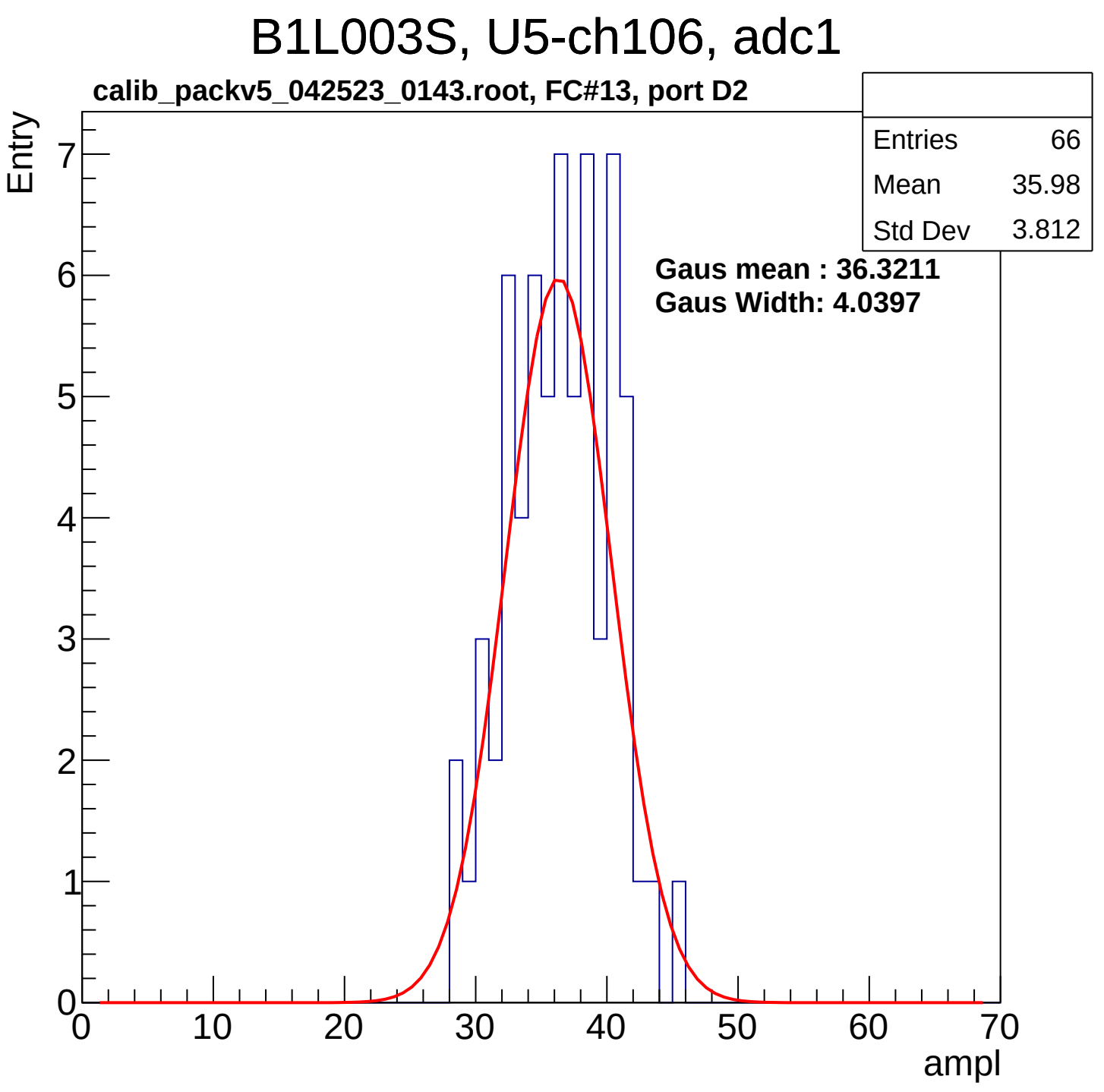
B1L003S, U5-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.98
Std Dev 3.812
Gaus mean : 36.3211
Gaus Width: 4.0397
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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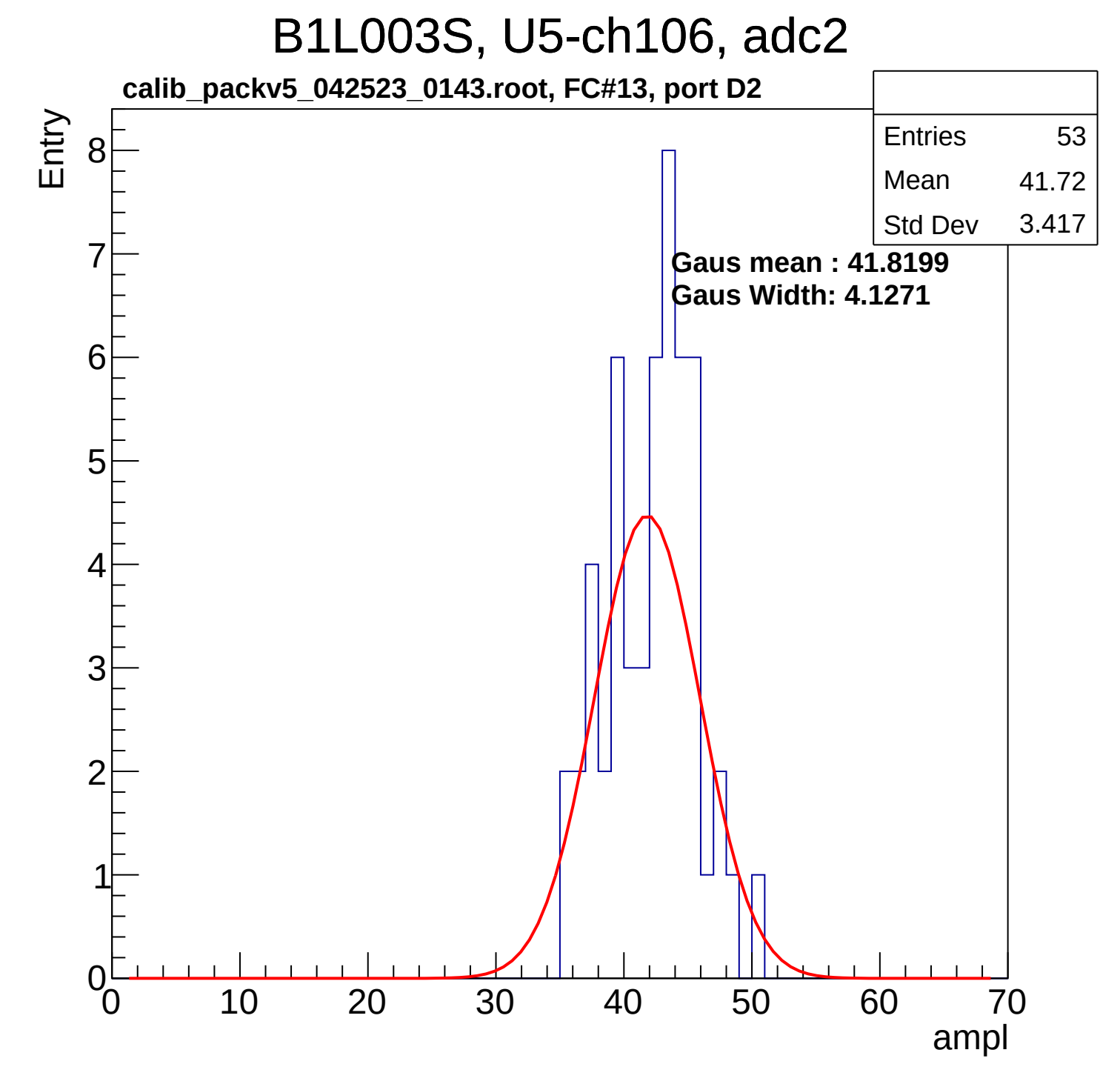
B1L003S, U5-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 41.72
Std Dev 3.417
Gaus mean : 41.8199
Gaus Width: 4.1271
Entry
ampl
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1
2
3
4
5
6
7
8
0
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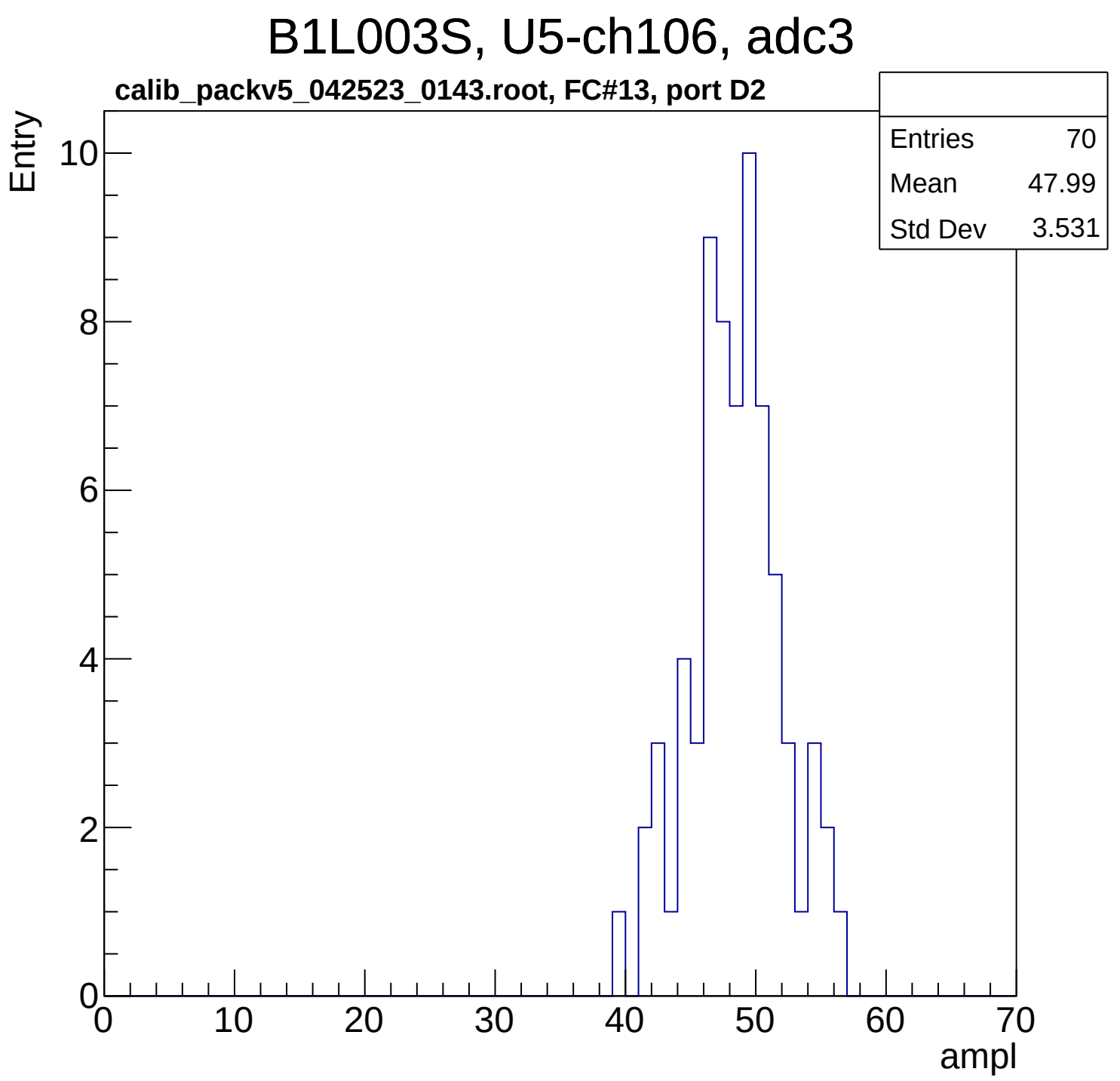
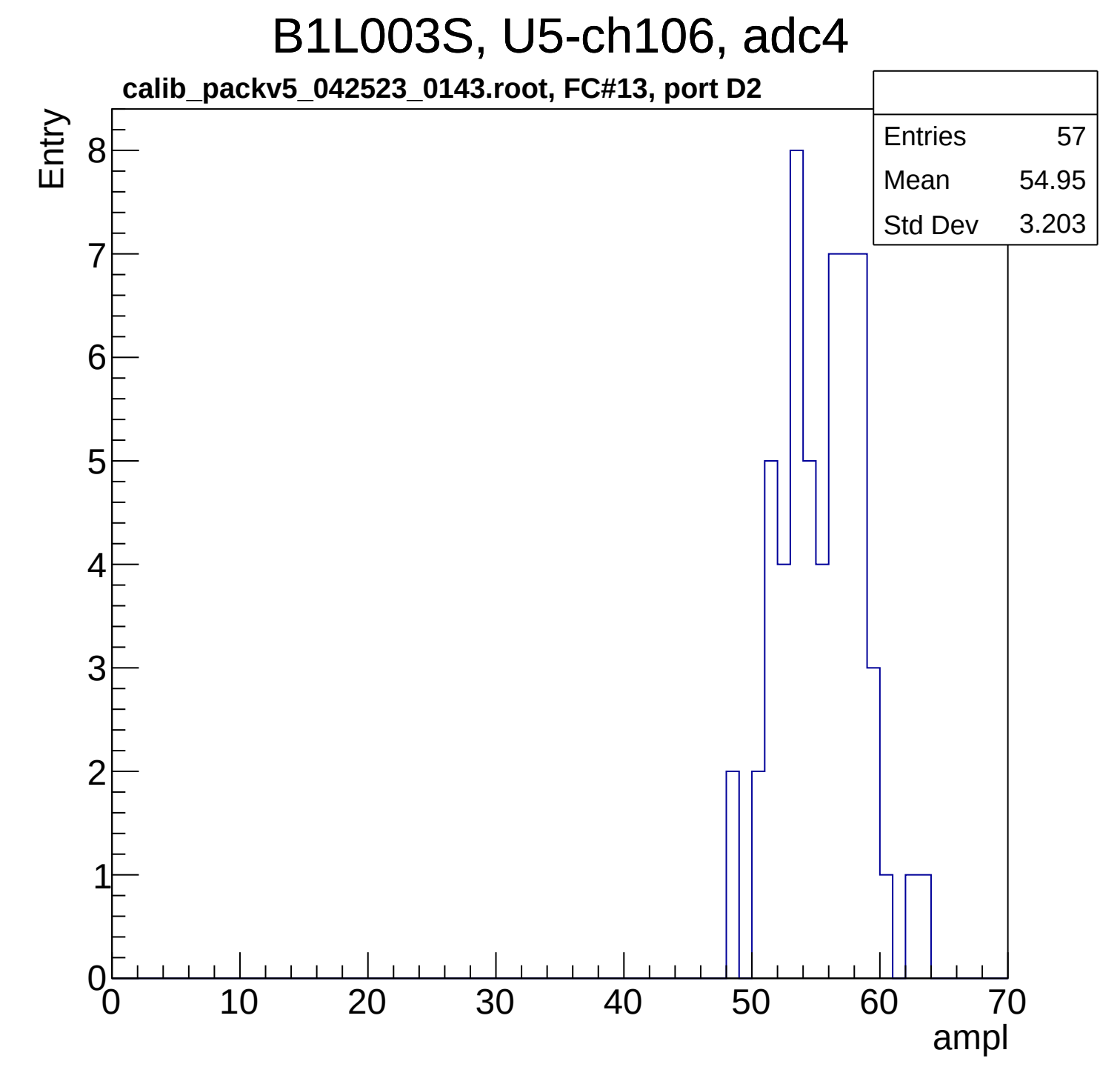
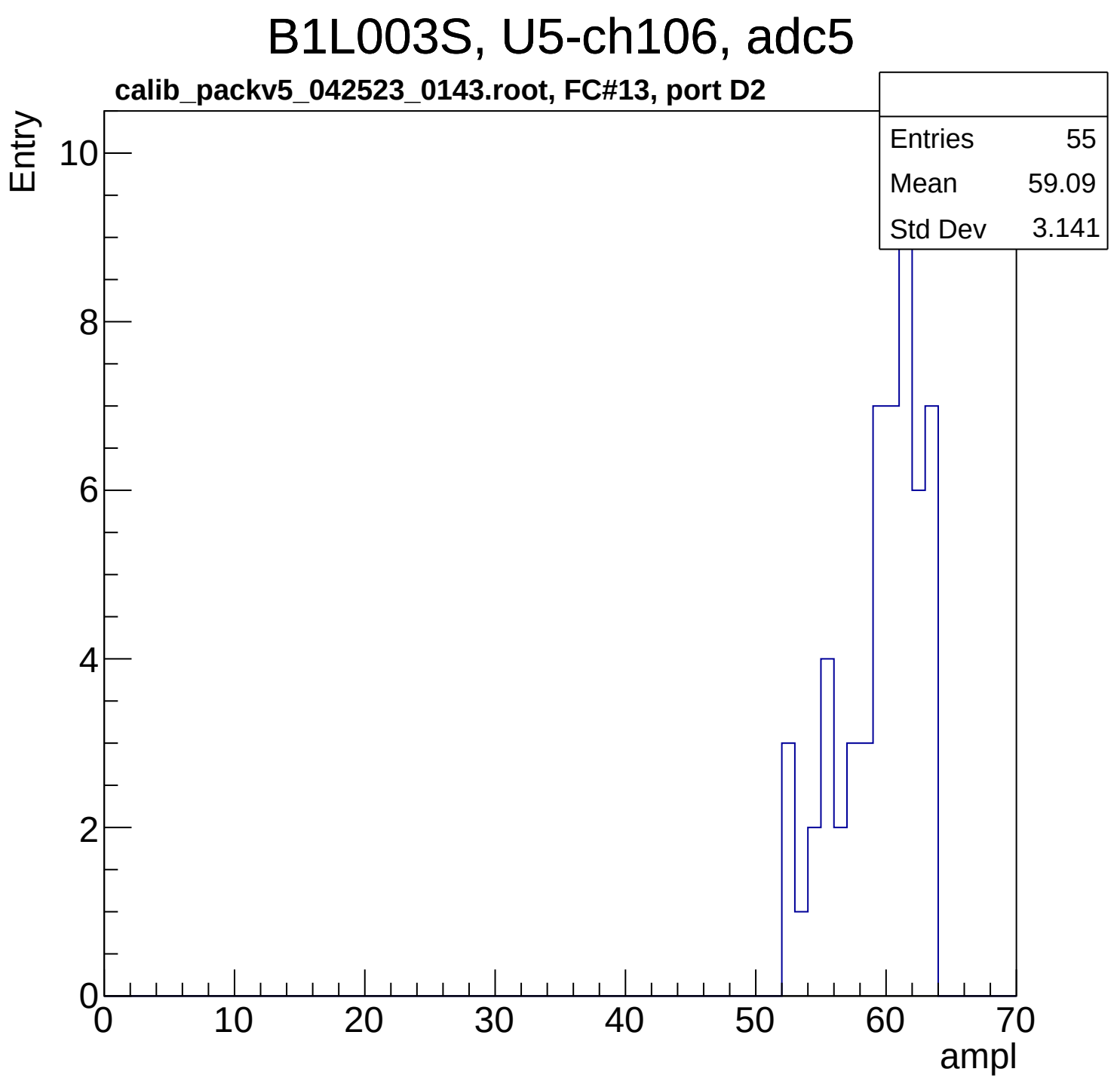
20
30
40
50
60
70


B1L003S, U5-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.99
Std Dev 3.531
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.95
Std Dev 3.203
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.09
Std Dev 3.141
Entry
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4
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10
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10
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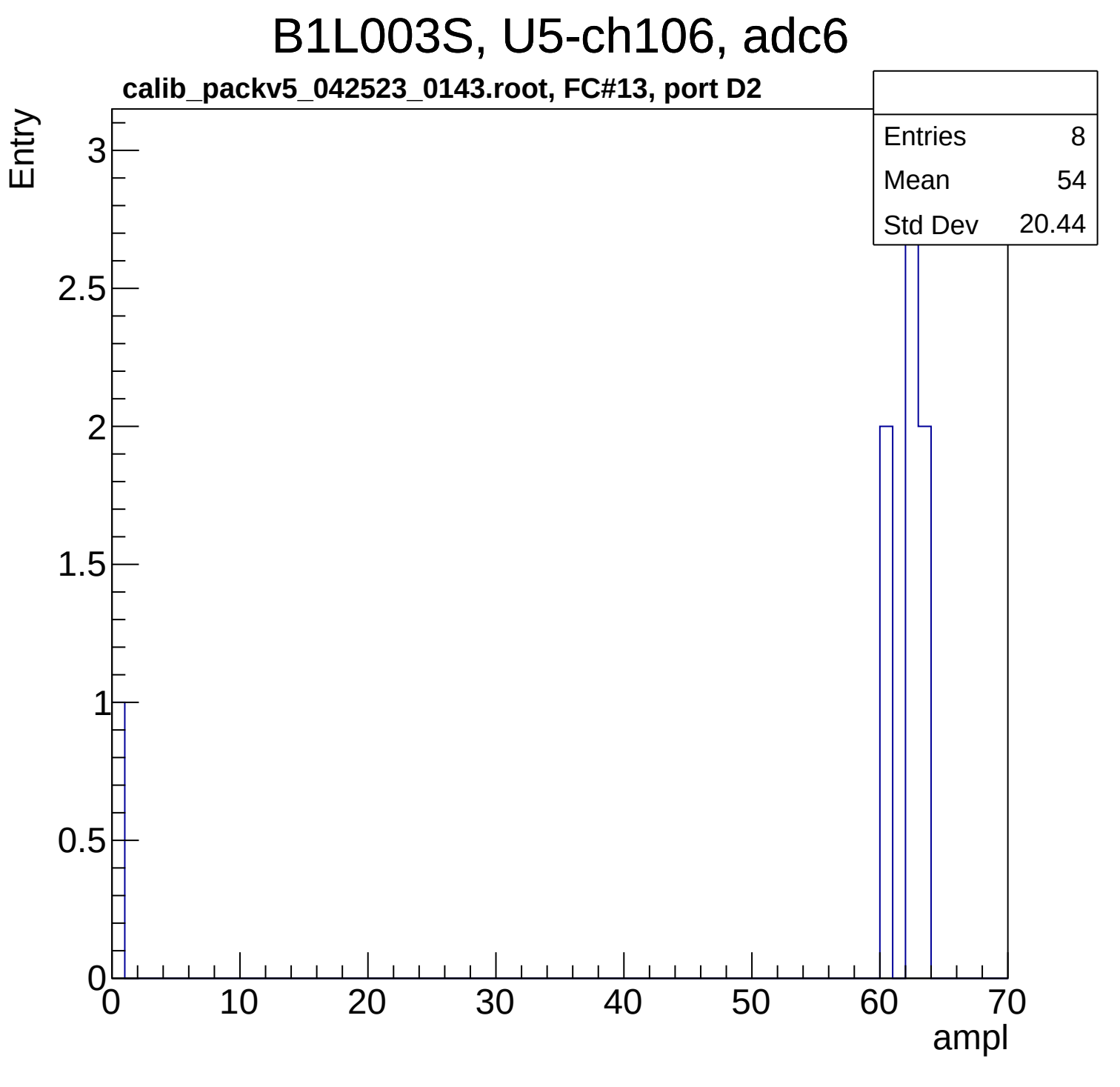
50
60
70
ampl


B1L003S, U5-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54
Std Dev 20.44
Entry
ampl
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1.5
2
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3
0
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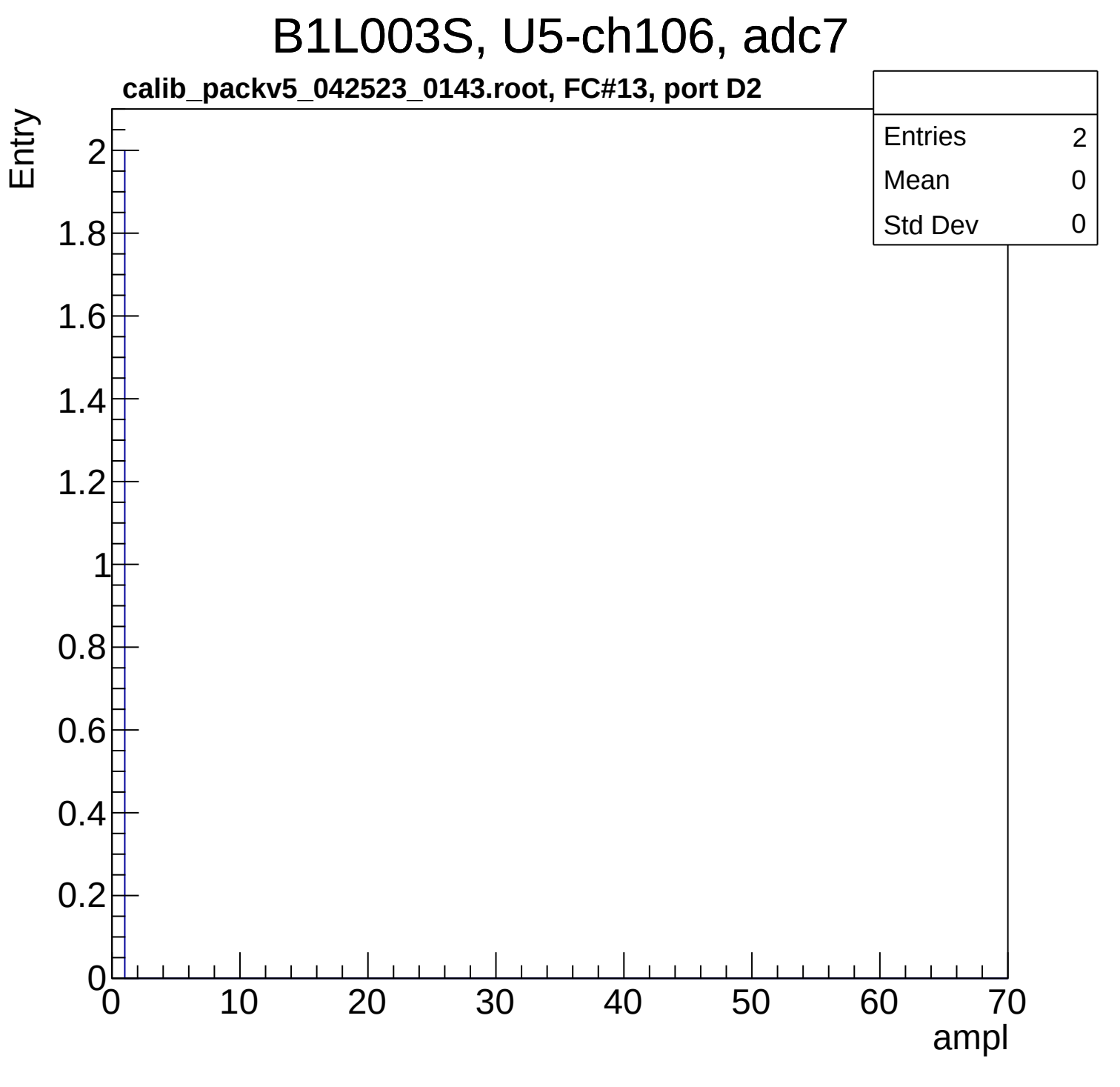
40
50
60
70


B1L003S, U5-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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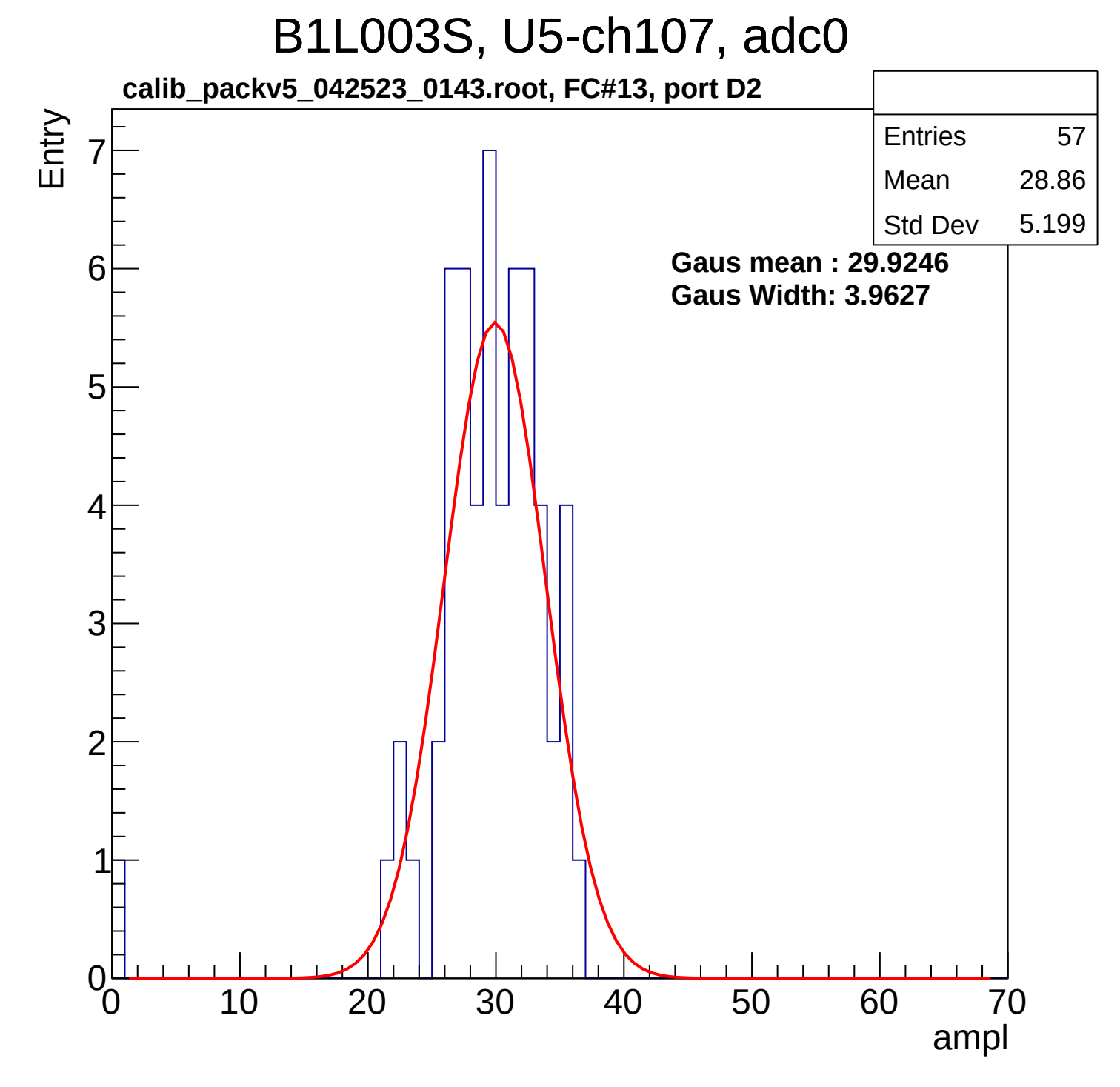
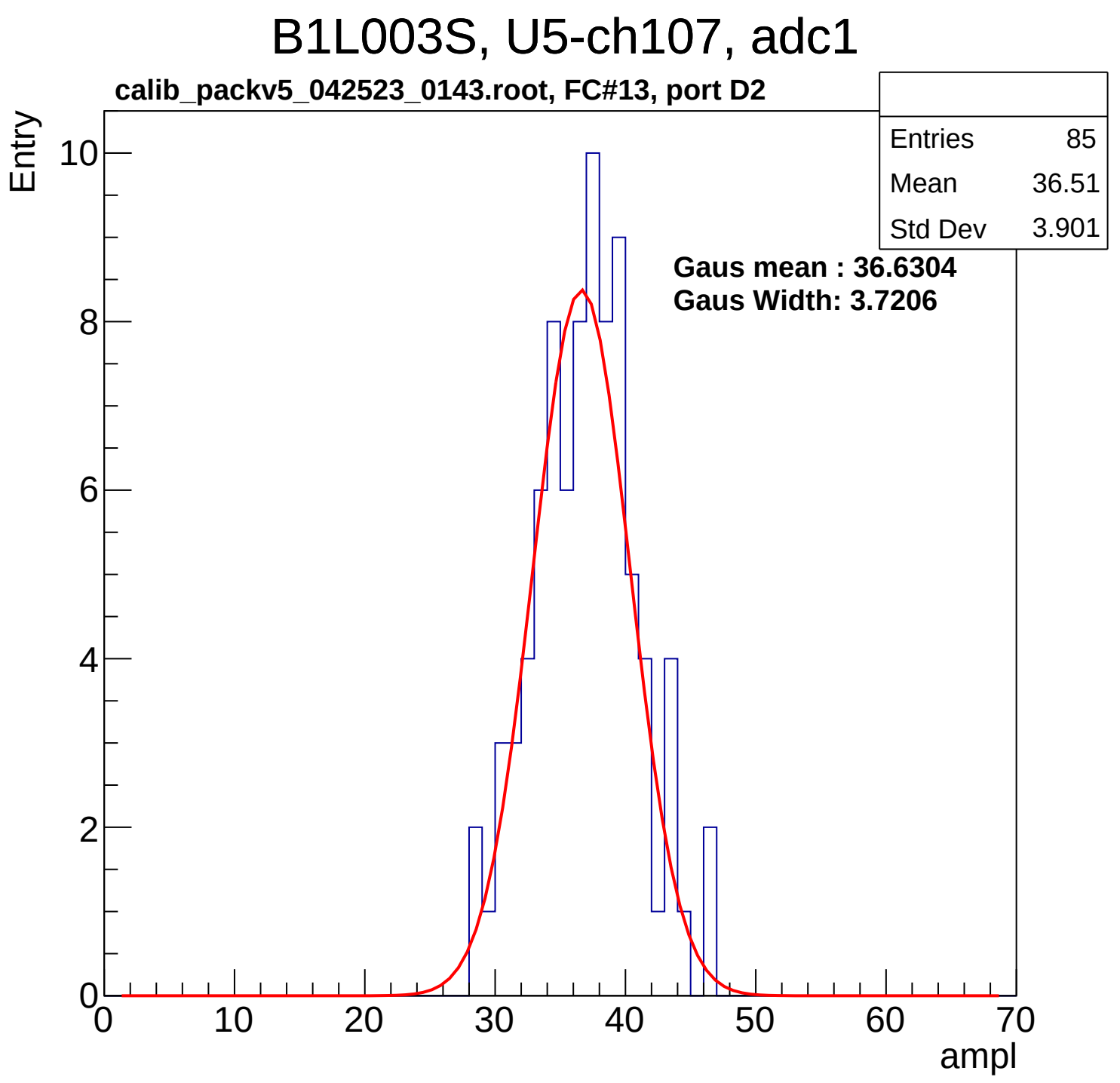
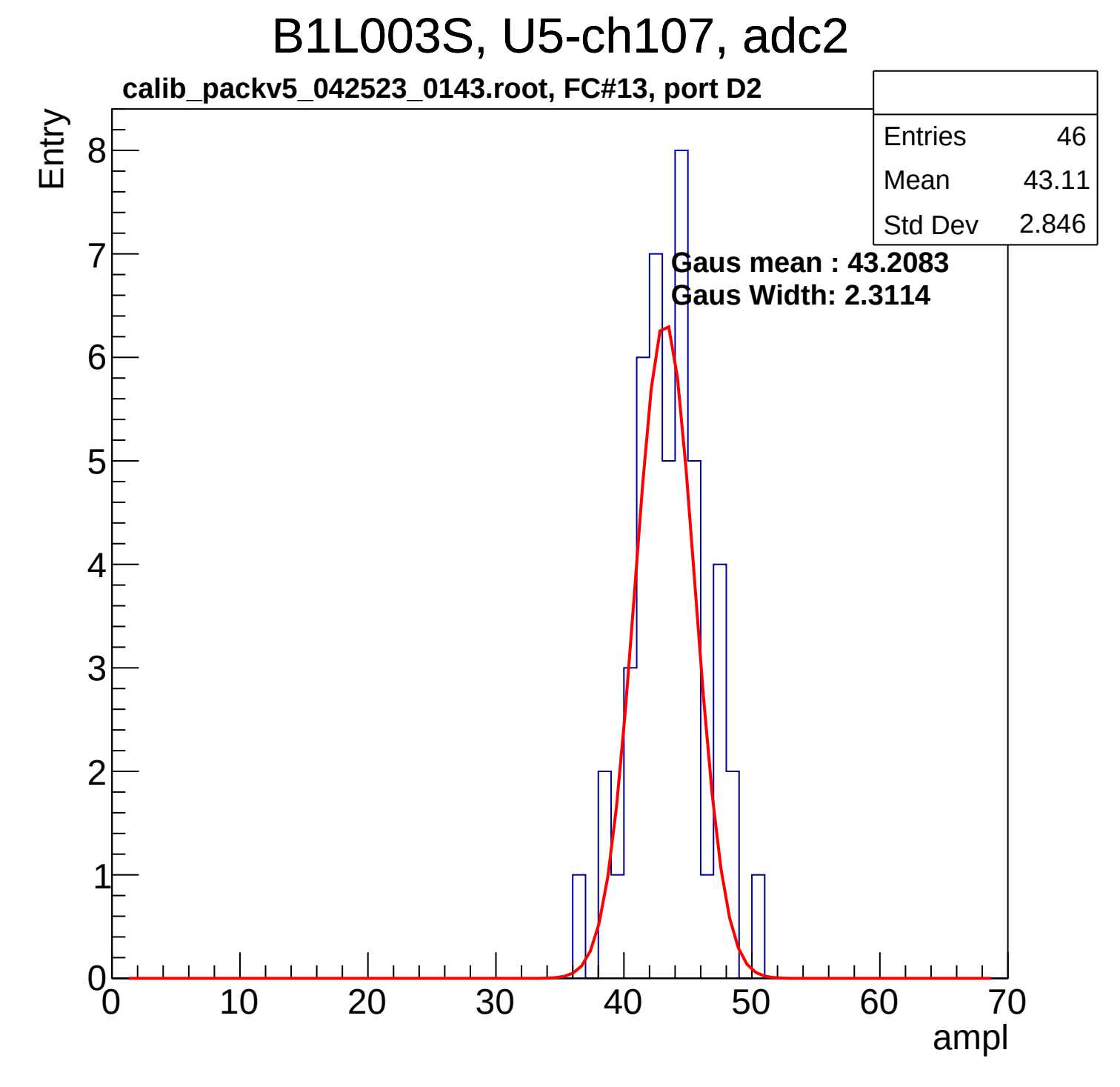

B1L003S, U5-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.86
Std Dev 5.199
Gaus mean : 29.9246
Gaus Width: 3.9627
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 36.51
Std Dev 3.901
Gaus mean : 36.6304
Gaus Width: 3.7206
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 43.11
Std Dev 2.846
Gaus mean : 43.2083
Gaus Width: 2.3114
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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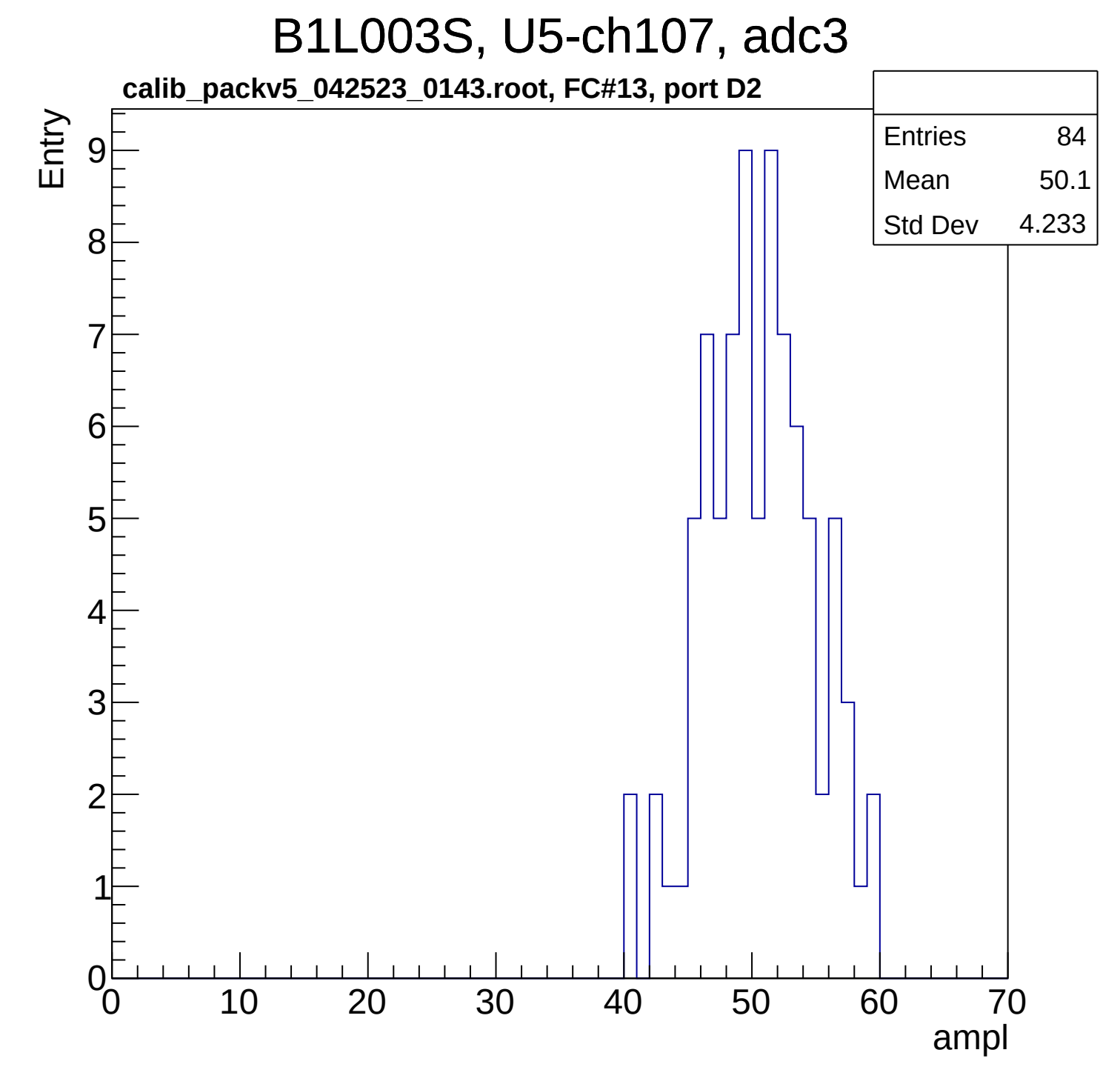
B1L003S, U5-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 50.1
Std Dev 4.233
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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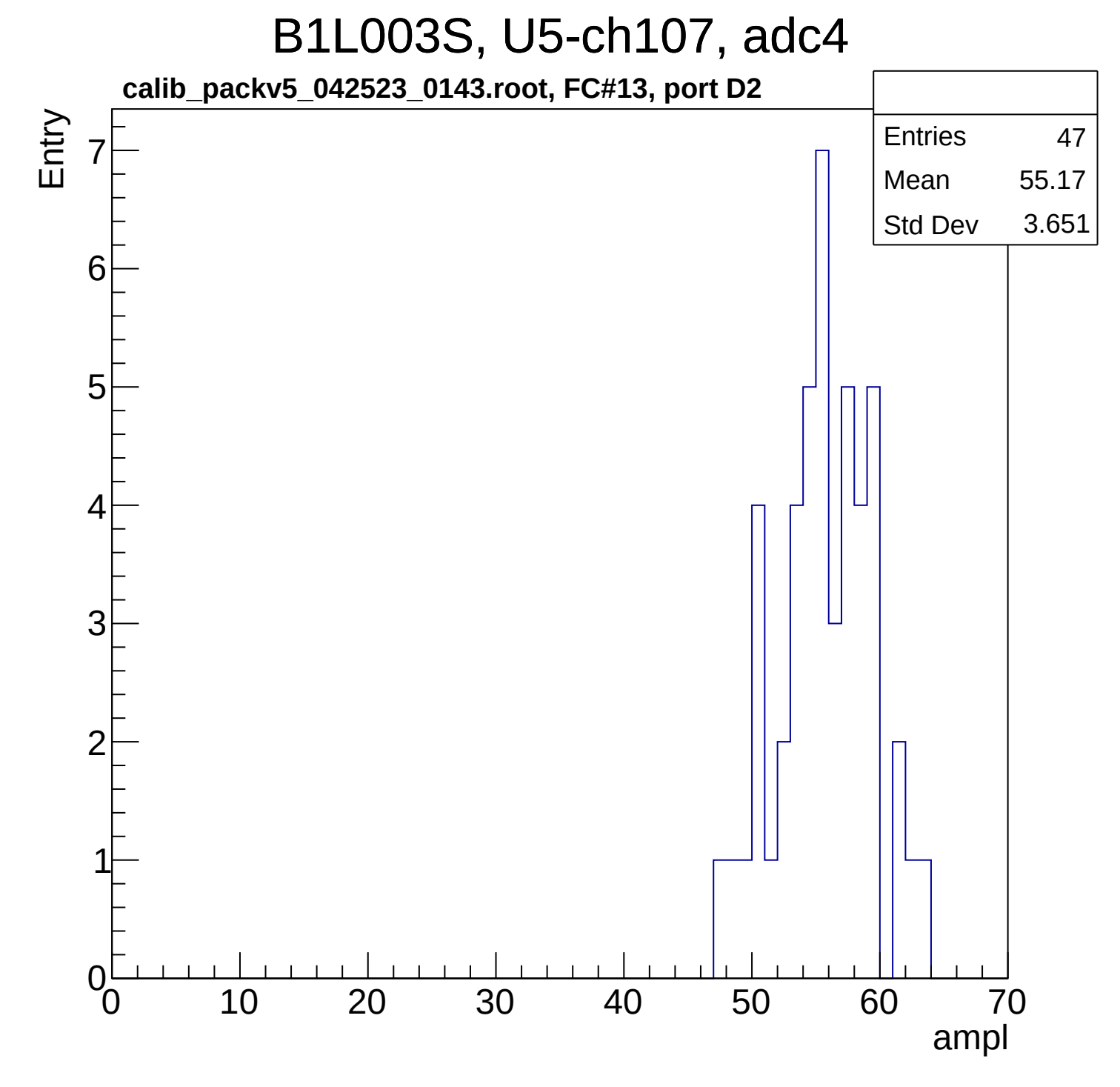
B1L003S, U5-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.17
Std Dev 3.651
Entry
ampl
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4
5
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7
0
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20
30
40
50
60
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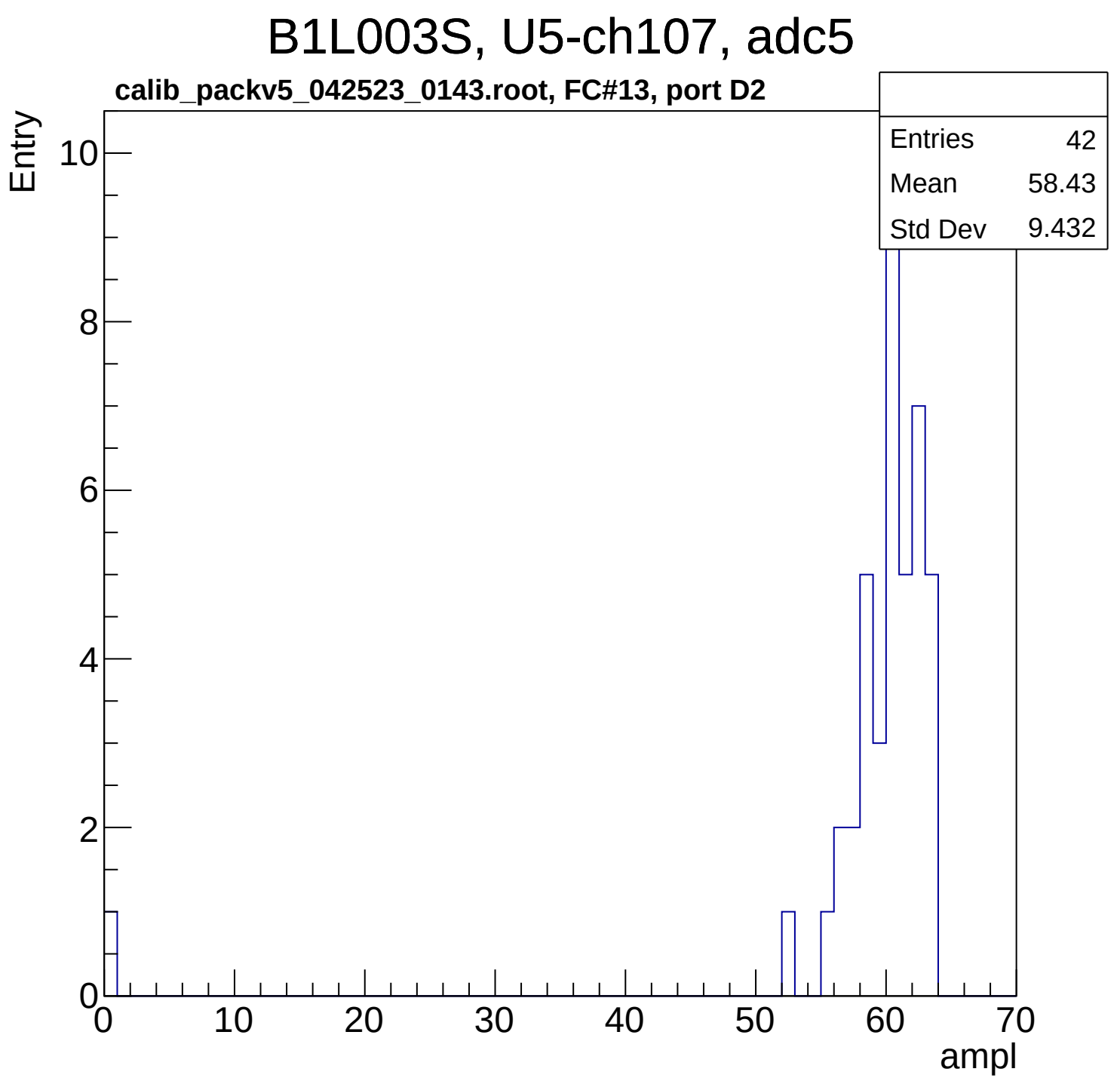
B1L003S, U5-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.43
Std Dev 9.432
Entry
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10
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10
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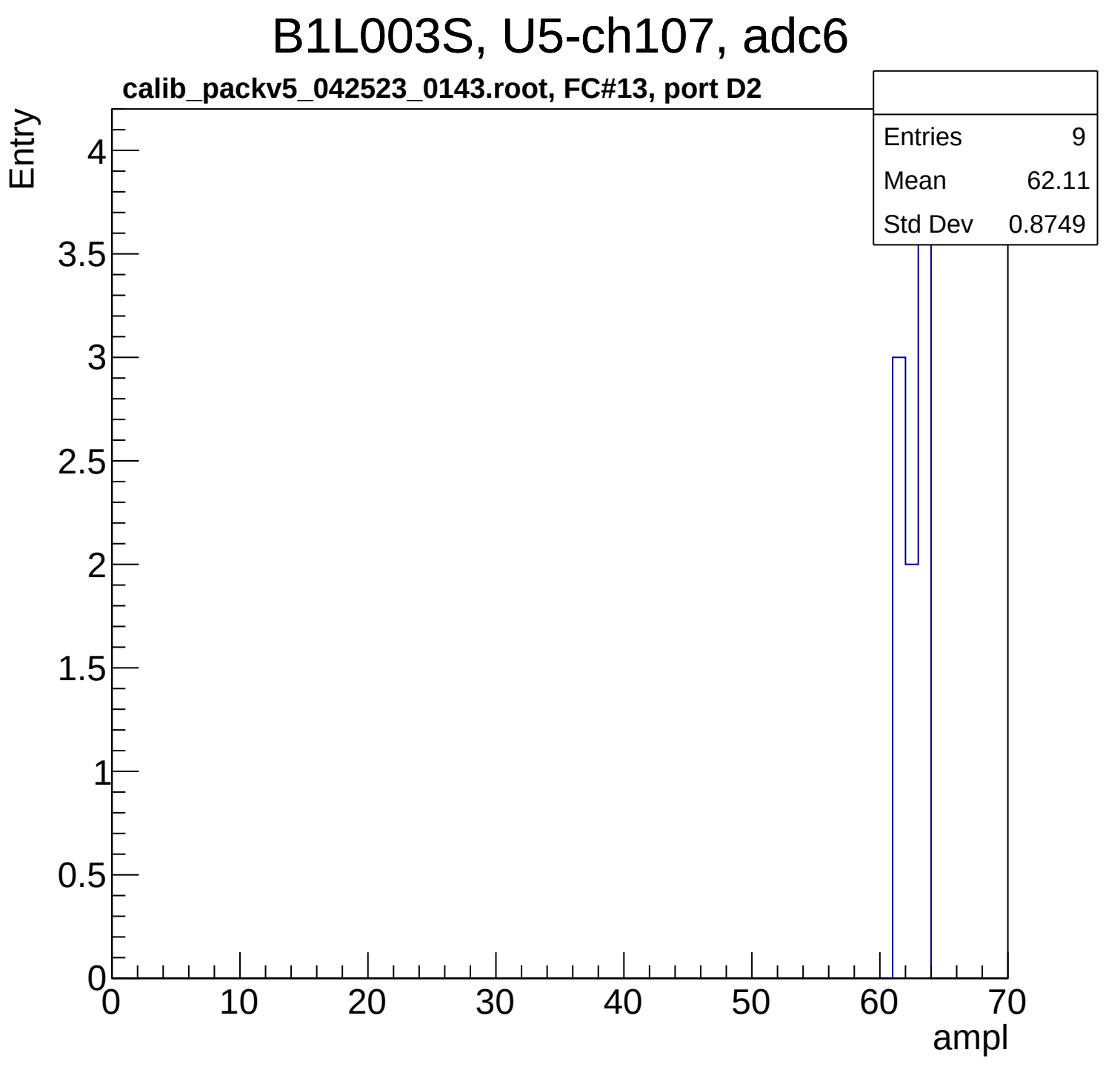
70
ampl


B1L003S, U5-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.8749
Entry
ampl
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1.5
2
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3.5
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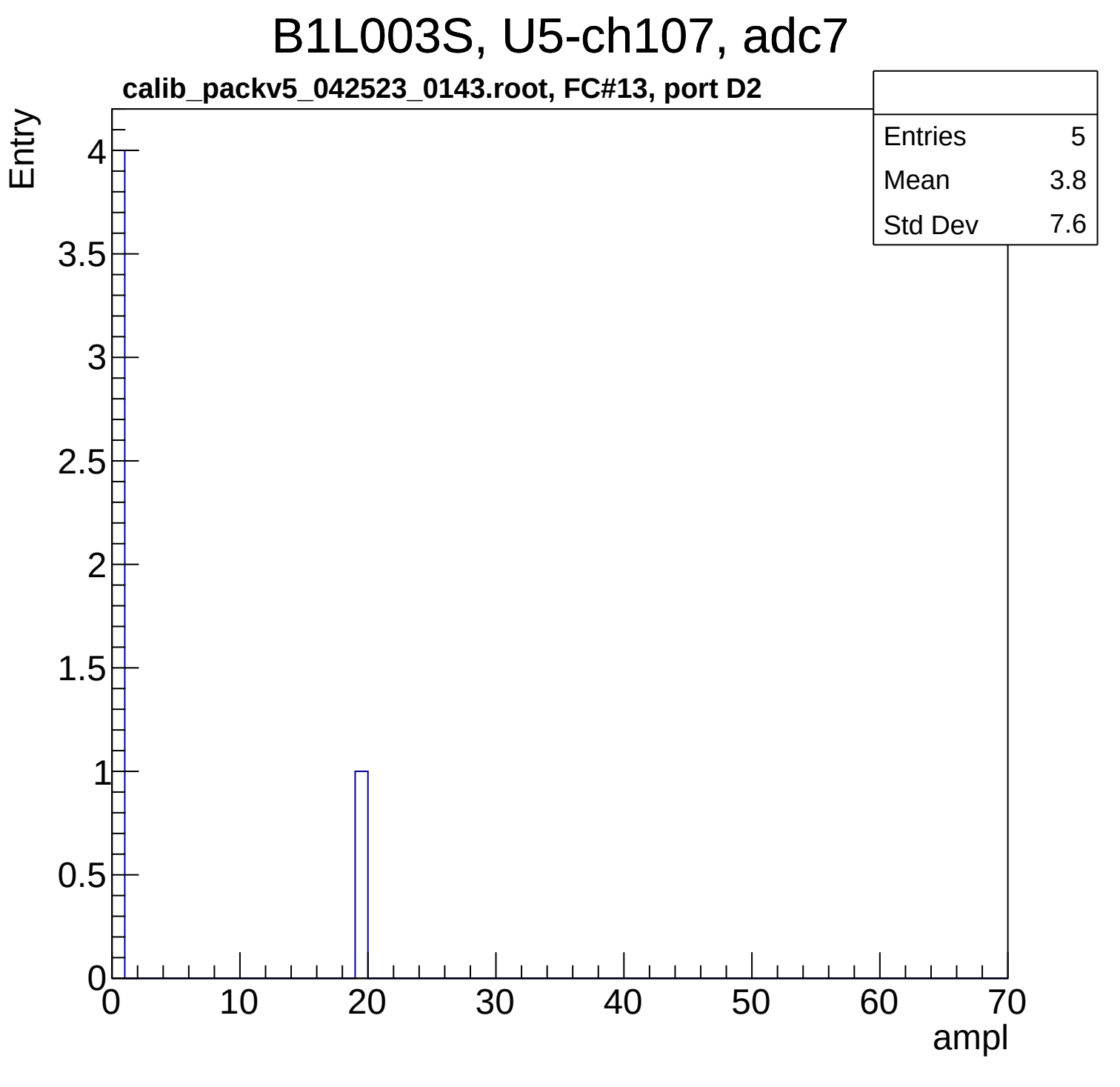
B1L003S, U5-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 3.8
Std Dev 7.6
Entry
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2.5
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3.5
4
0
10
20
30
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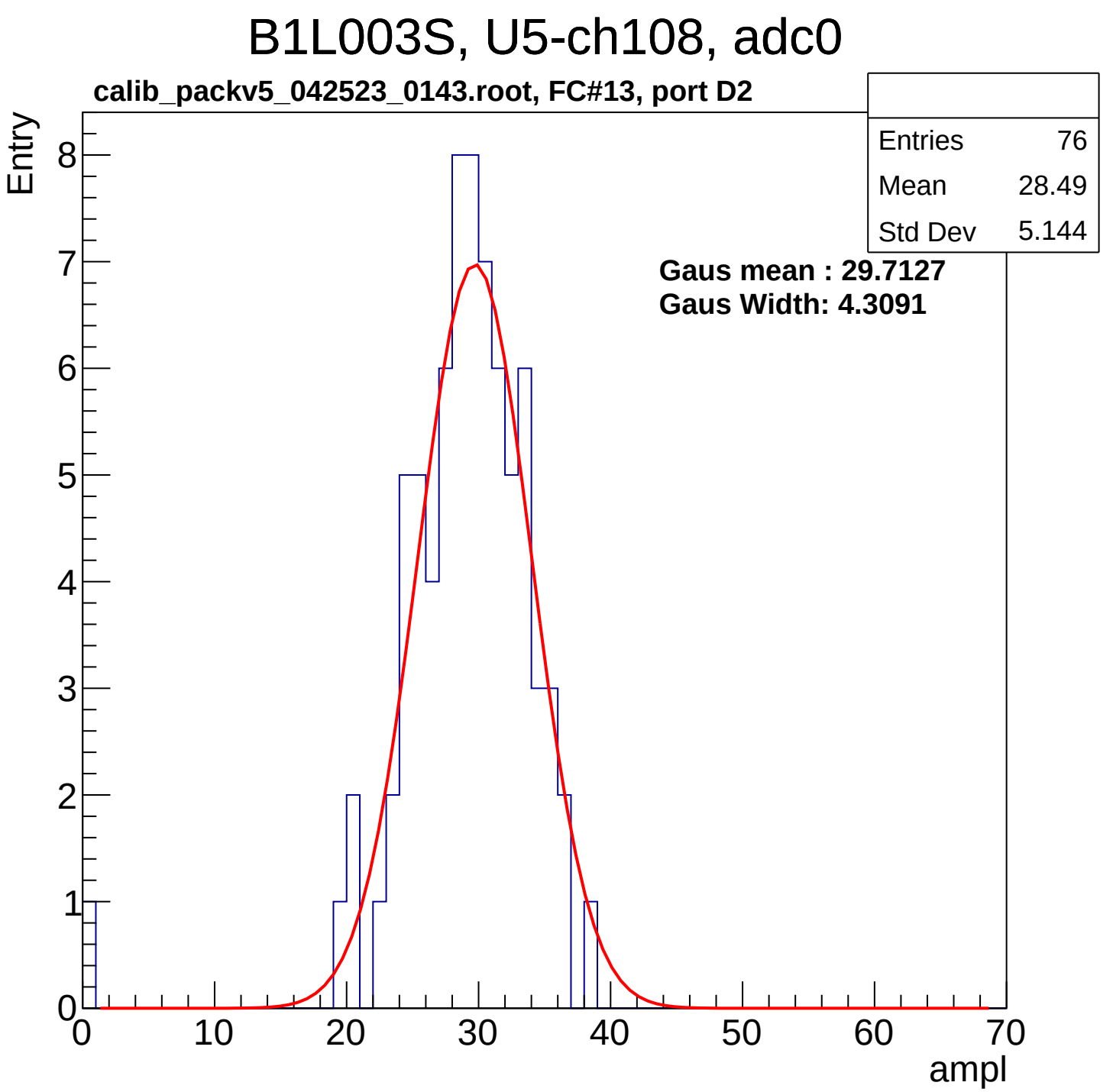
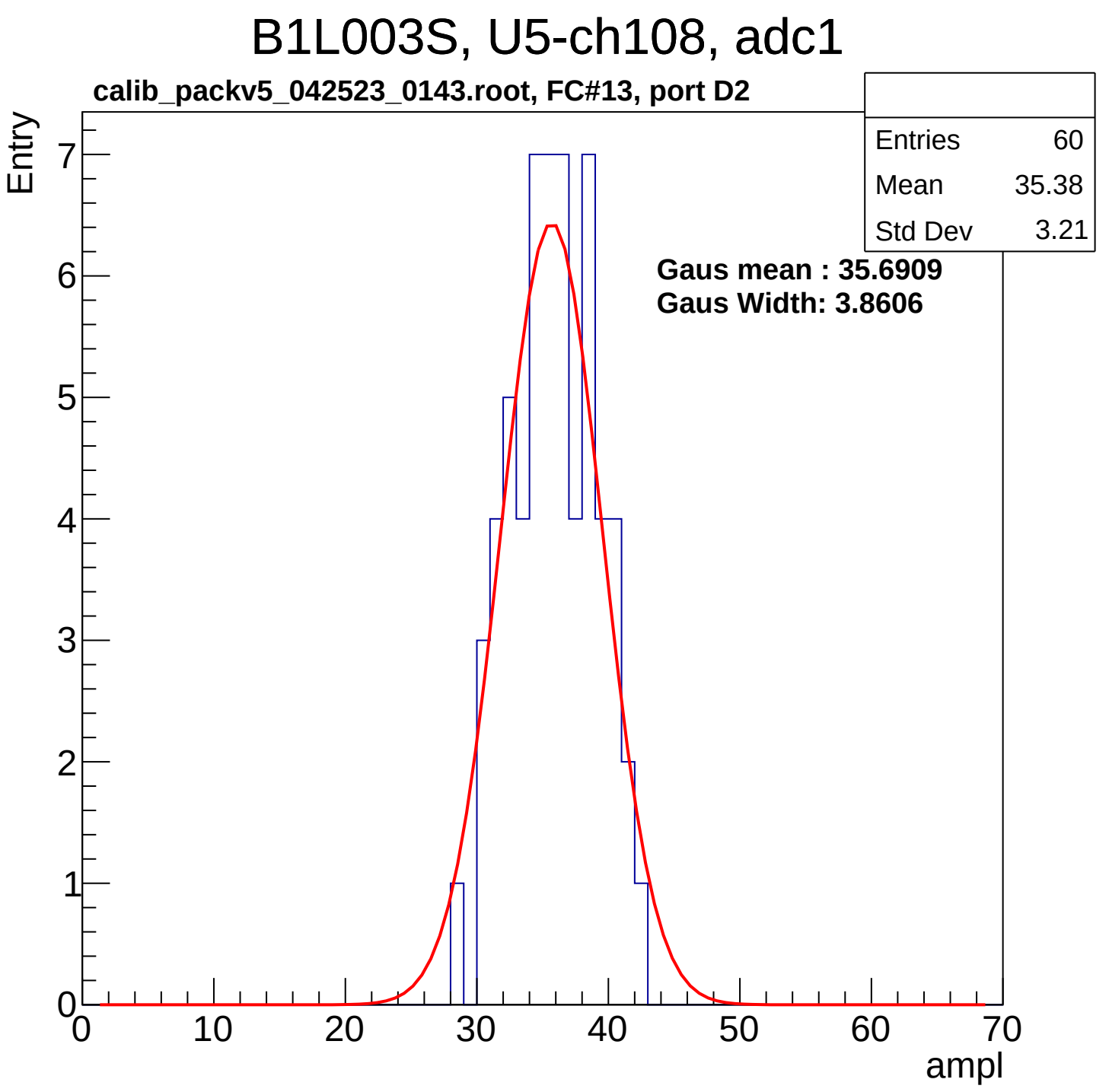
70
ampl


B1L003S, U5-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.49
Std Dev 5.144
Gaus mean : 29.7127
Gaus Width: 4.3091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.38
Std Dev 3.21
Gaus mean : 35.6909
Gaus Width: 3.8606
Entry
ampl
0
1
2
3
4
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6
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0
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20
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40
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60
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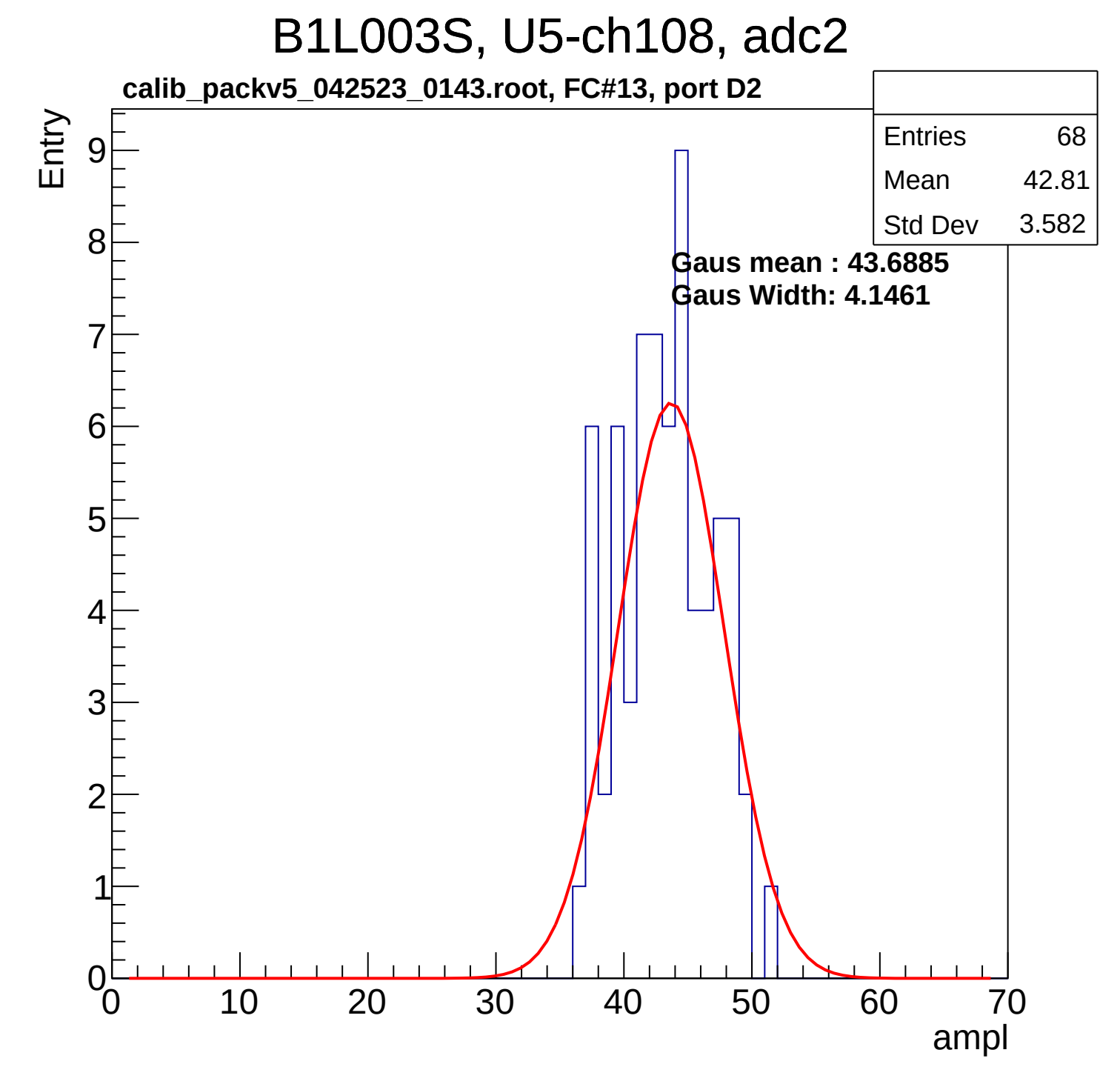
B1L003S, U5-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.81
Std Dev 3.582
Gaus mean : 43.6885
Gaus Width: 4.1461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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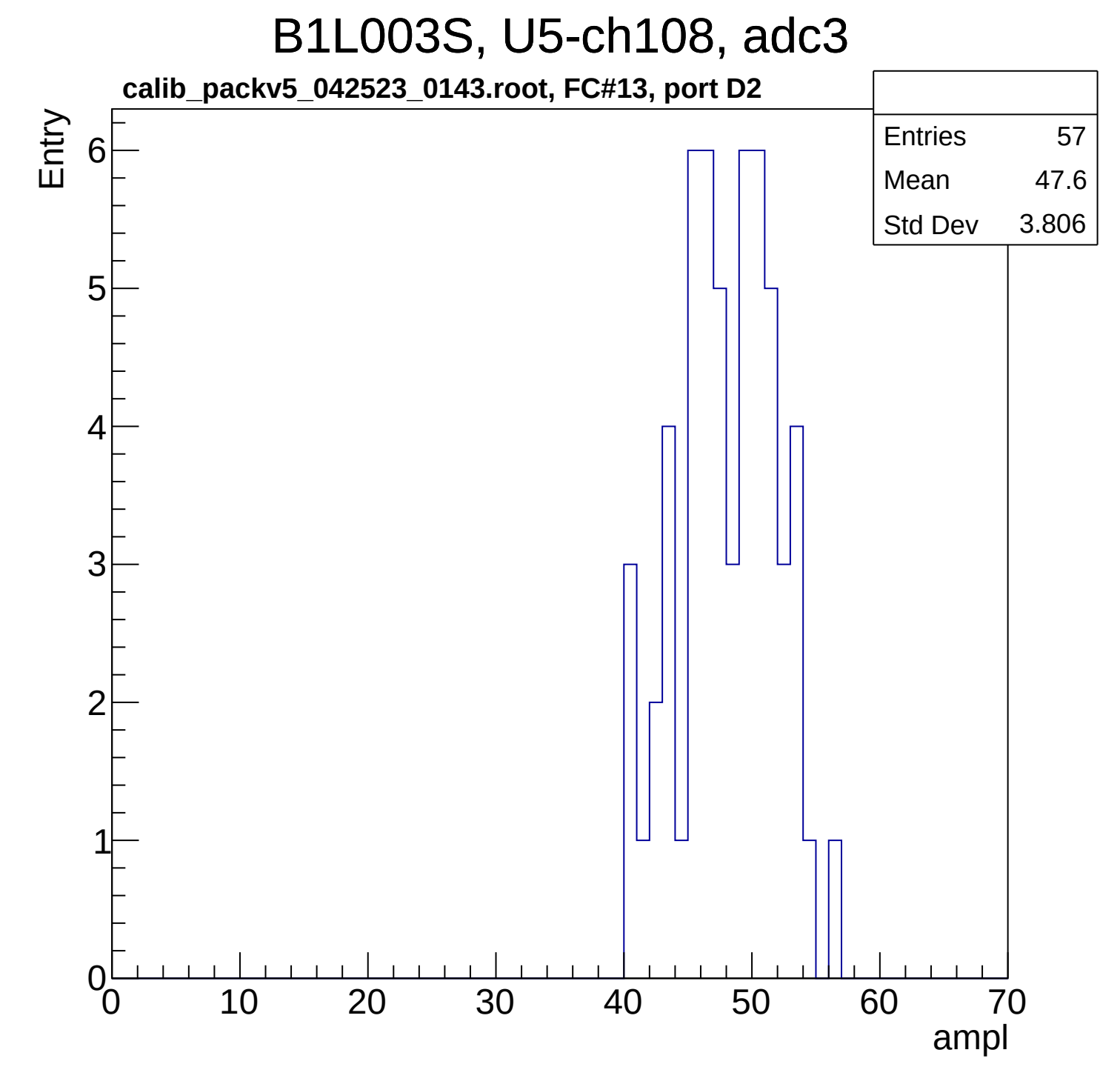
B1L003S, U5-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 47.6
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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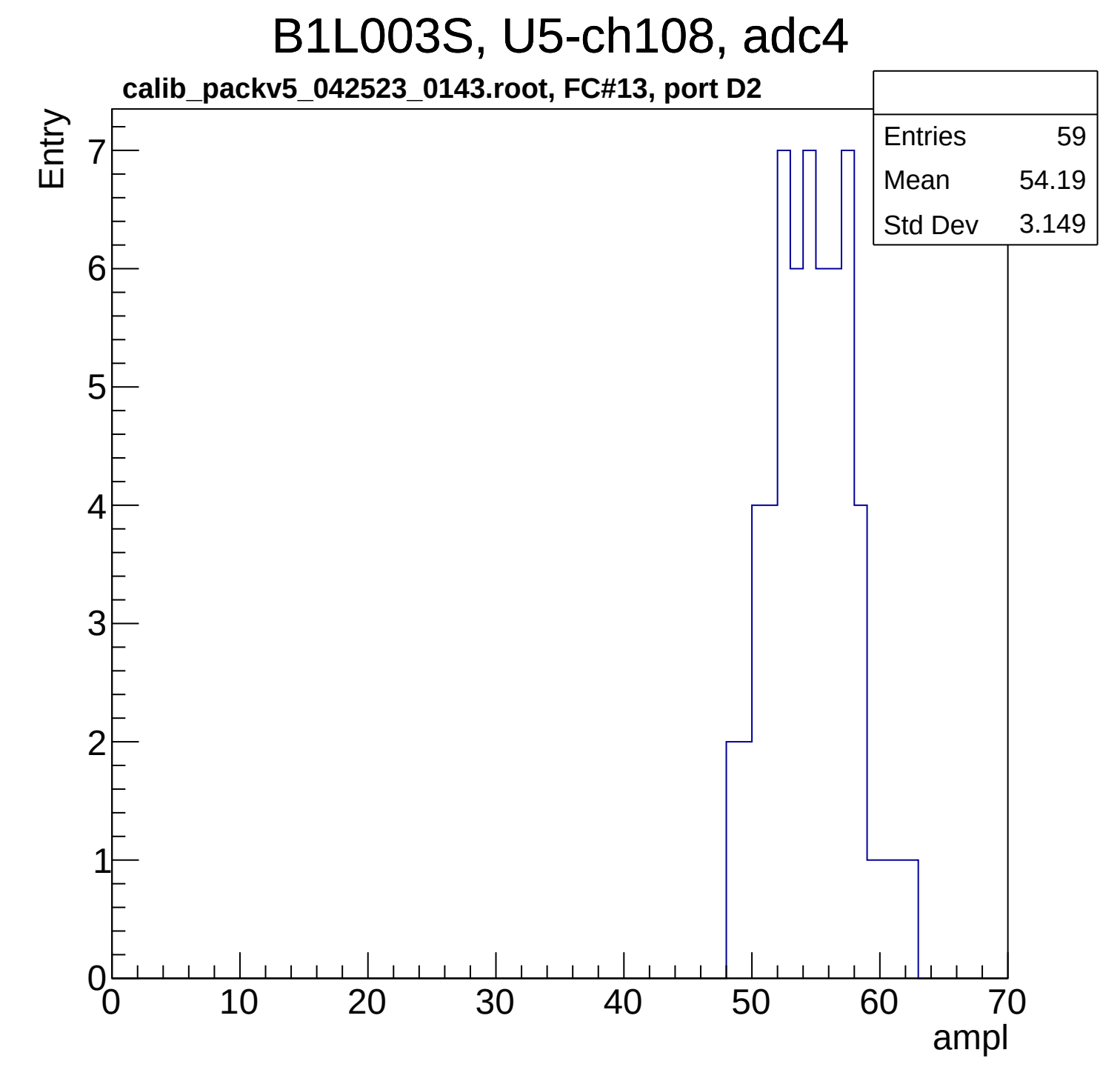
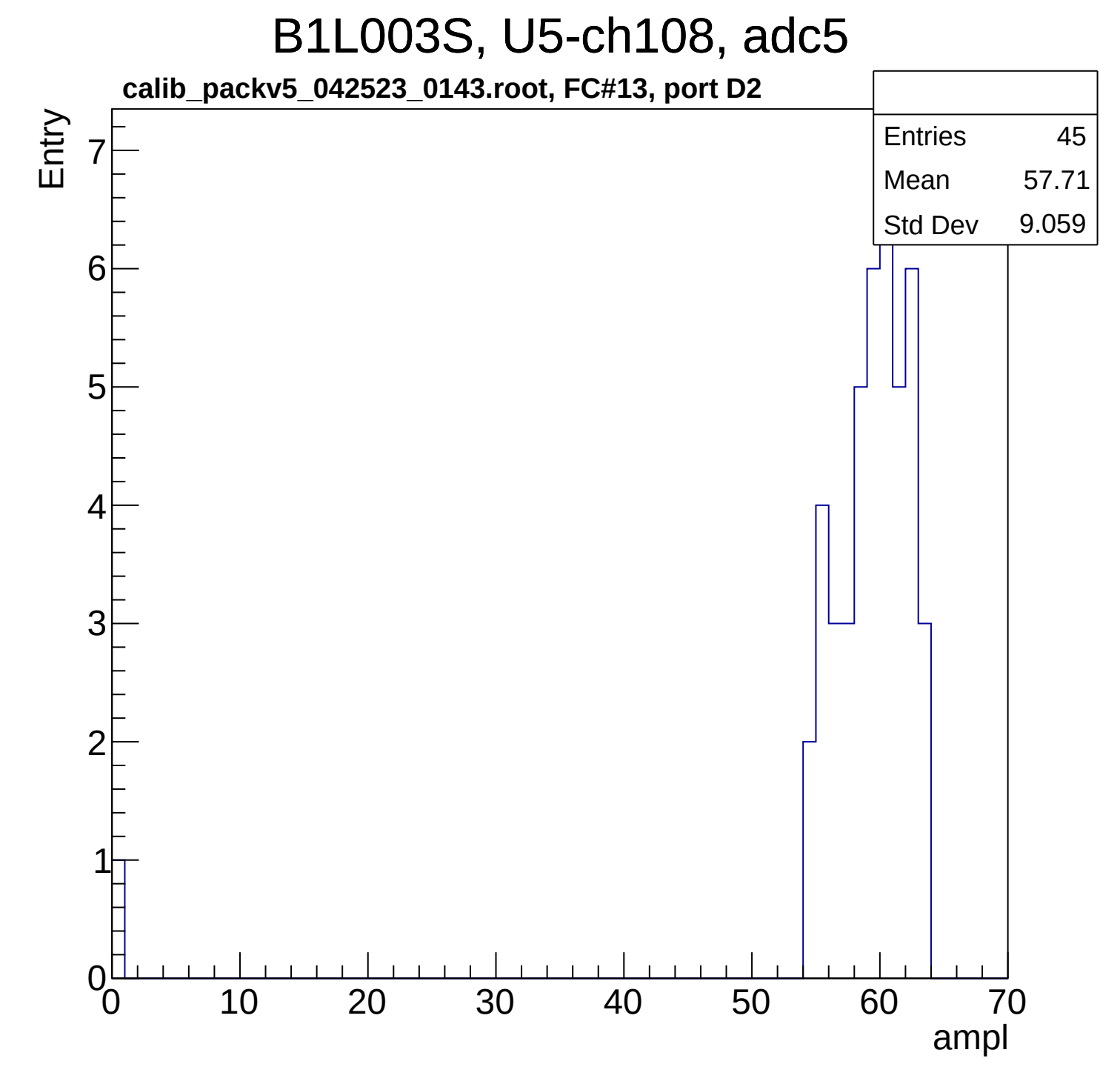

B1L003S, U5-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.19
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 57.71
Std Dev 9.059
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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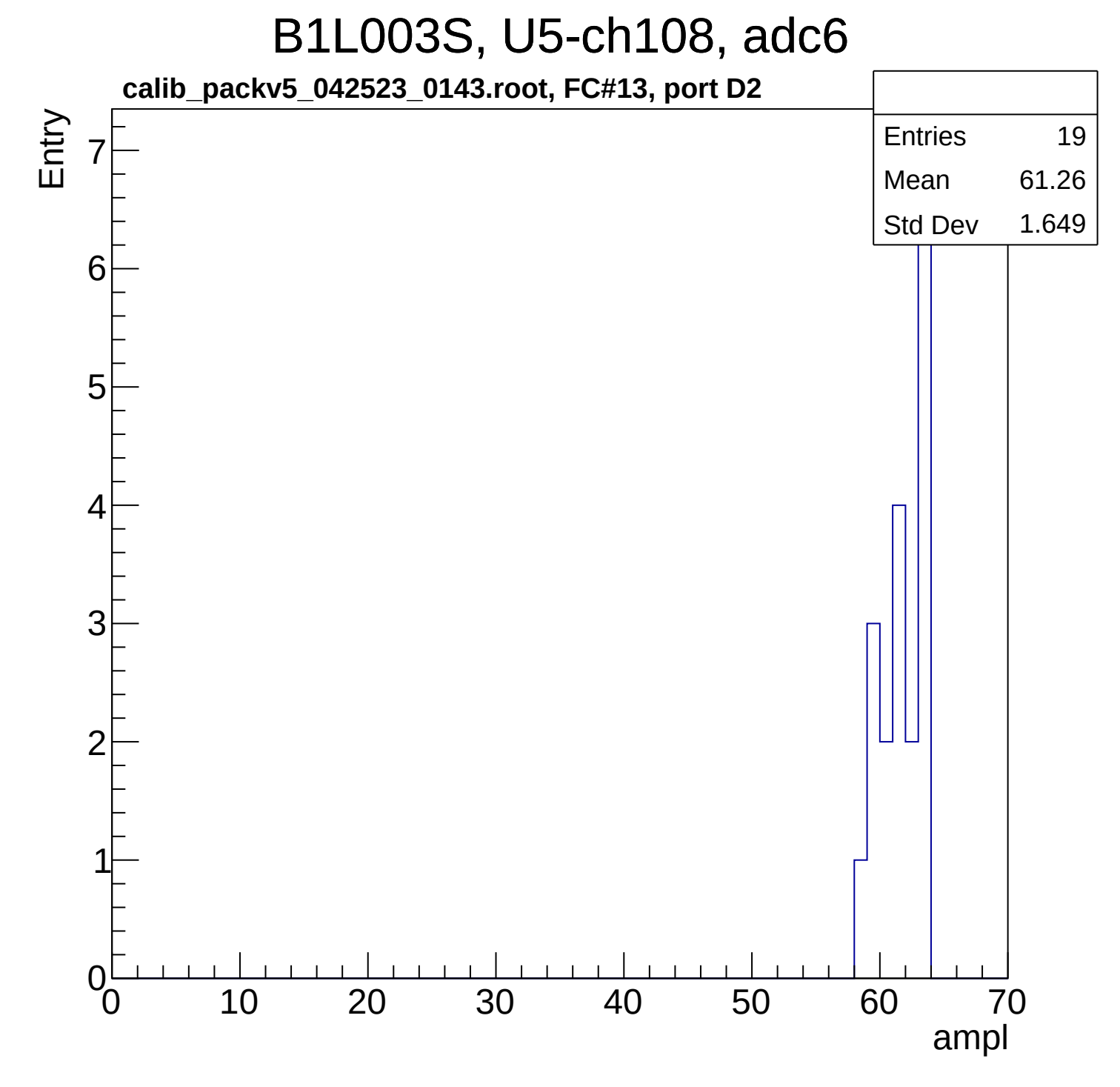
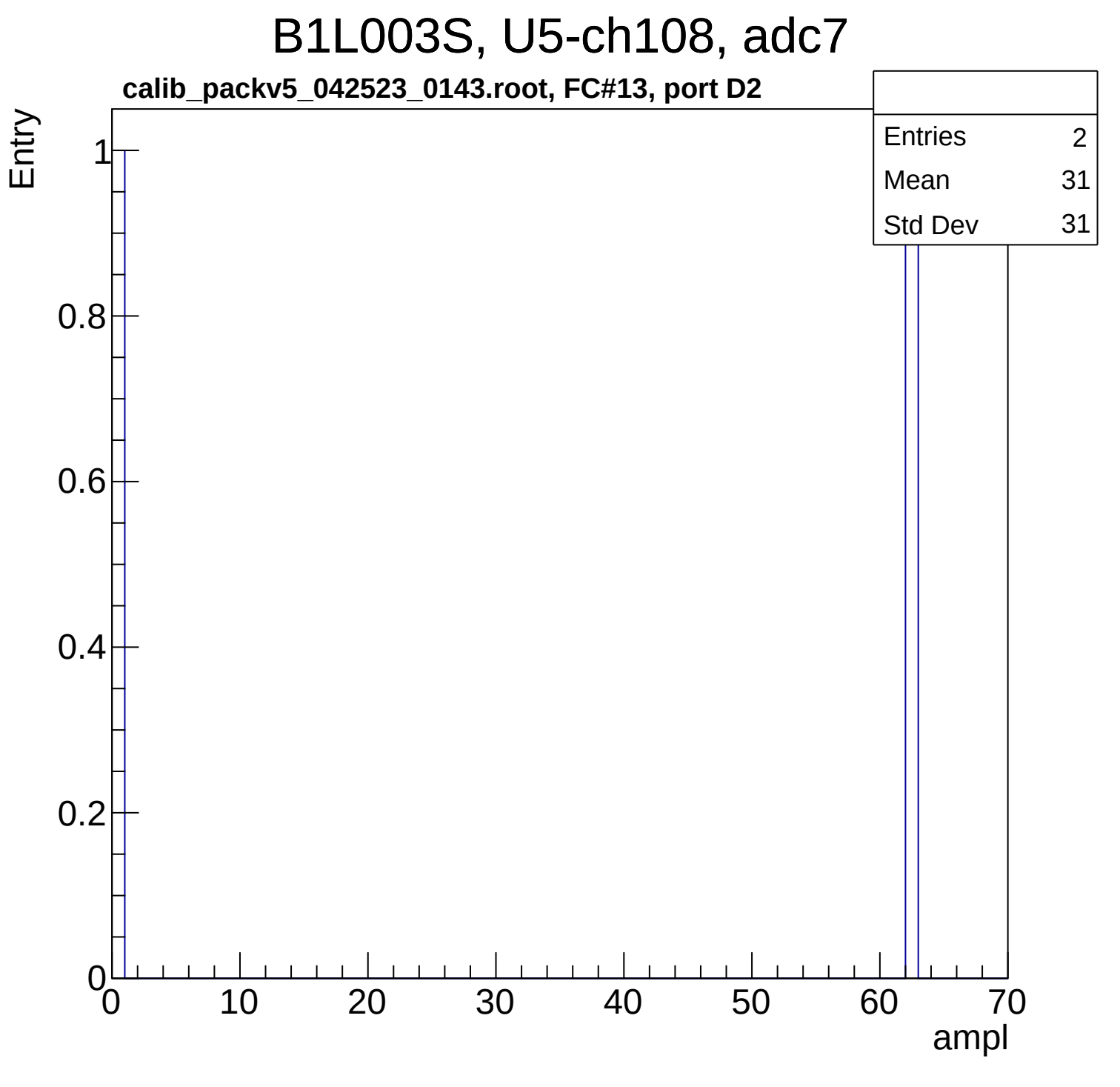
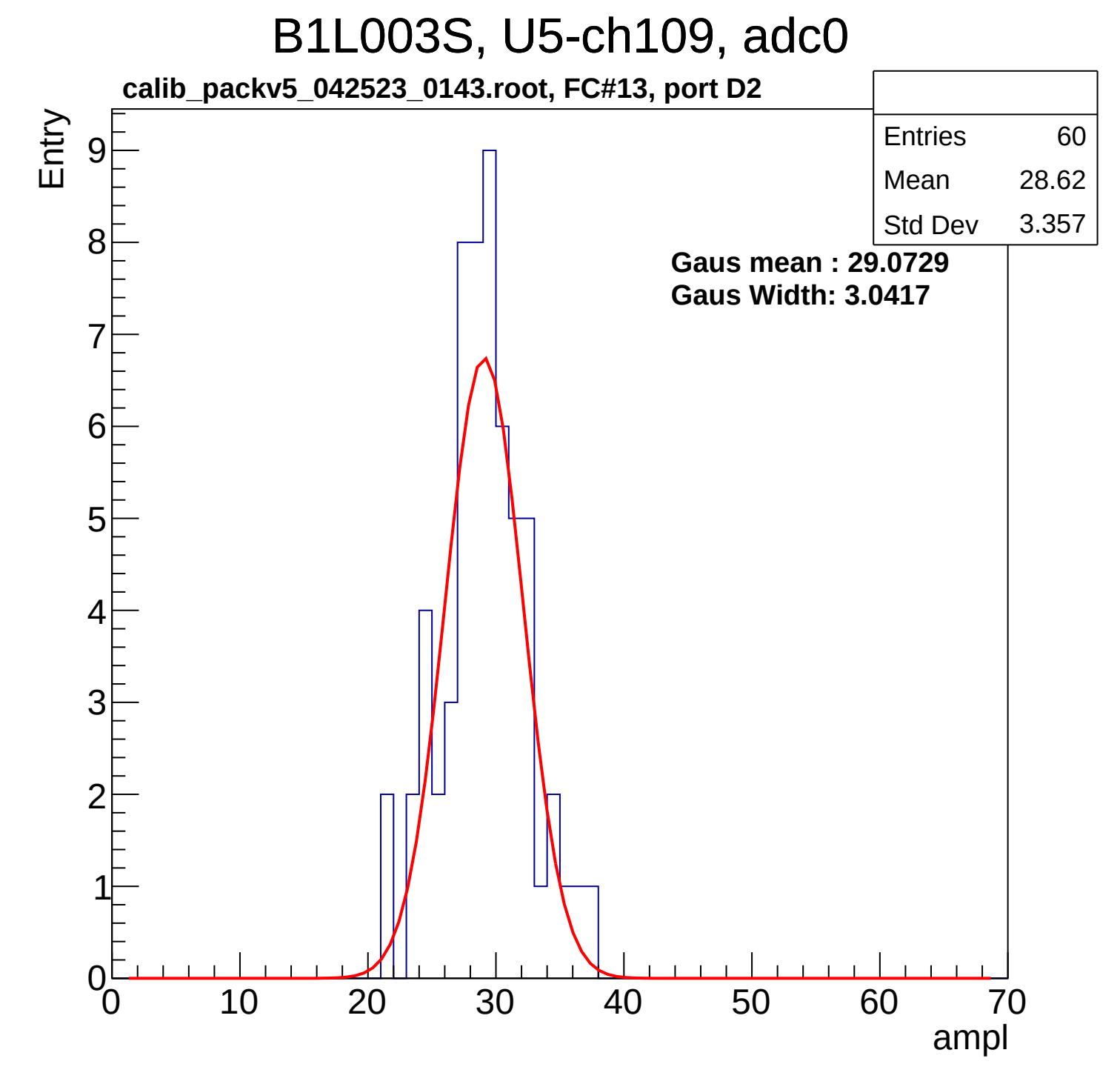
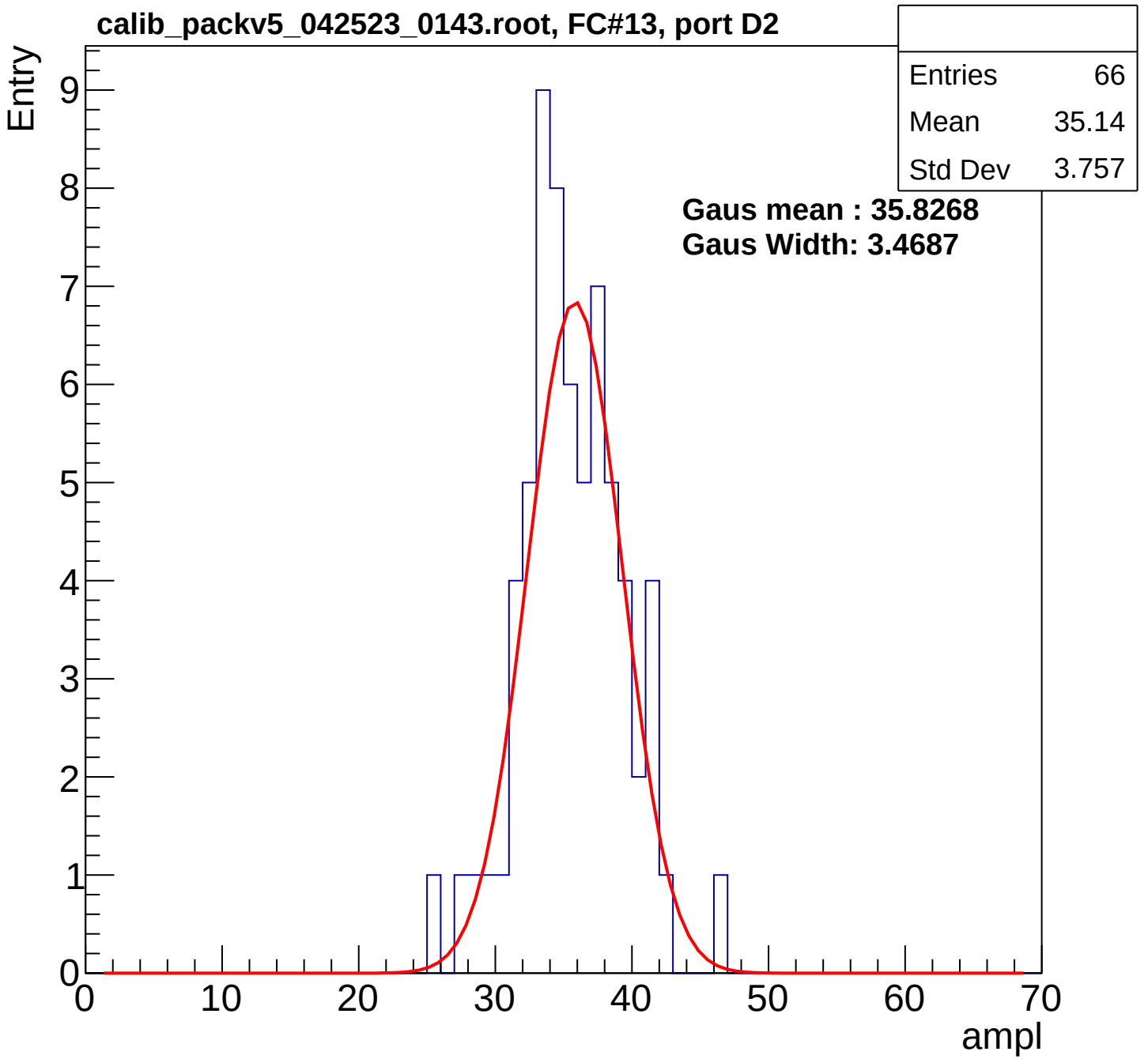
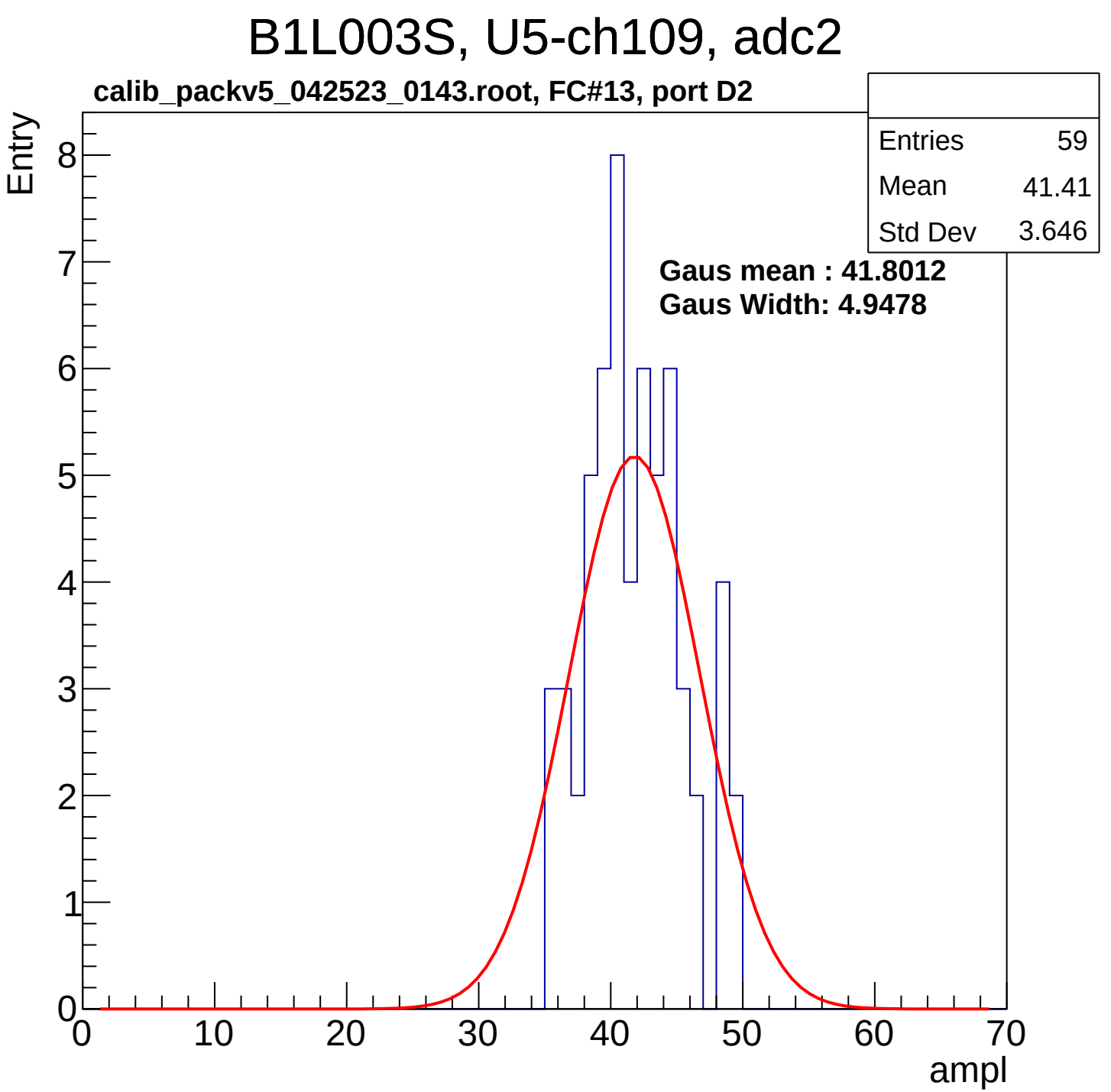

B1L003S, U5-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 61.26
Std Dev 1.649
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.62
Std Dev 3.357
Gaus mean : 29.0729
Gaus Width: 3.0417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.14
Std Dev 3.757
Gaus mean : 35.8268
Gaus Width: 3.4687
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U5-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 41.41
Std Dev 3.646
Gaus mean : 41.8012
Gaus Width: 4.9478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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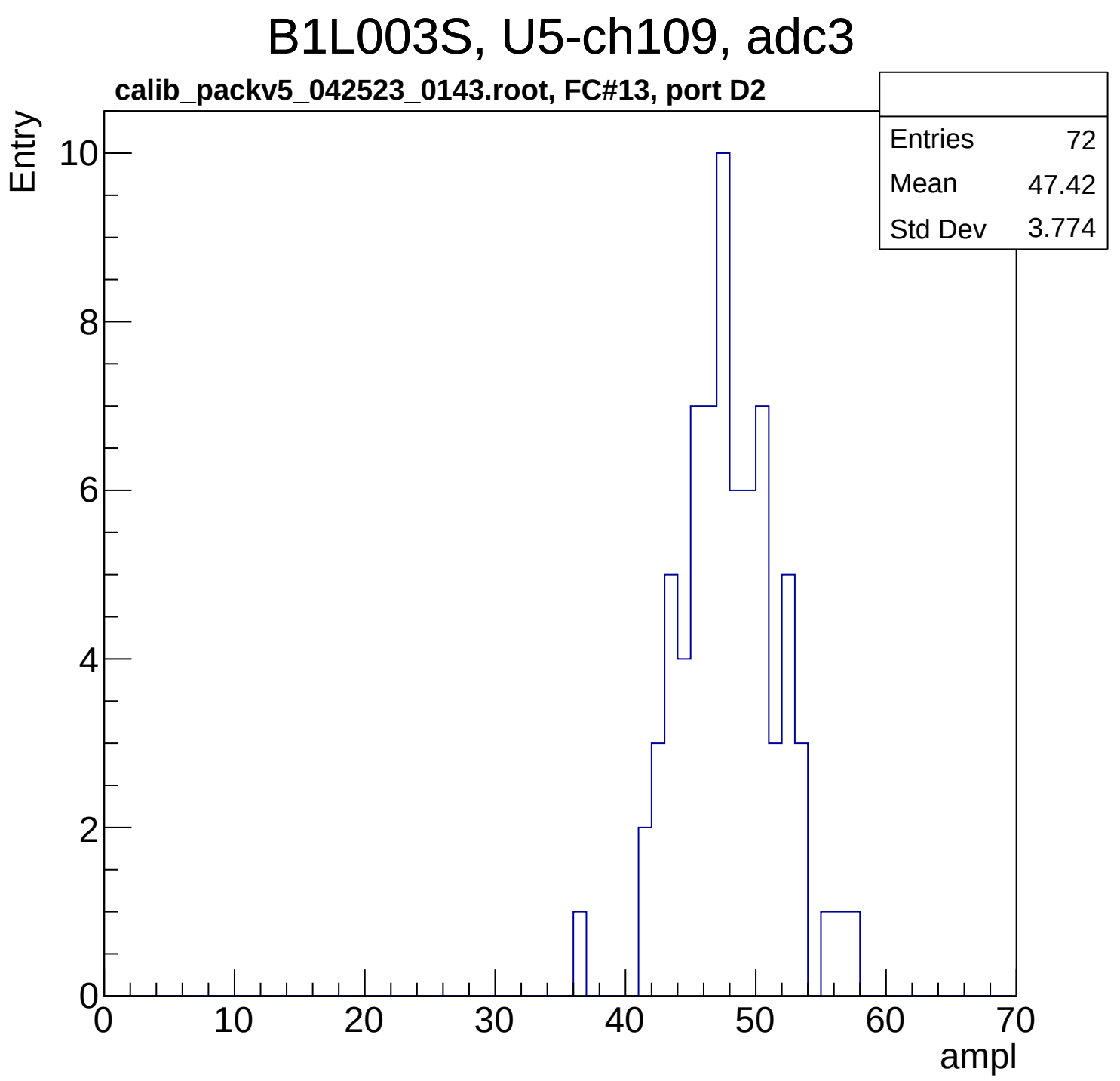
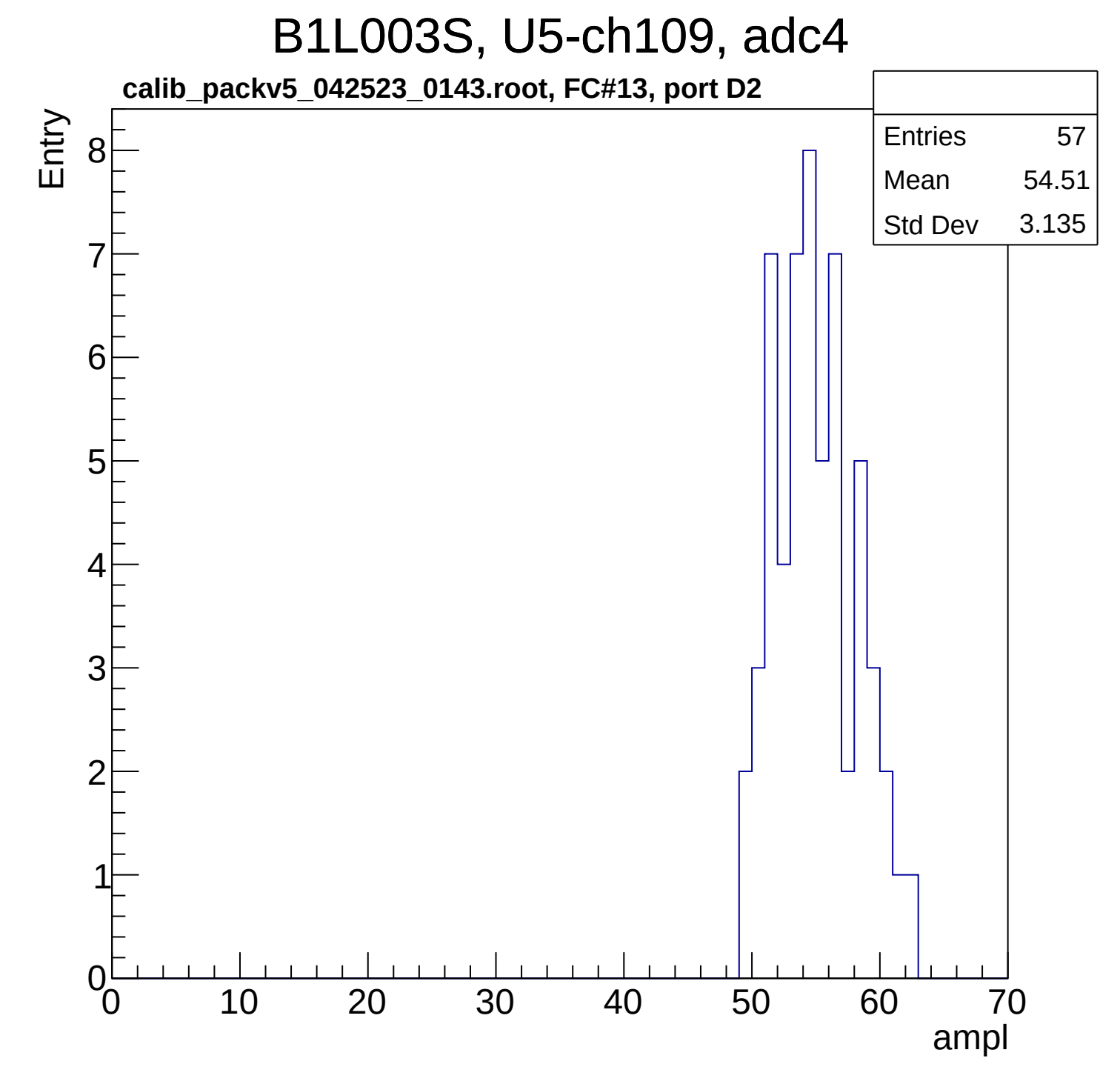
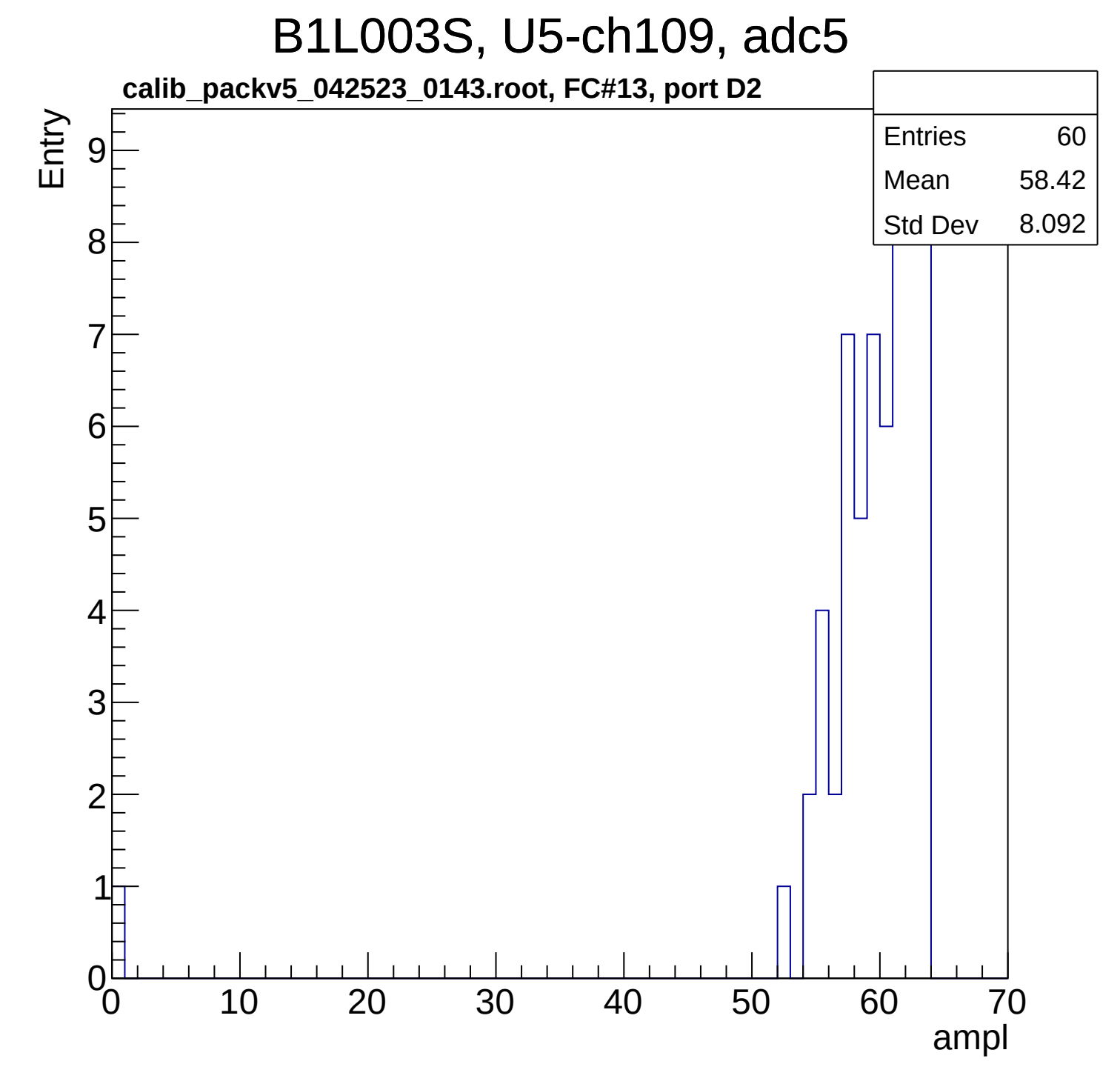
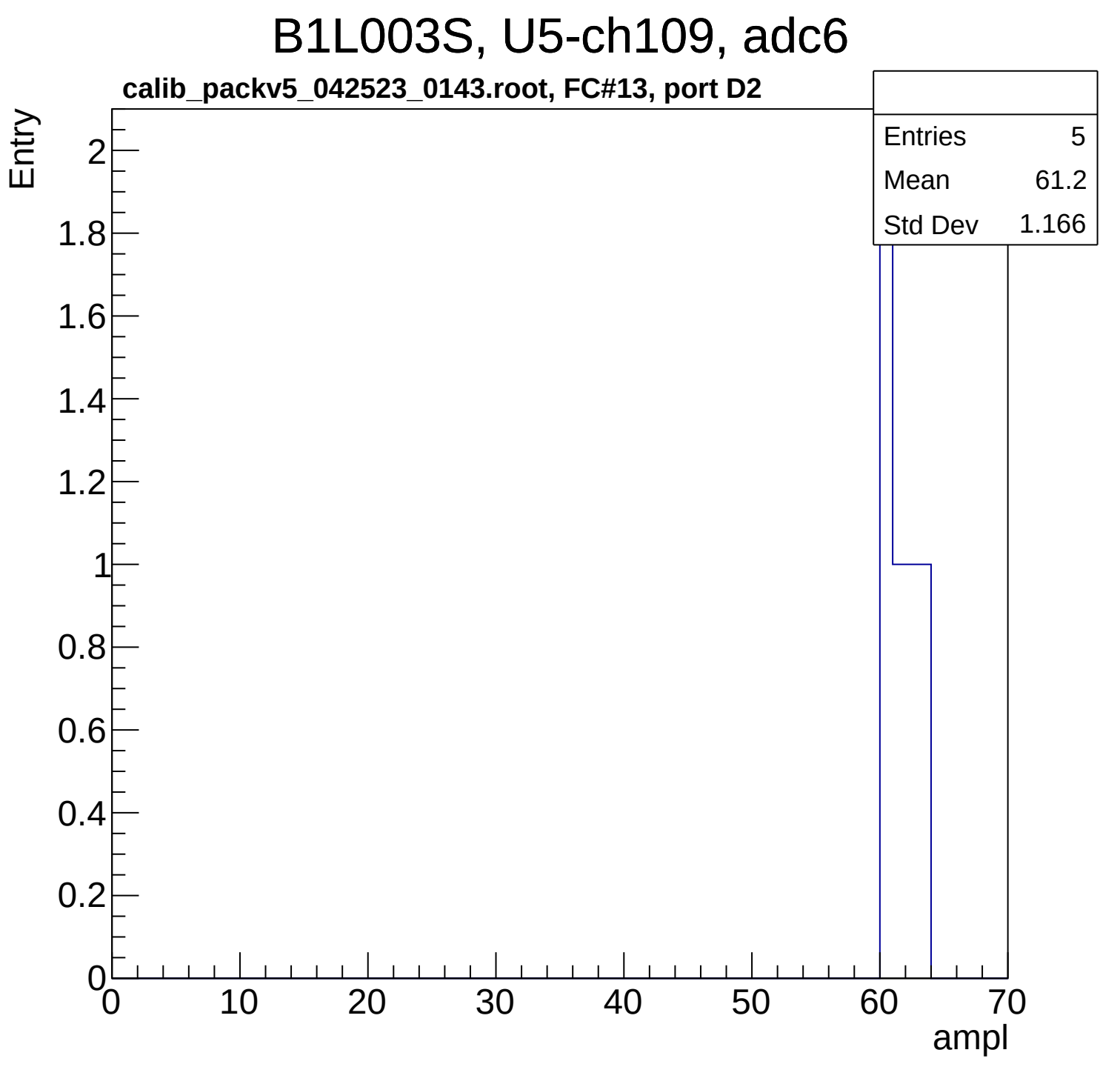
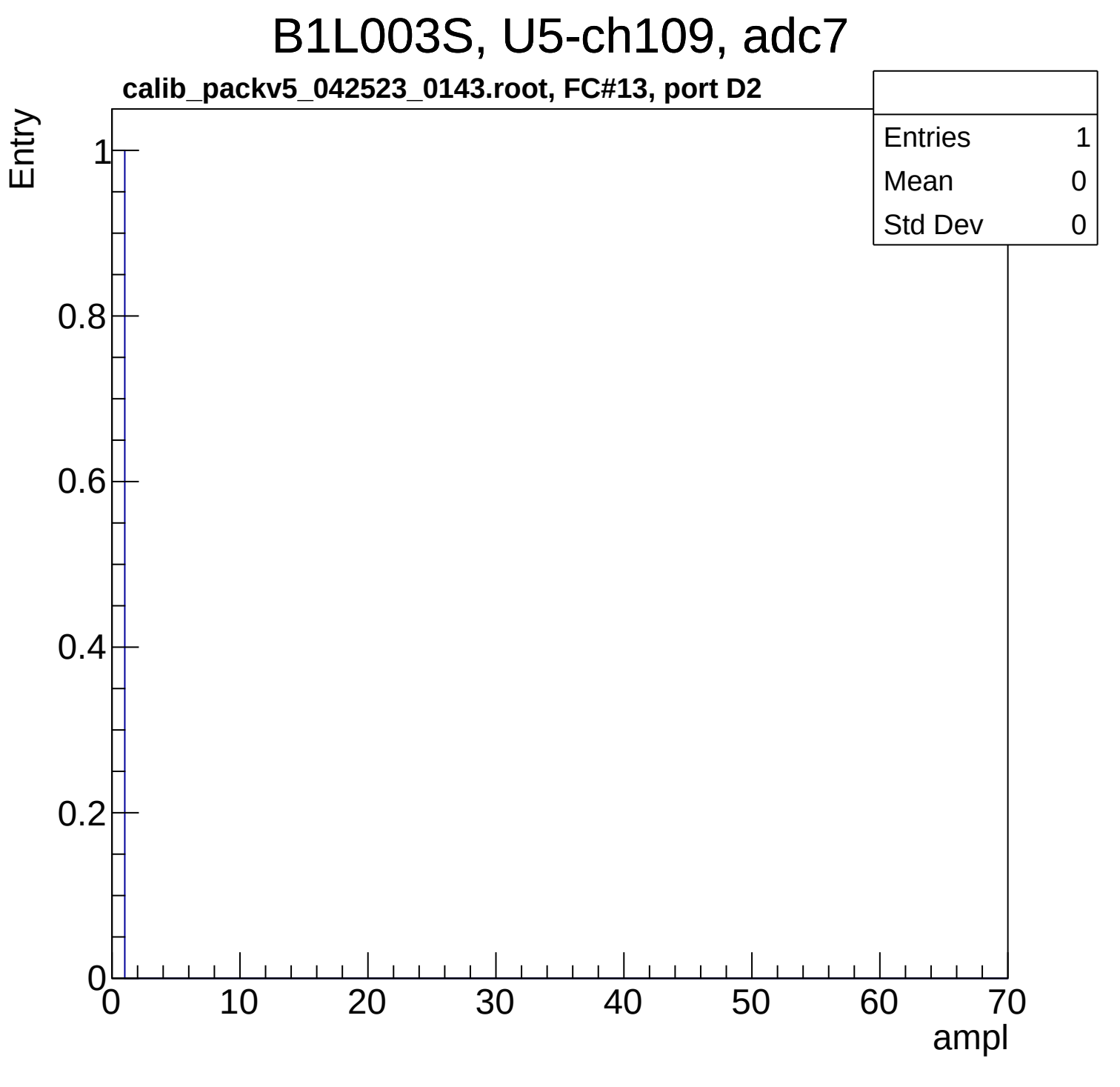
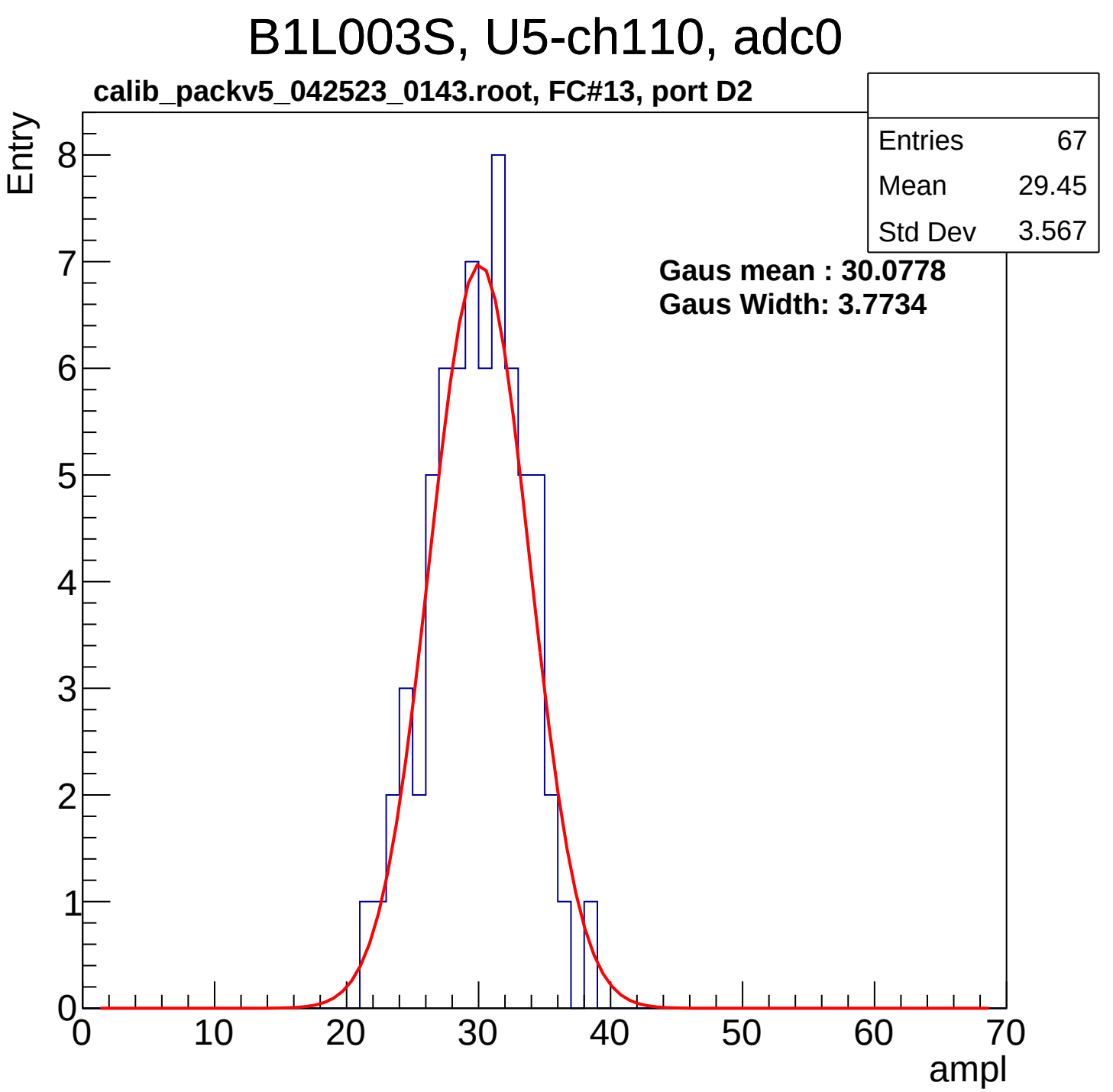

B1L003S, U5-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 47.42
Std Dev 3.774
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

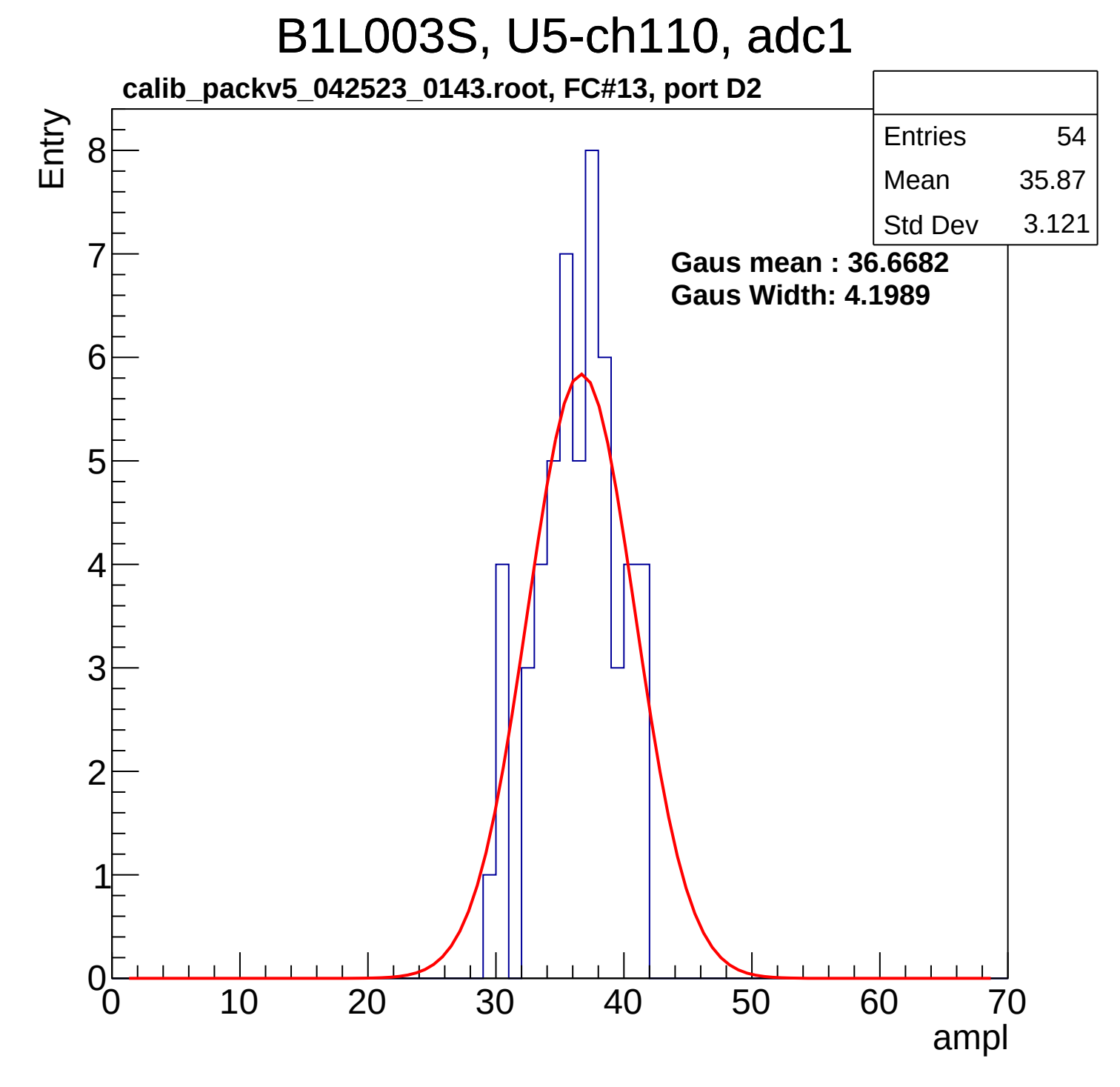

B1L003S, U5-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.51
Std Dev 3.135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

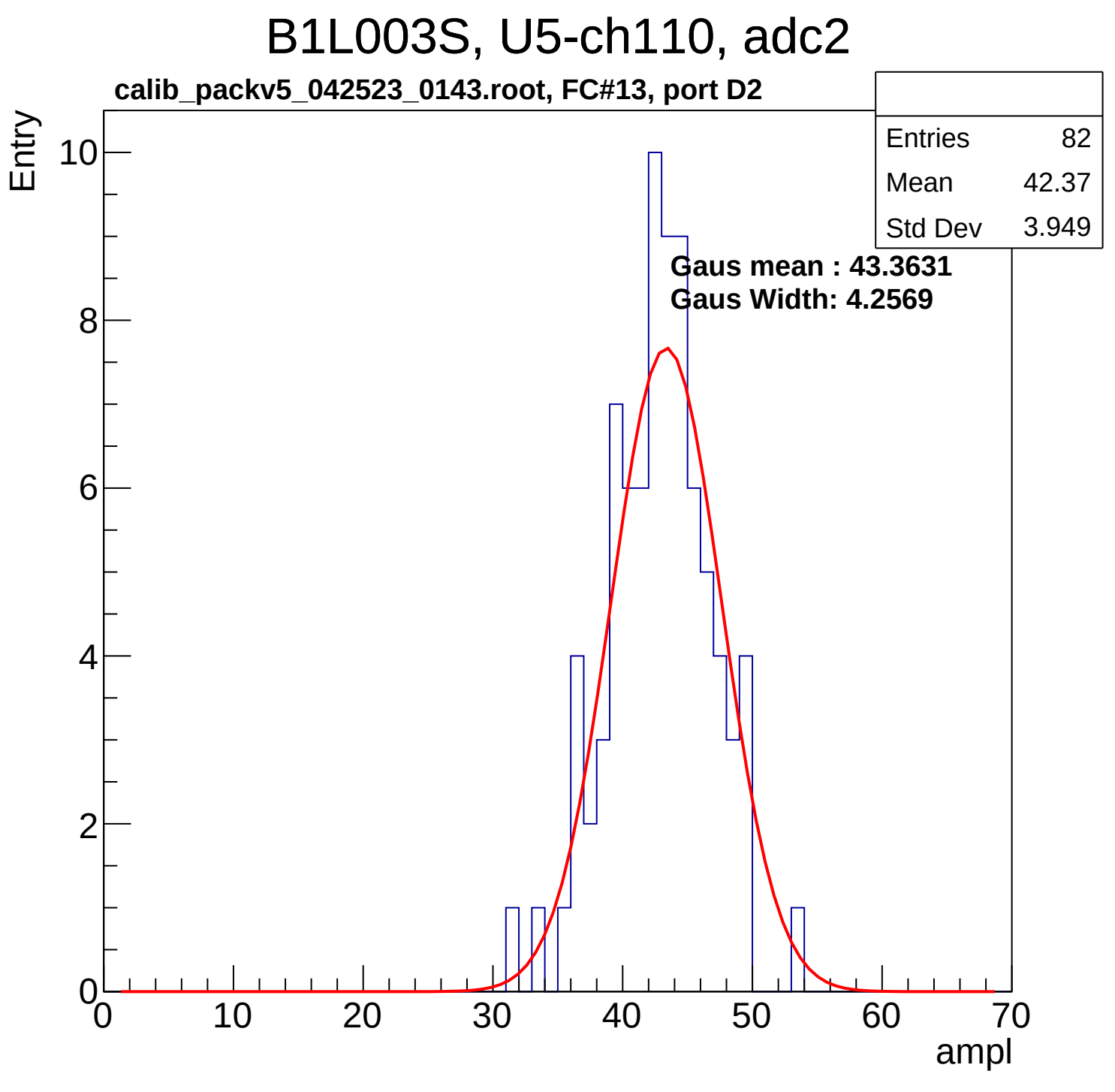

B1L003S, U5-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.42
Std Dev 8.092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

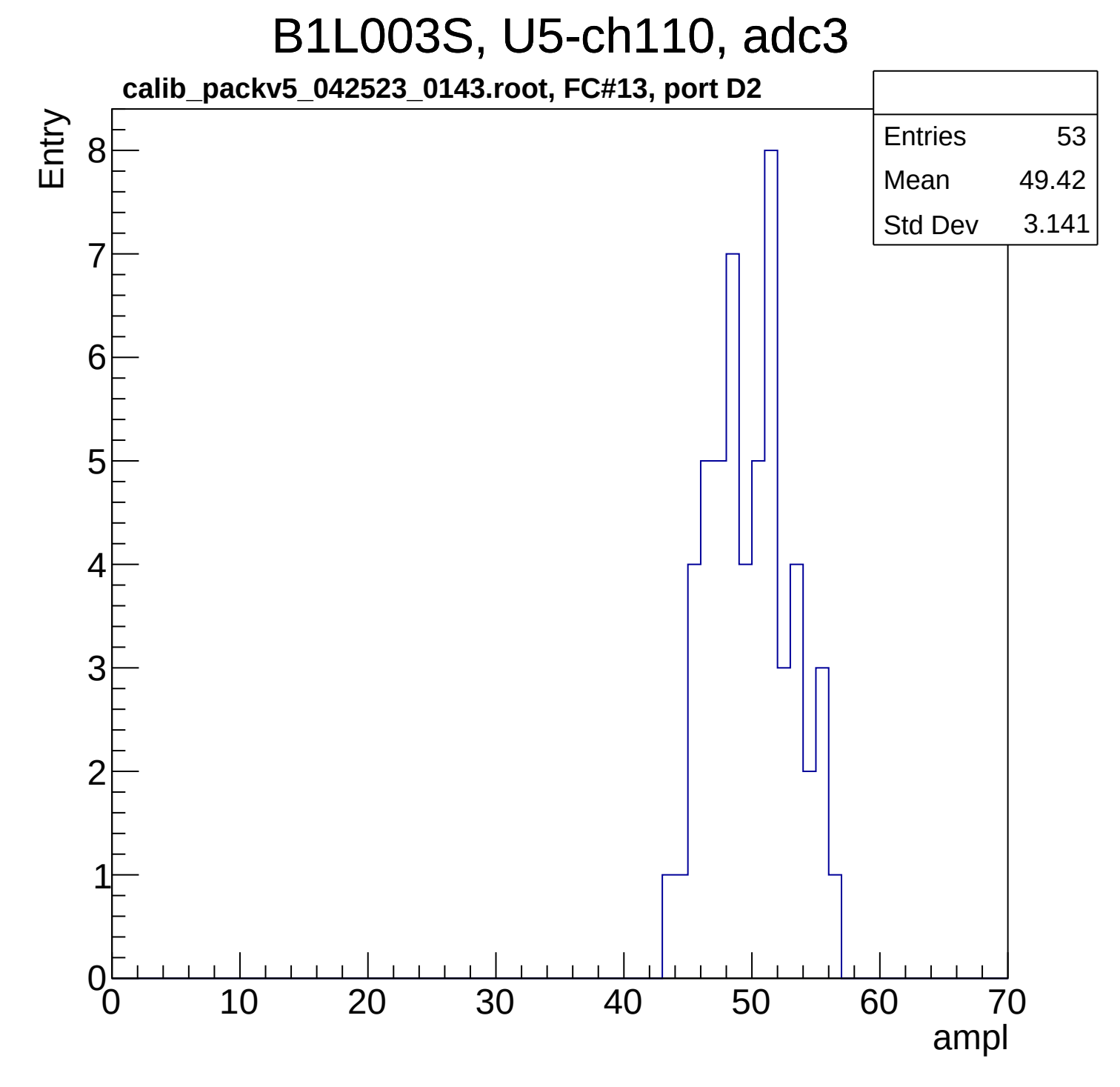

B1L003S, U5-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.2
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

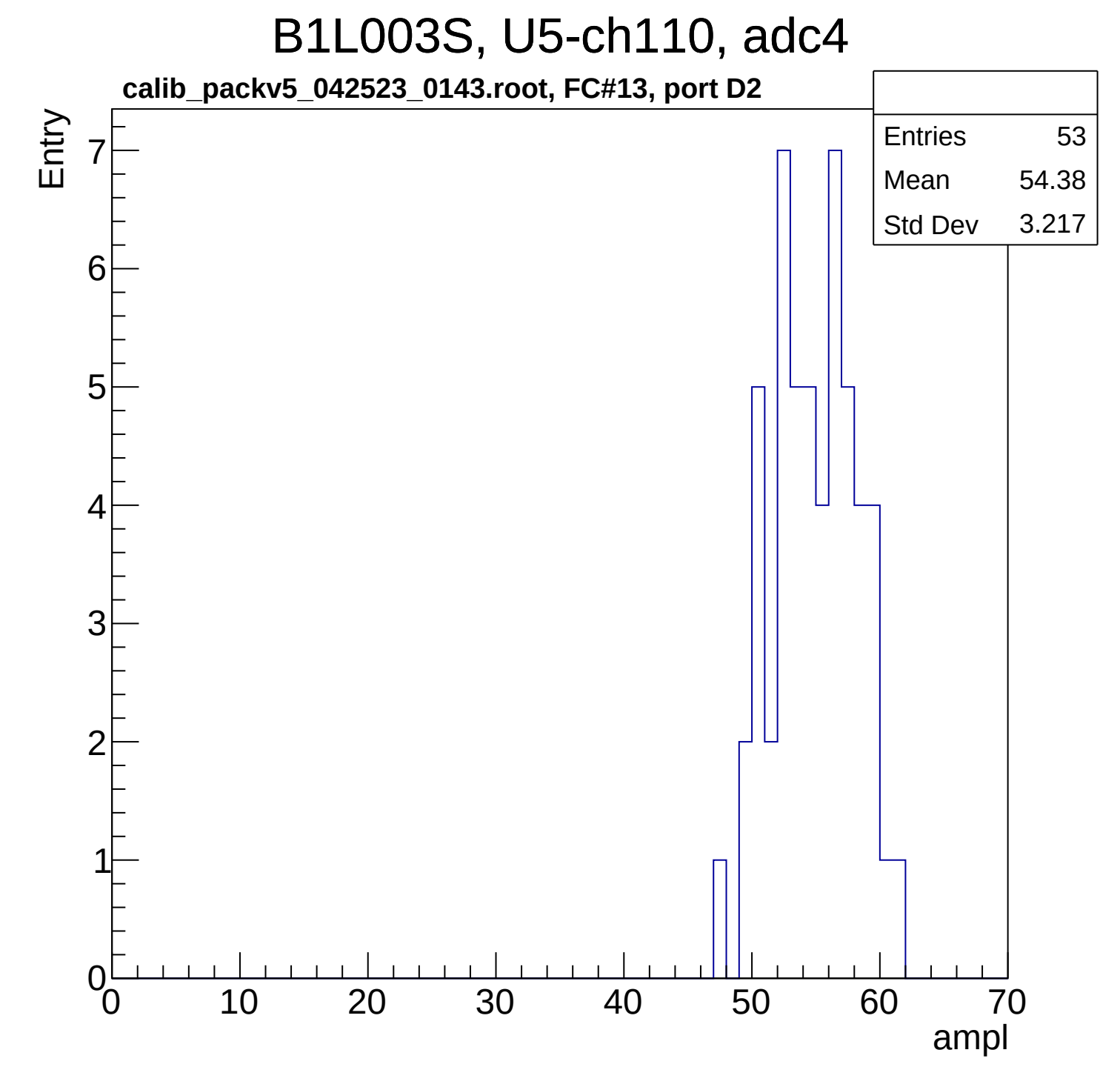

B1L003S, U5-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.45
Std Dev 3.567
Gaus mean : 30.0778
Gaus Width: 3.7734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.87
Std Dev 3.121
Gaus mean : 36.6682
Gaus Width: 4.1989
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 42.37
Std Dev 3.949
Gaus mean : 43.3631
Gaus Width: 4.2569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.42
Std Dev 3.141
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.38
Std Dev 3.217
Entry
ampl
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1
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3
4
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0
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20
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40
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60
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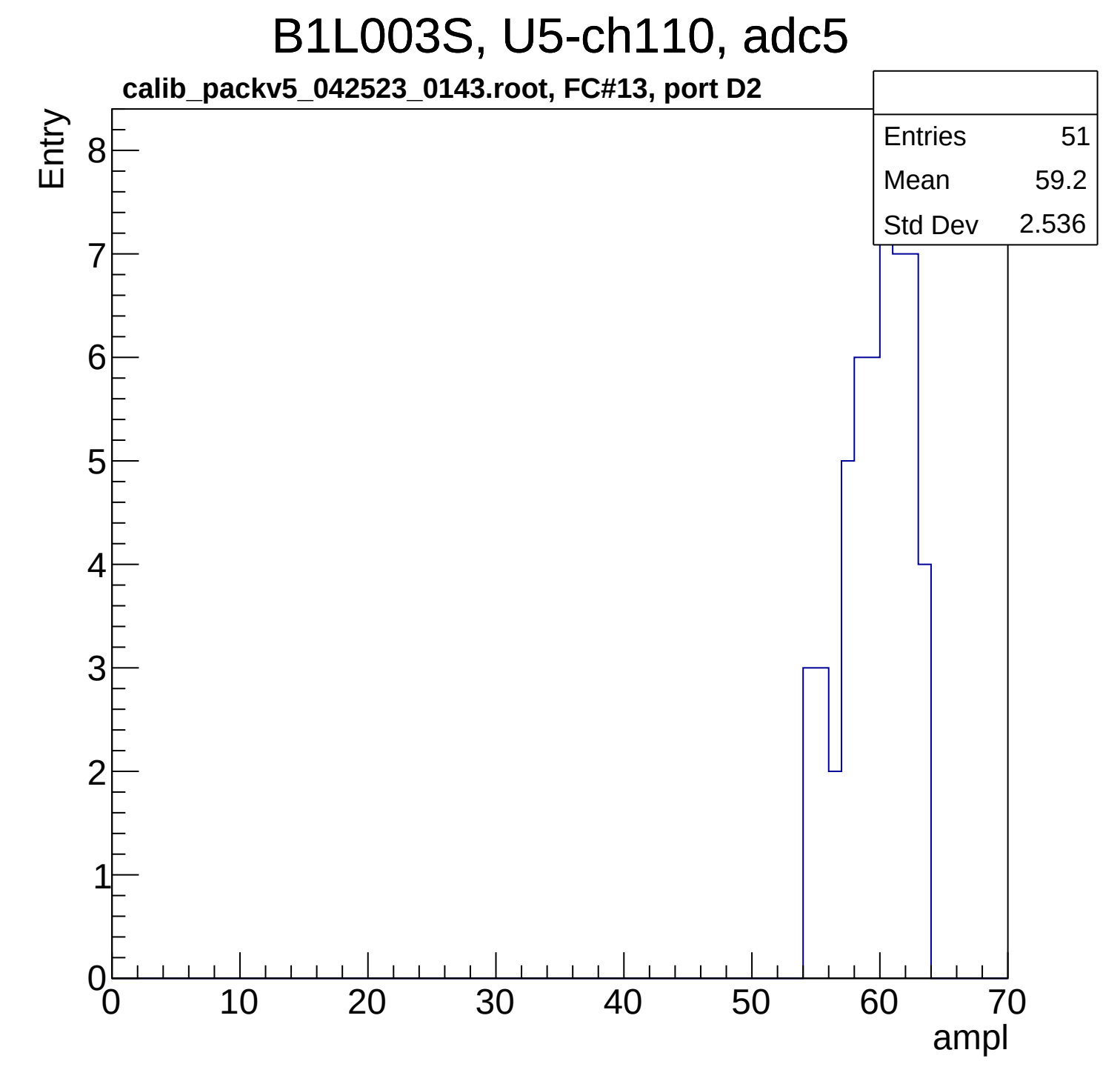
B1L003S, U5-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.2
Std Dev 2.536
Entry
ampl
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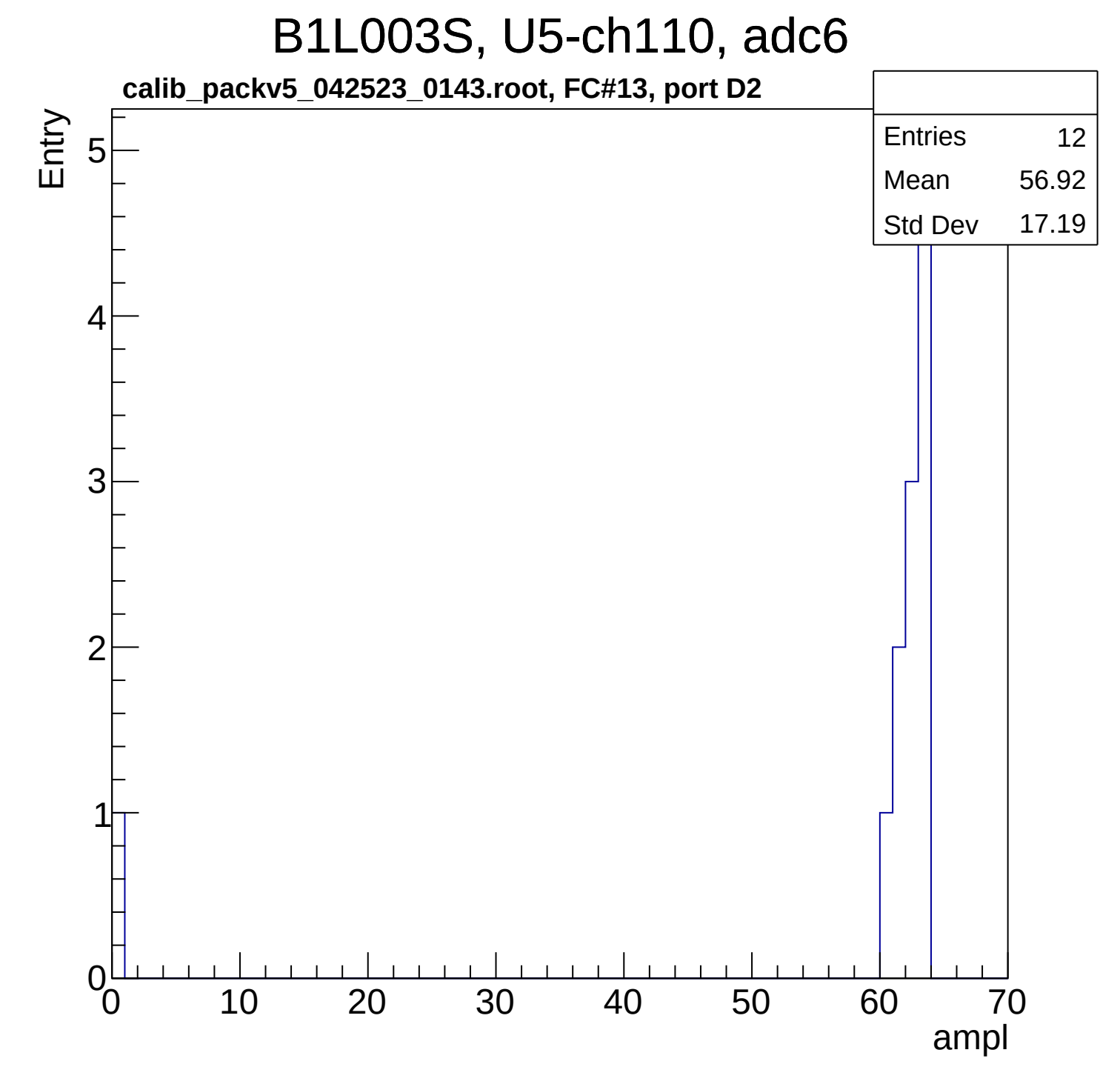
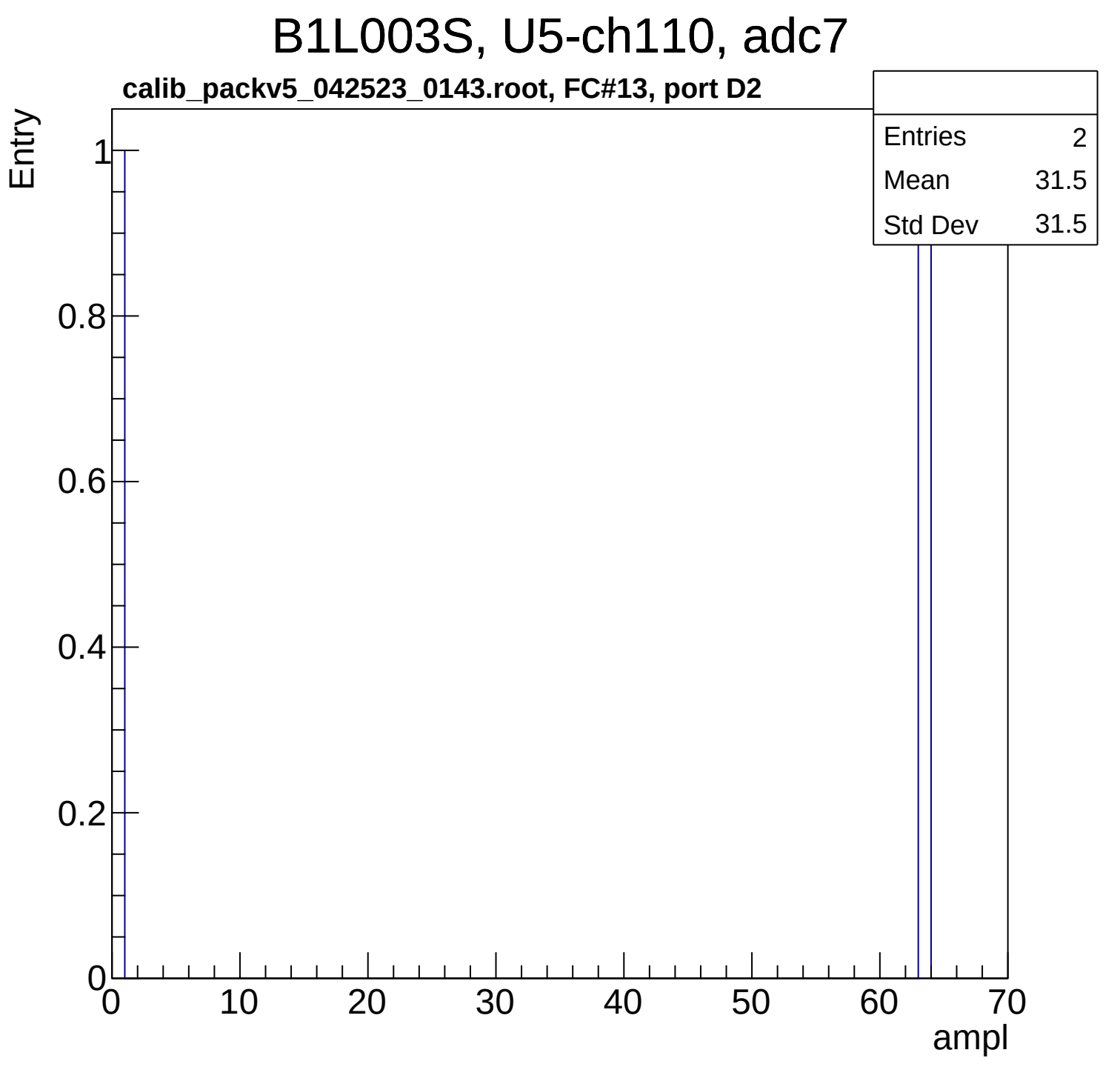
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.92
Std Dev 17.19
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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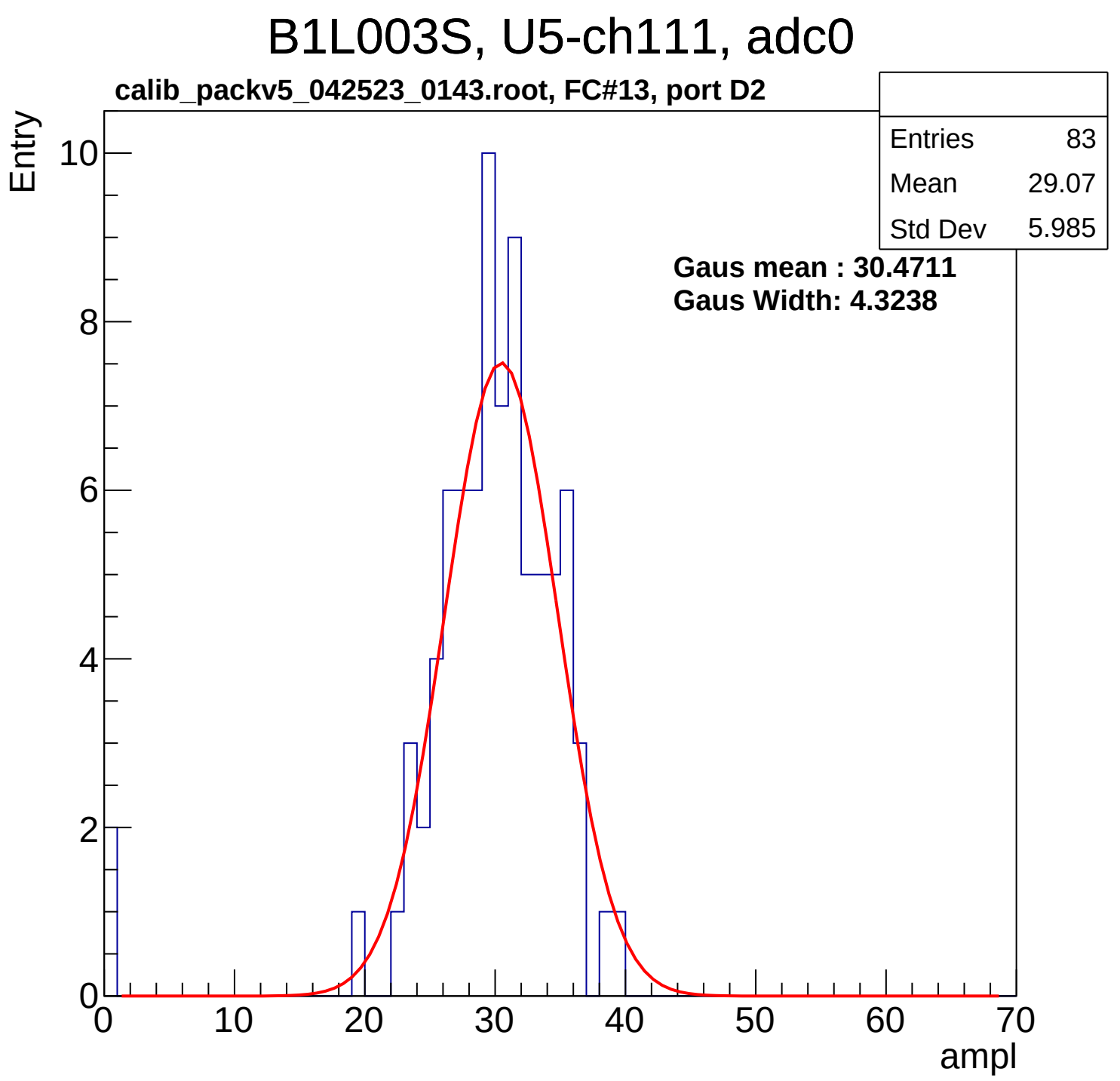
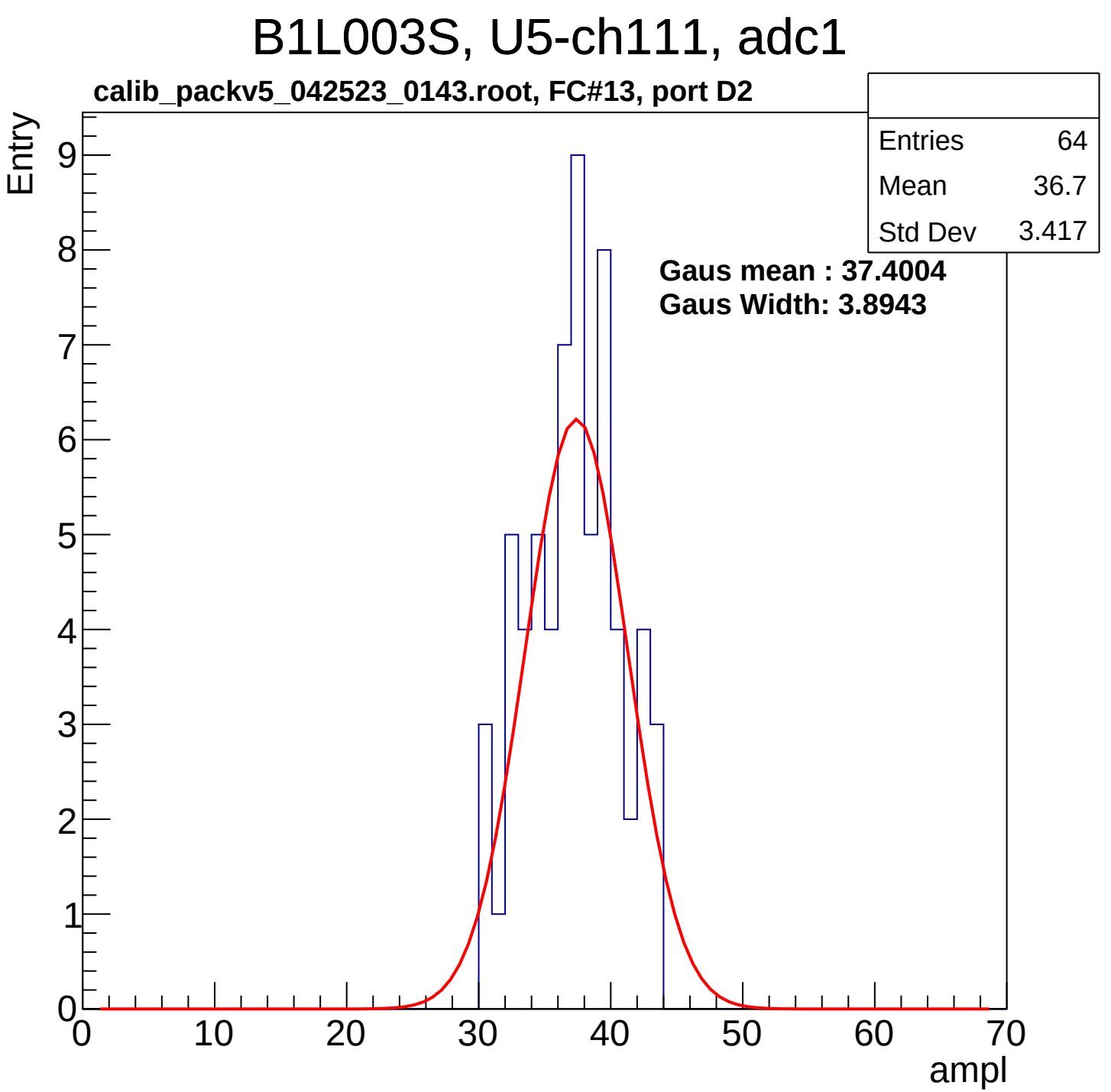
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 29.07
Std Dev 5.985
Gaus mean : 30.4711
Gaus Width: 4.3238
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.7
Std Dev 3.417
Gaus mean : 37.4004
Gaus Width: 3.8943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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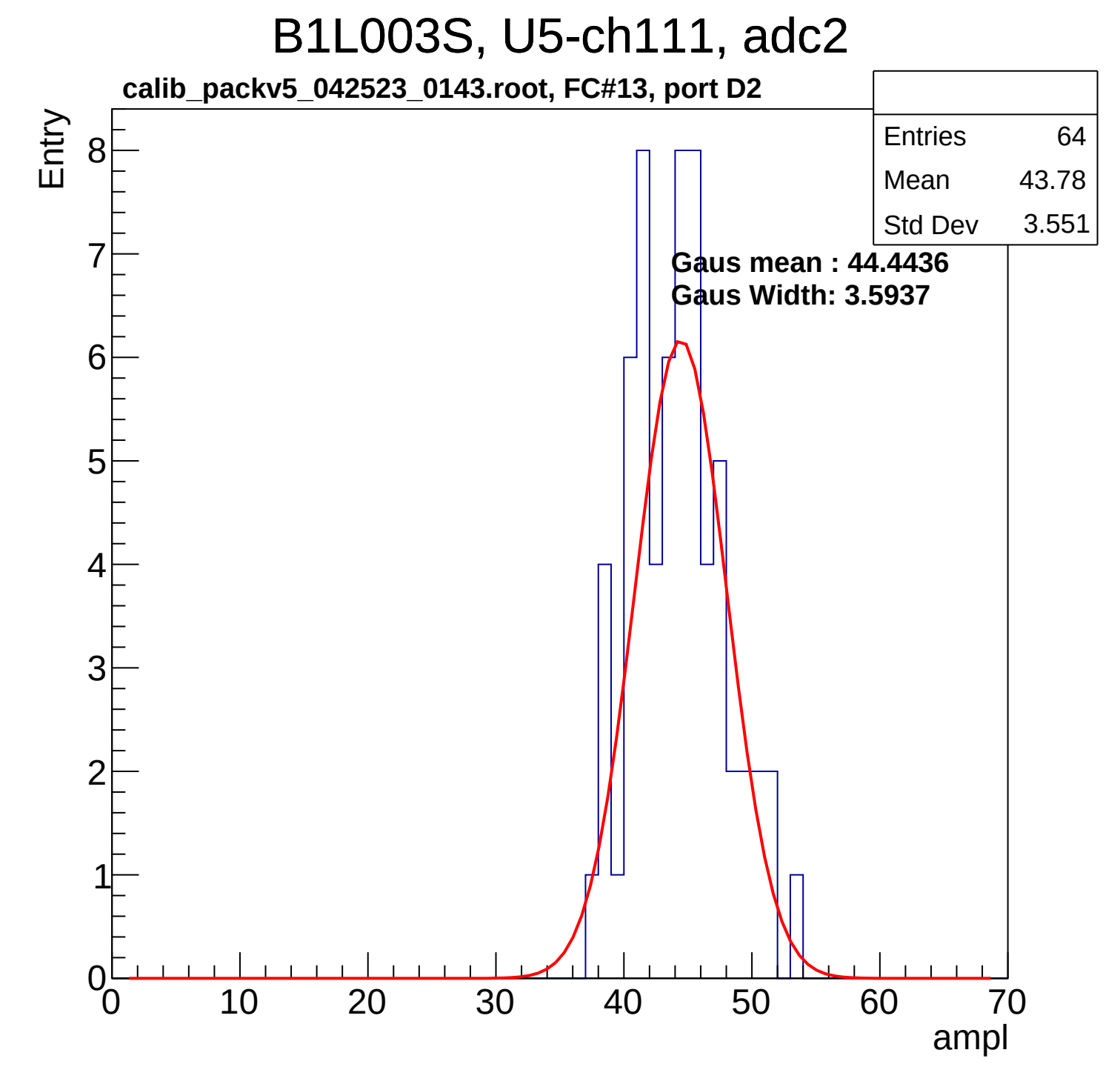
B1L003S, U5-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.78
Std Dev 3.551
Gaus mean : 44.4436
Gaus Width: 3.5937
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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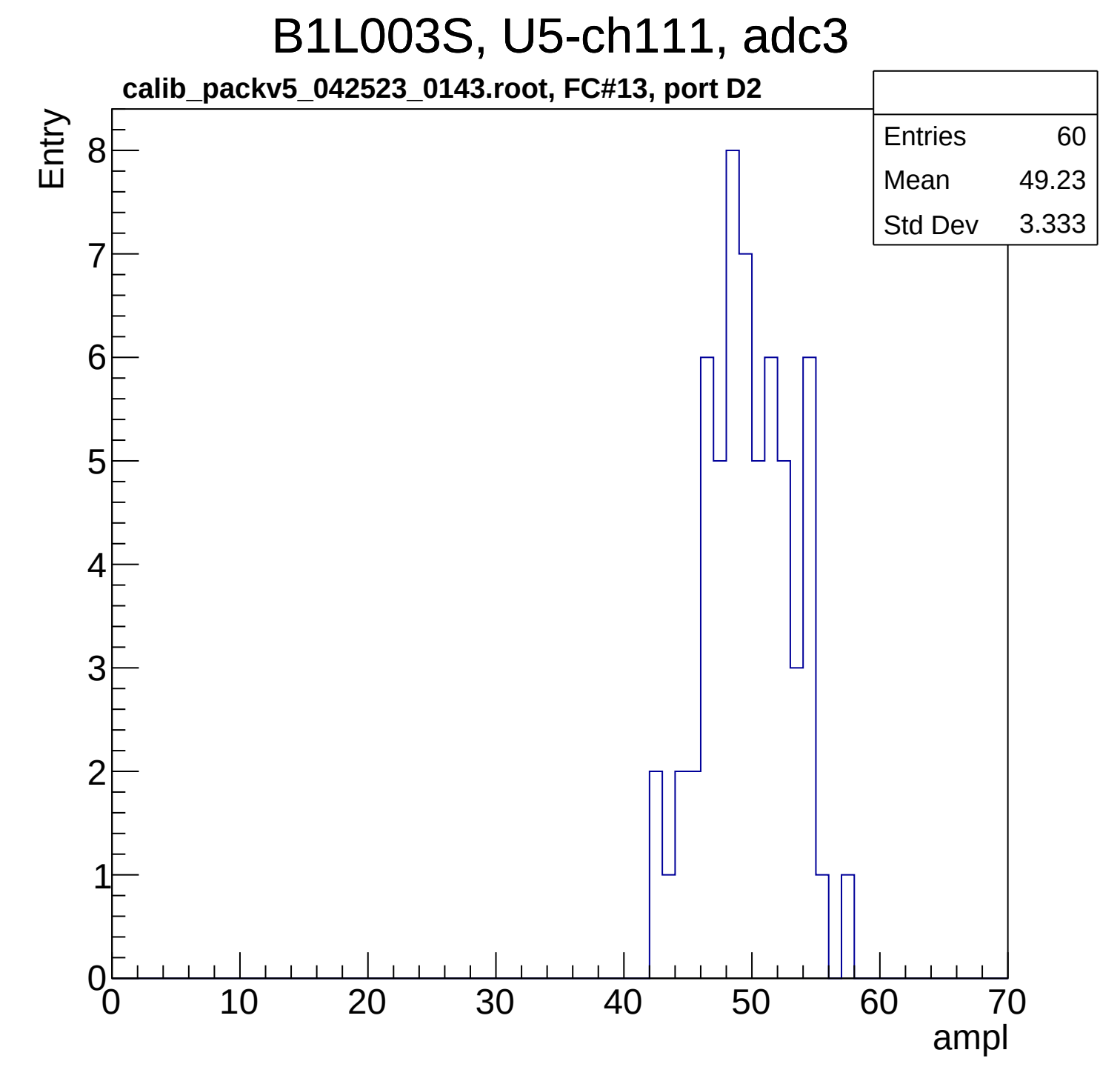
B1L003S, U5-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.23
Std Dev 3.333
Entry
ampl
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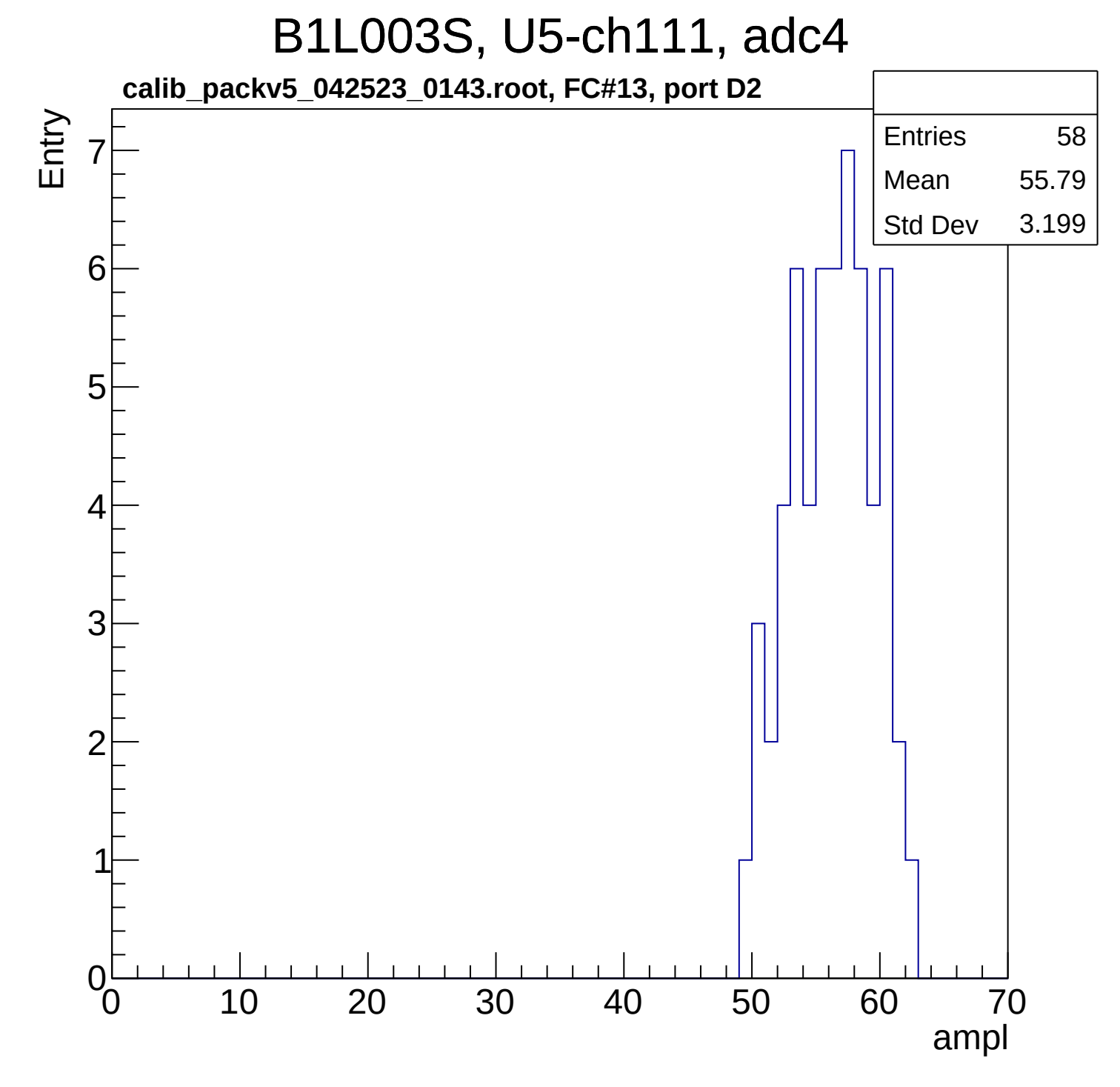
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.79
Std Dev 3.199
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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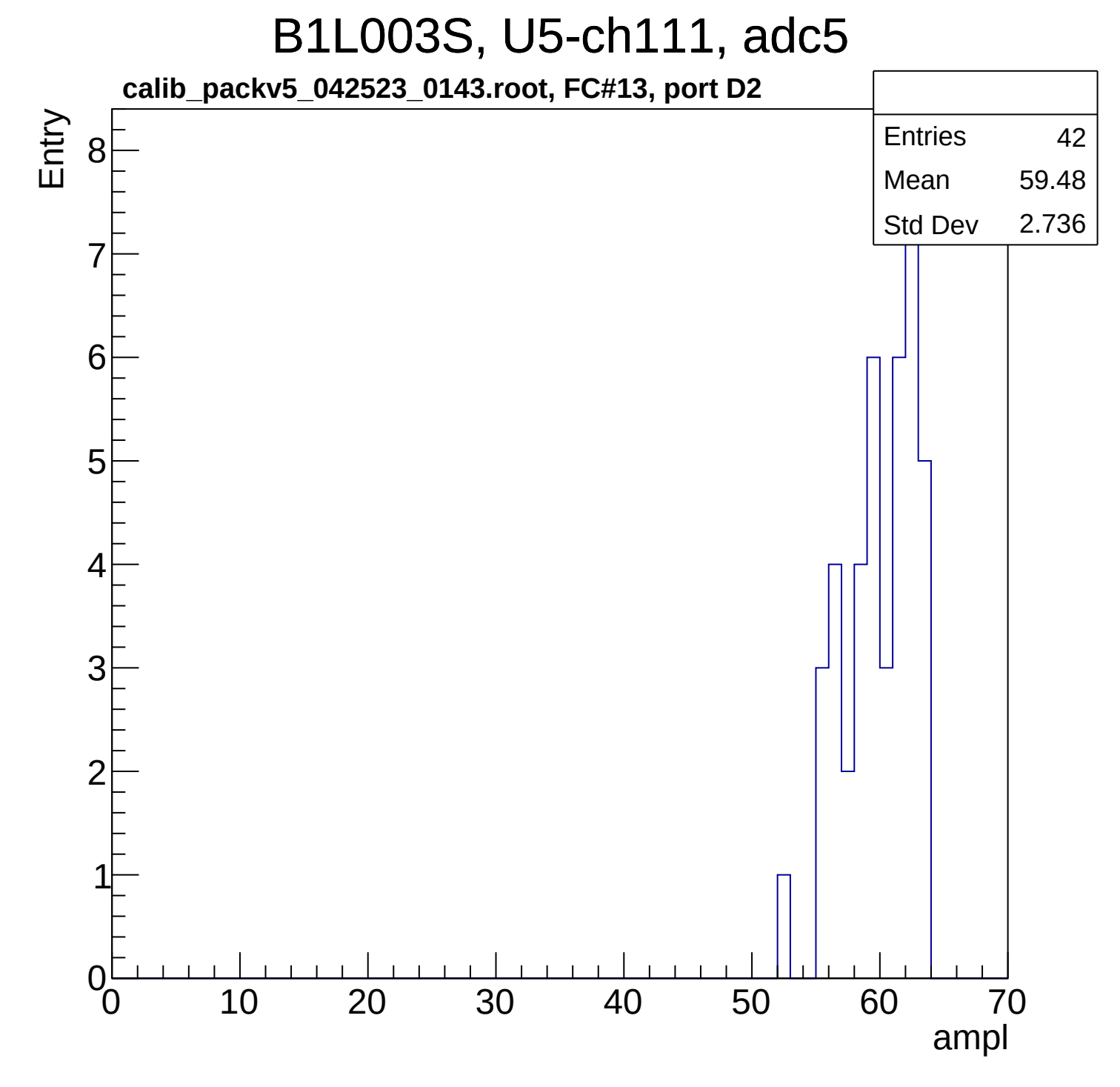
B1L003S, U5-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.48
Std Dev 2.736
Entry
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2
3
4
5
6
7
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10
20
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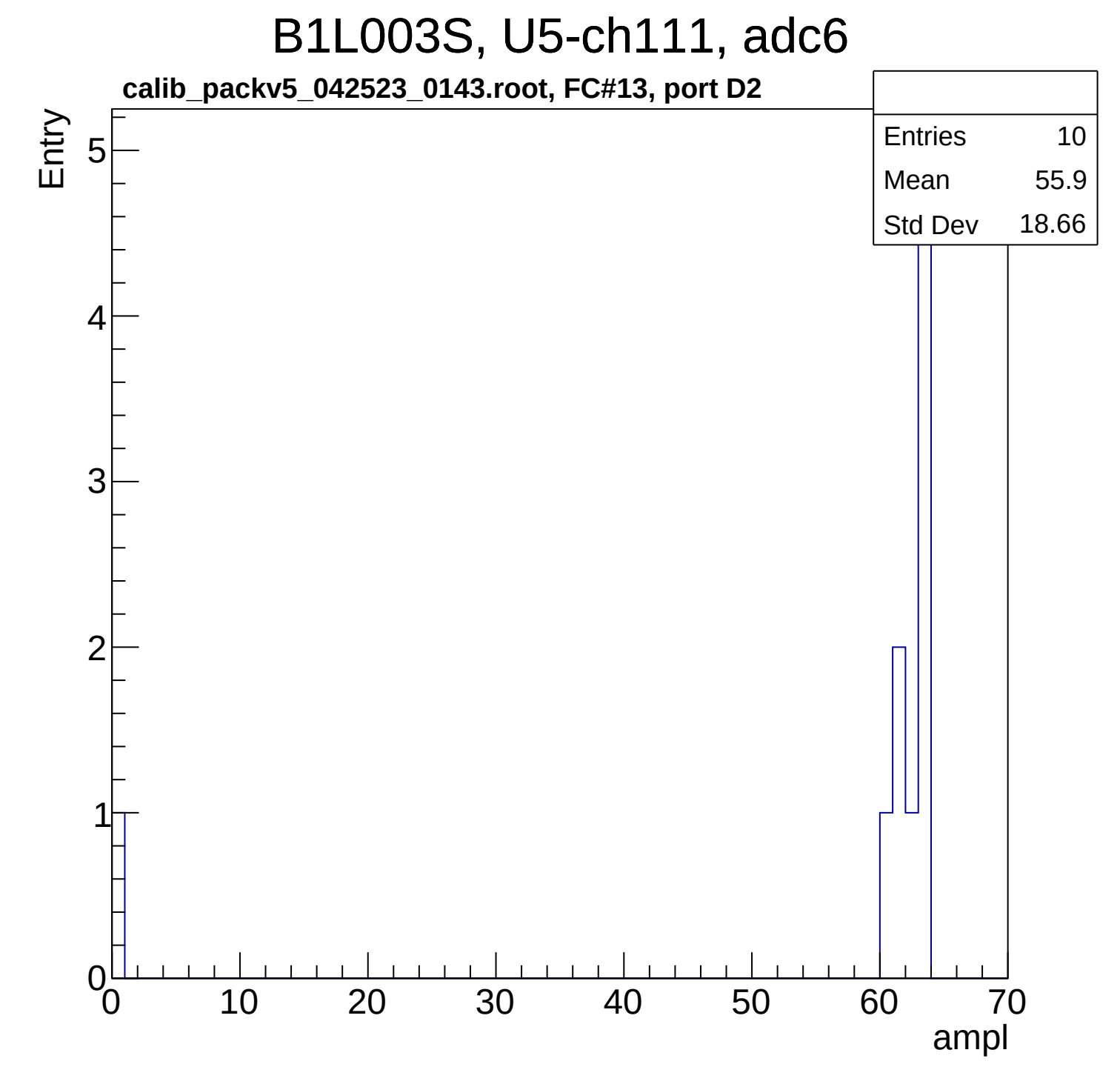
50
60
70
ampl


B1L003S, U5-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.9
Std Dev 18.66
Entry
ampl
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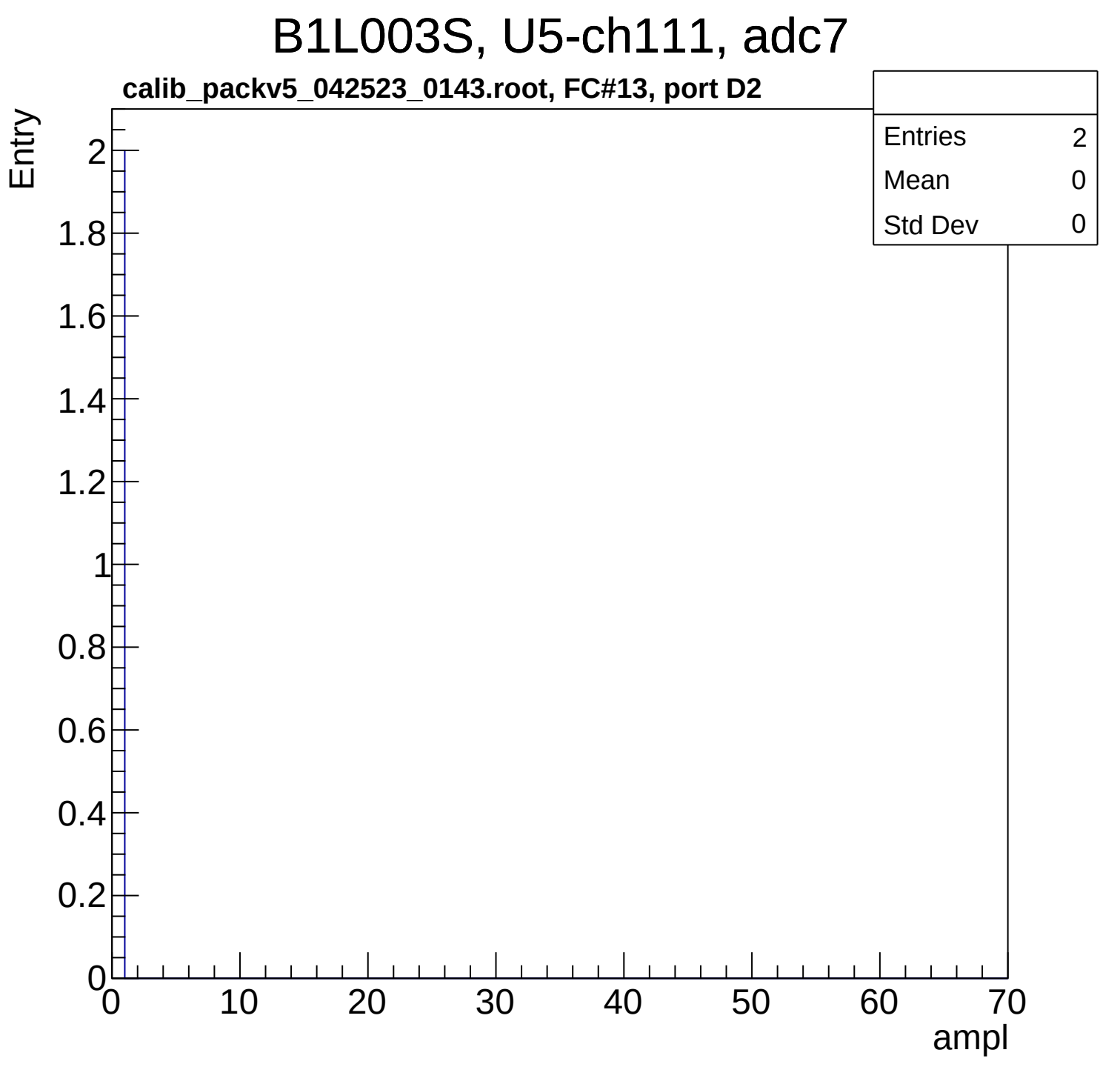
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U5-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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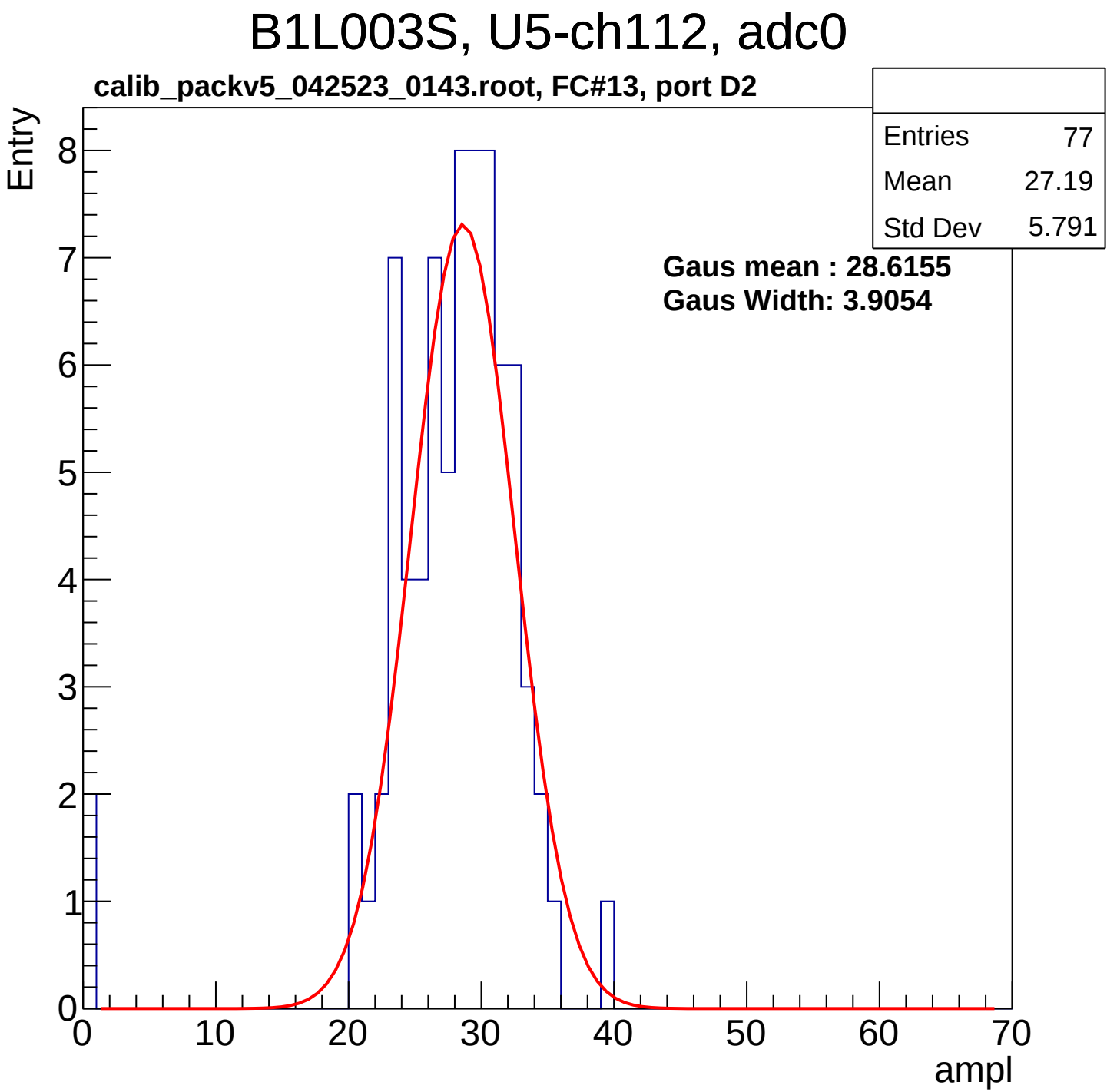
B1L003S, U5-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 27.19
Std Dev 5.791
Gaus mean : 28.6155
Gaus Width: 3.9054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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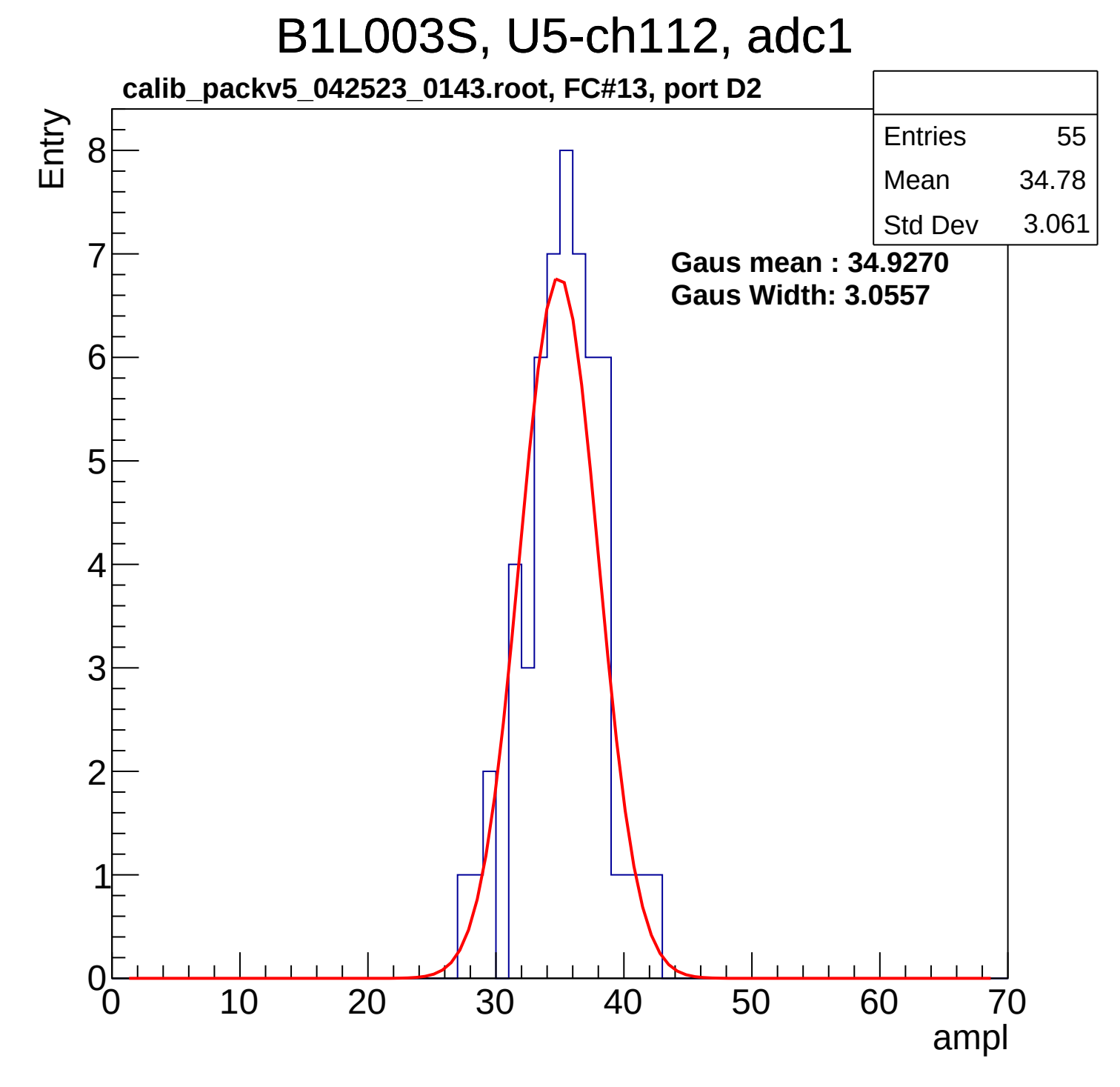
B1L003S, U5-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.78
Std Dev 3.061
Gaus mean : 34.9270
Gaus Width: 3.0557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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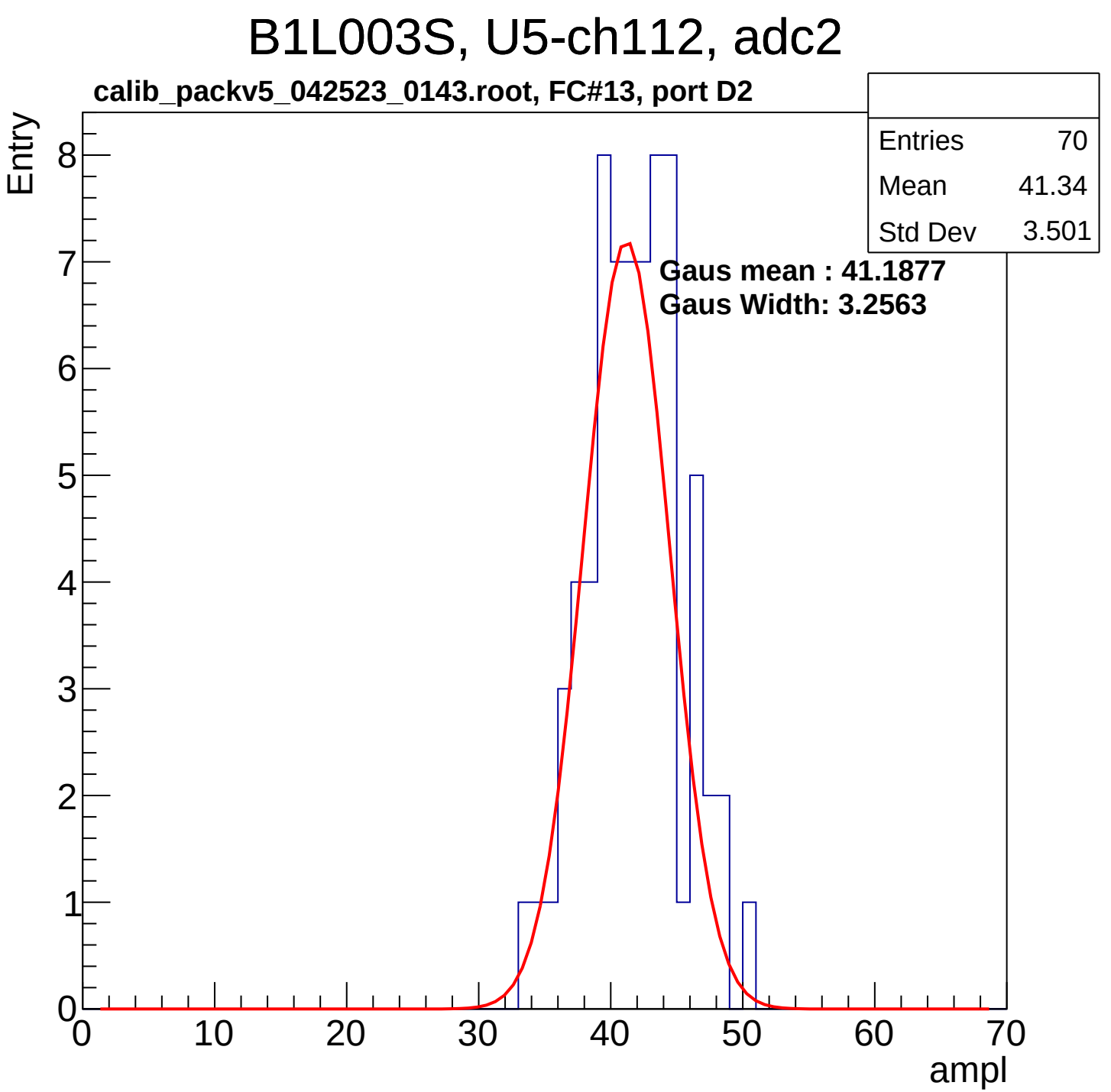
B1L003S, U5-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.34
Std Dev 3.501
Gaus mean : 41.1877
Gaus Width: 3.2563
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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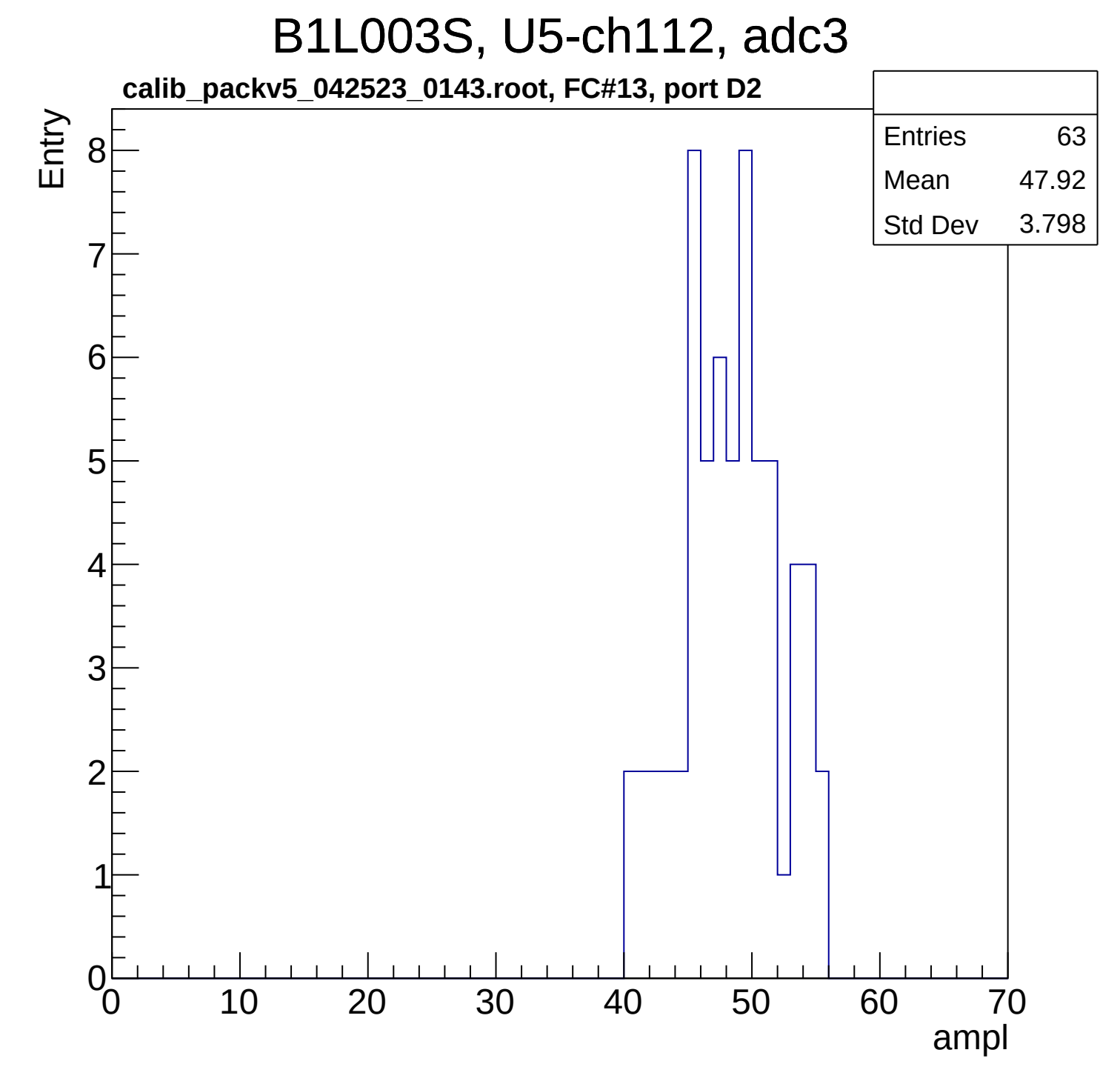
B1L003S, U5-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 47.92
Std Dev 3.798
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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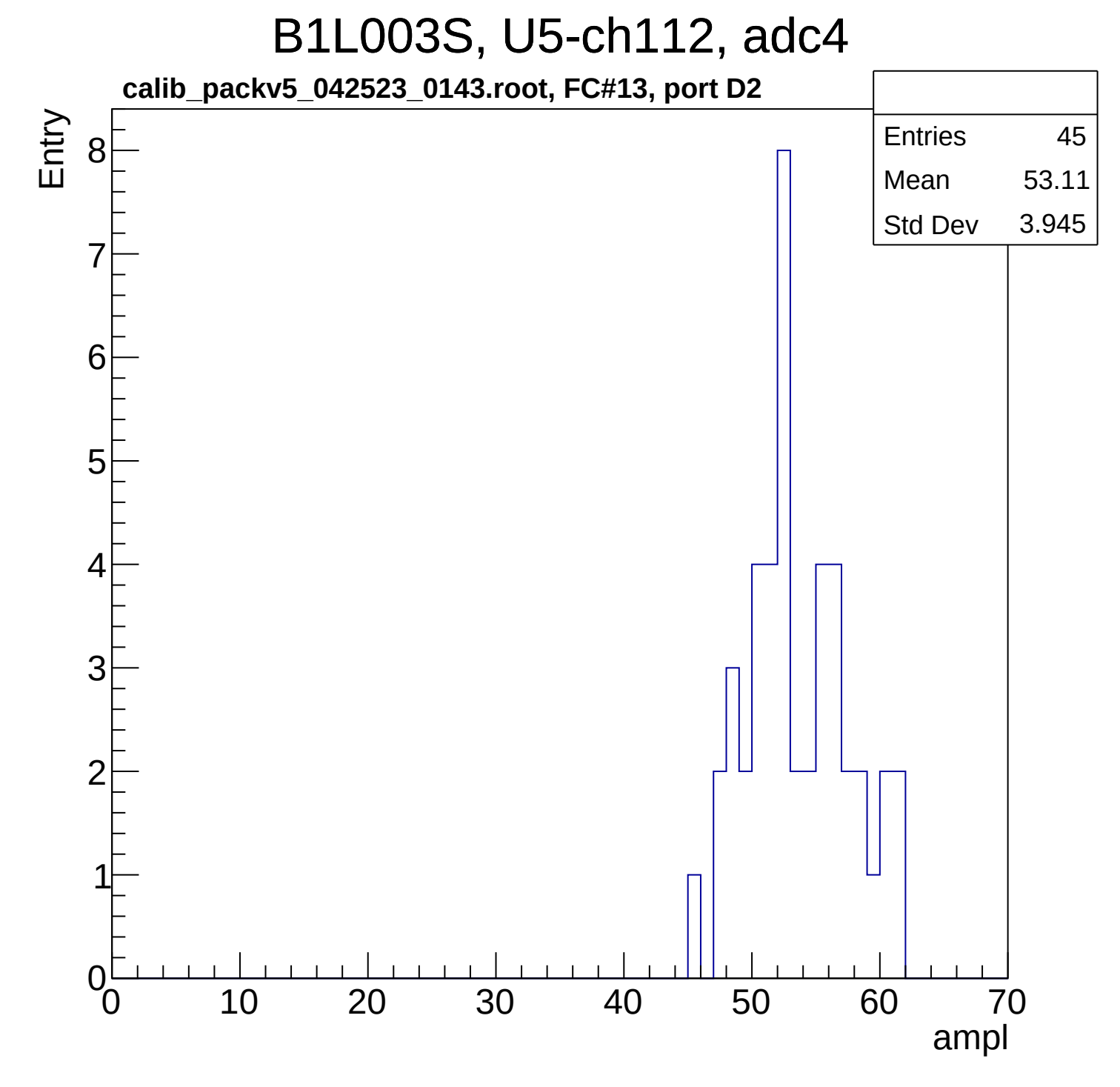
B1L003S, U5-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 53.11
Std Dev 3.945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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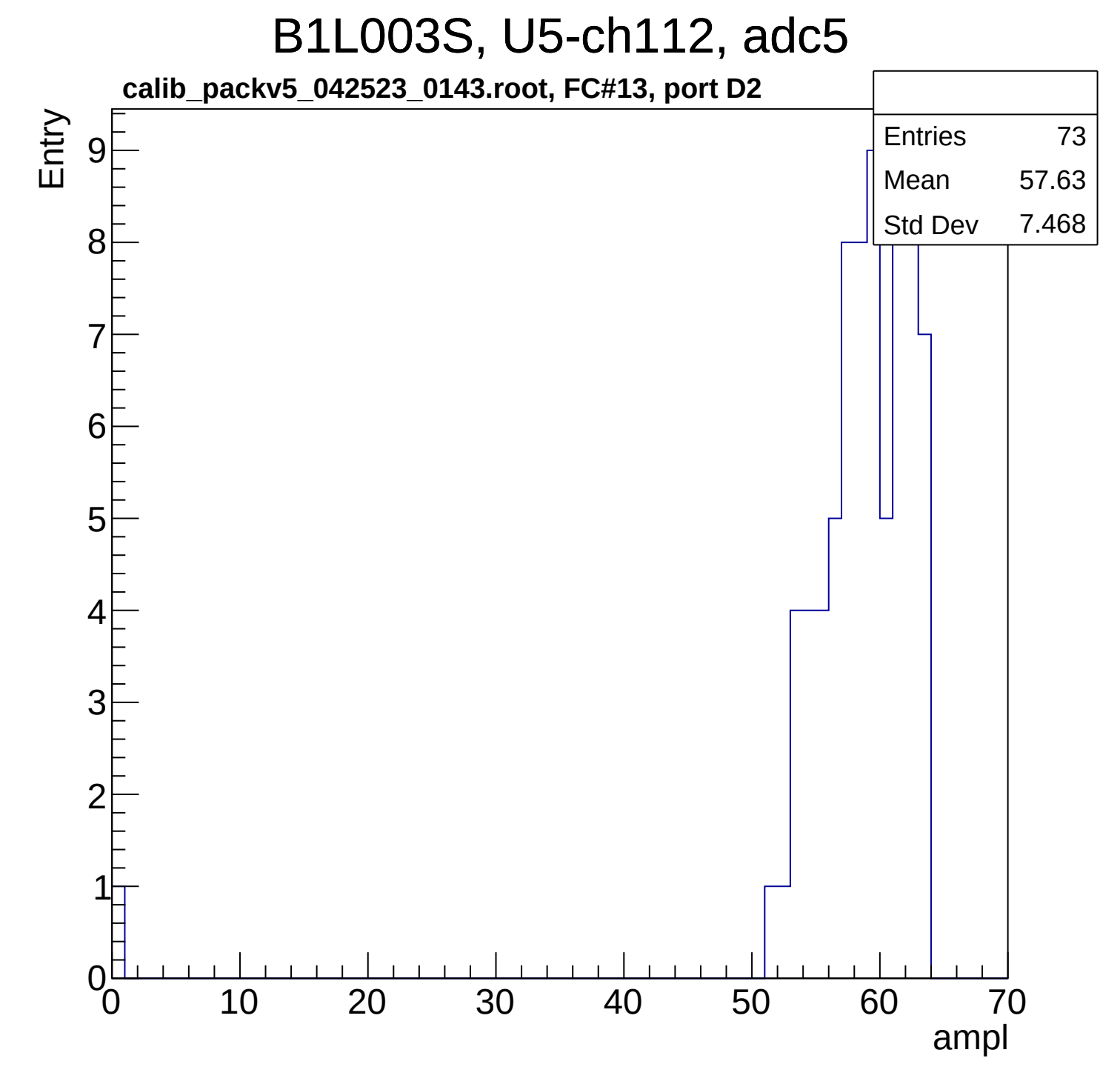
B1L003S, U5-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 57.63
Std Dev 7.468
Entry
ampl
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2
3
4
5
6
7
8
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0
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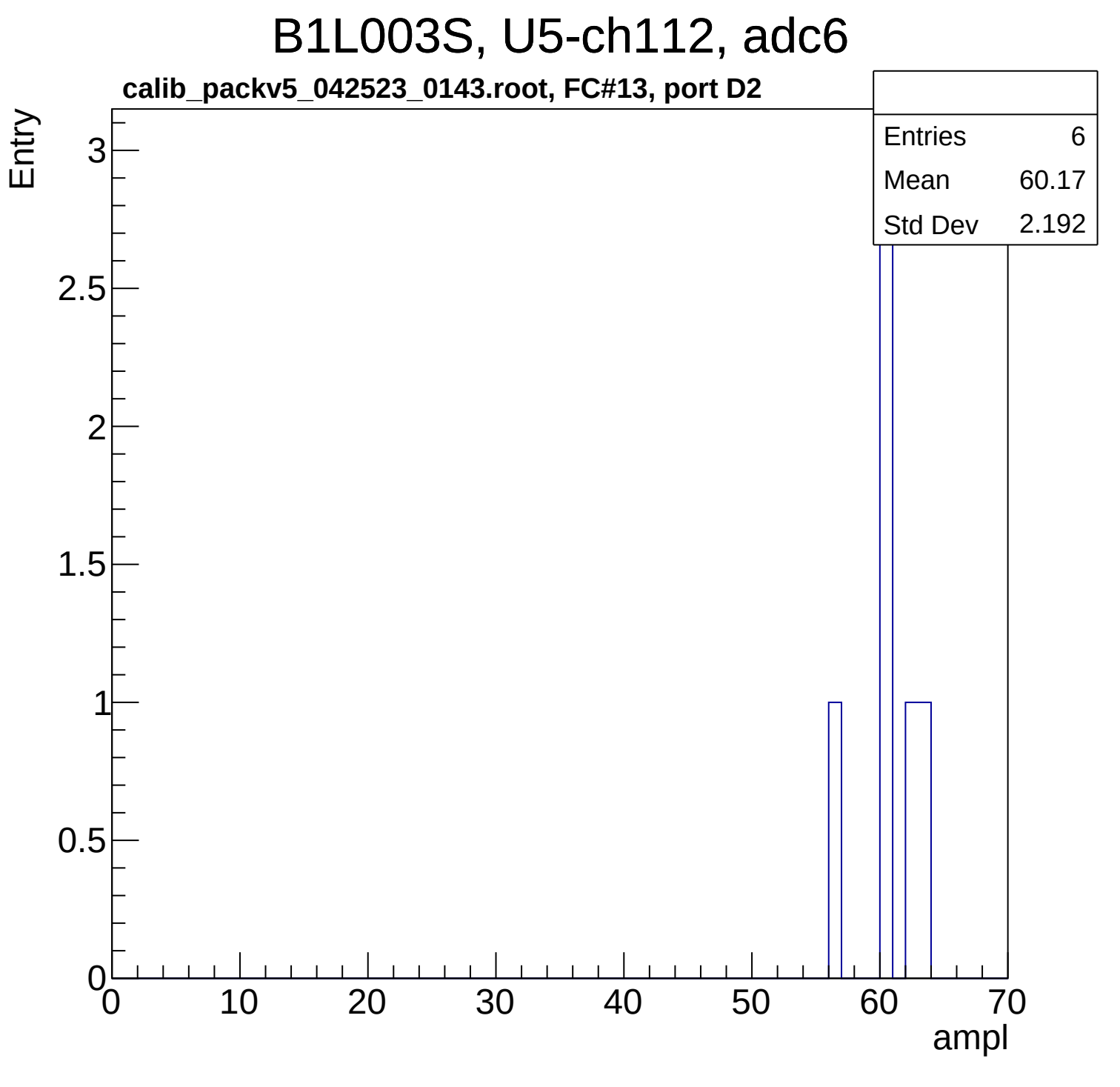
B1L003S, U5-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 60.17
Std Dev 2.192
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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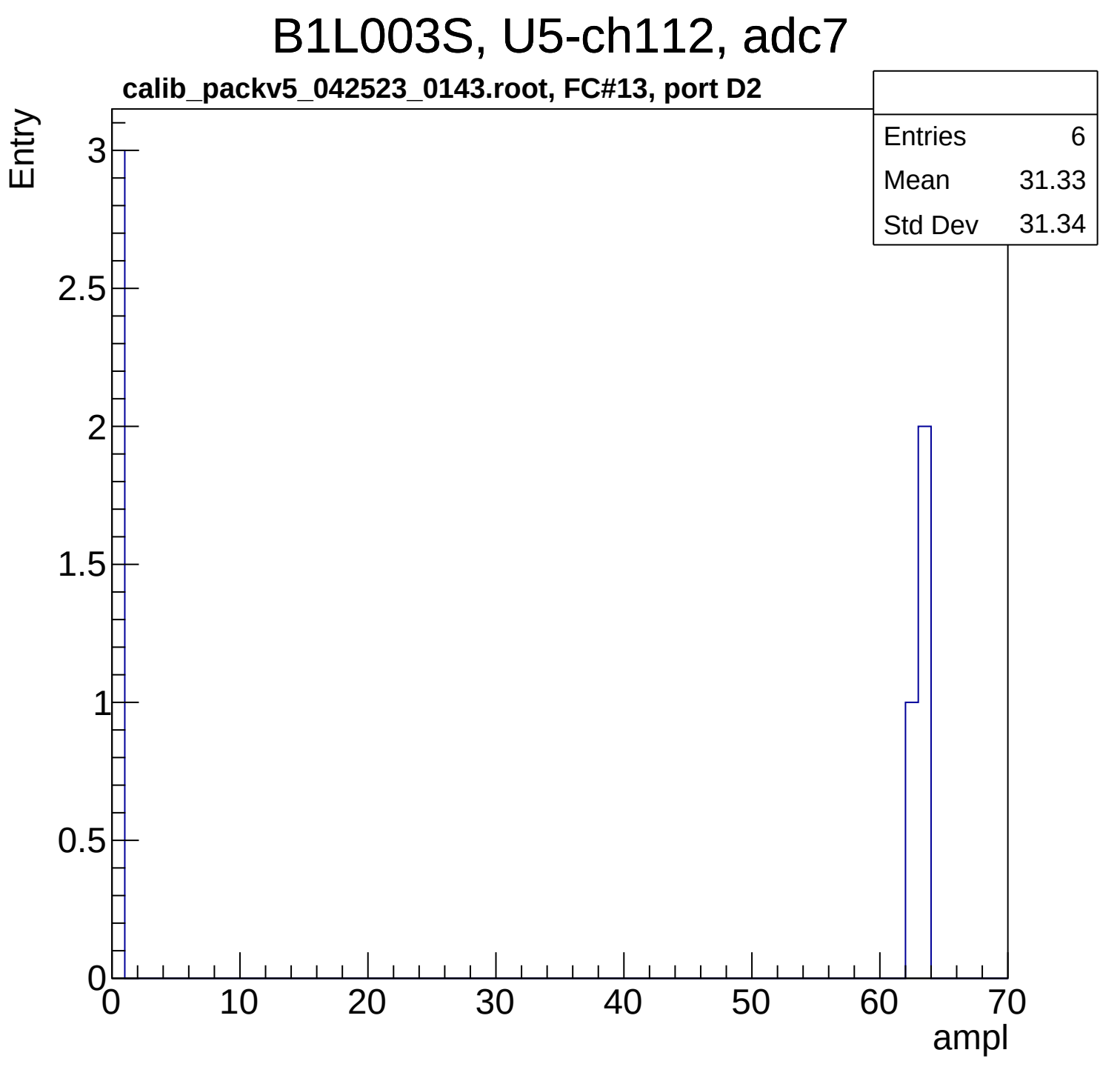
B1L003S, U5-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 31.33
Std Dev 31.34
Entry
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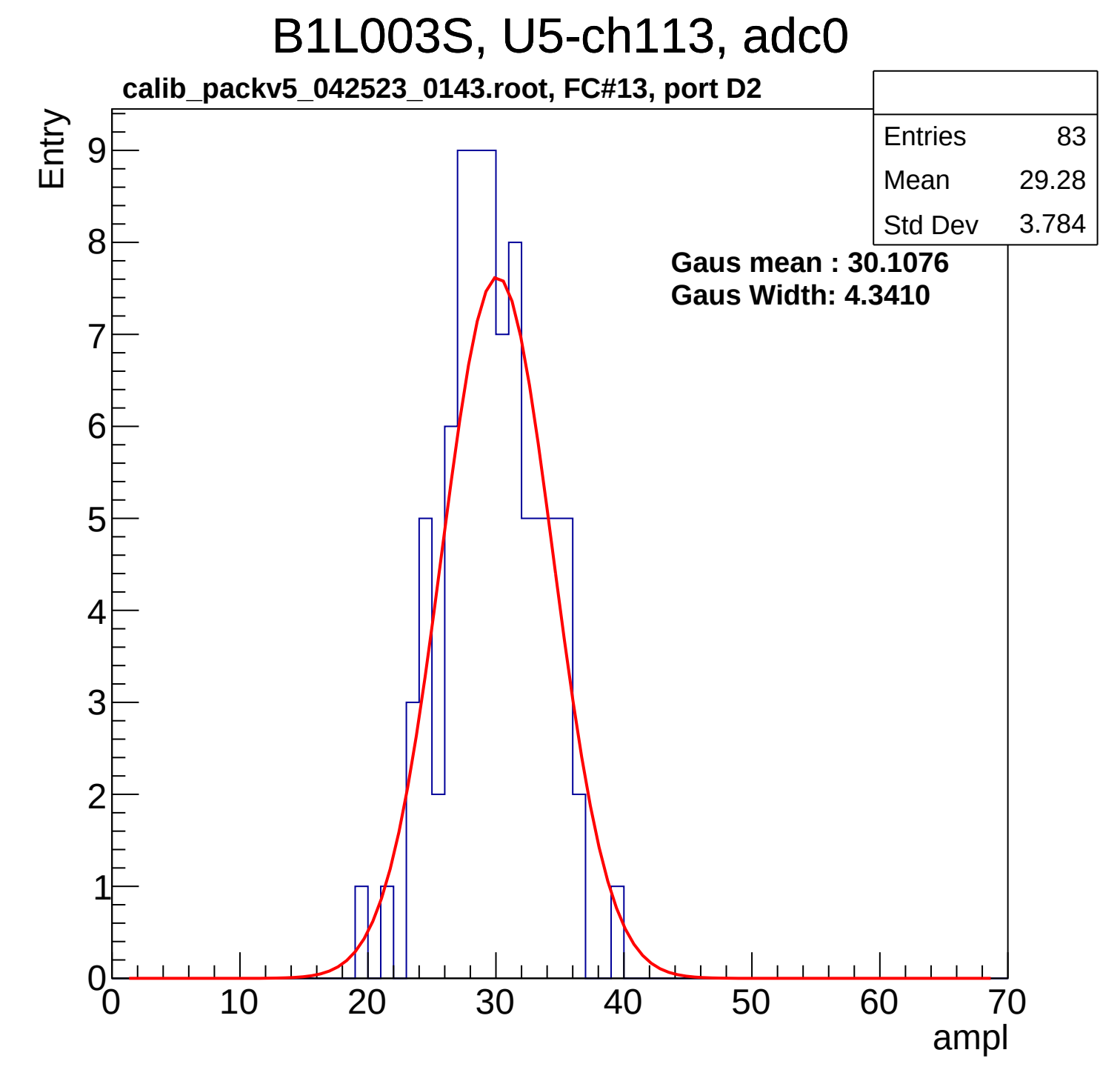
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 29.28
Std Dev 3.784
Gaus mean : 30.1076
Gaus Width: 4.3410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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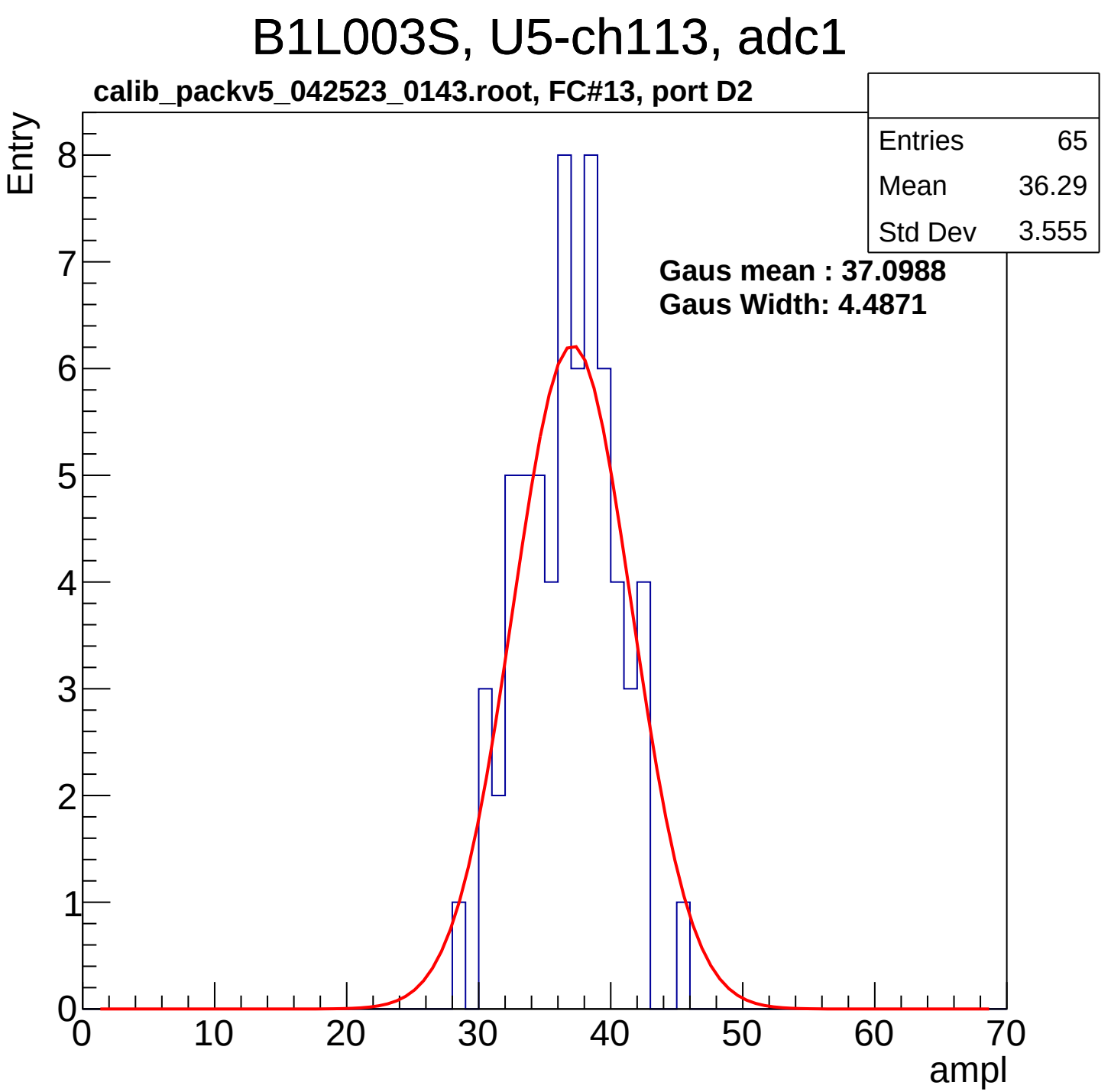
B1L003S, U5-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 36.29
Std Dev 3.555
Gaus mean : 37.0988
Gaus Width: 4.4871
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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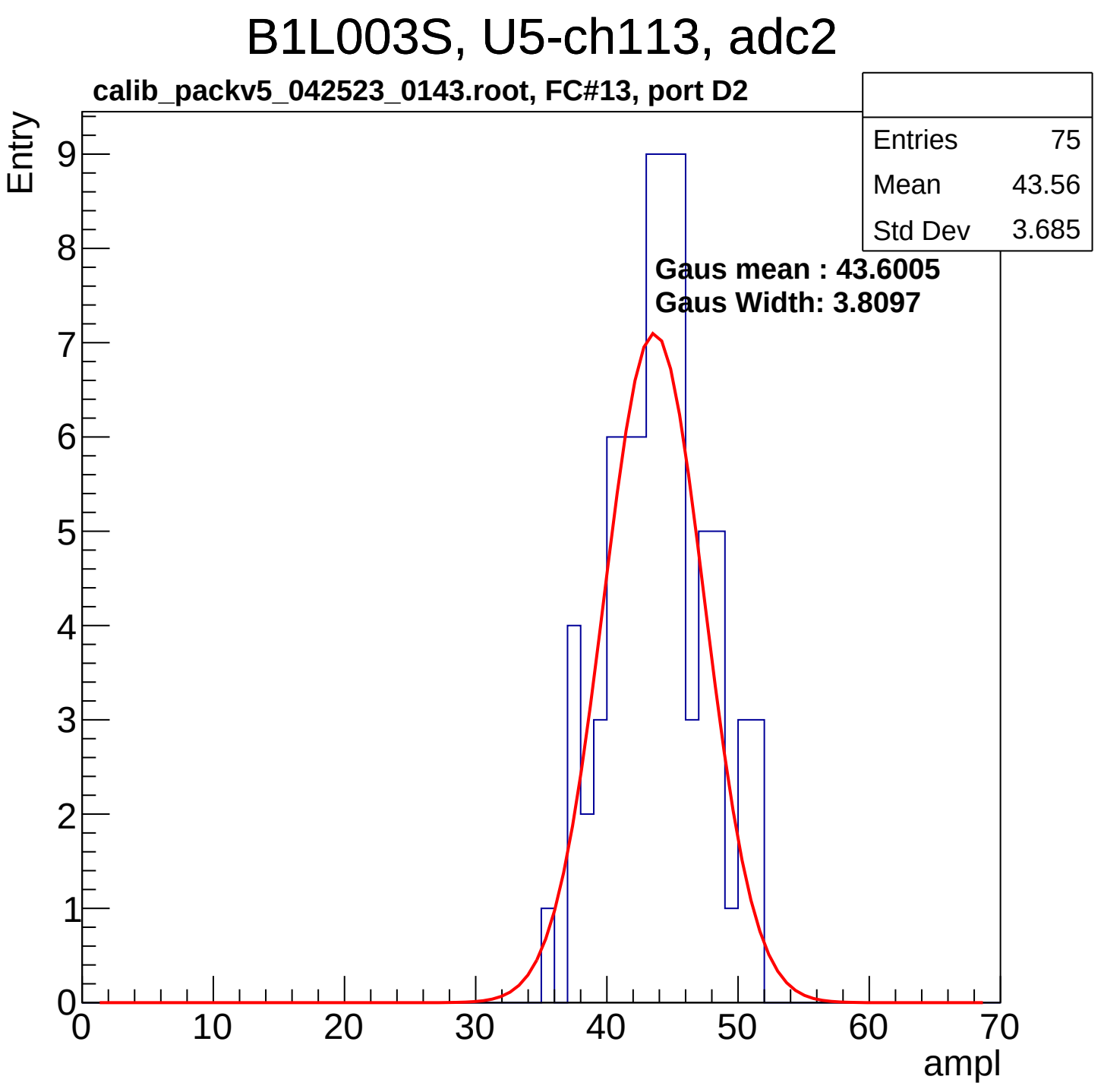
B1L003S, U5-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 43.56
Std Dev 3.685
Gaus mean : 43.6005
Gaus Width: 3.8097
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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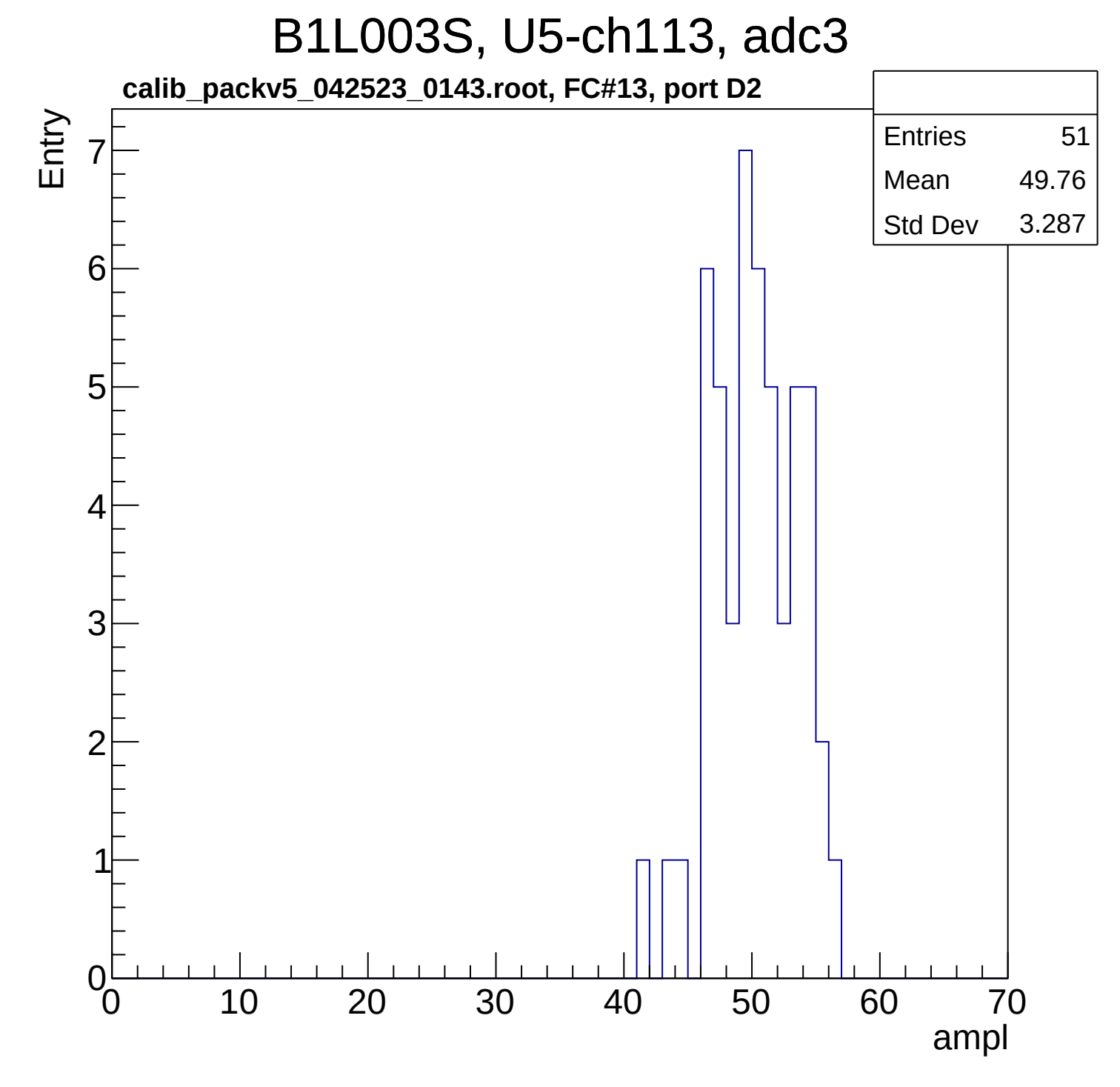
B1L003S, U5-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 49.76
Std Dev 3.287
Entry
ampl
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1
2
3
4
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6
7
0
10
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60
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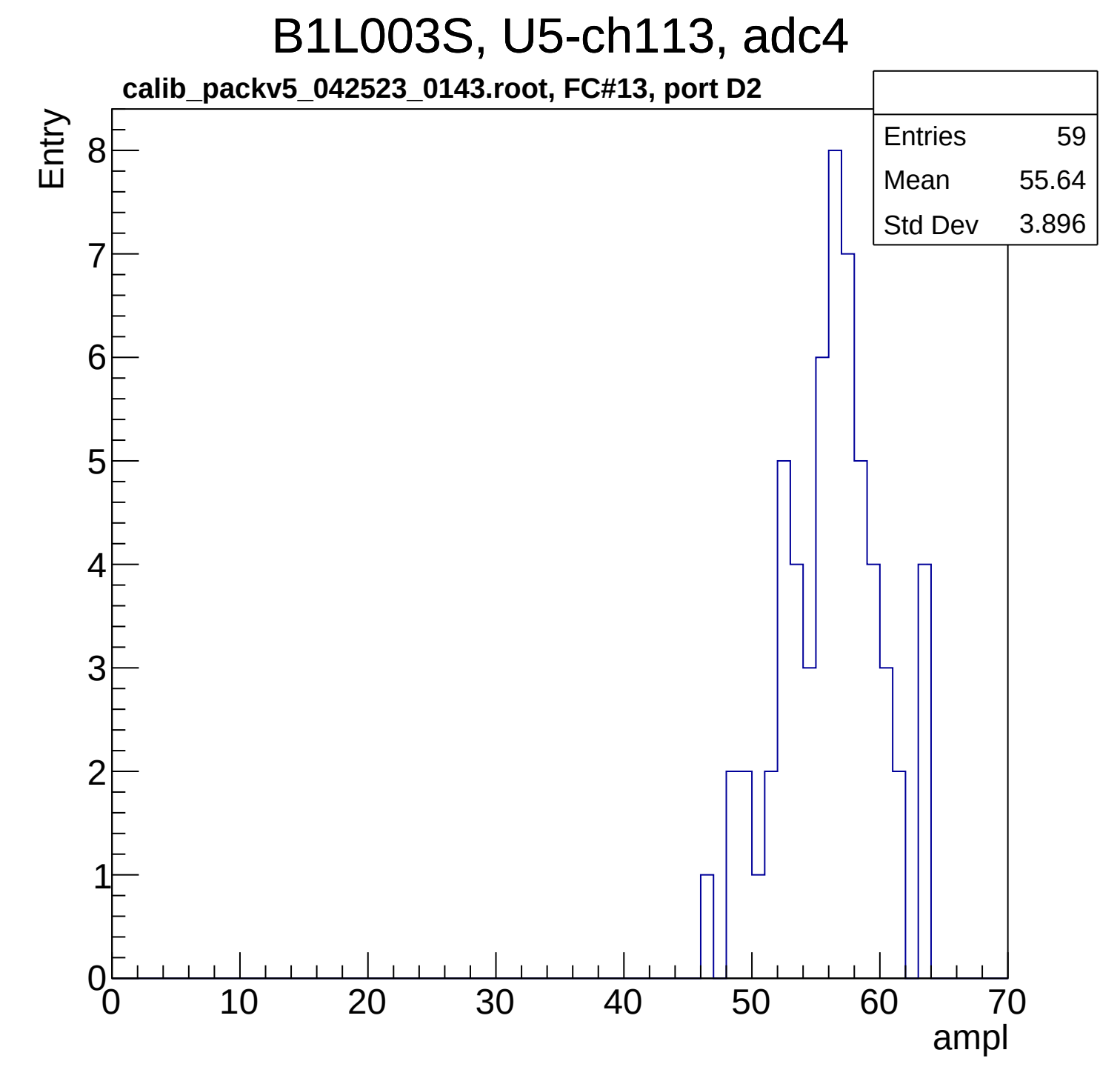
B1L003S, U5-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.64
Std Dev 3.896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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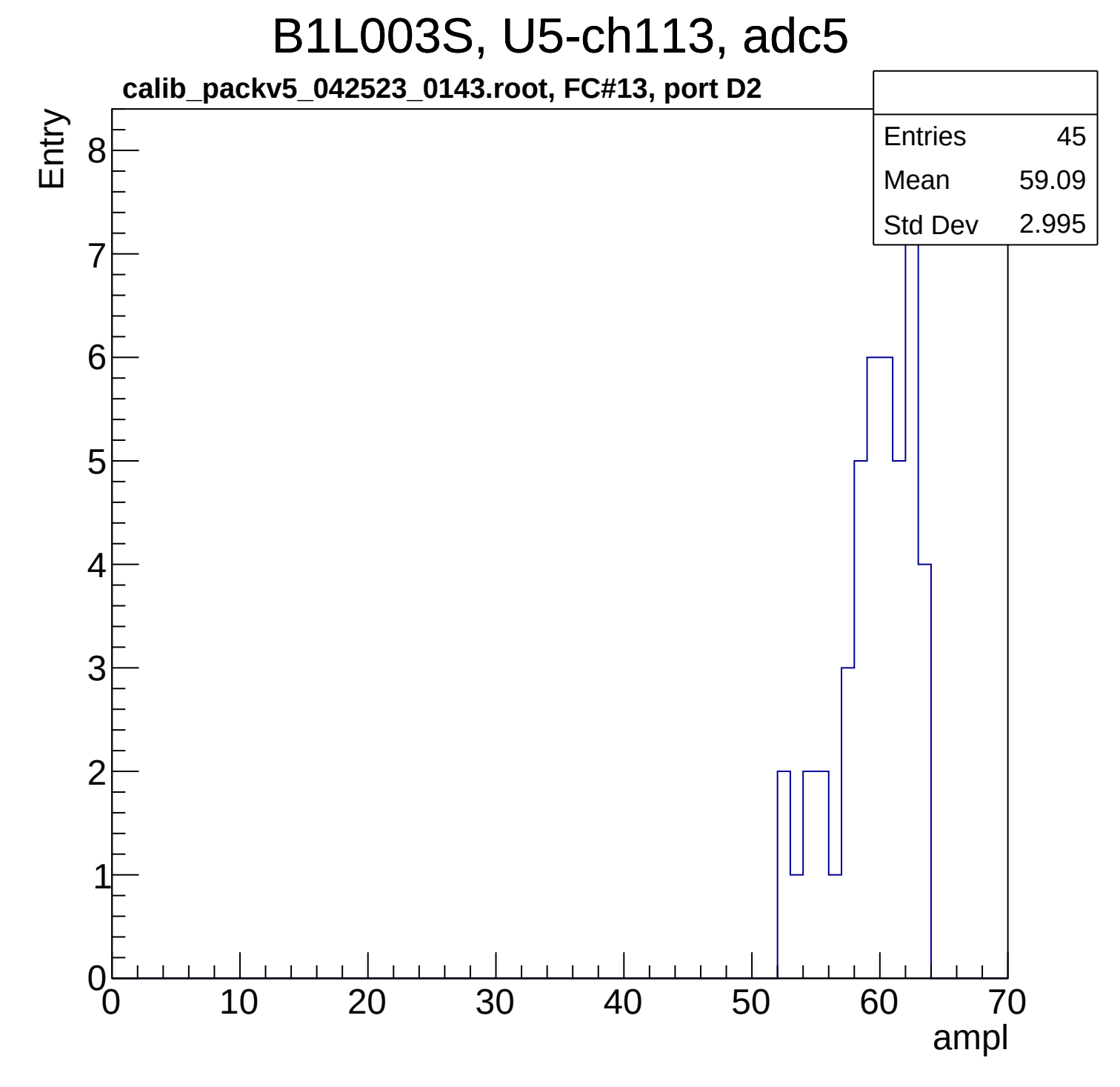
B1L003S, U5-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.09
Std Dev 2.995
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
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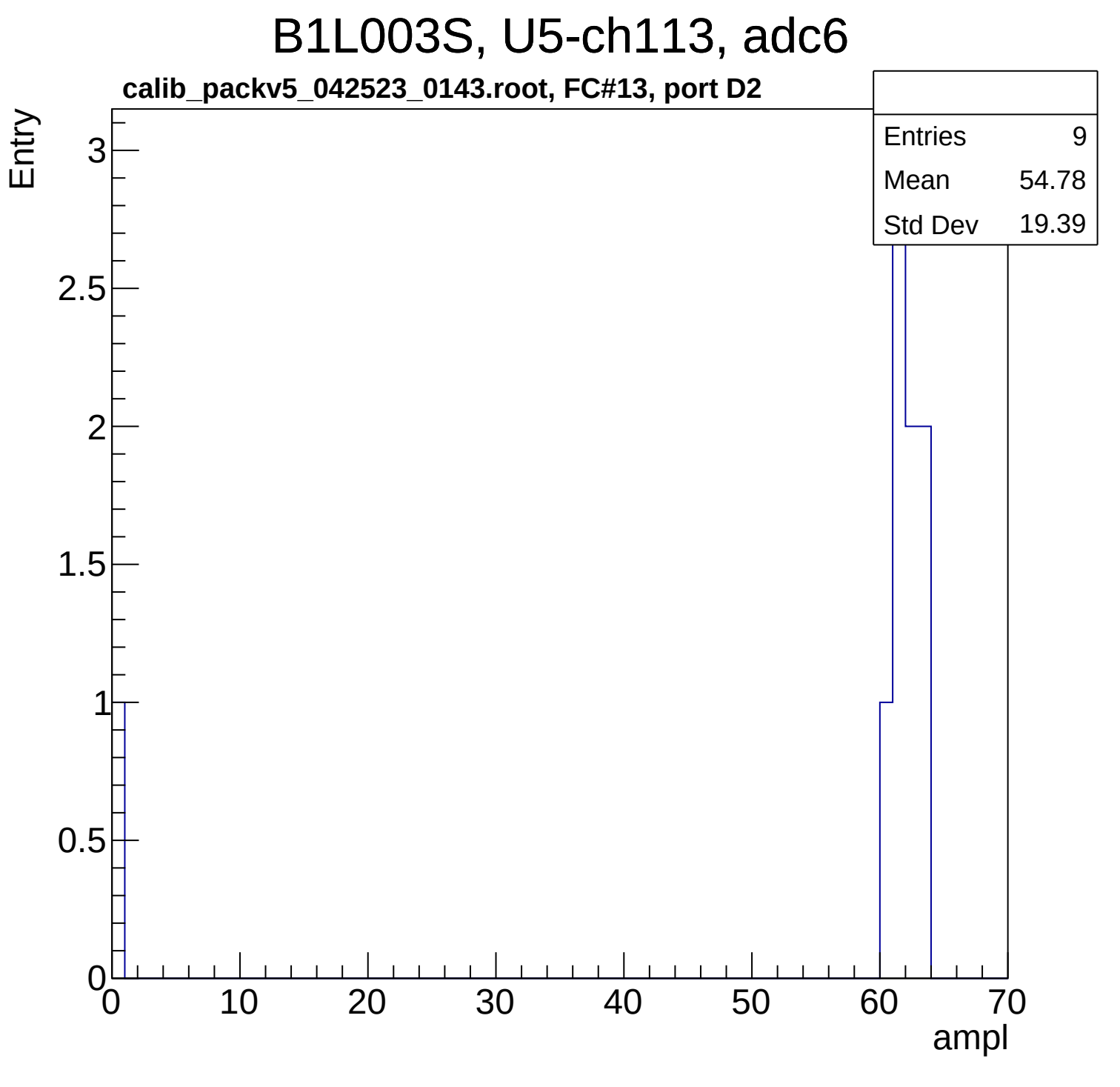
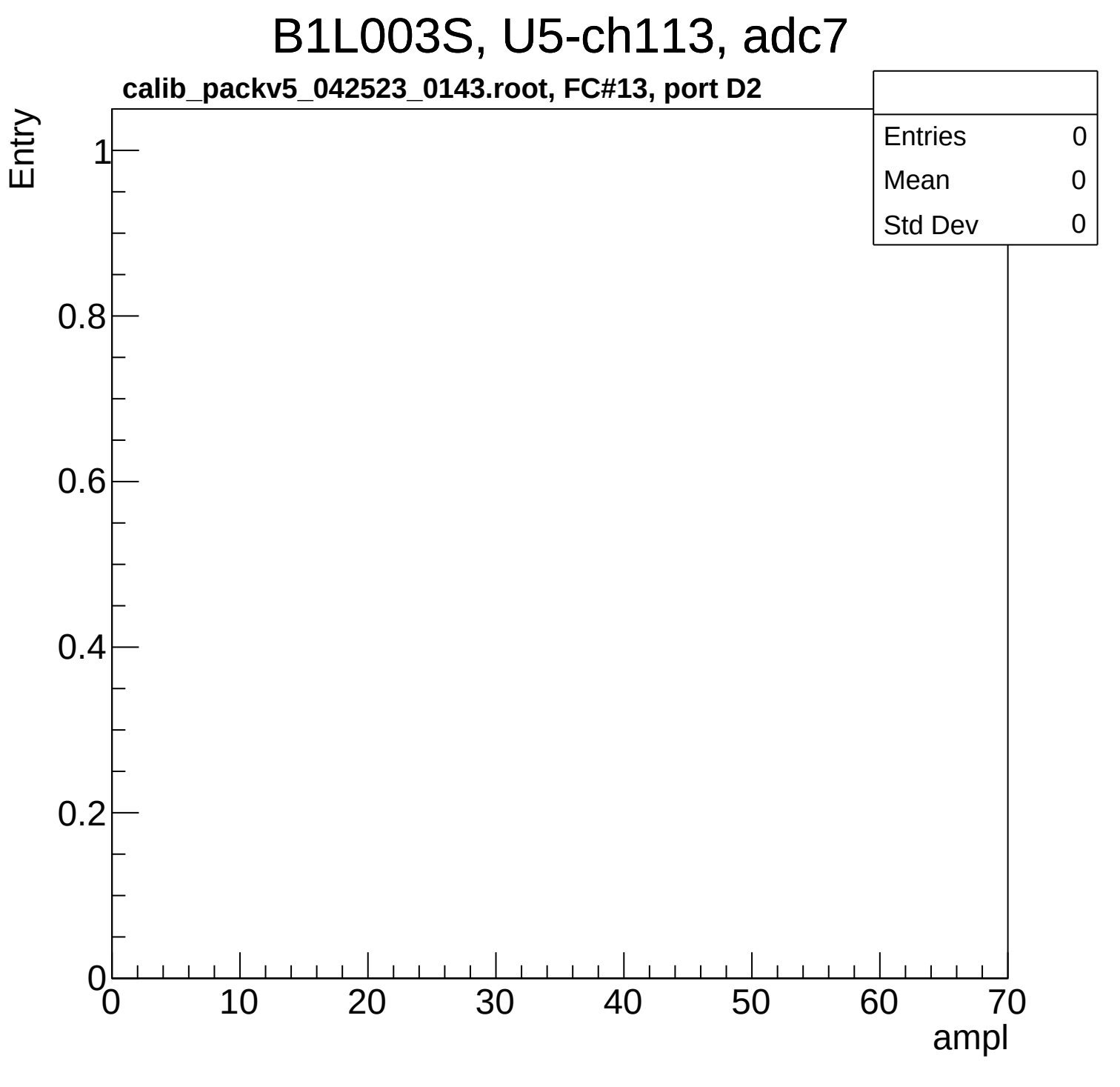
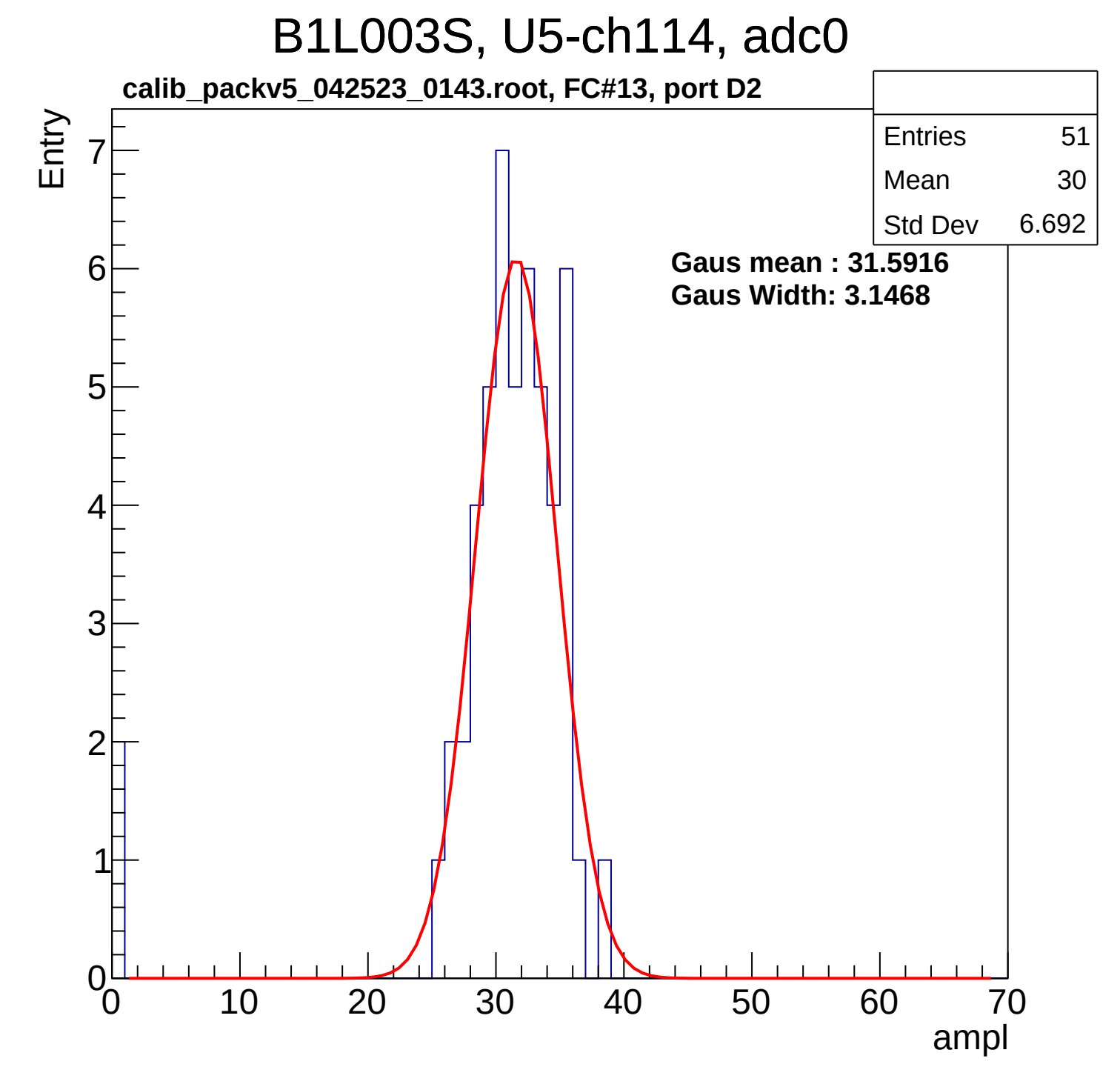
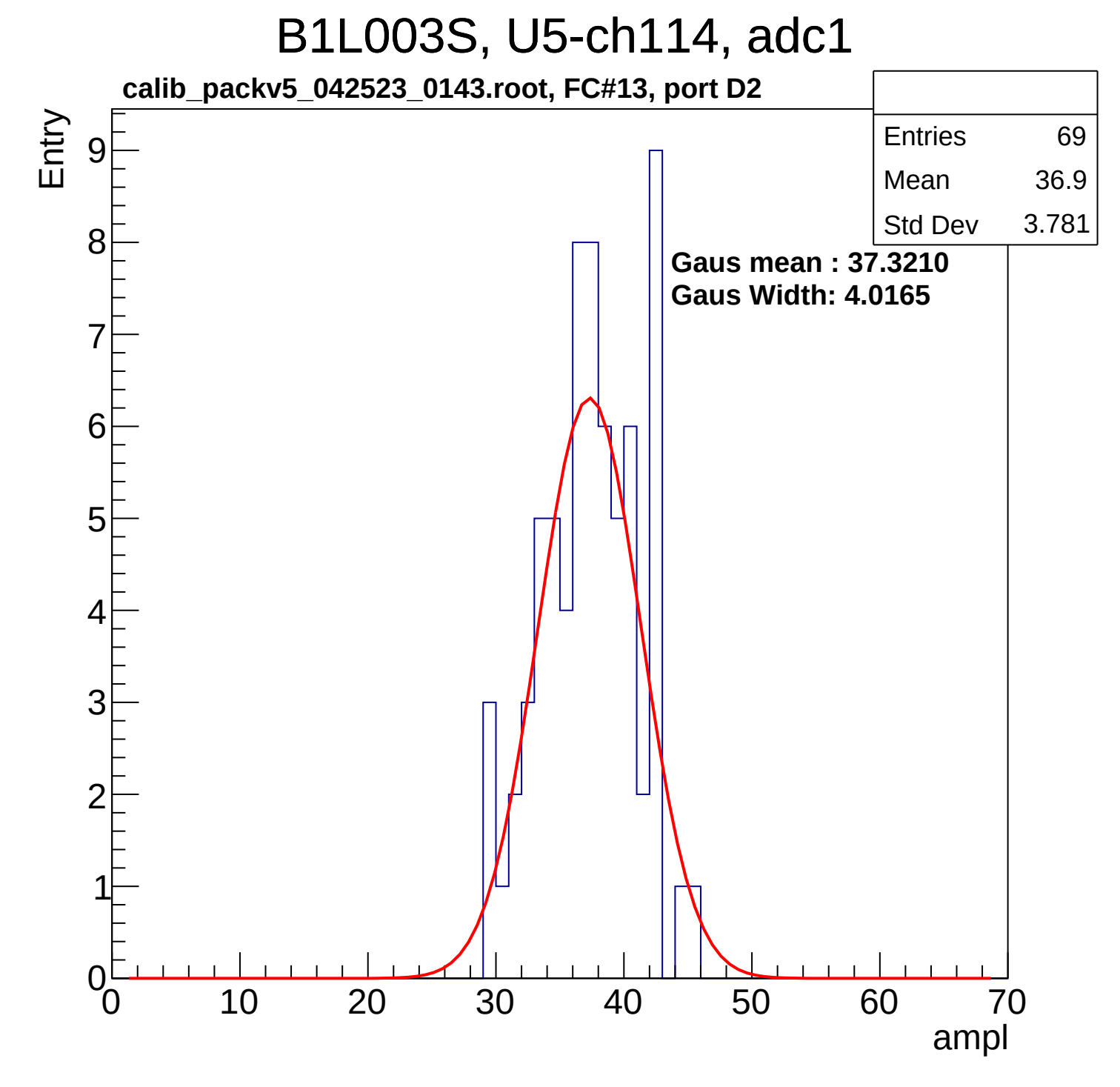

B1L003S, U5-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.78
Std Dev 19.39
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 30
Std Dev 6.692
Gaus mean : 31.5916
Gaus Width: 3.1468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.9
Std Dev 3.781
Gaus mean : 37.3210
Gaus Width: 4.0165
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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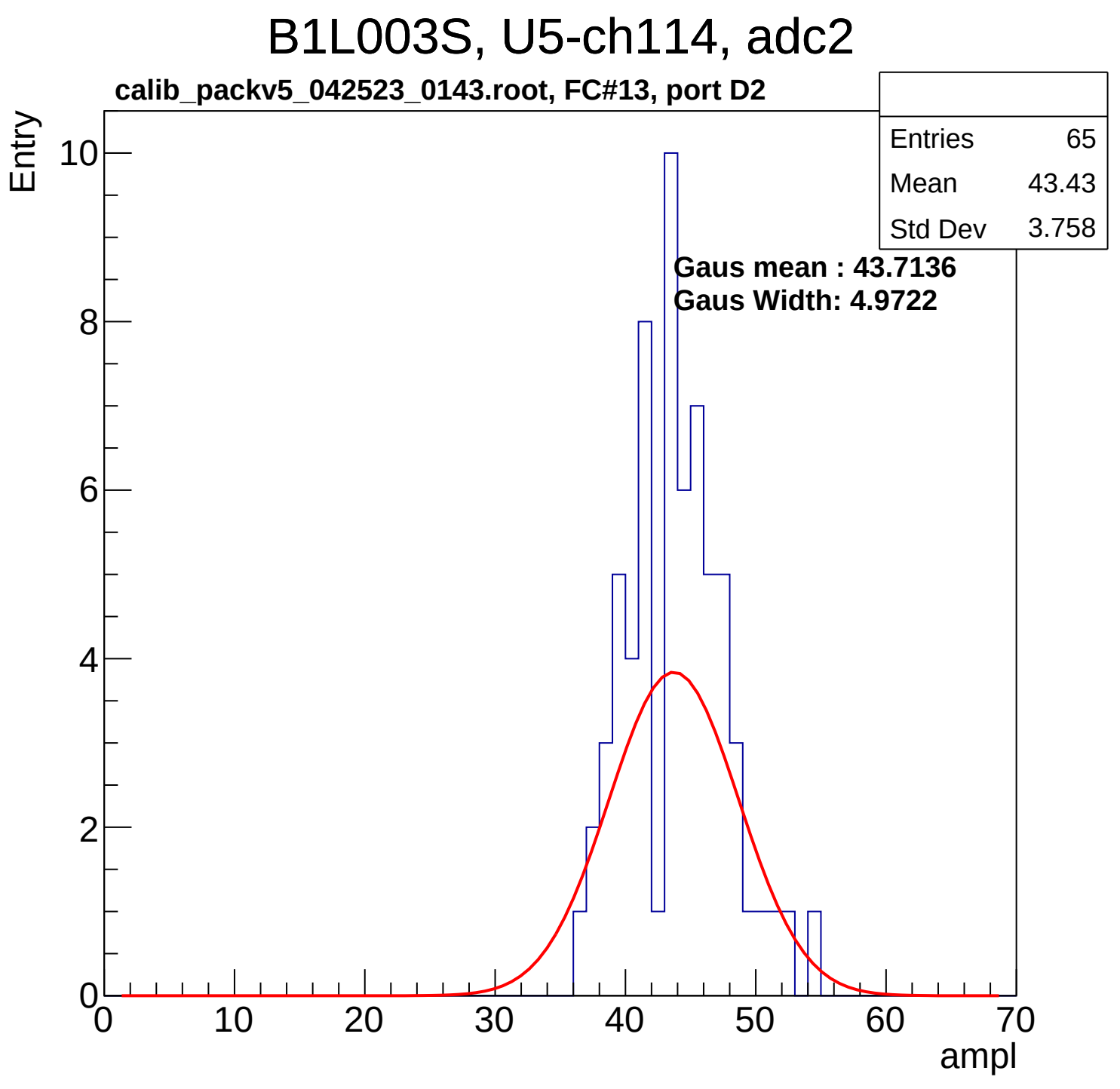
B1L003S, U5-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.43
Std Dev 3.758
Gaus mean : 43.7136
Gaus Width: 4.9722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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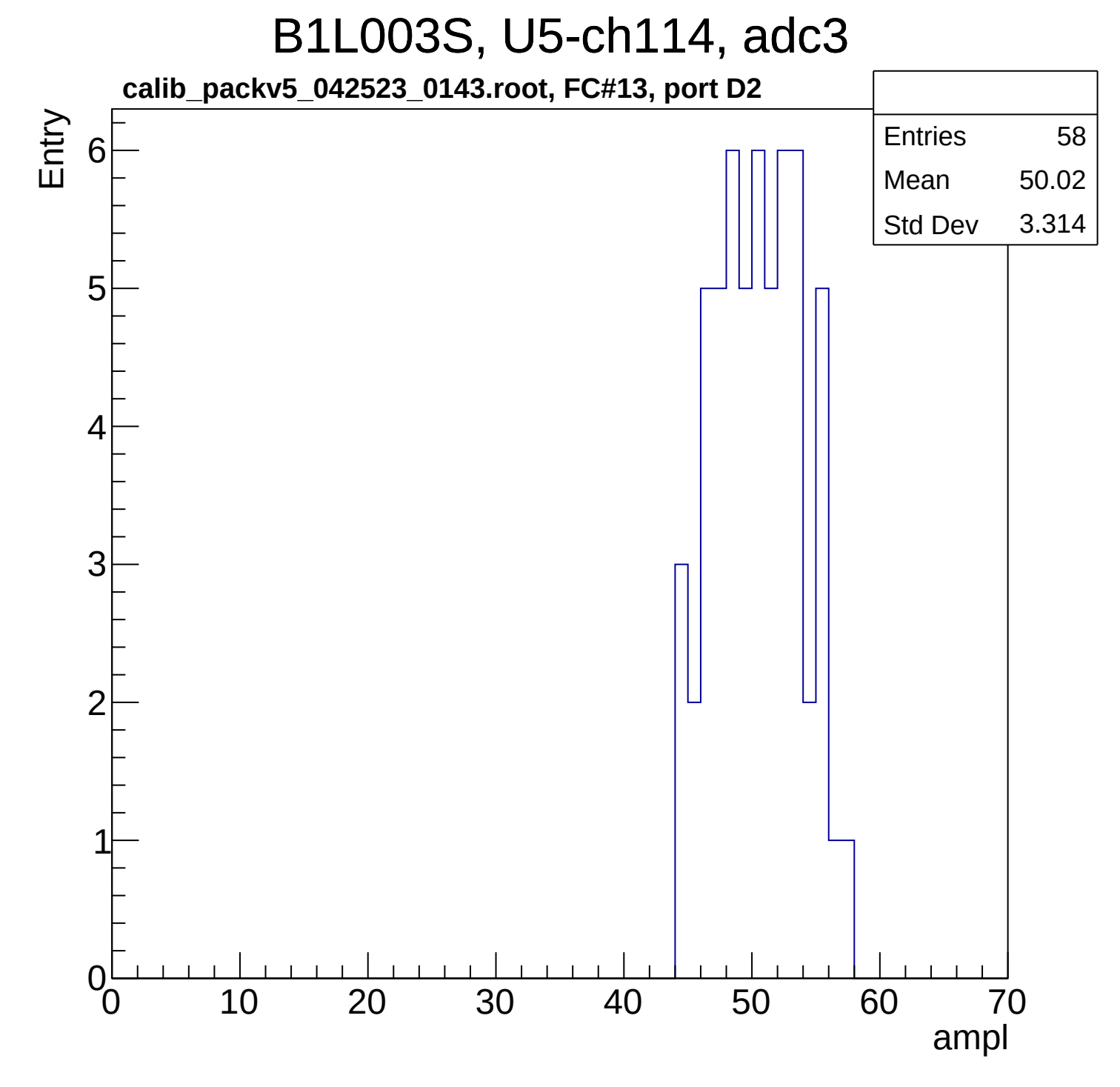
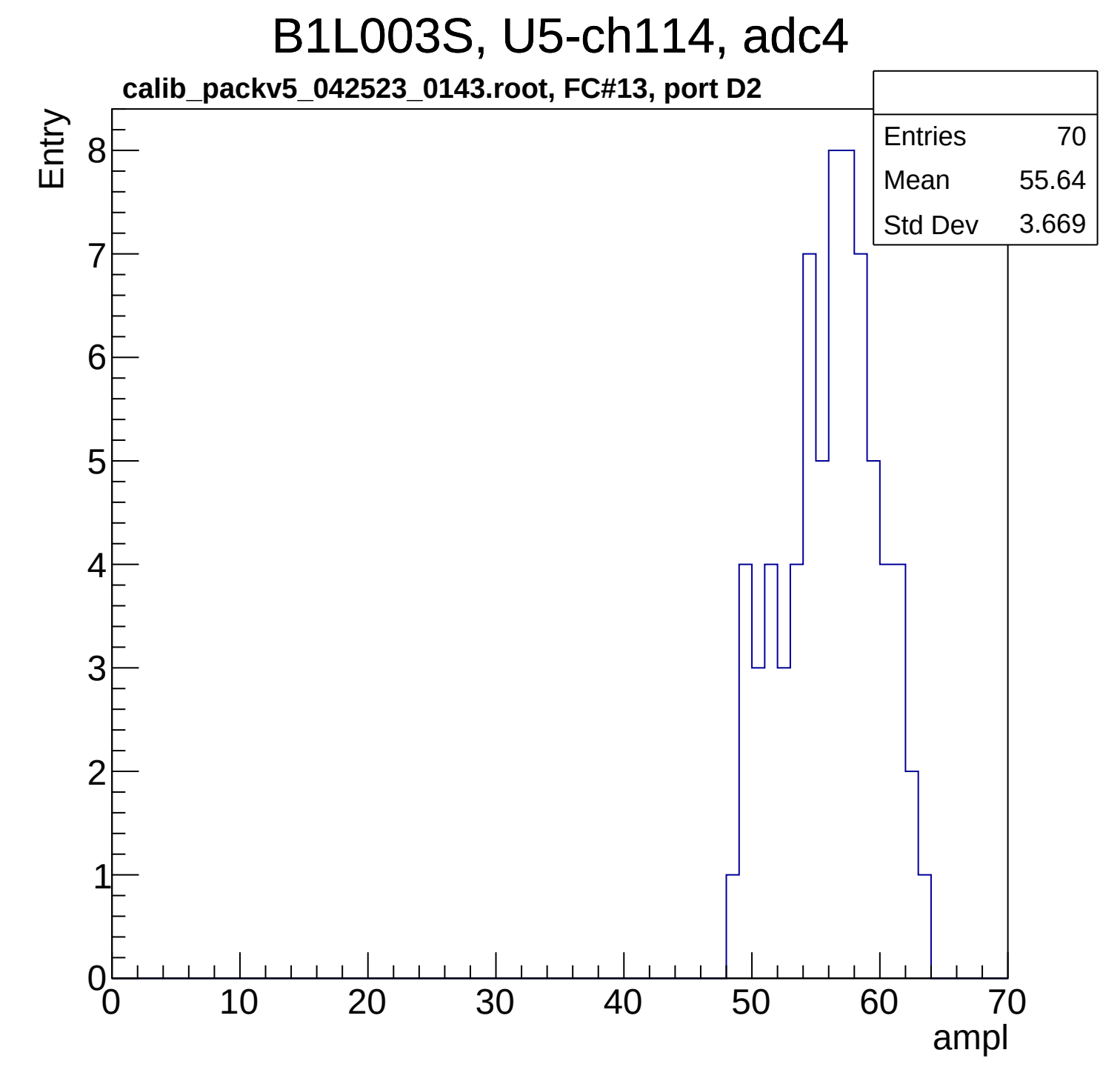

B1L003S, U5-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 50.02
Std Dev 3.314
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 55.64
Std Dev 3.669
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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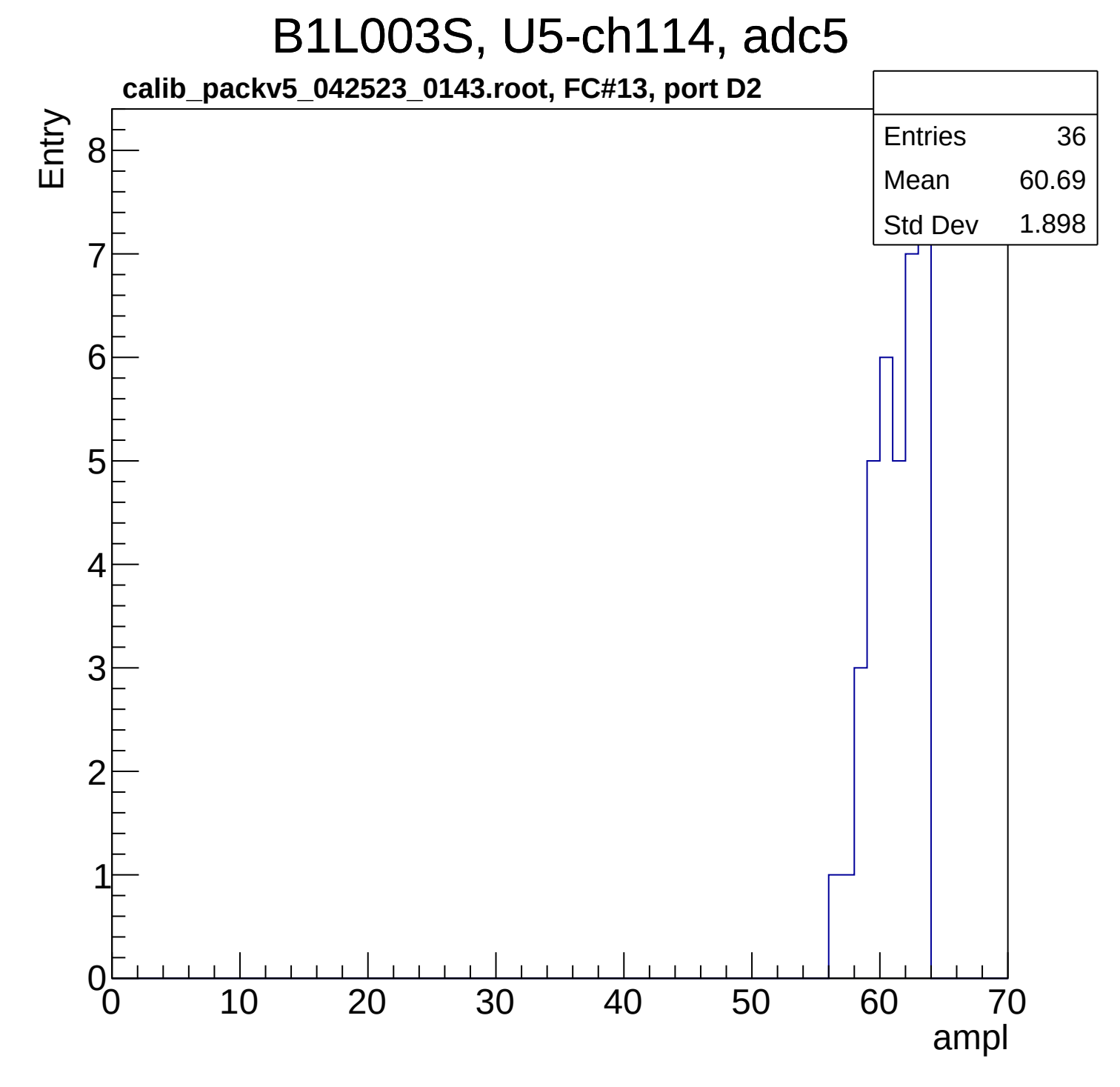
B1L003S, U5-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 60.69
Std Dev 1.898
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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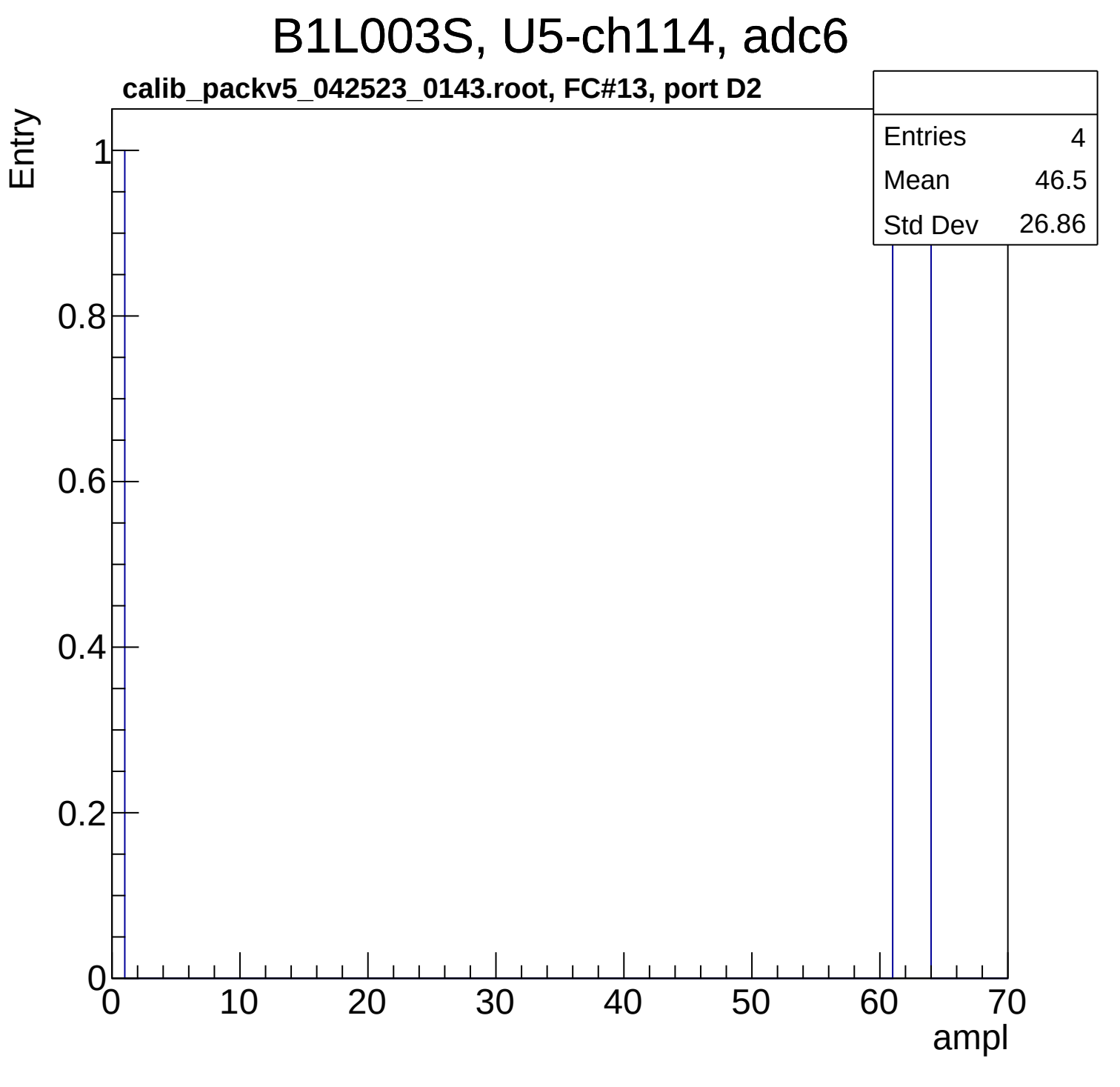
B1L003S, U5-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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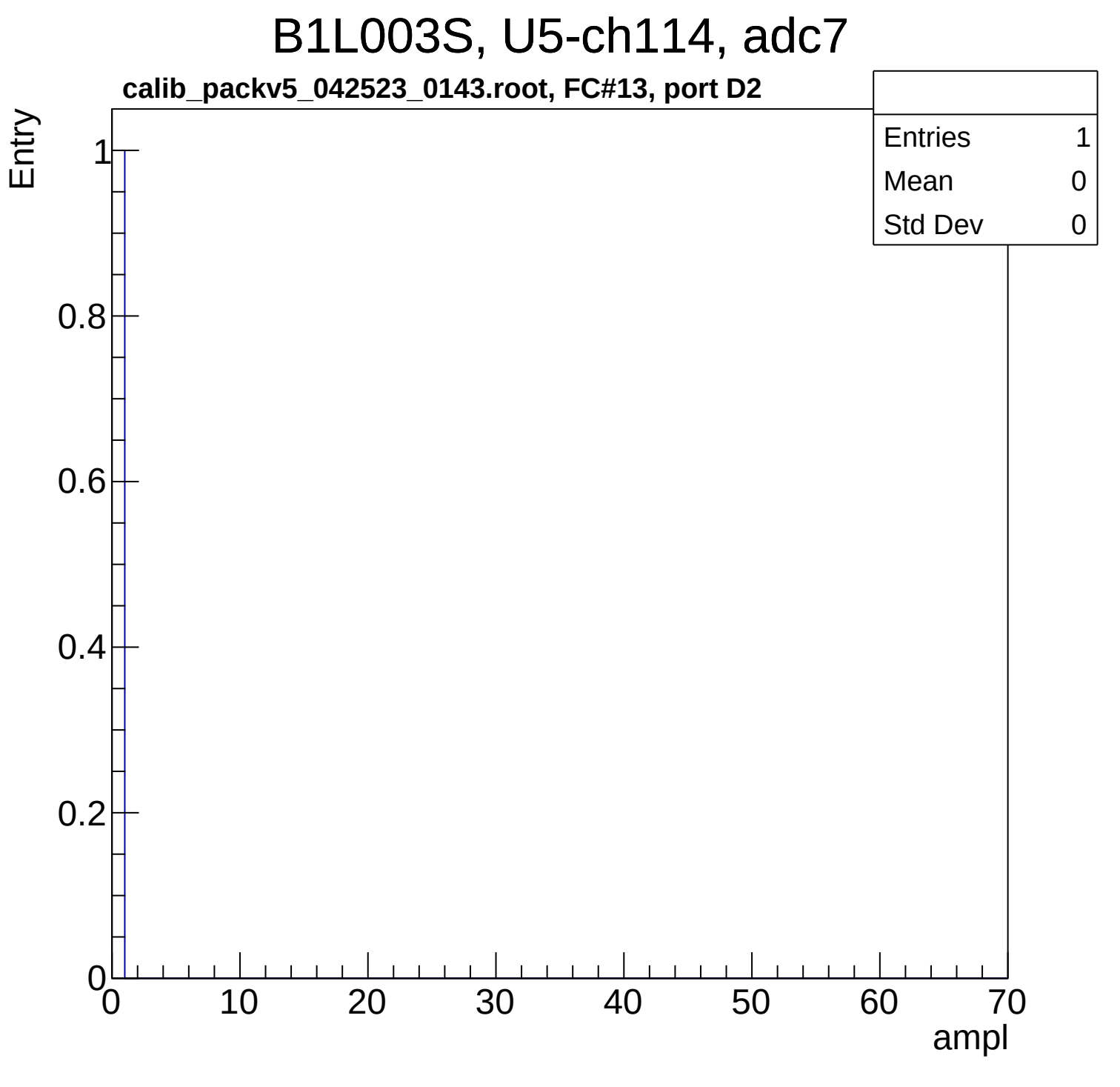
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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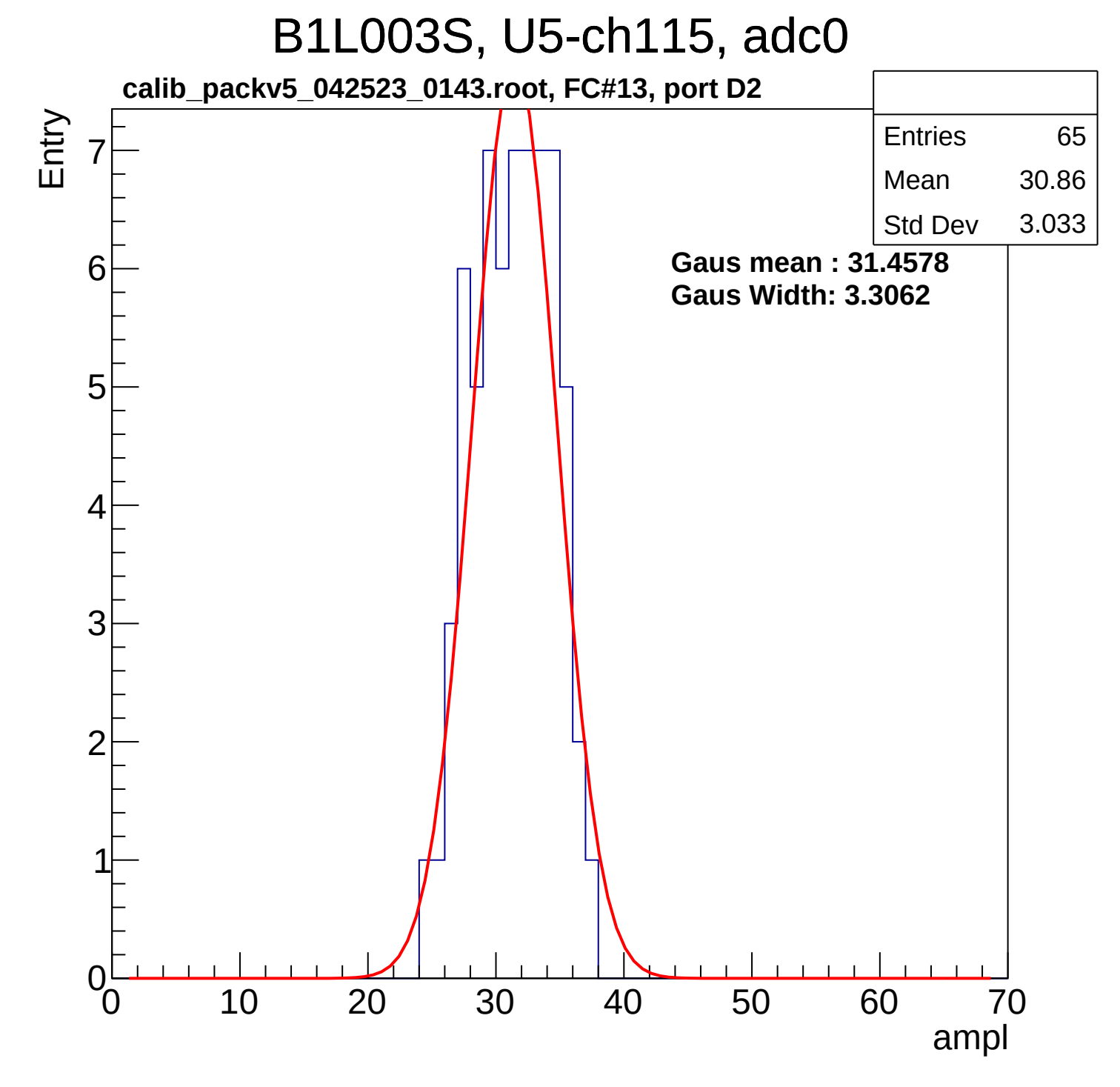
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.86
Std Dev 3.033
Gaus mean : 31.4578
Gaus Width: 3.3062
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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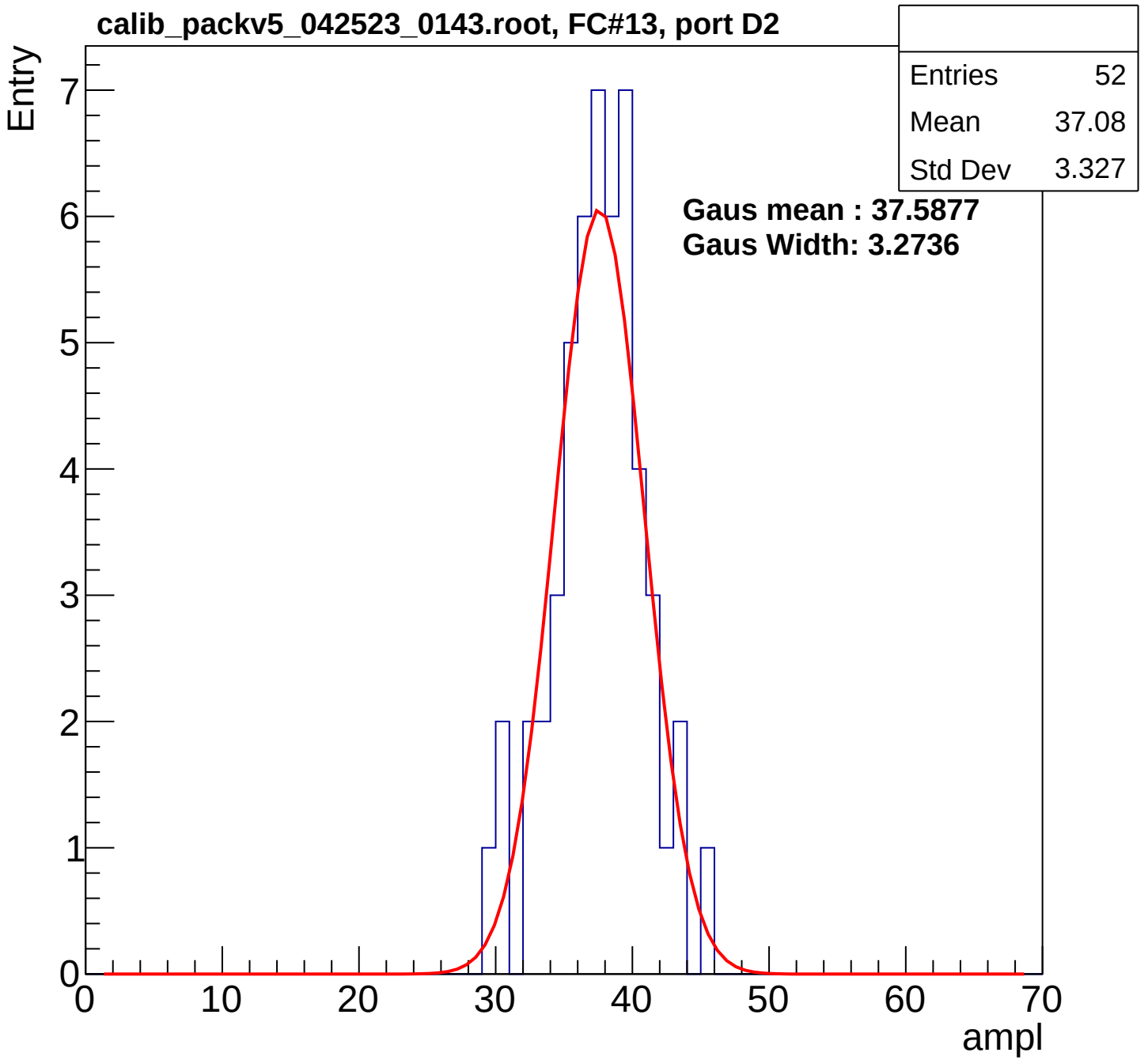
B1L003S, U5-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 37.08
Std Dev 3.327
Gaus mean : 37.5877
Gaus Width: 3.2736
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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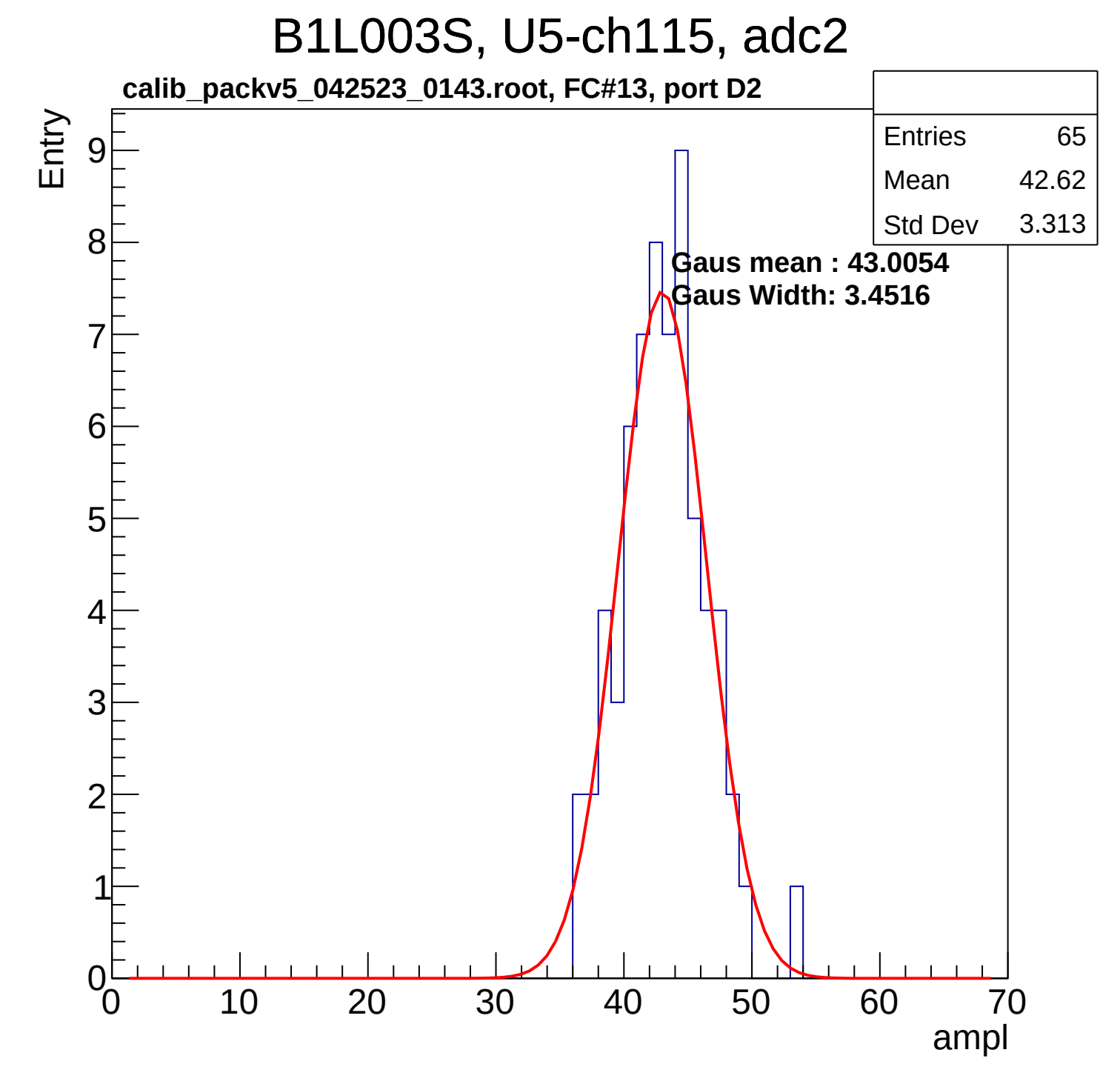
B1L003S, U5-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.62
Std Dev 3.313
Gaus mean : 43.0054
Gaus Width: 3.4516
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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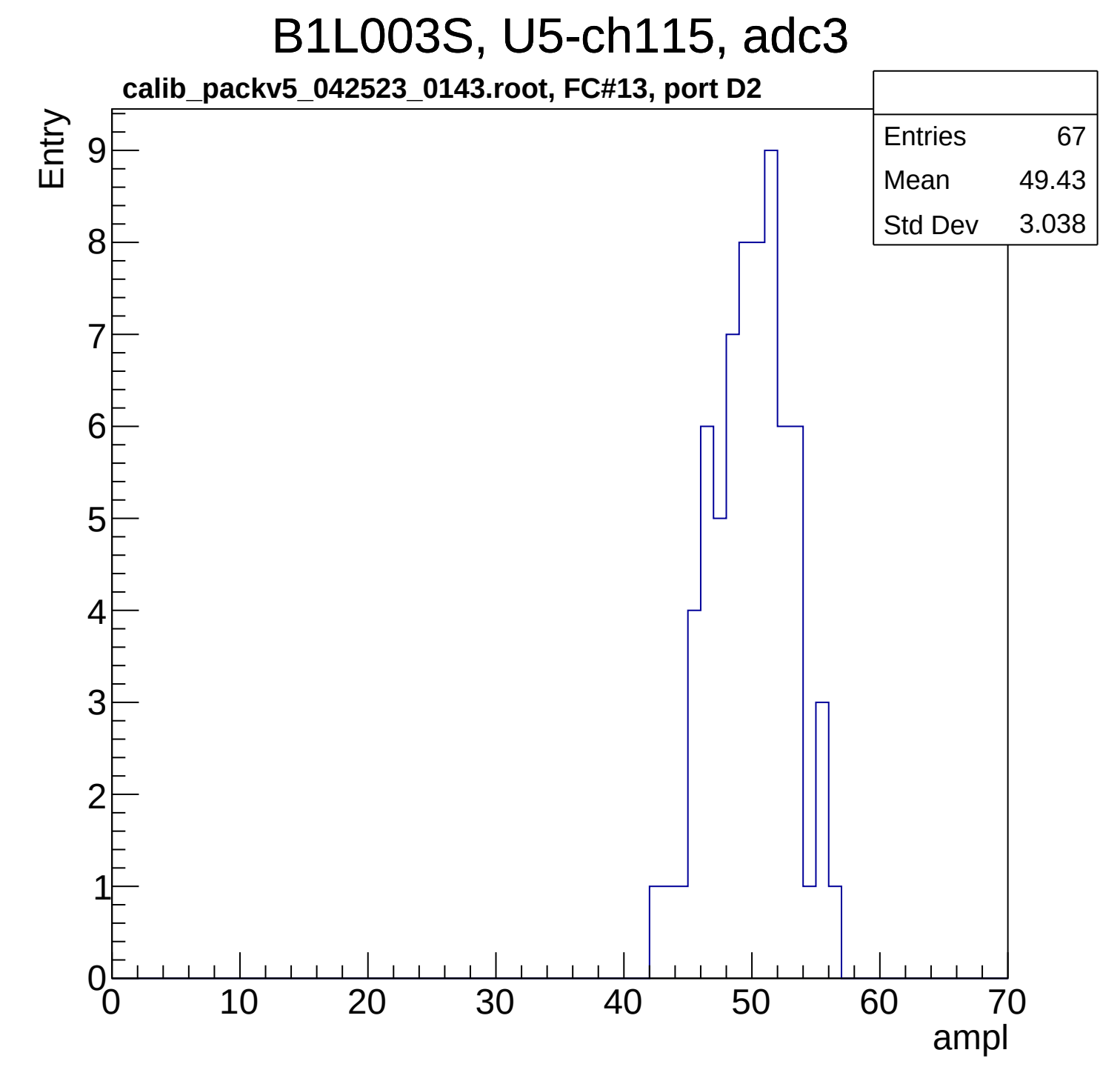
B1L003S, U5-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.43
Std Dev 3.038
Entry
ampl
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2
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4
5
6
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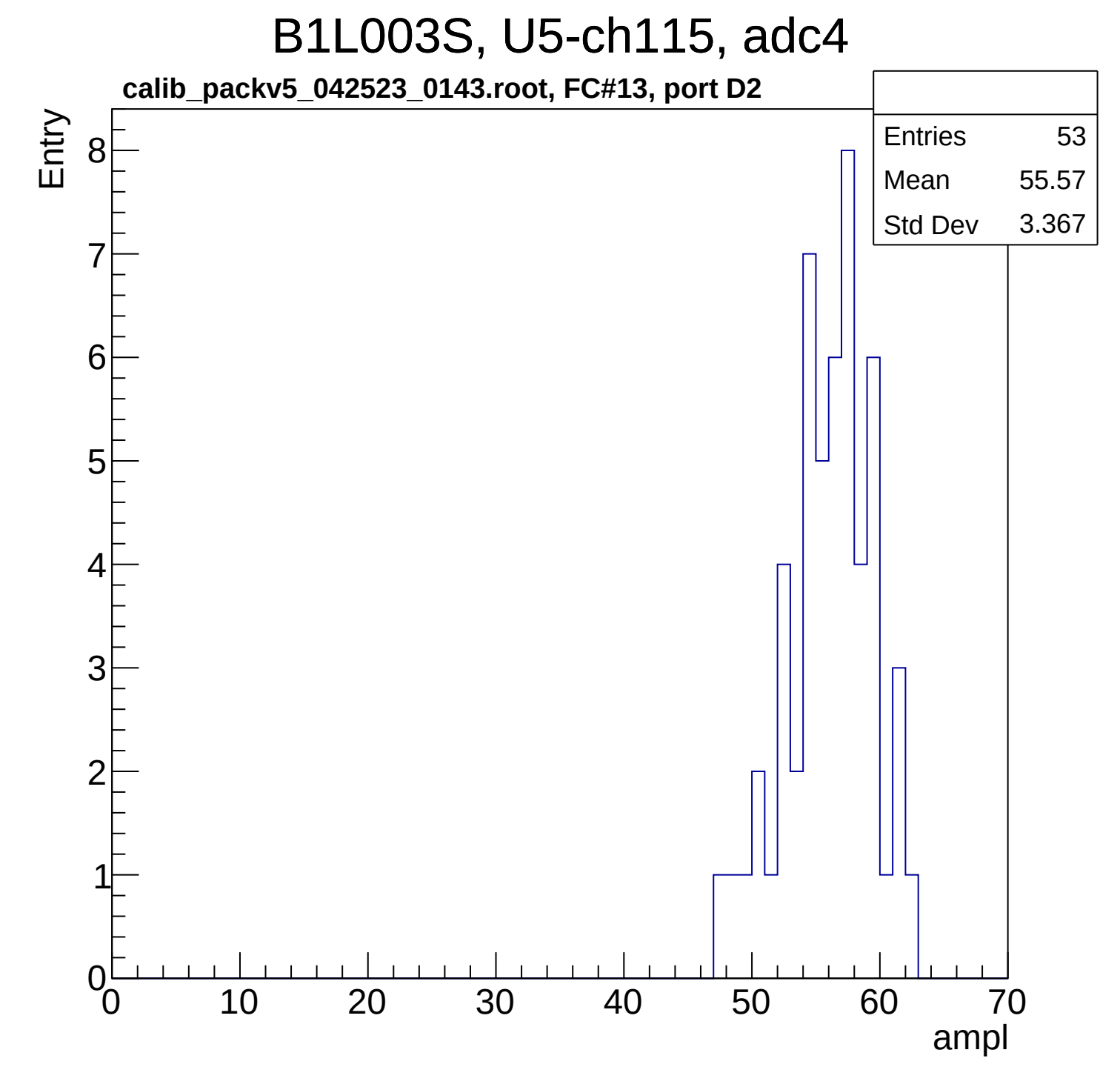
B1L003S, U5-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.57
Std Dev 3.367
Entry
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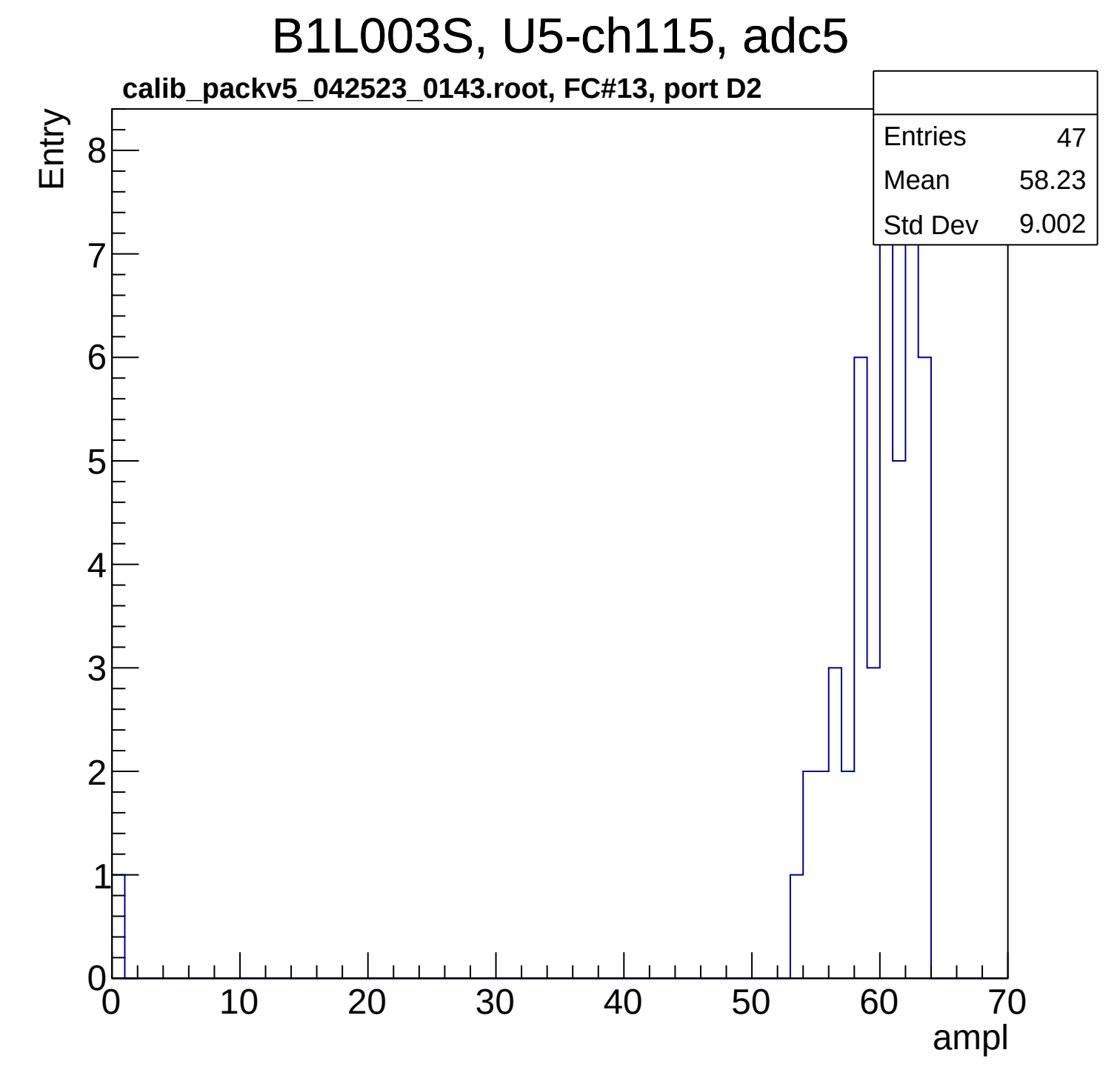
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.23
Std Dev 9.002
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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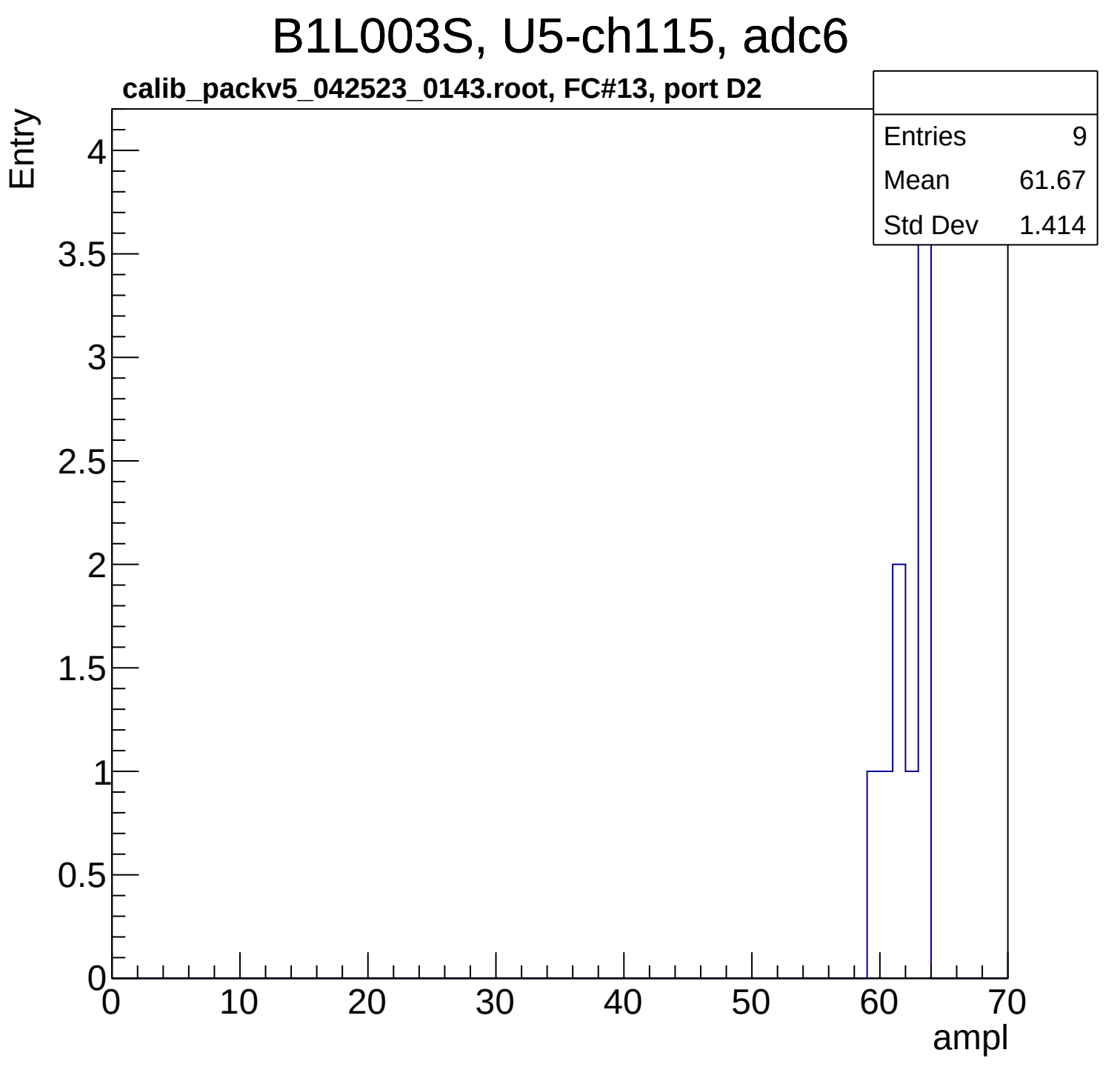
B1L003S, U5-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.414
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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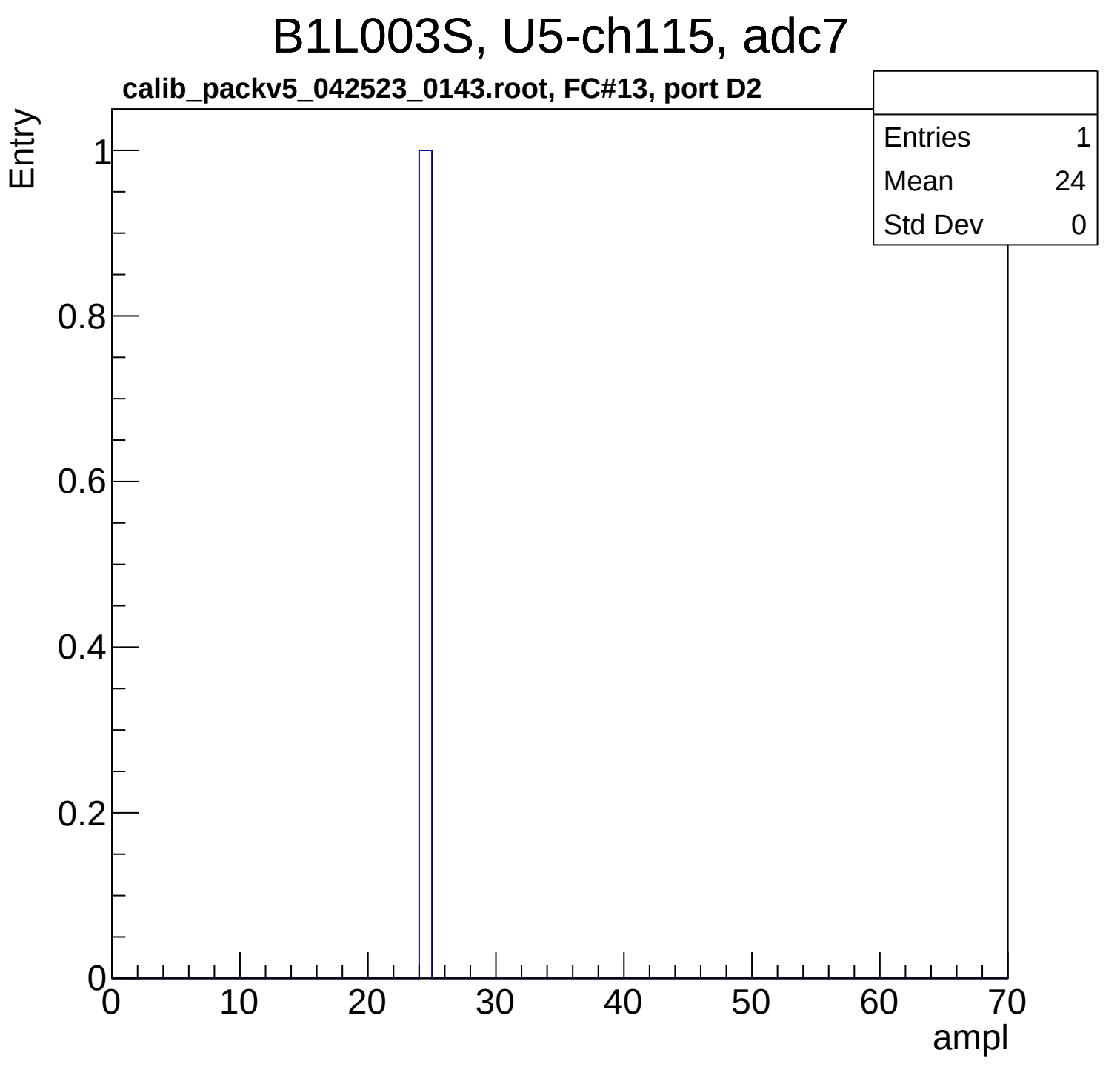
B1L003S, U5-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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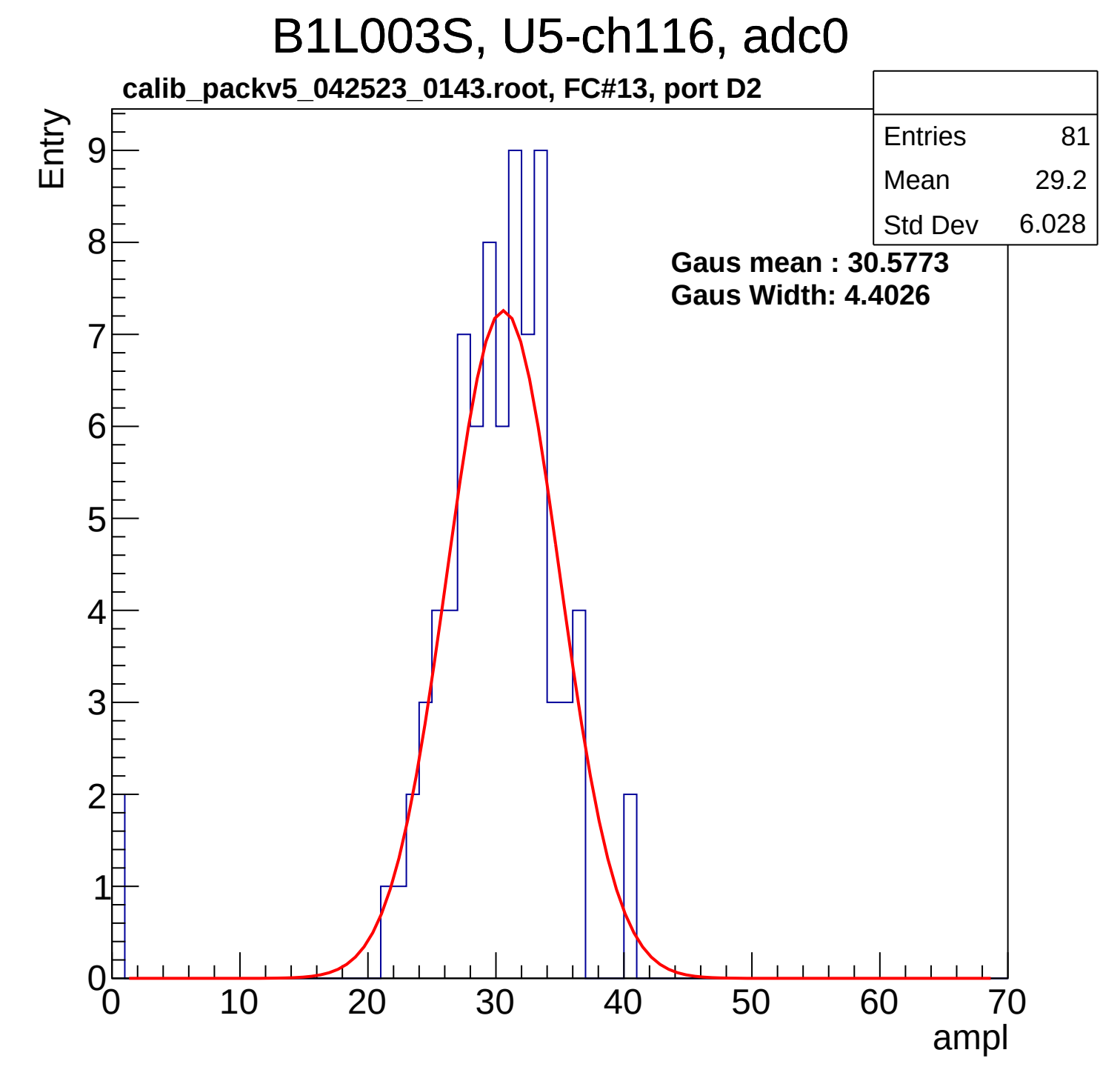
40
50
60
70
ampl


B1L003S, U5-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 29.2
Std Dev 6.028
Gaus mean : 30.5773
Gaus Width: 4.4026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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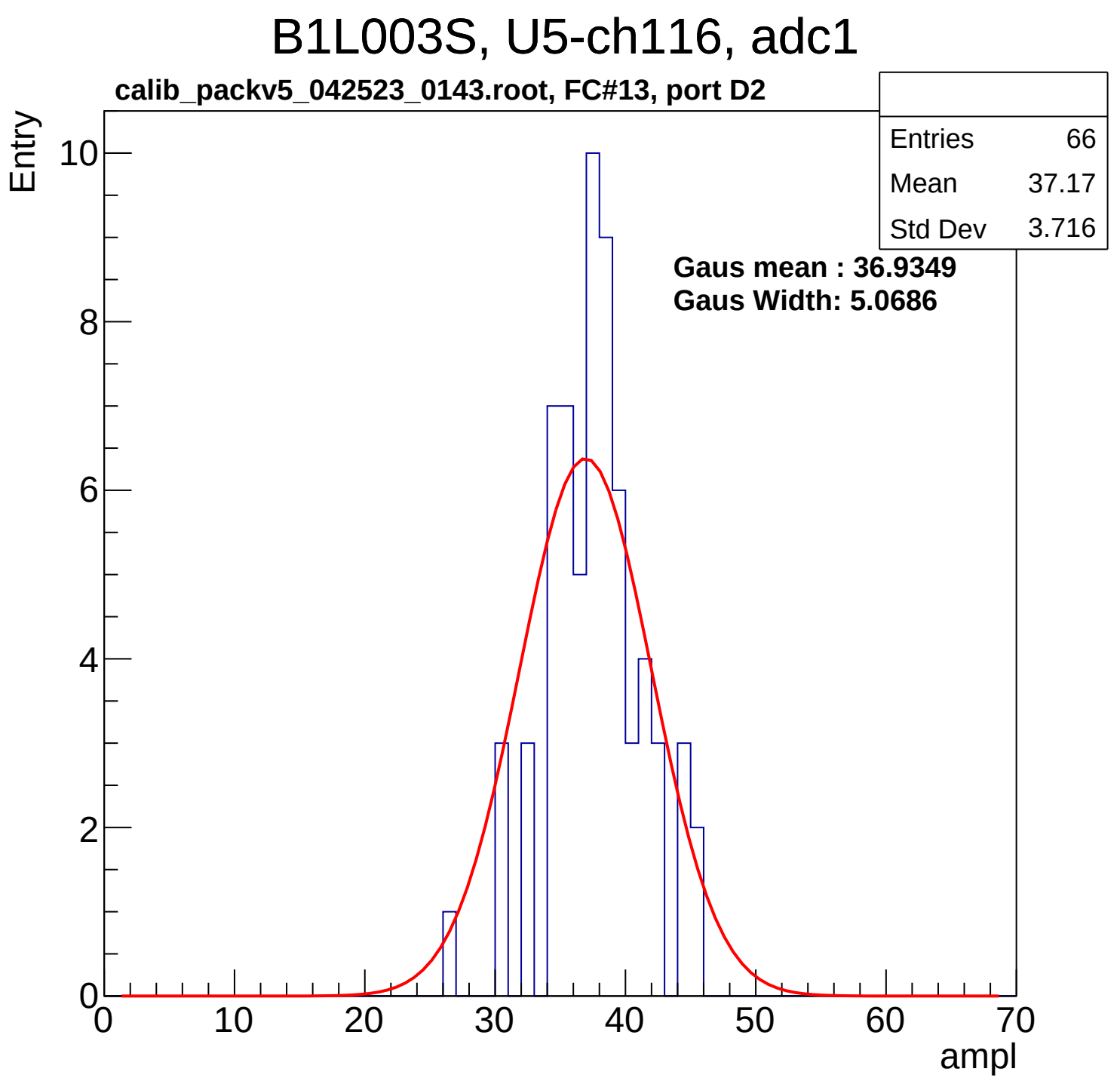
B1L003S, U5-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.17
Std Dev 3.716
Gaus mean : 36.9349
Gaus Width: 5.0686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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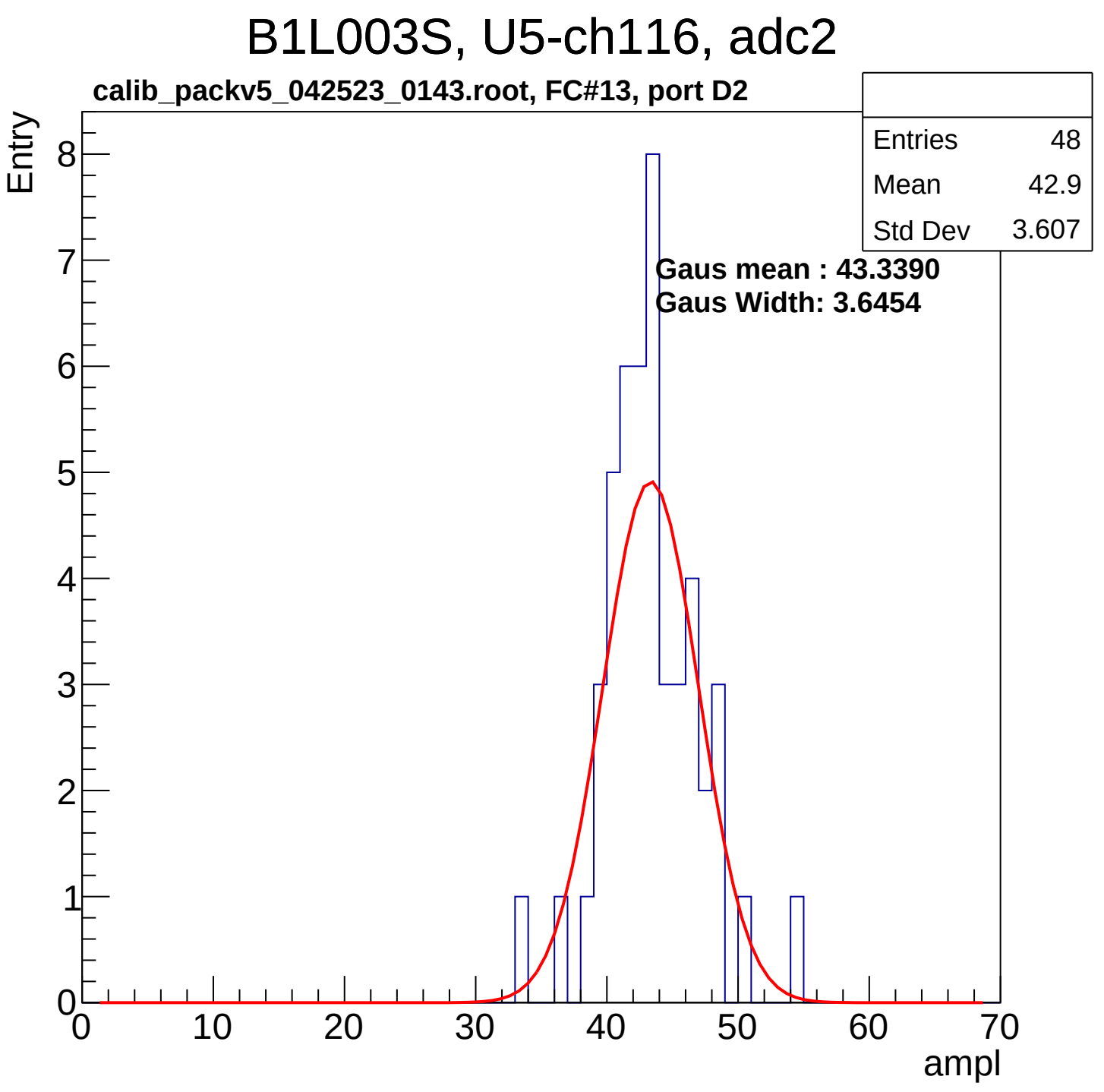
B1L003S, U5-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 42.9
Std Dev 3.607
Gaus mean : 43.3390
Gaus Width: 3.6454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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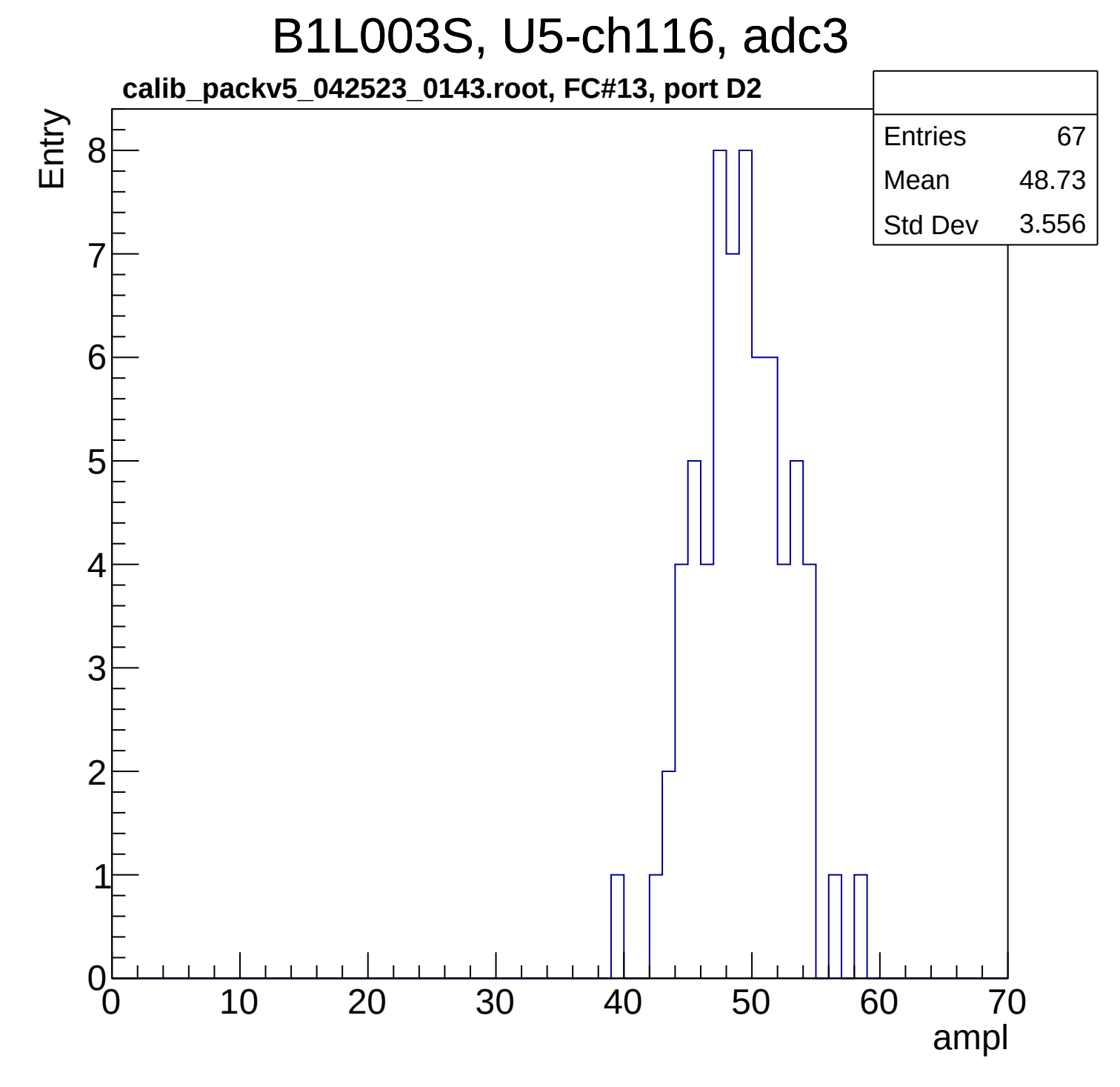
B1L003S, U5-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.73
Std Dev 3.556
Entry
ampl
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1
2
3
4
5
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7
8
0
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40
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60
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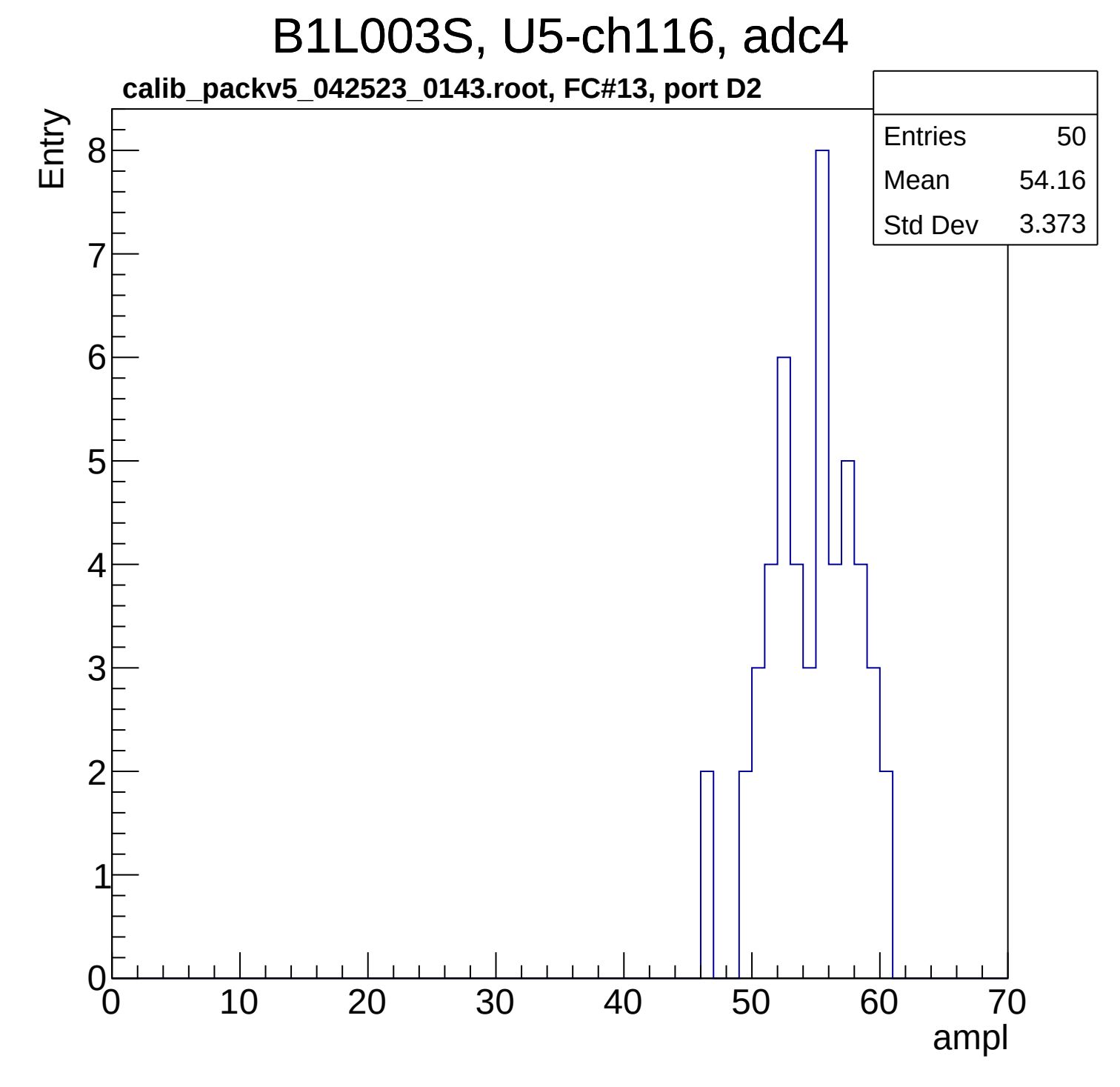
B1L003S, U5-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.16
Std Dev 3.373
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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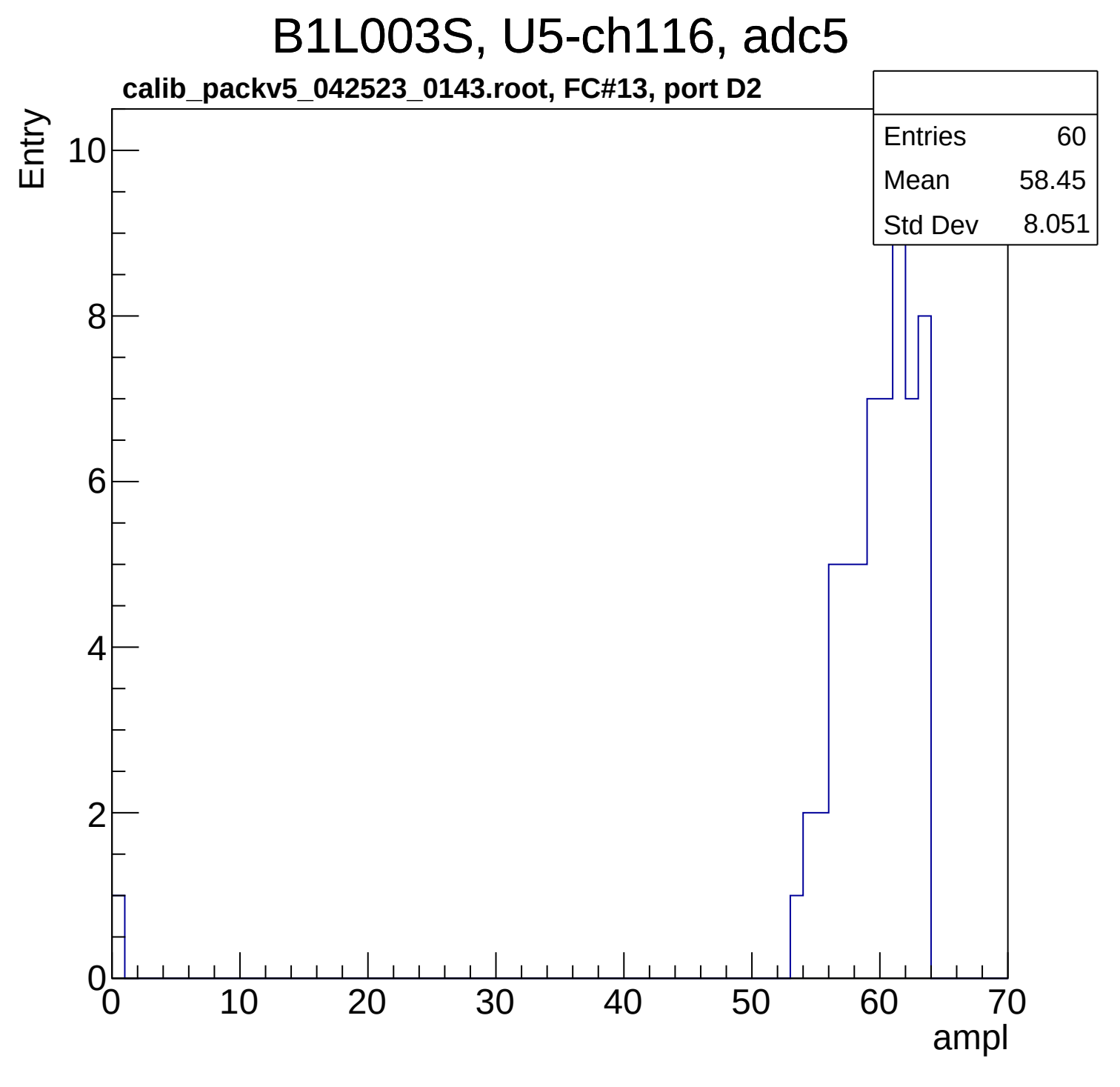
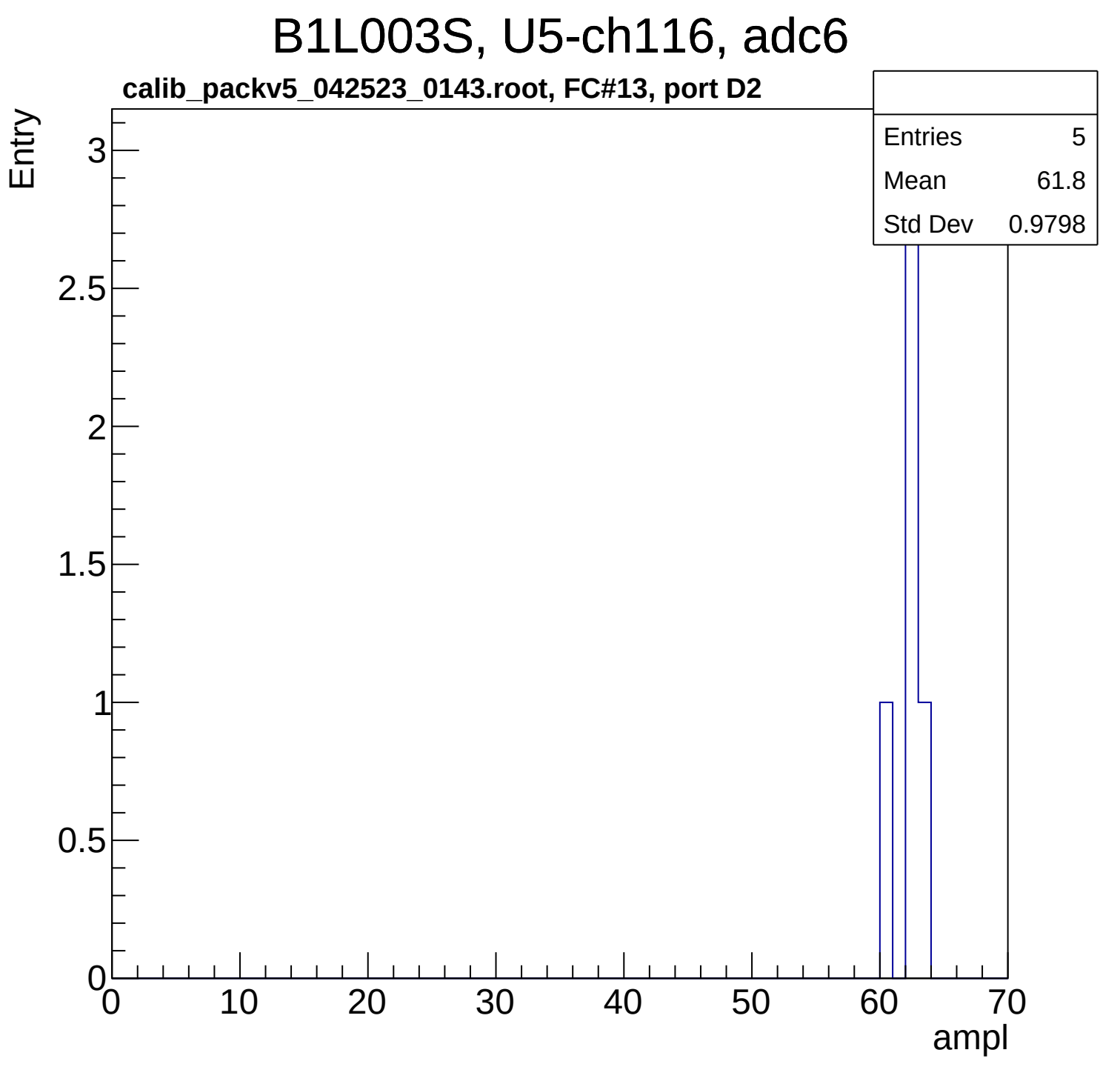

B1L003S, U5-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.45
Std Dev 8.051
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
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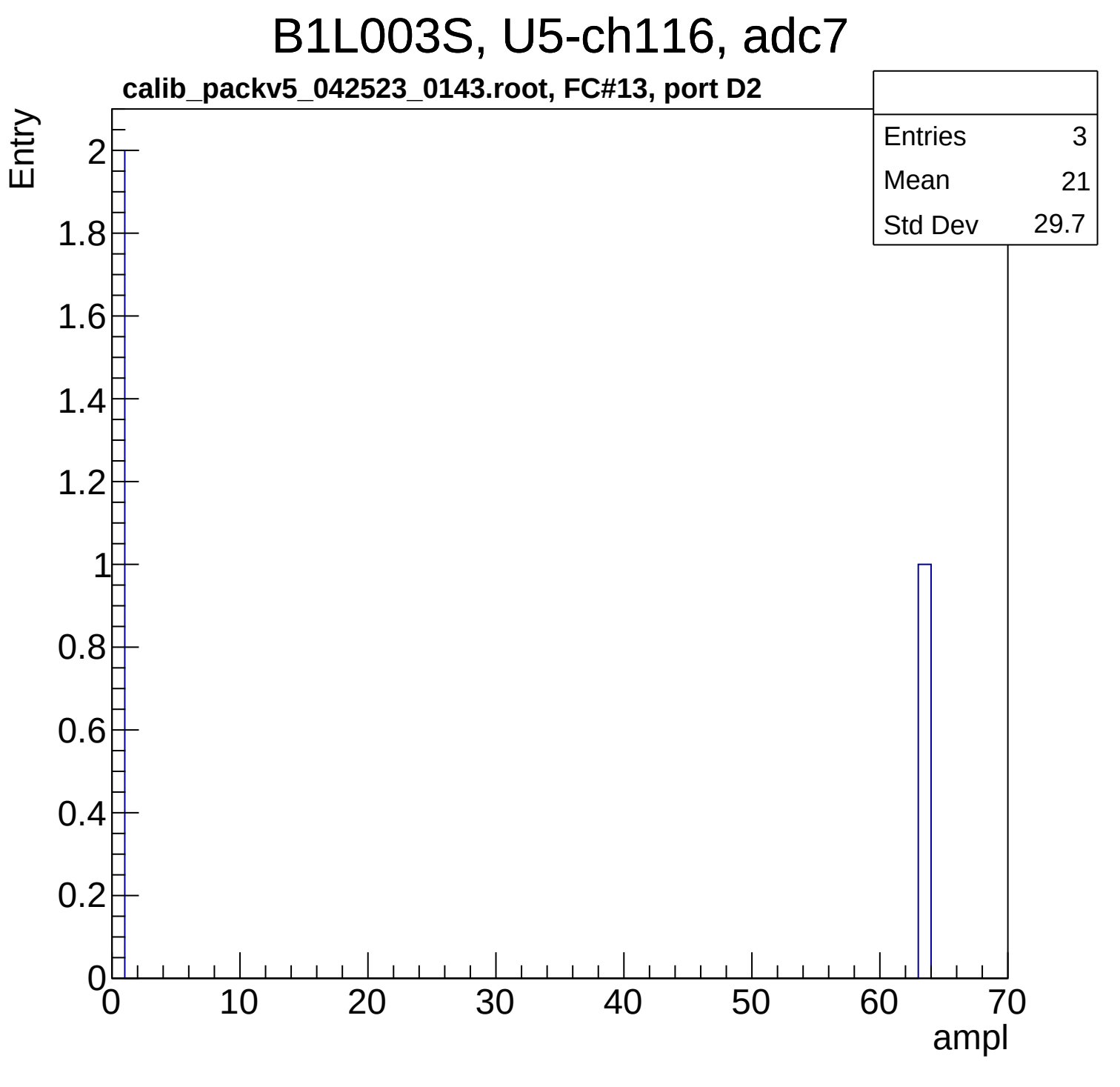
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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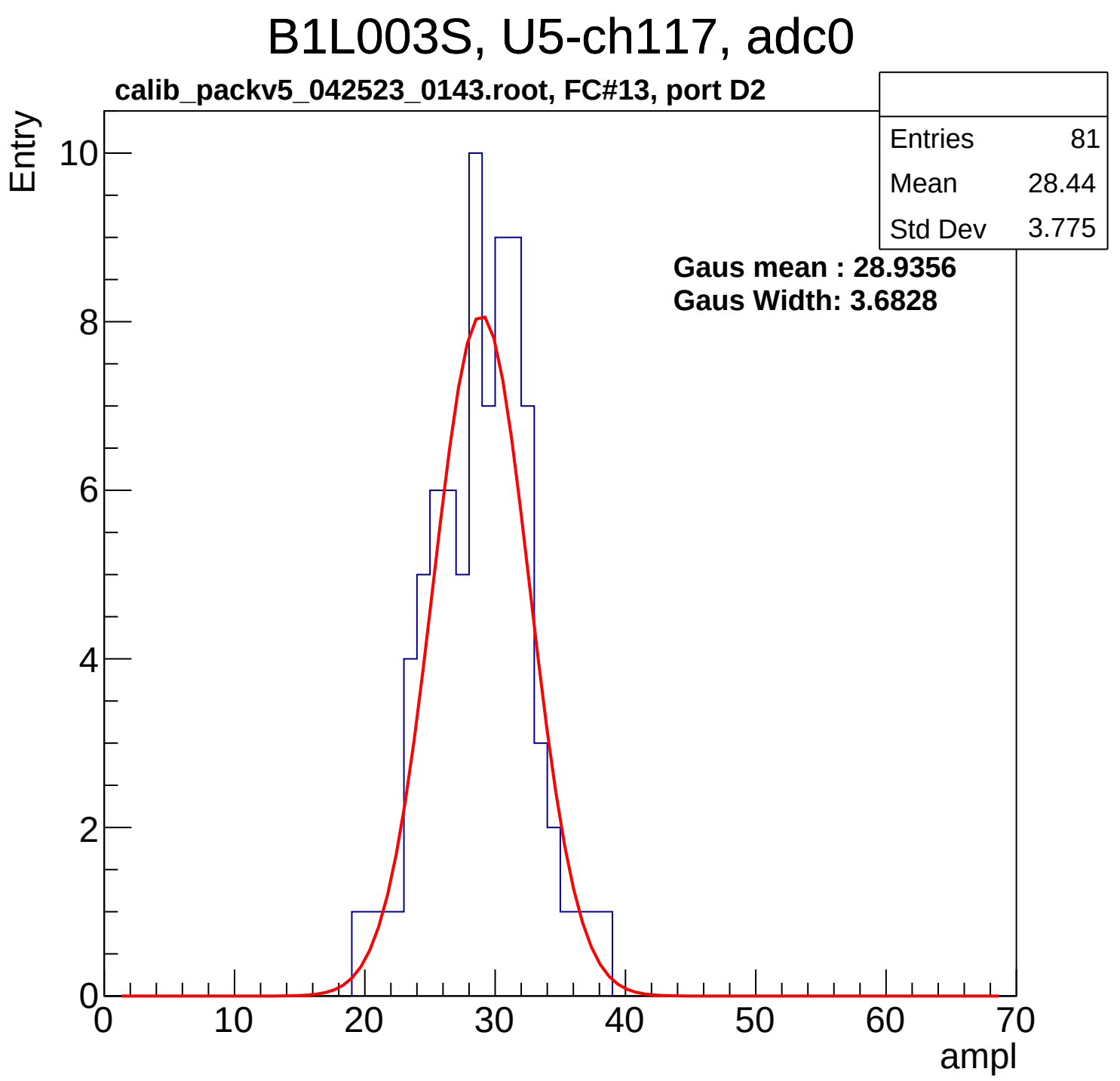
B1L003S, U5-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.44
Std Dev 3.775
Gaus mean : 28.9356
Gaus Width: 3.6828
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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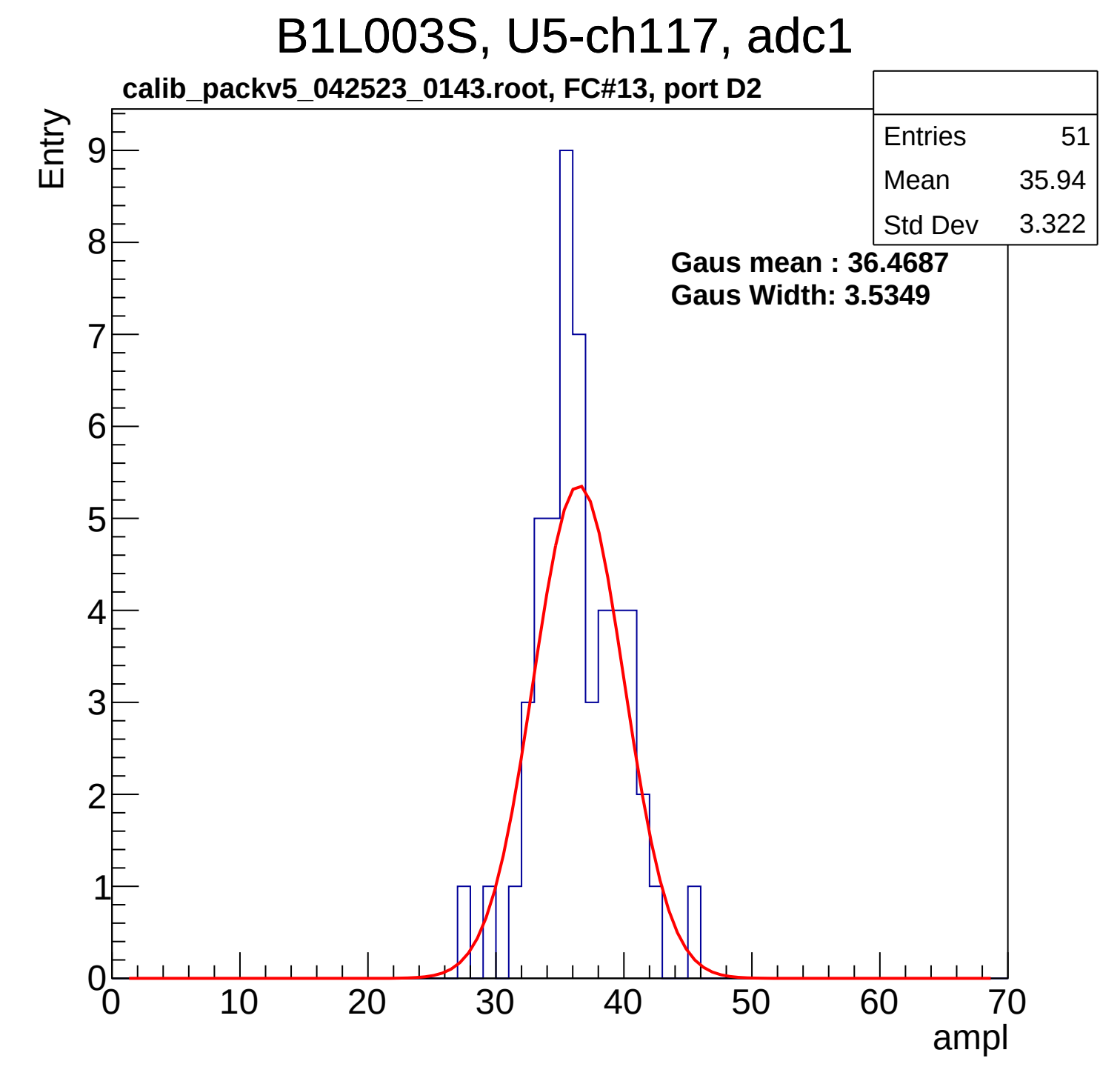
B1L003S, U5-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 35.94
Std Dev 3.322
Gaus mean : 36.4687
Gaus Width: 3.5349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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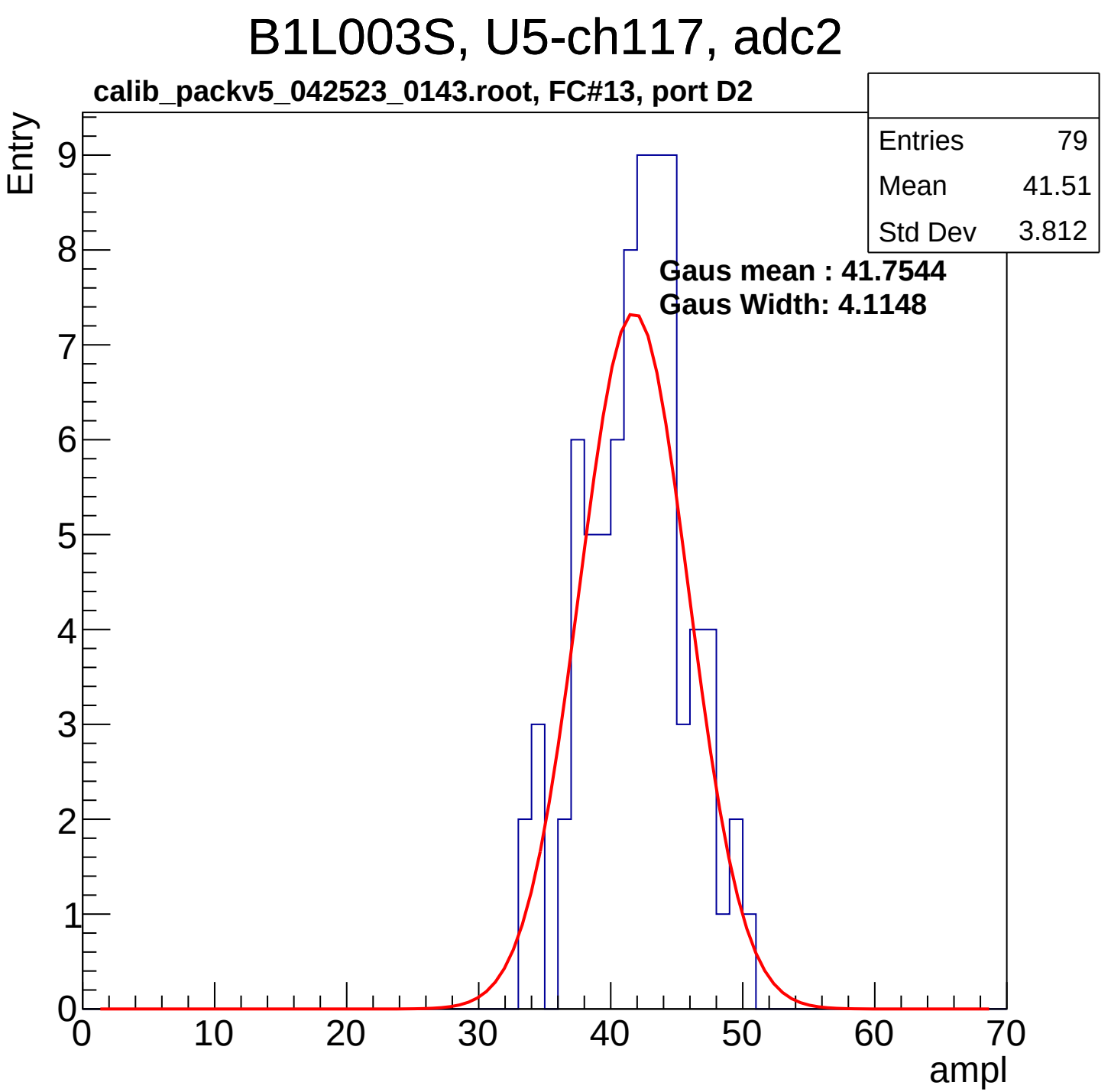
B1L003S, U5-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 41.51
Std Dev 3.812
Gaus mean : 41.7544
Gaus Width: 4.1148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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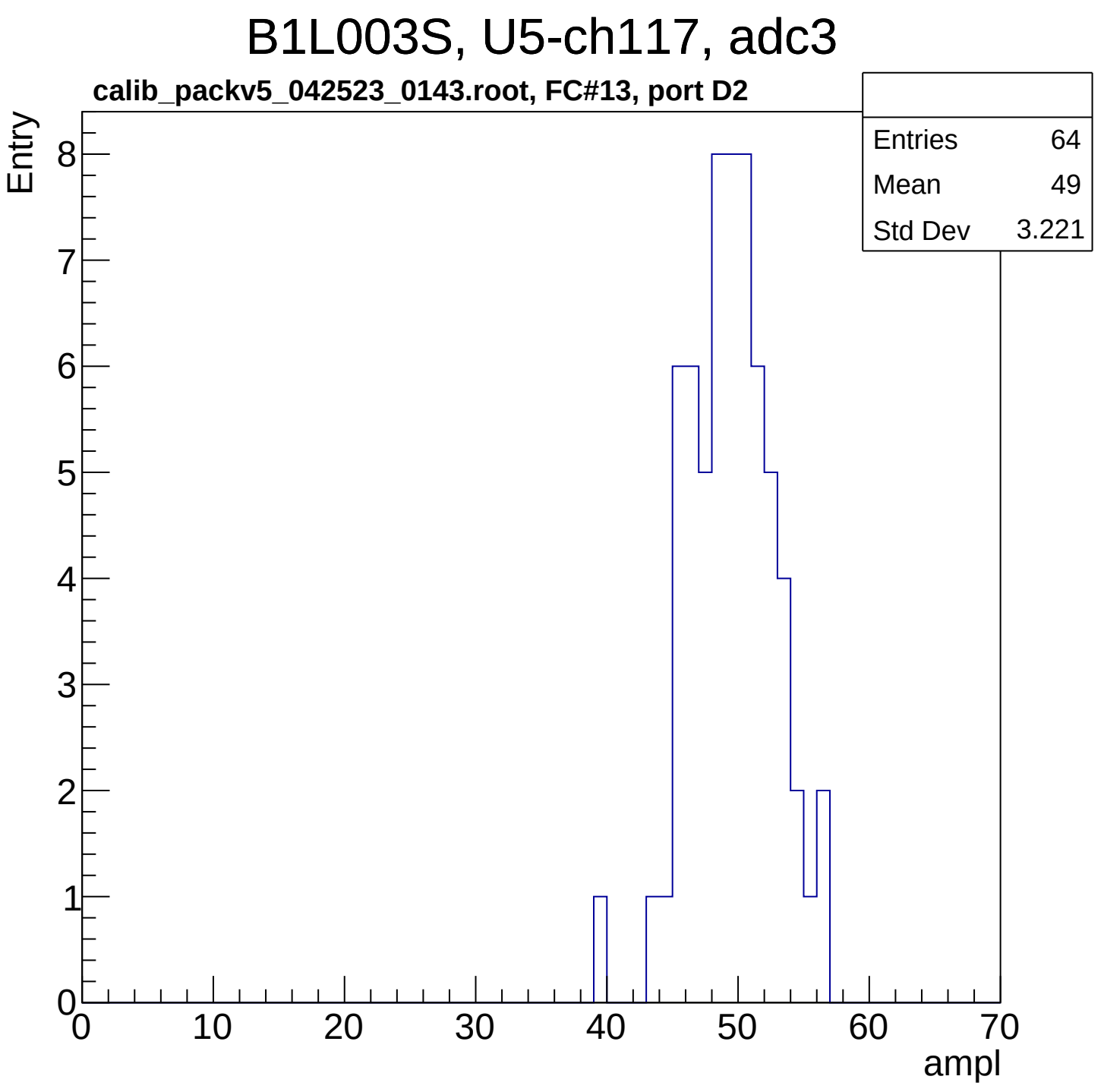
B1L003S, U5-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49
Std Dev 3.221
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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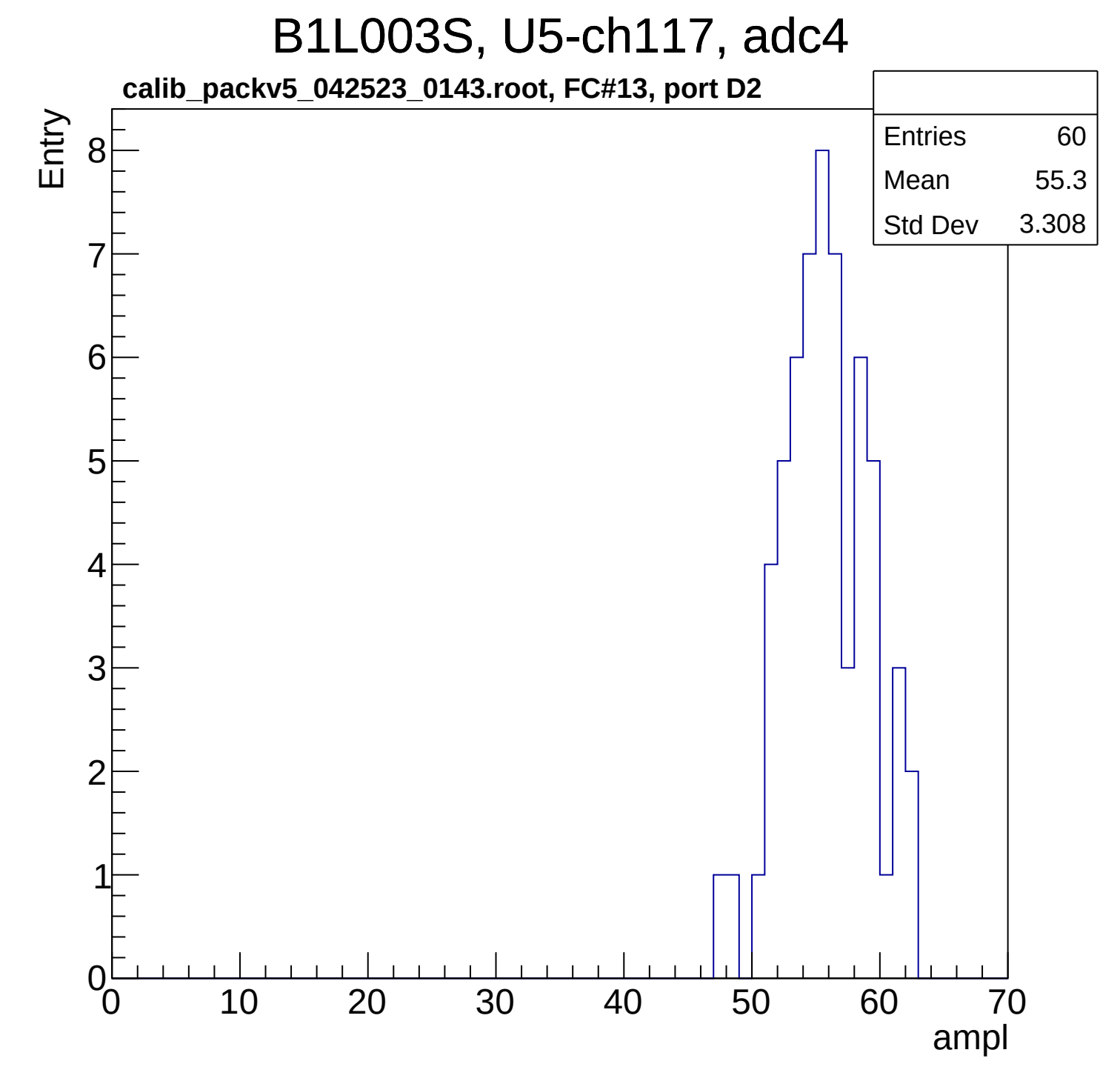
B1L003S, U5-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.3
Std Dev 3.308
Entry
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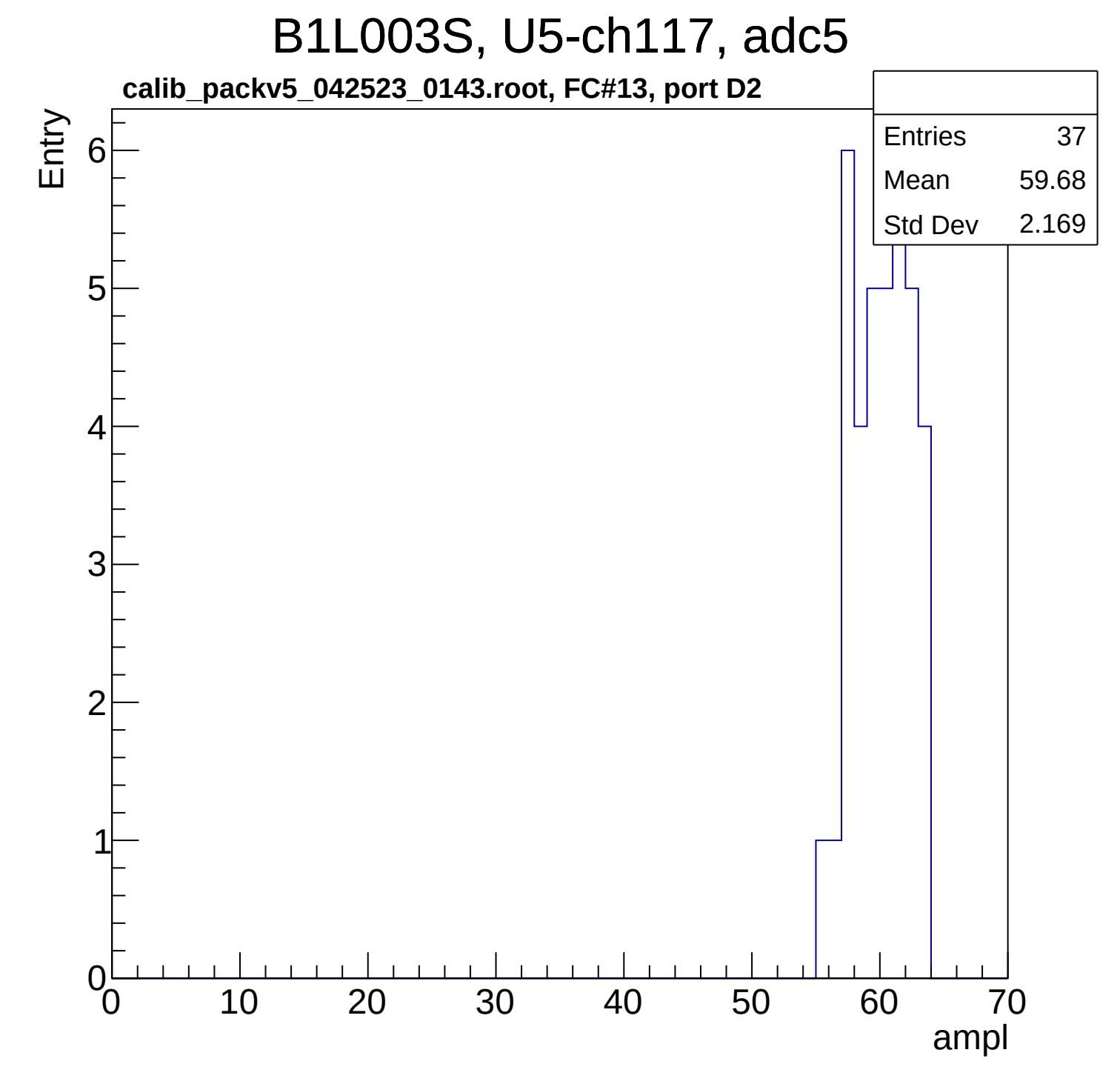
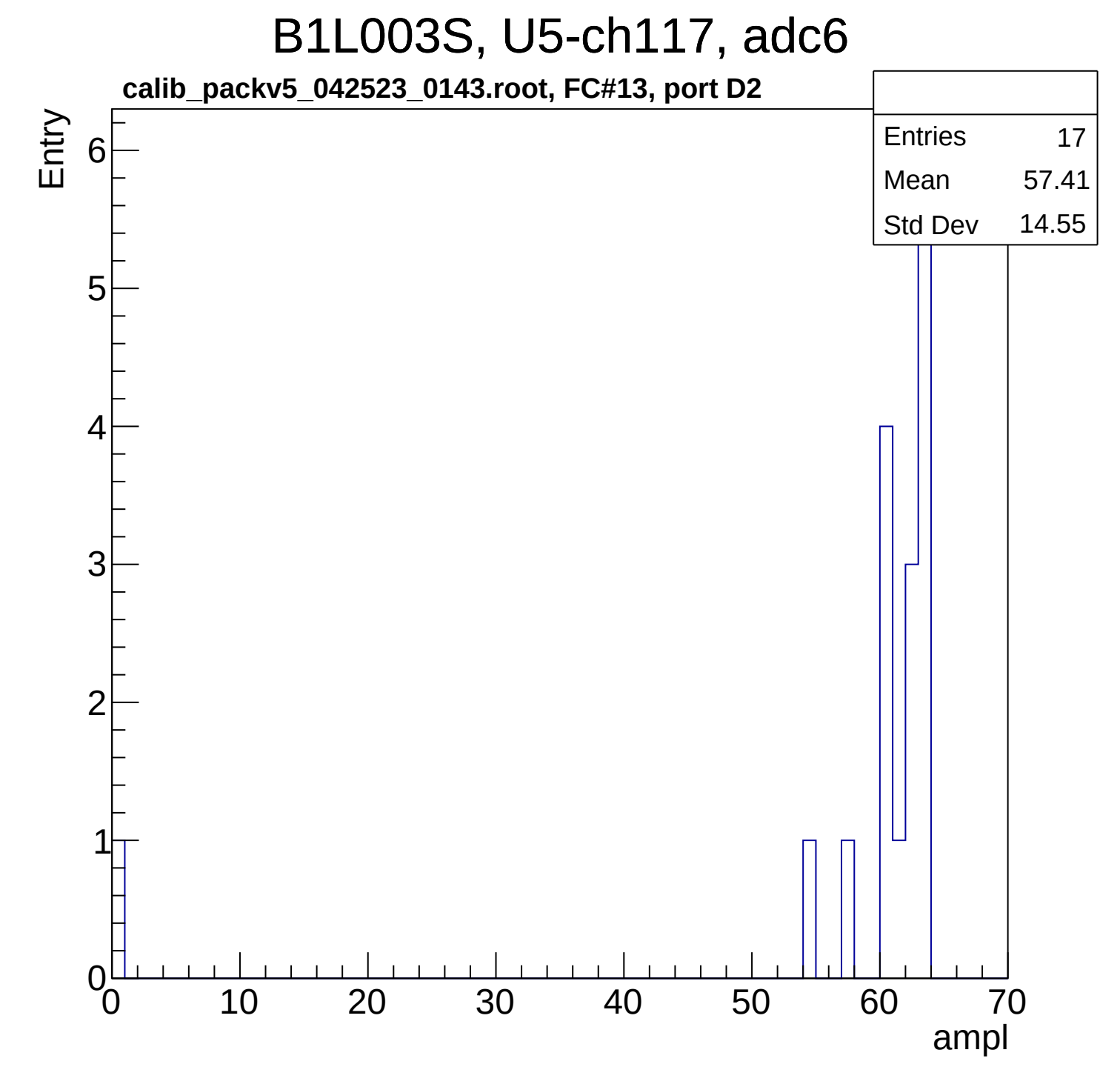
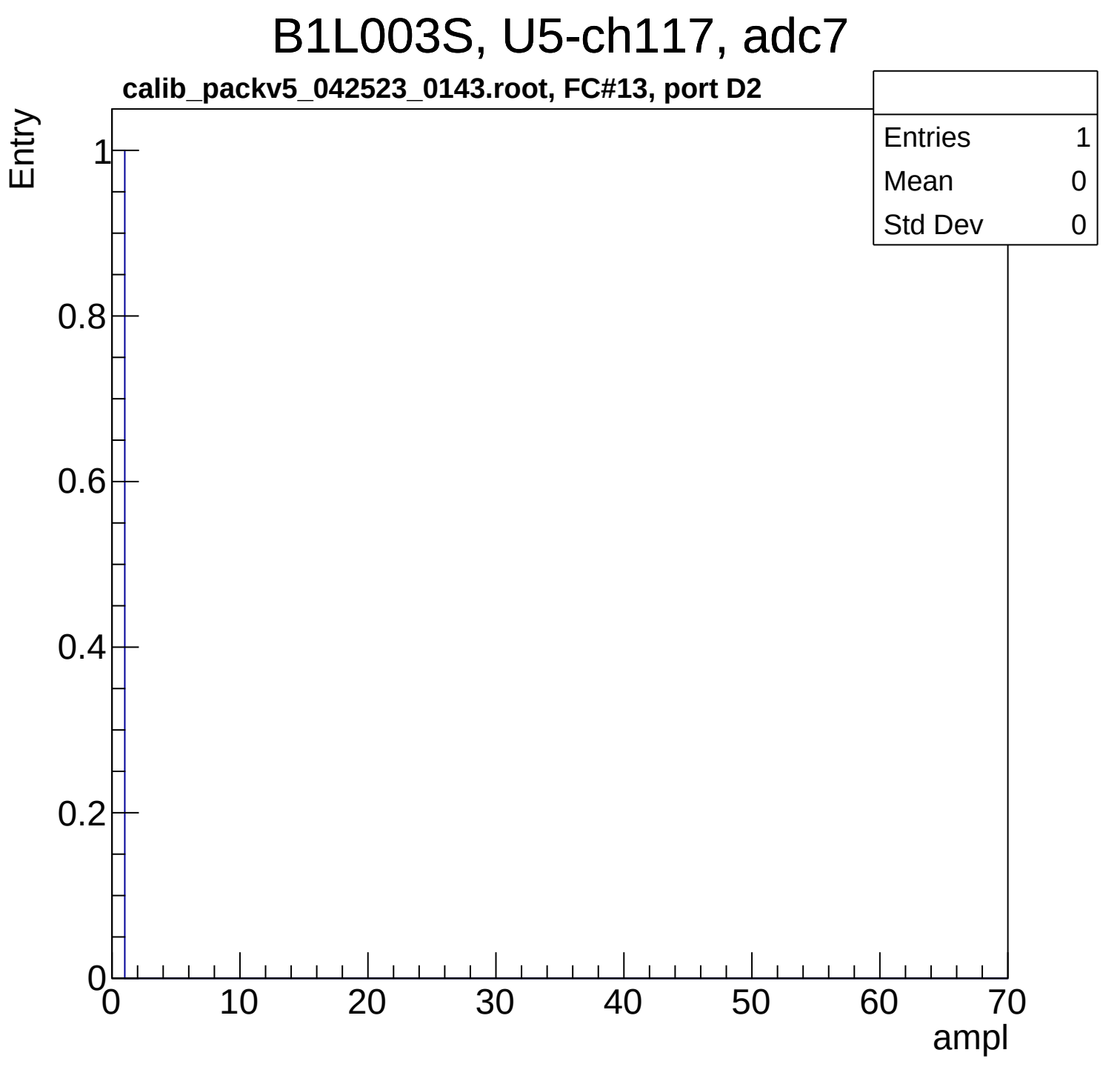
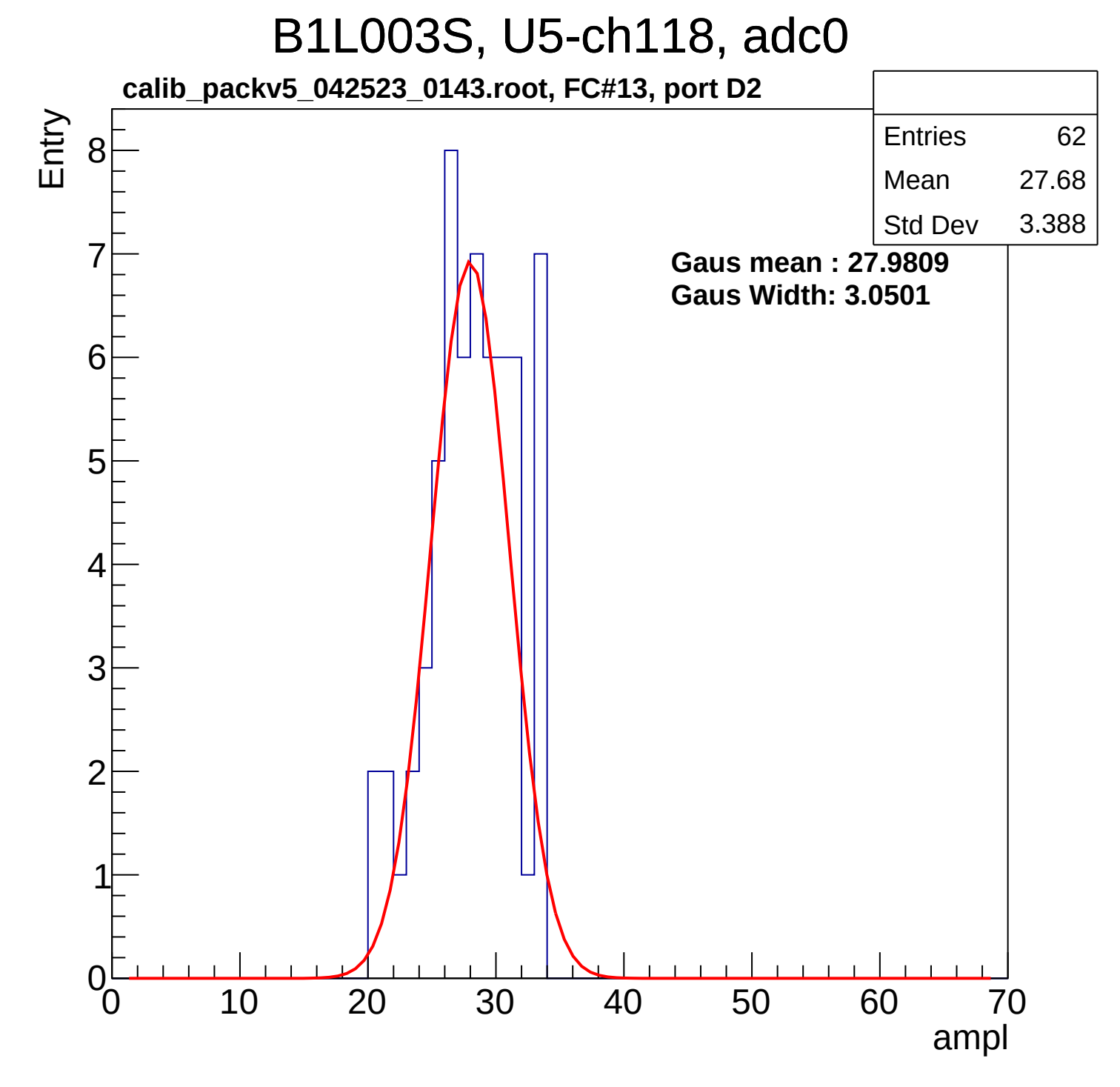
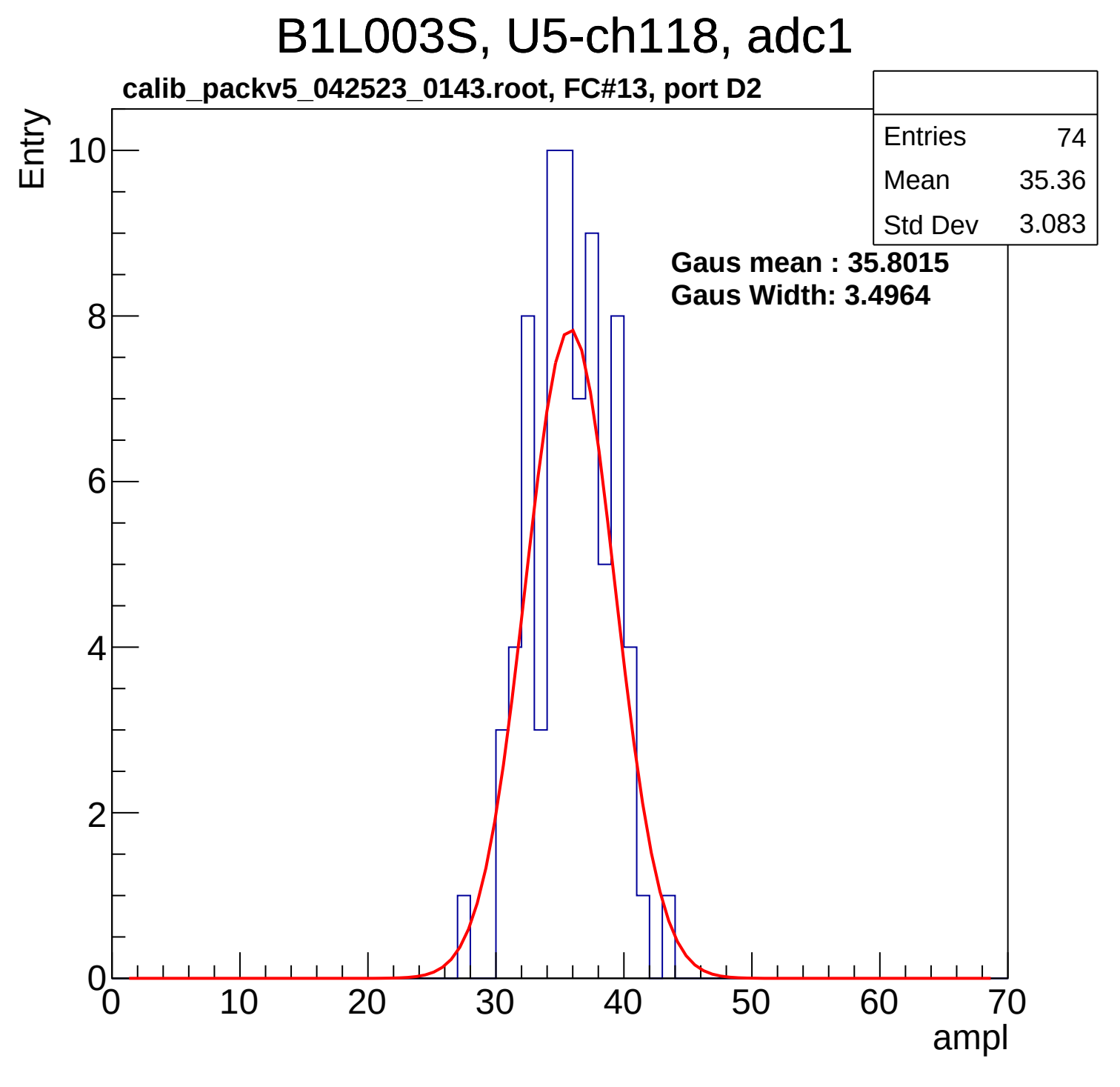
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 59.68
Std Dev 2.169
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.41
Std Dev 14.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 27.68
Std Dev 3.388
Gaus mean : 27.9809
Gaus Width: 3.0501
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.36
Std Dev 3.083
Gaus mean : 35.8015
Gaus Width: 3.4964
Entry
ampl
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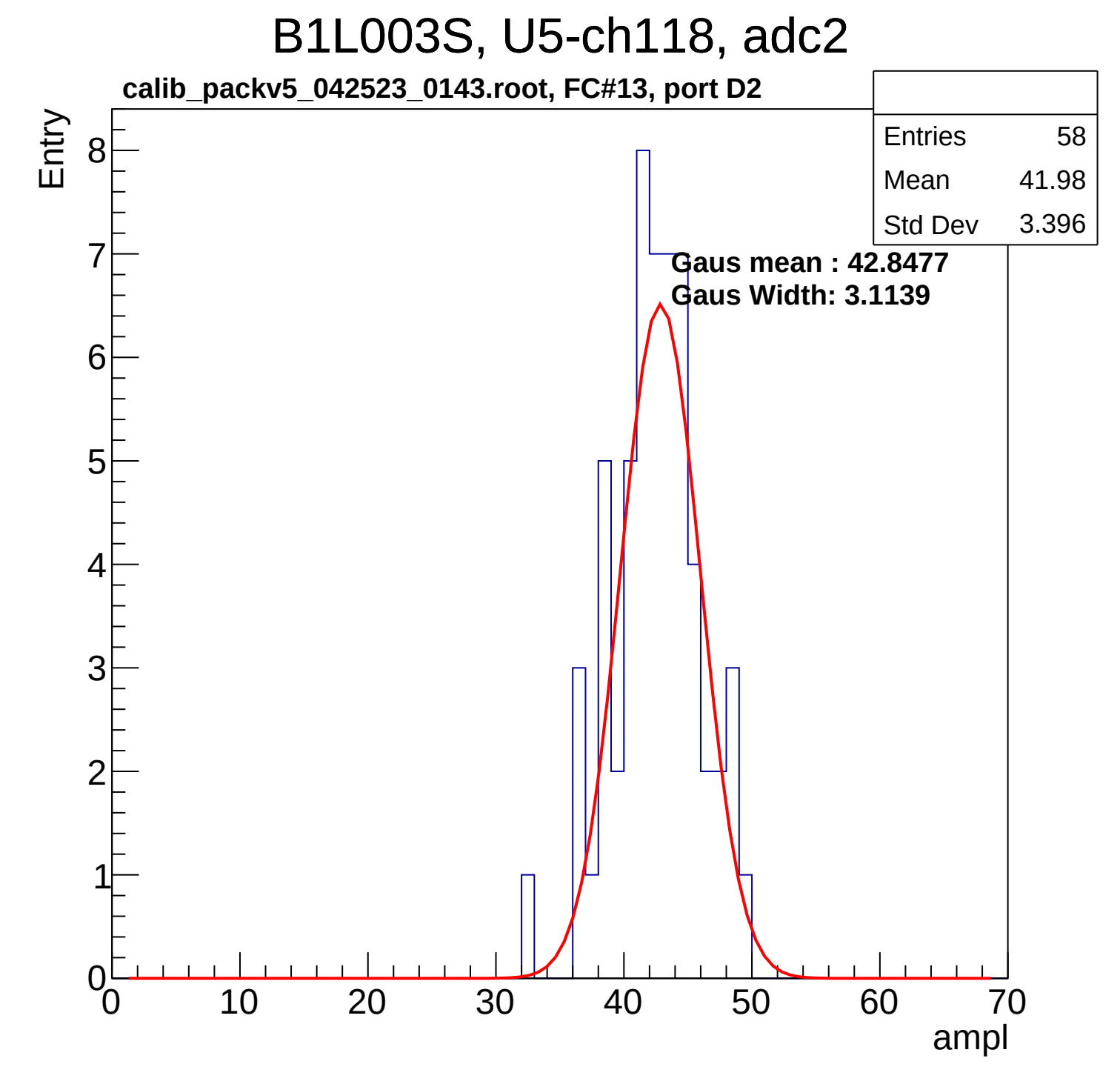
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.98
Std Dev 3.396
Gaus mean : 42.8477
Gaus Width: 3.1139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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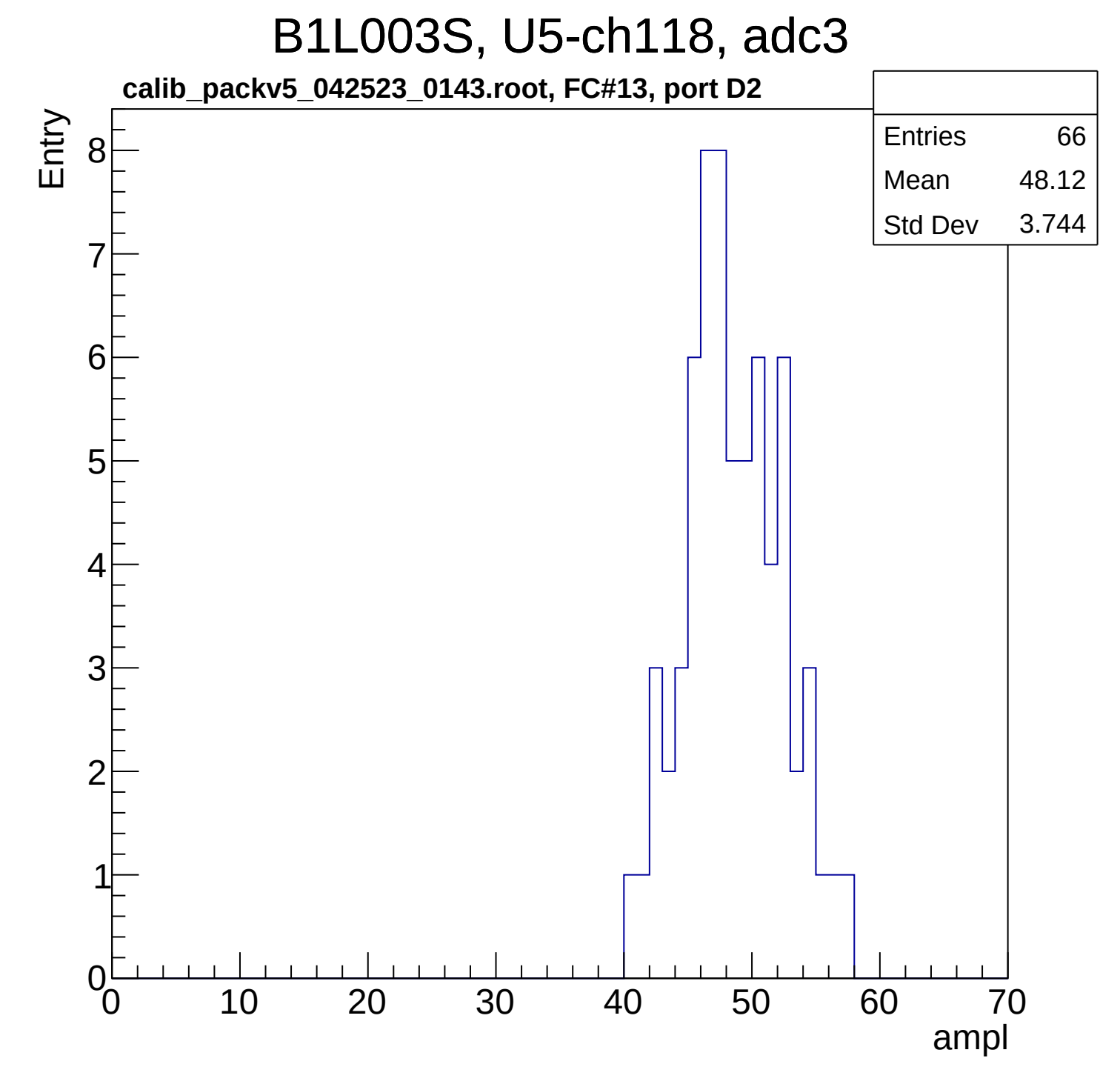
B1L003S, U5-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.12
Std Dev 3.744
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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50
60
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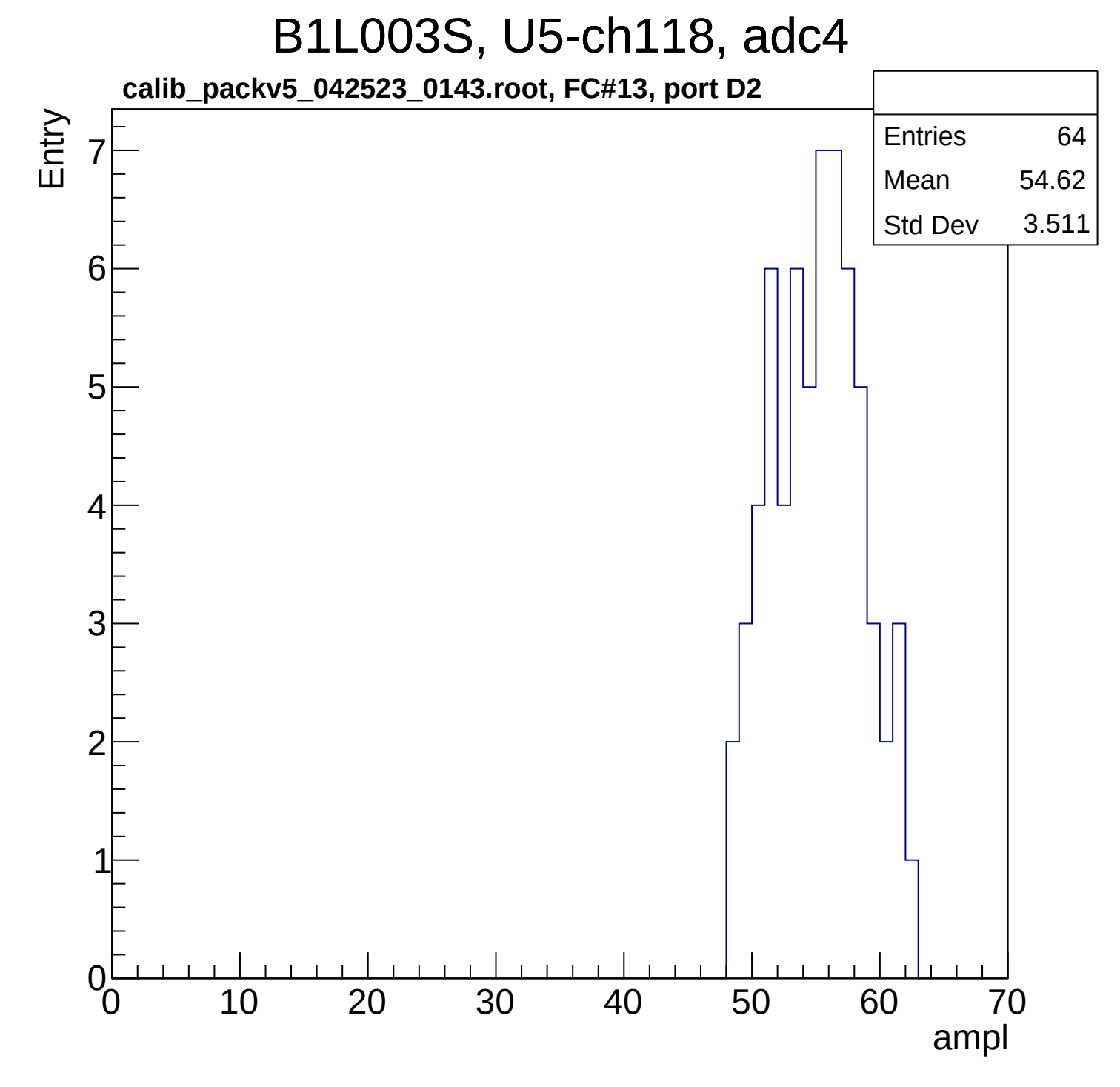
B1L003S, U5-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.62
Std Dev 3.511
Entry
ampl
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1
2
3
4
5
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7
0
10
20
30
40
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60
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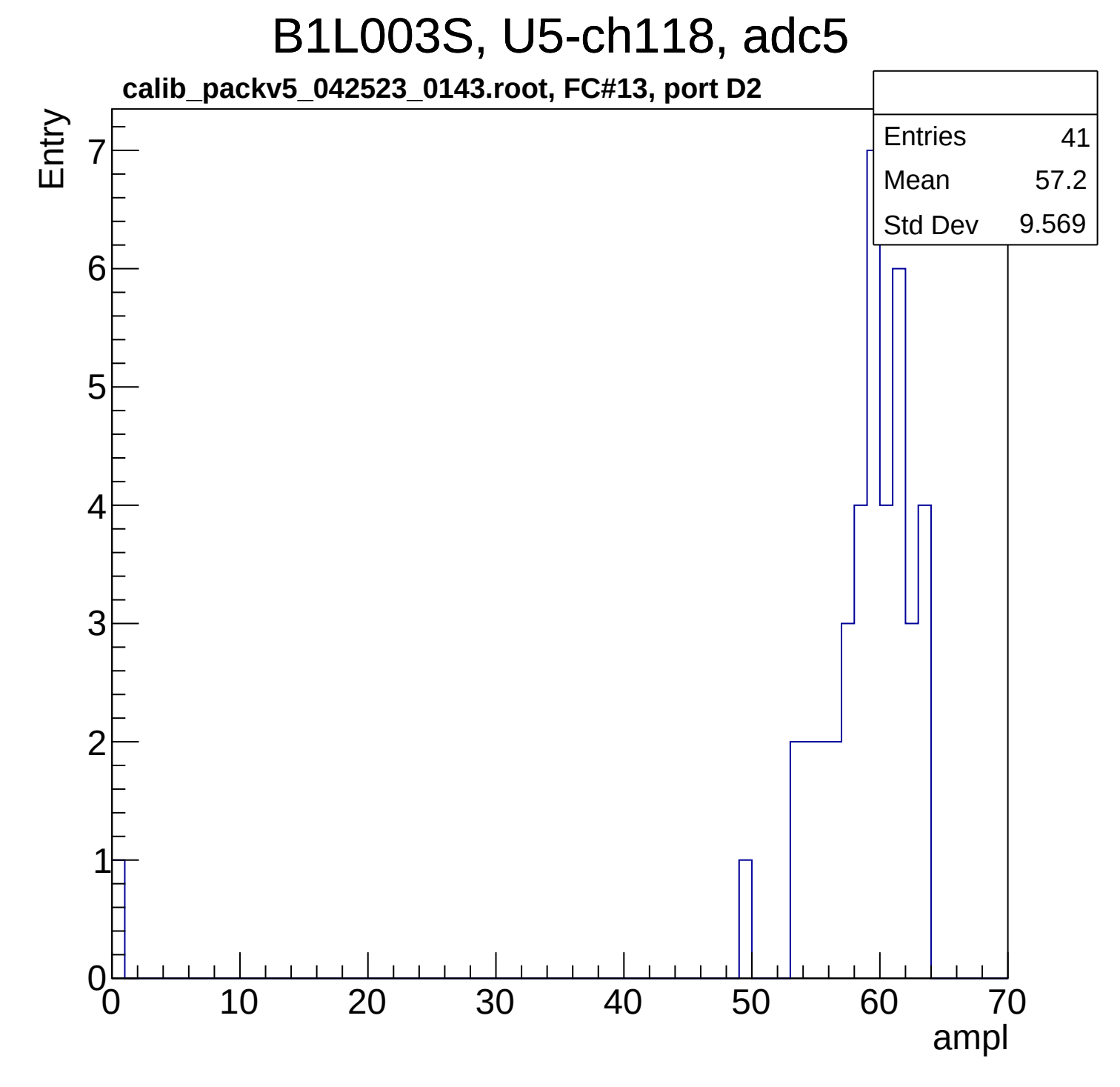
B1L003S, U5-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 57.2
Std Dev 9.569
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
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60
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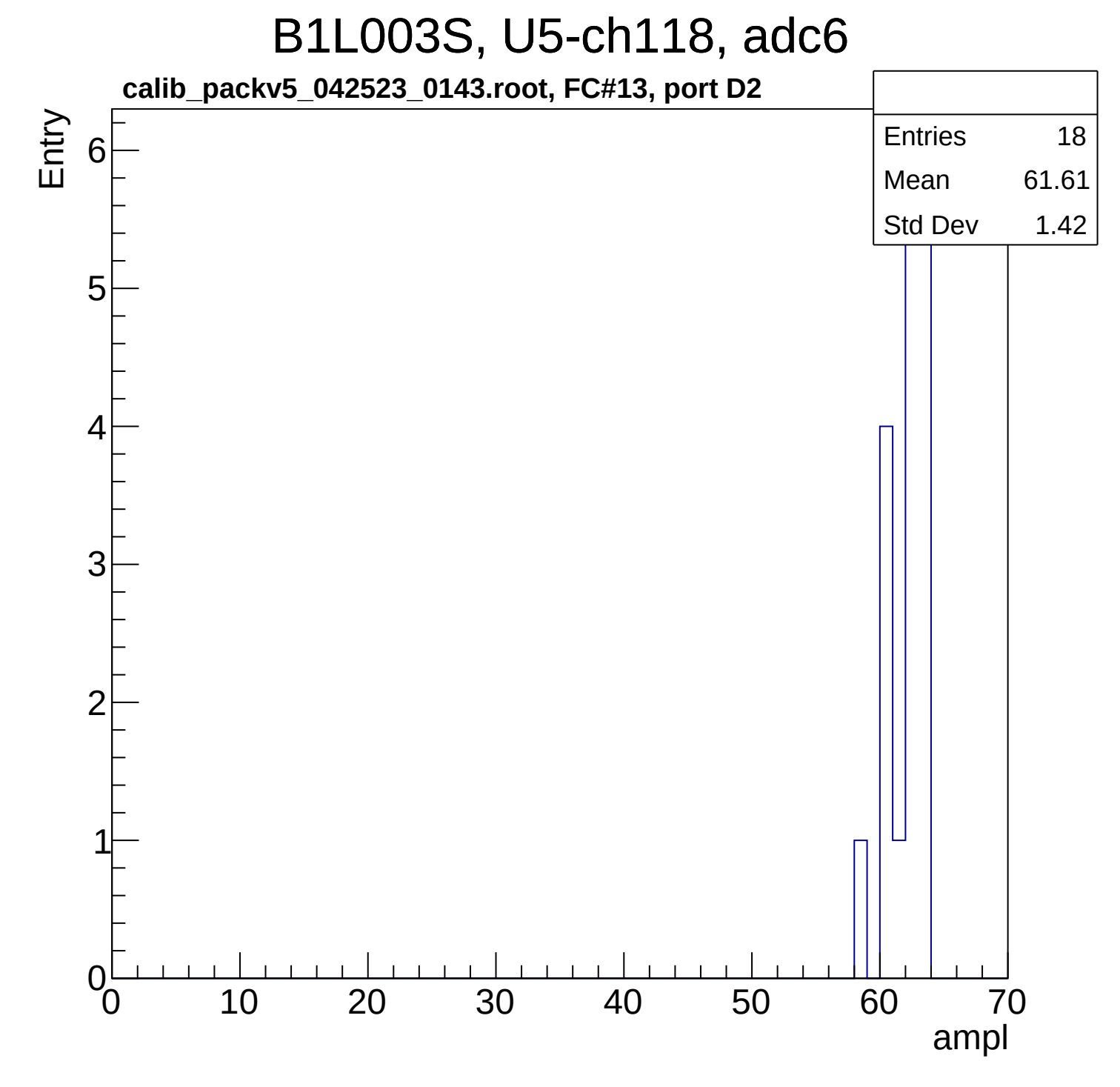
B1L003S, U5-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.61
Std Dev 1.42
Entry
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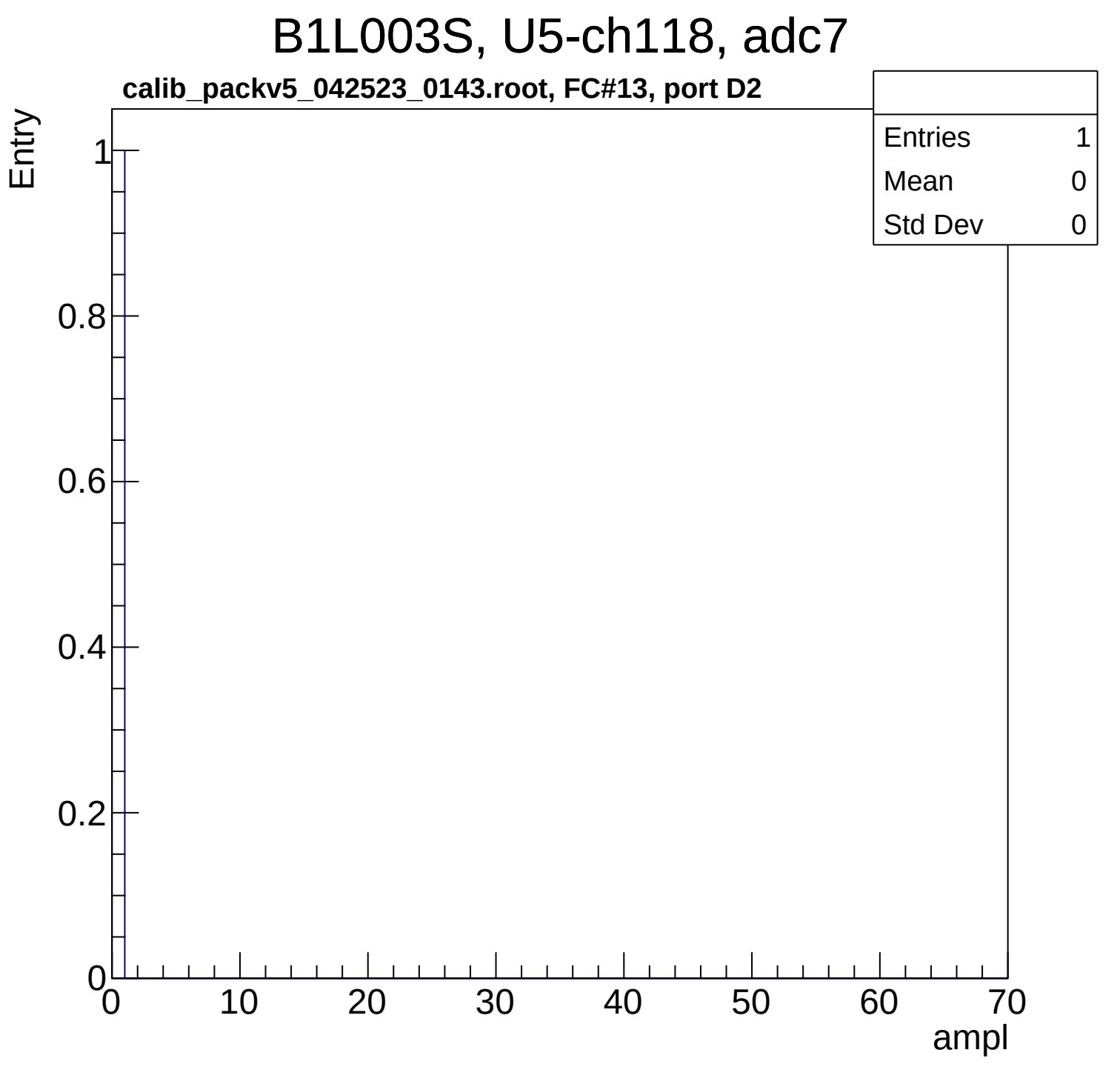
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
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0
10
20
30
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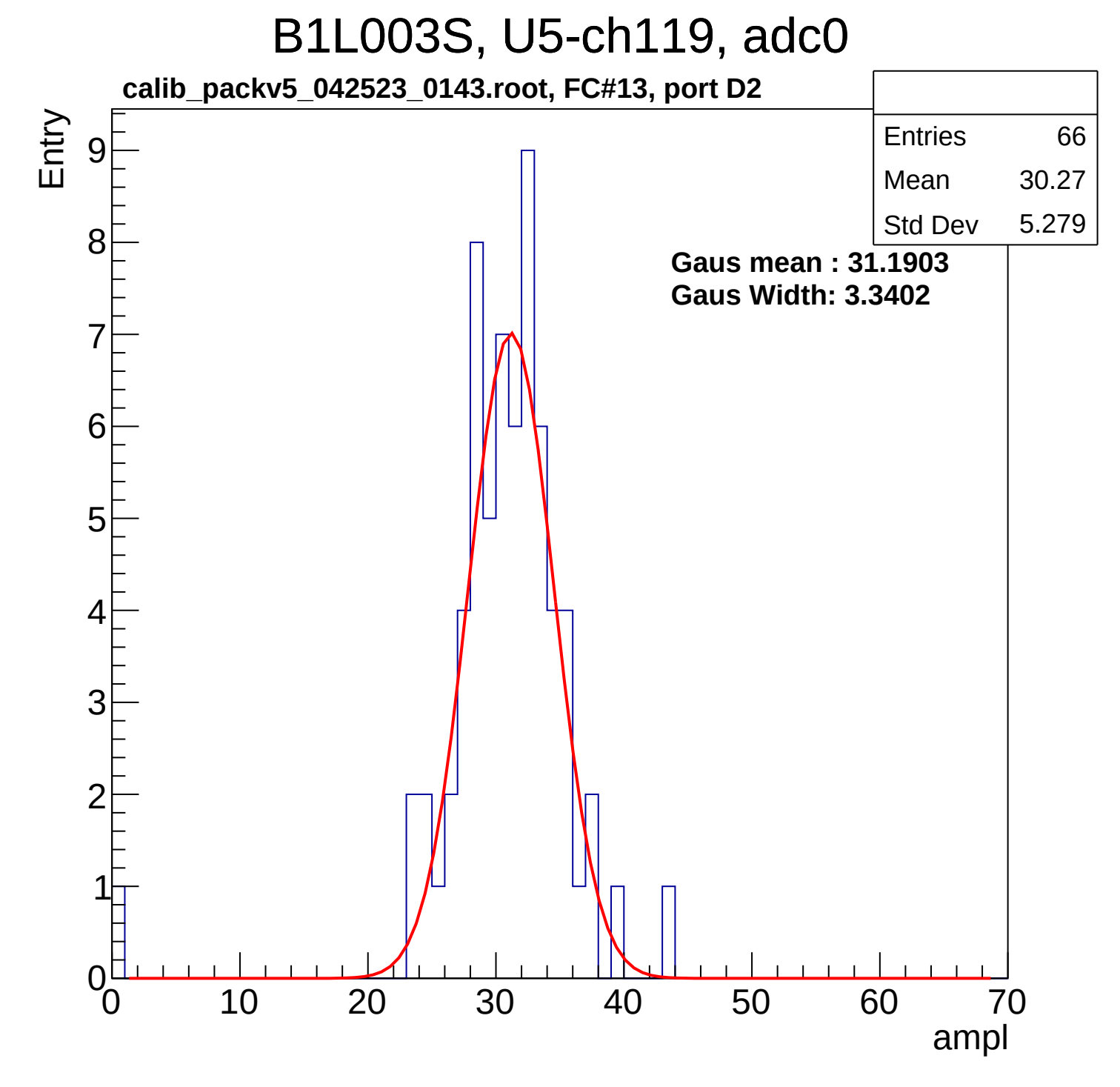
50
60
70
ampl


B1L003S, U5-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.27
Std Dev 5.279
Gaus mean : 31.1903
Gaus Width: 3.3402
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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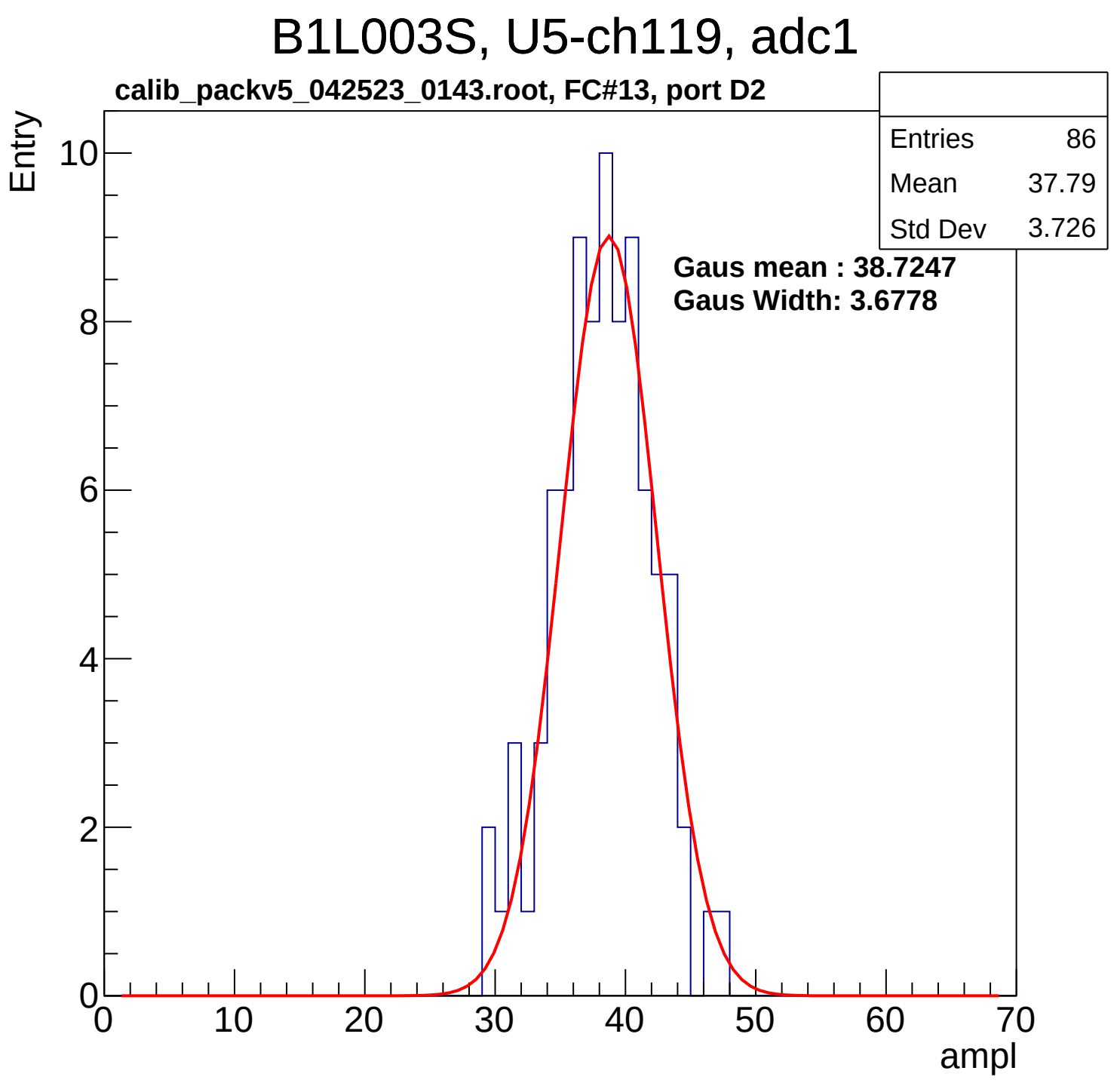
B1L003S, U5-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 37.79
Std Dev 3.726
Gaus mean : 38.7247
Gaus Width: 3.6778
Entry
ampl
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2
4
6
8
10
0
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40
50
60
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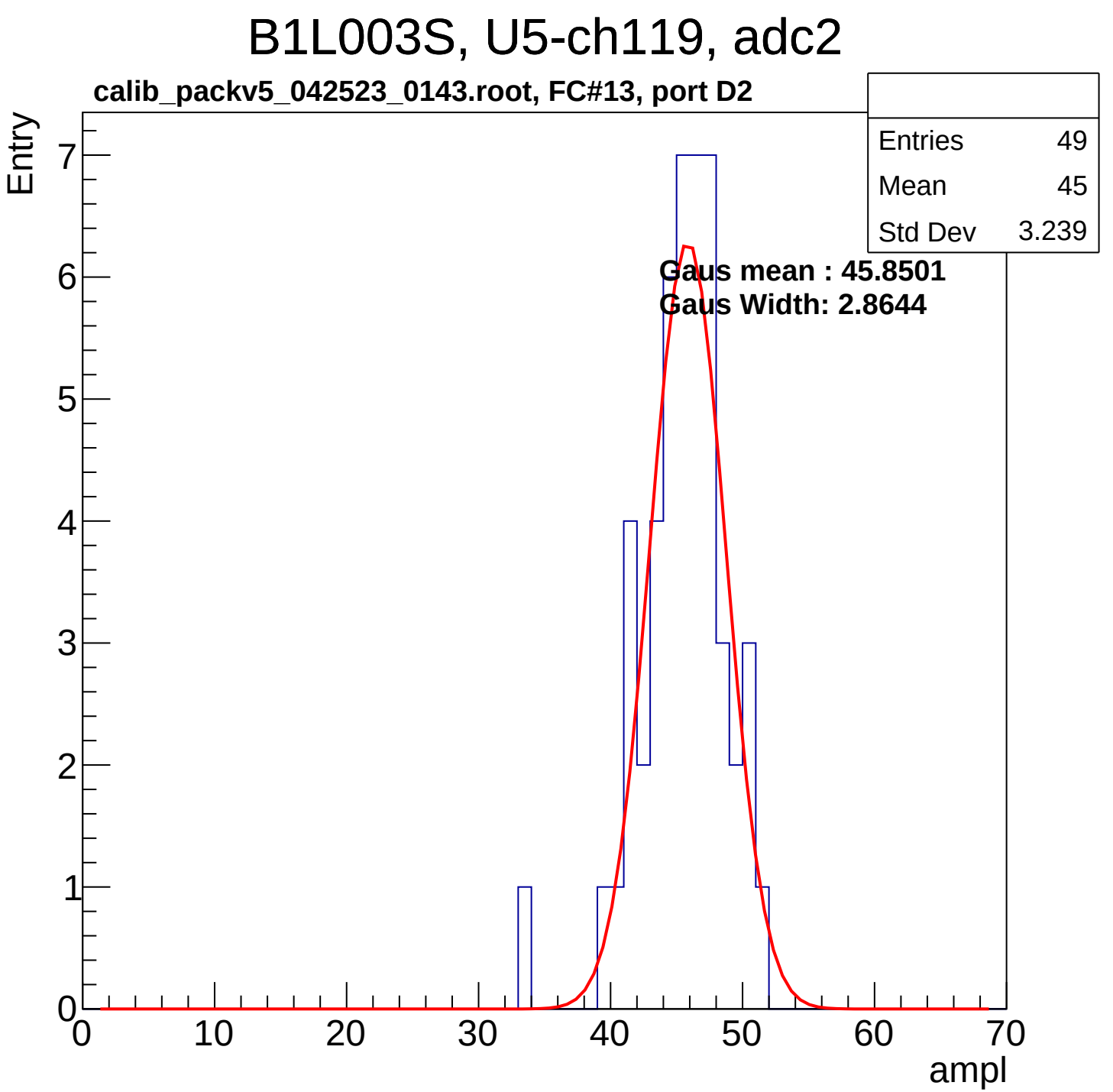
B1L003S, U5-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 45
Std Dev 3.239
Gaus mean : 45.8501
Gaus Width: 2.8644
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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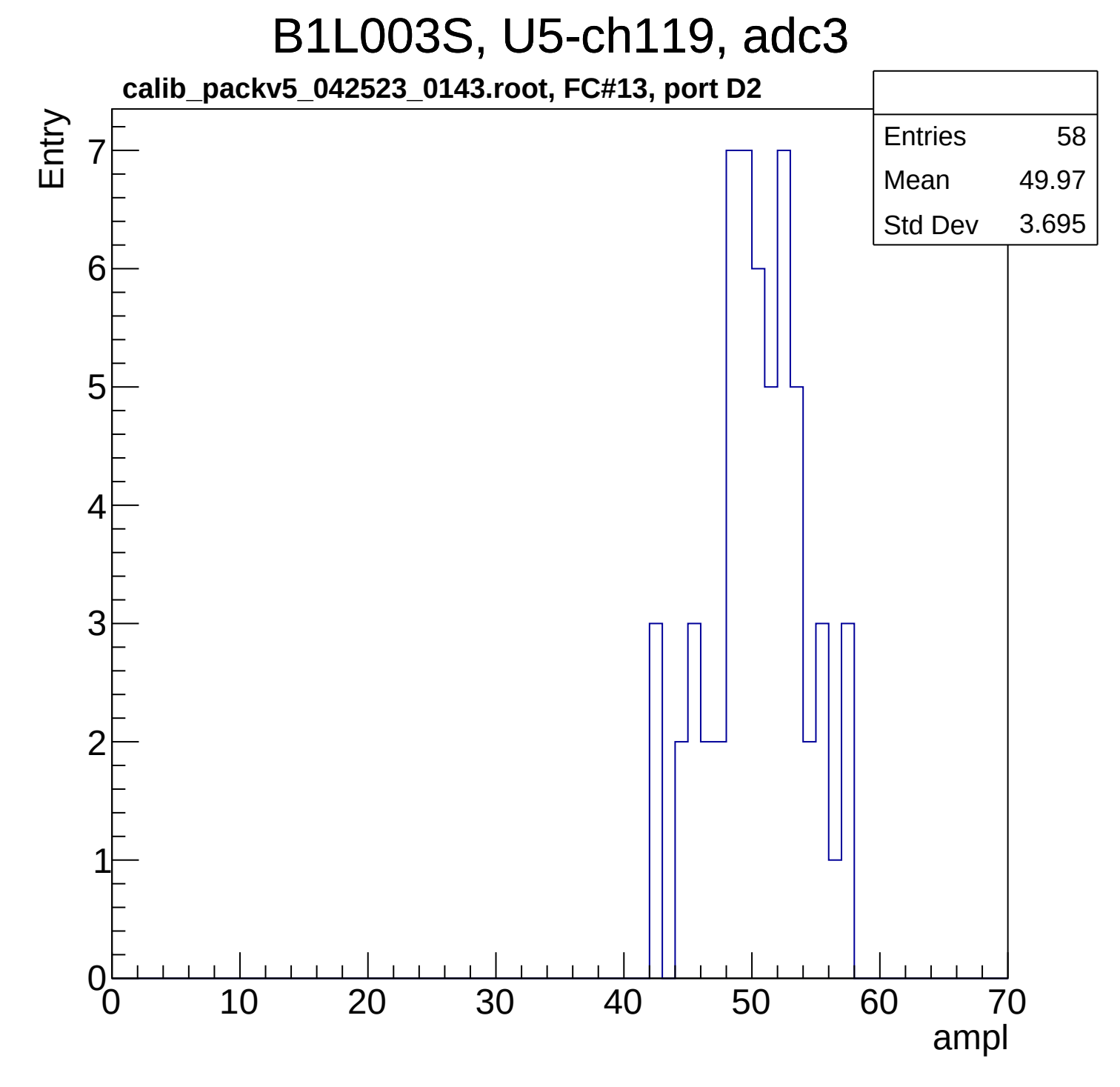
B1L003S, U5-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.97
Std Dev 3.695
Entry
ampl
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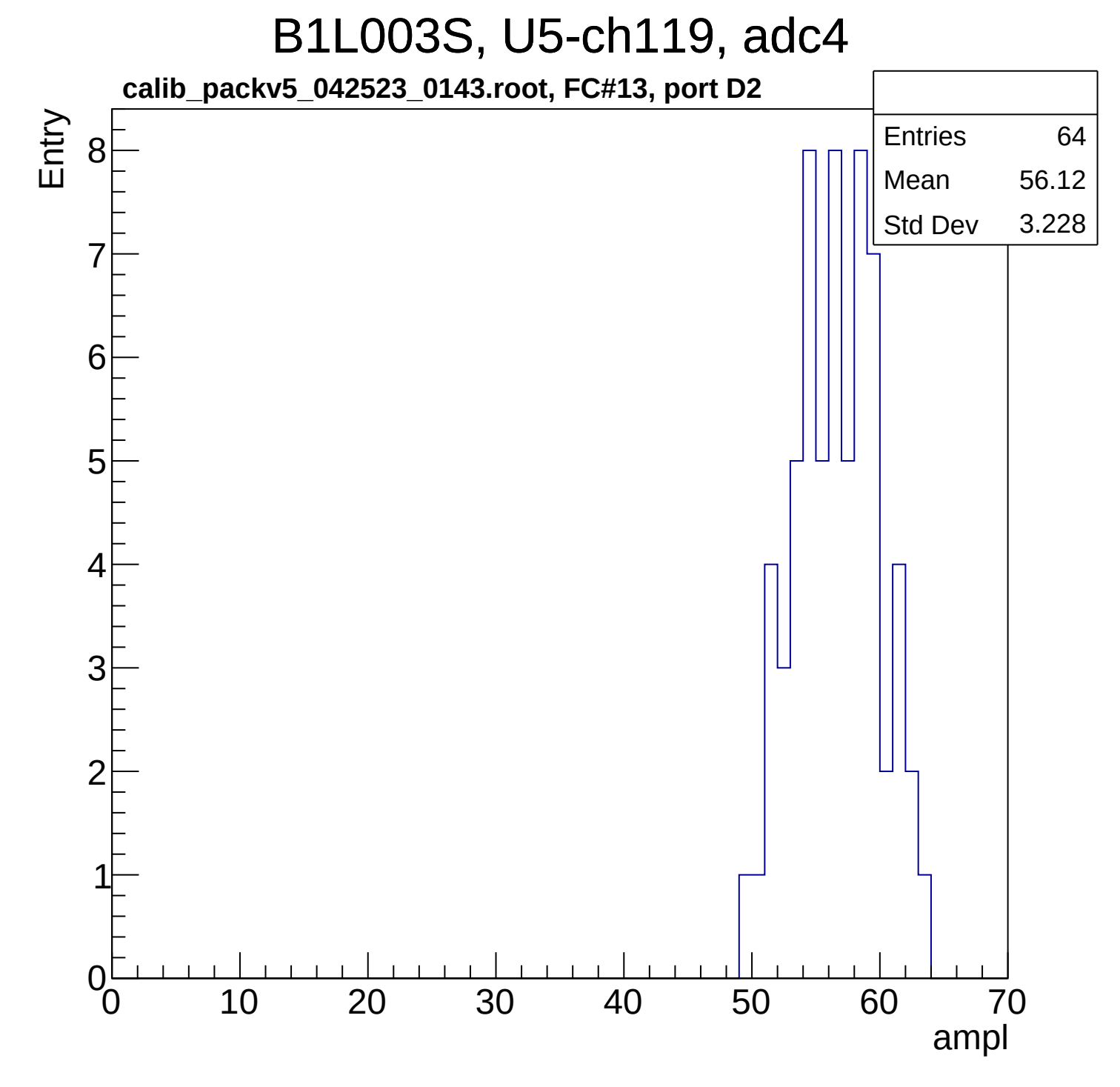
B1L003S, U5-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 56.12
Std Dev 3.228
Entry
ampl
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5
6
7
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0
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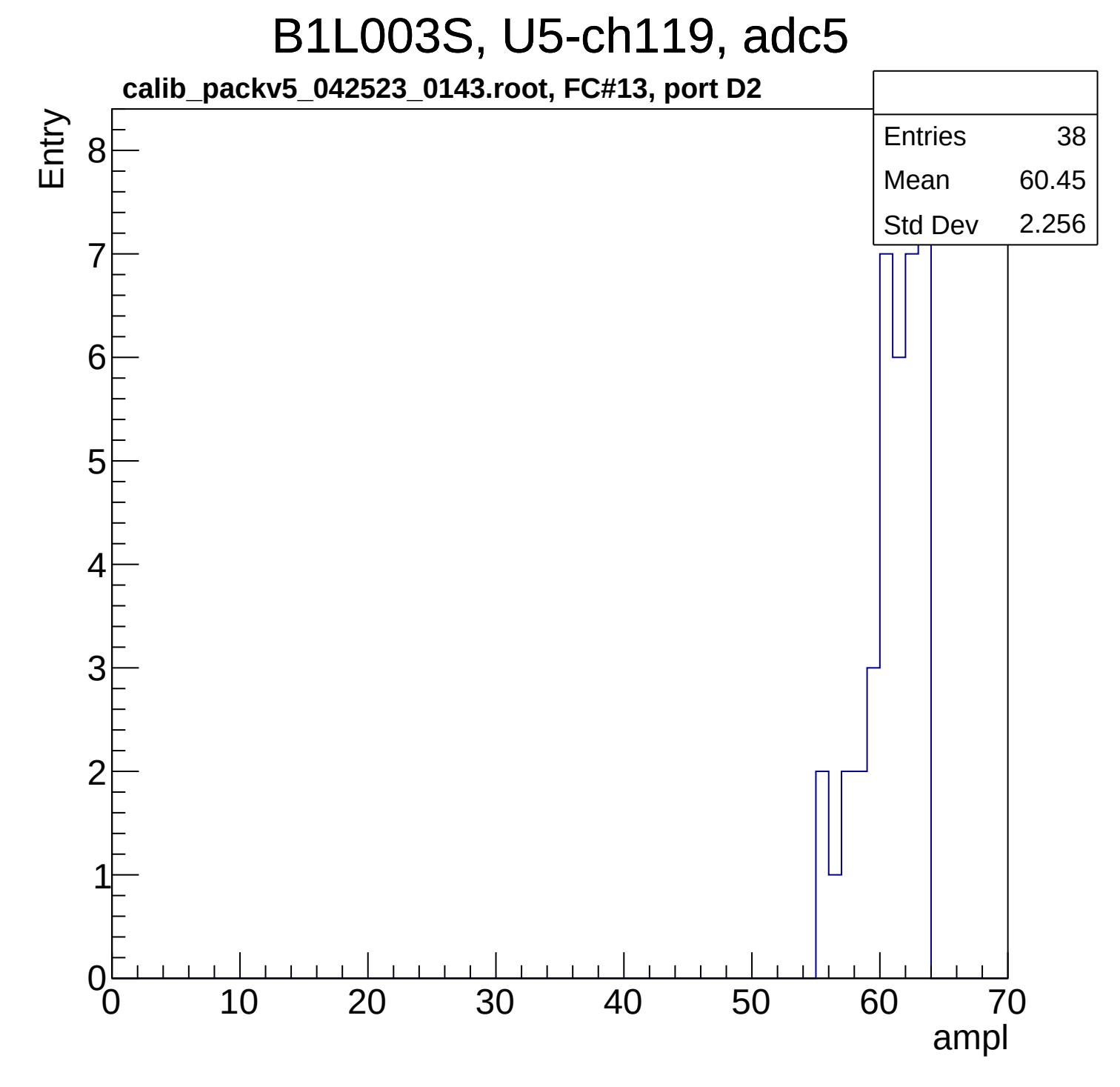
40
50
60
70


B1L003S, U5-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.45
Std Dev 2.256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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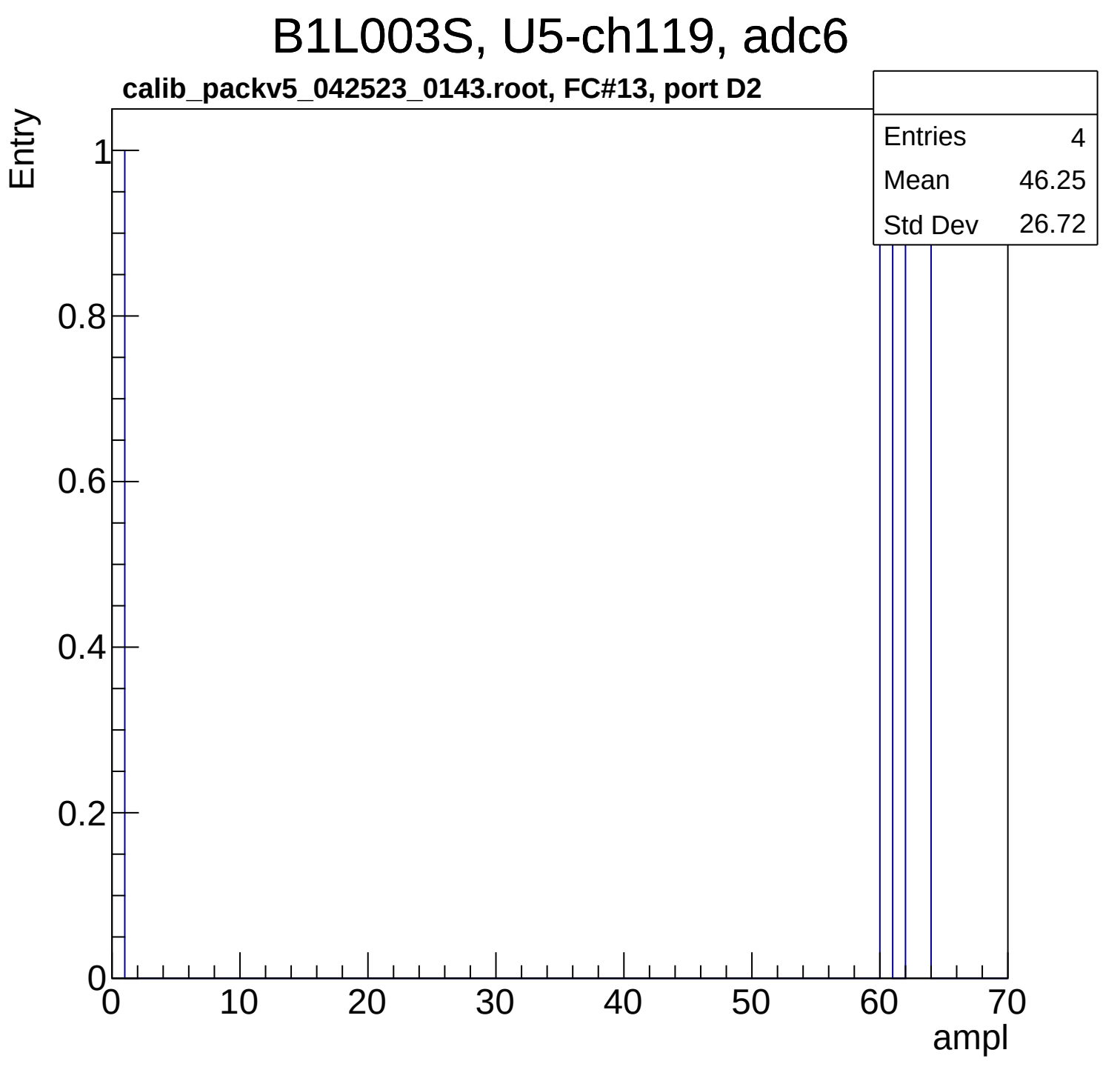
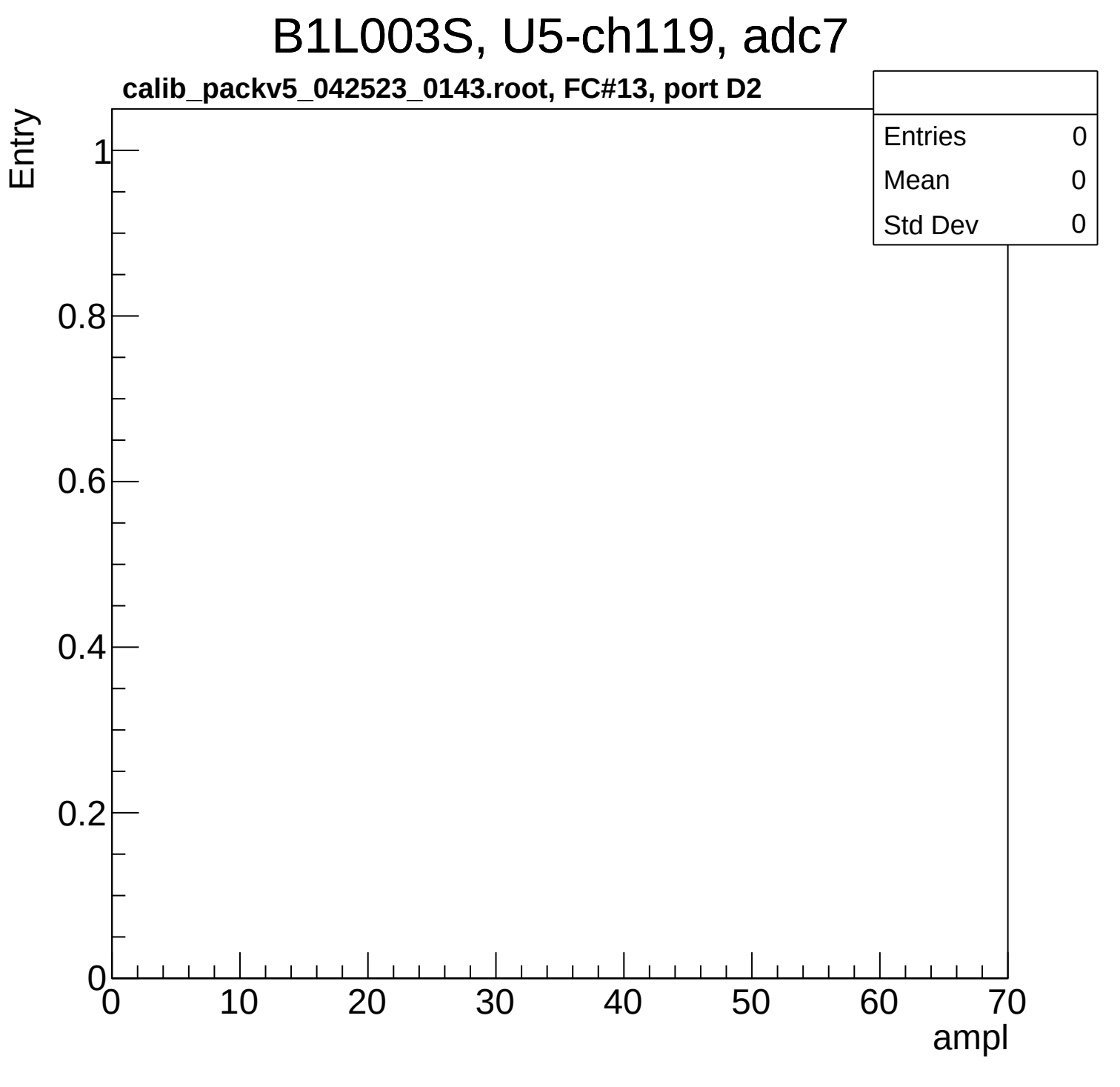

B1L003S, U5-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46.25
Std Dev 26.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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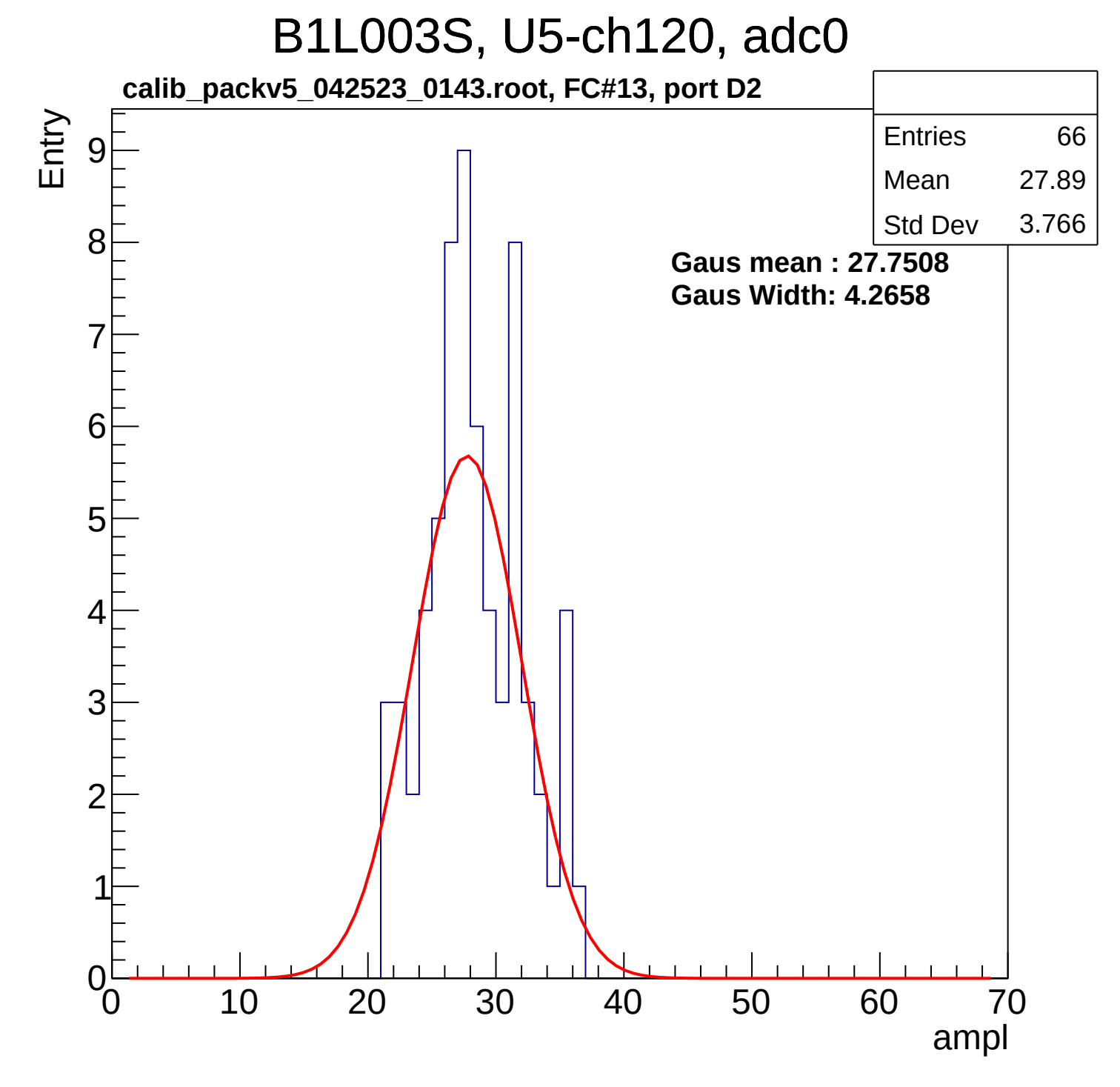
40
50
60
70
ampl


B1L003S, U5-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 27.89
Std Dev 3.766
Gaus mean : 27.7508
Gaus Width: 4.2658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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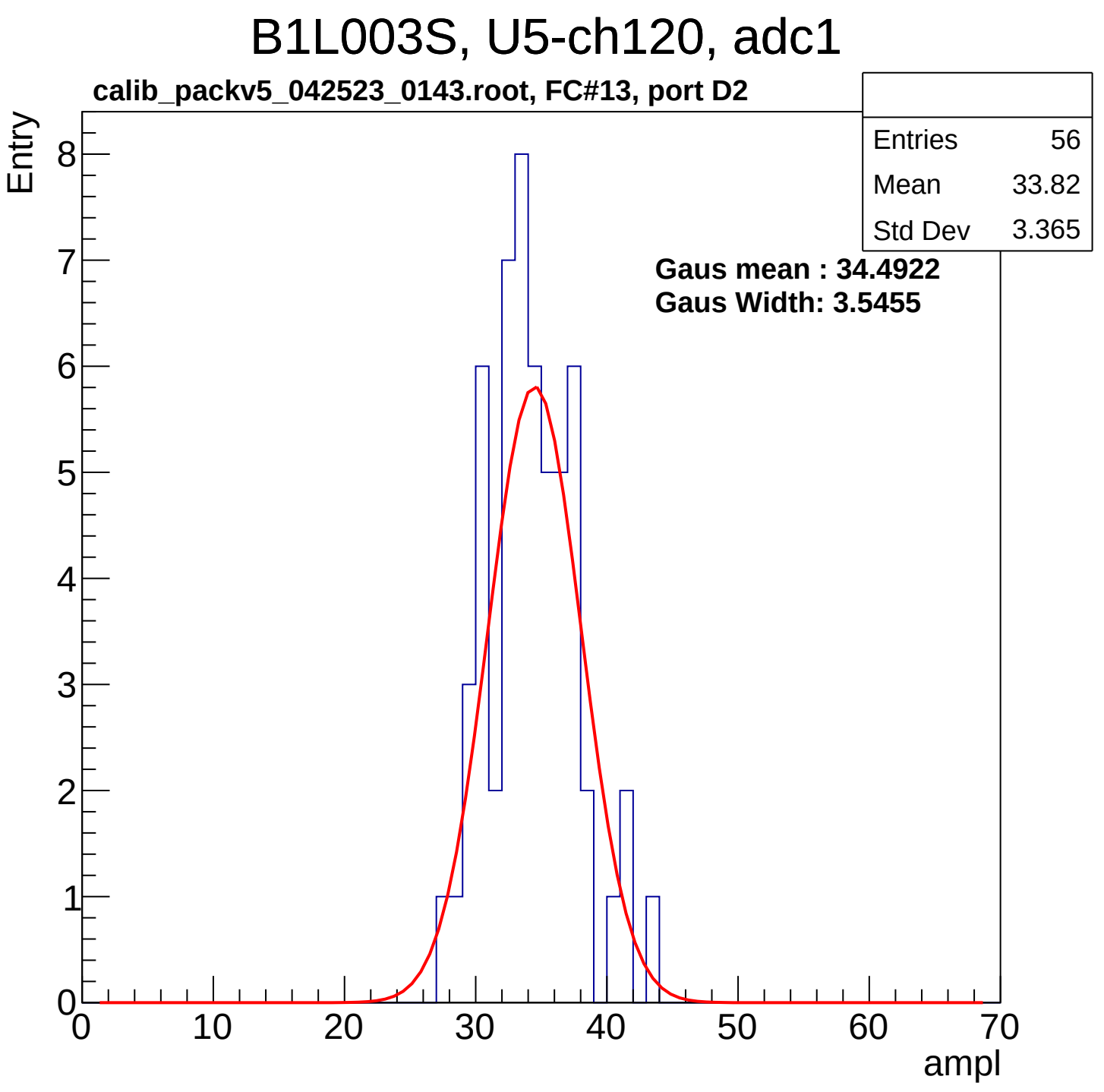
B1L003S, U5-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 33.82
Std Dev 3.365
Gaus mean : 34.4922
Gaus Width: 3.5455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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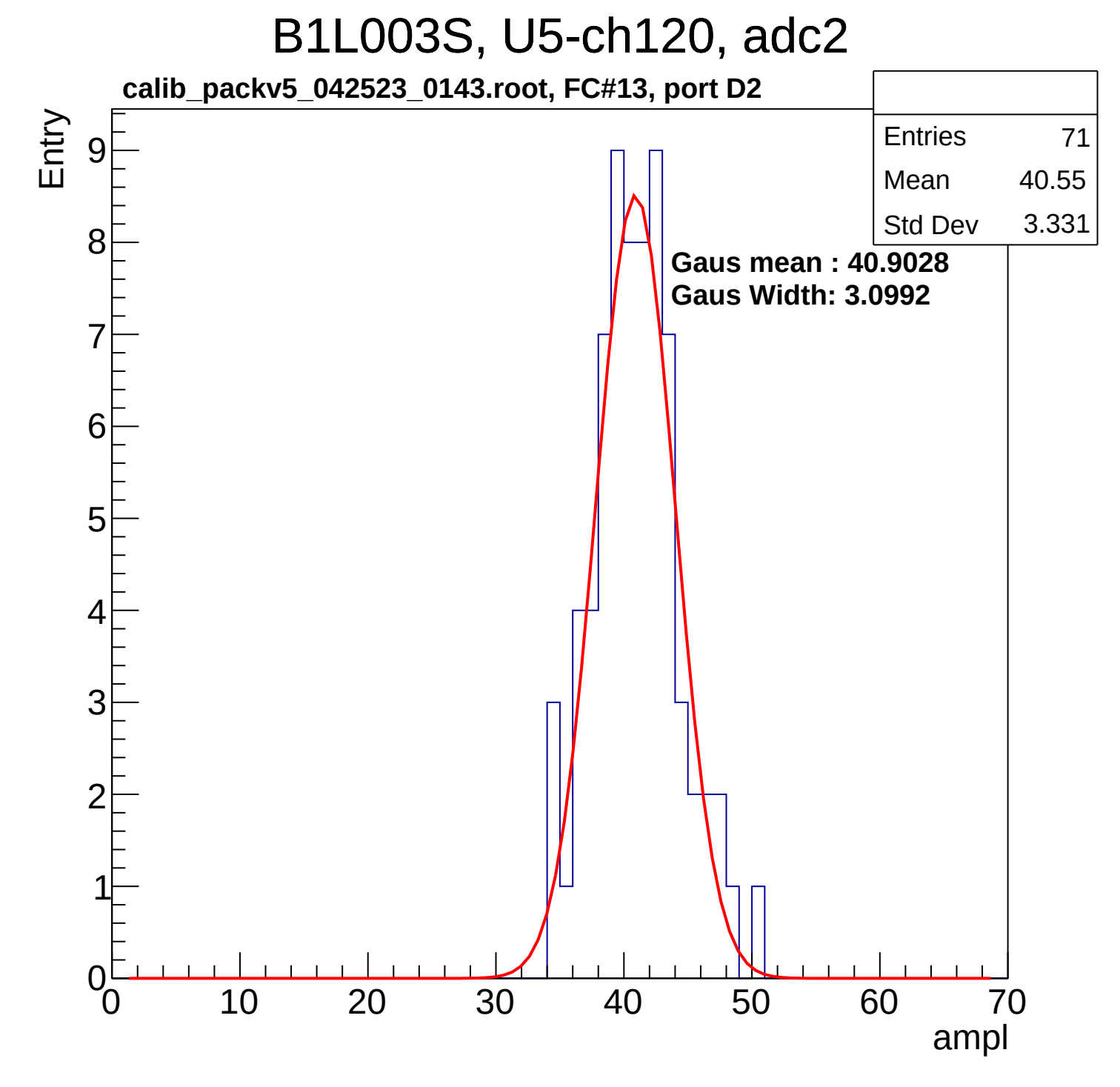
B1L003S, U5-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 40.55
Std Dev 3.331
Gaus mean : 40.9028
Gaus Width: 3.0992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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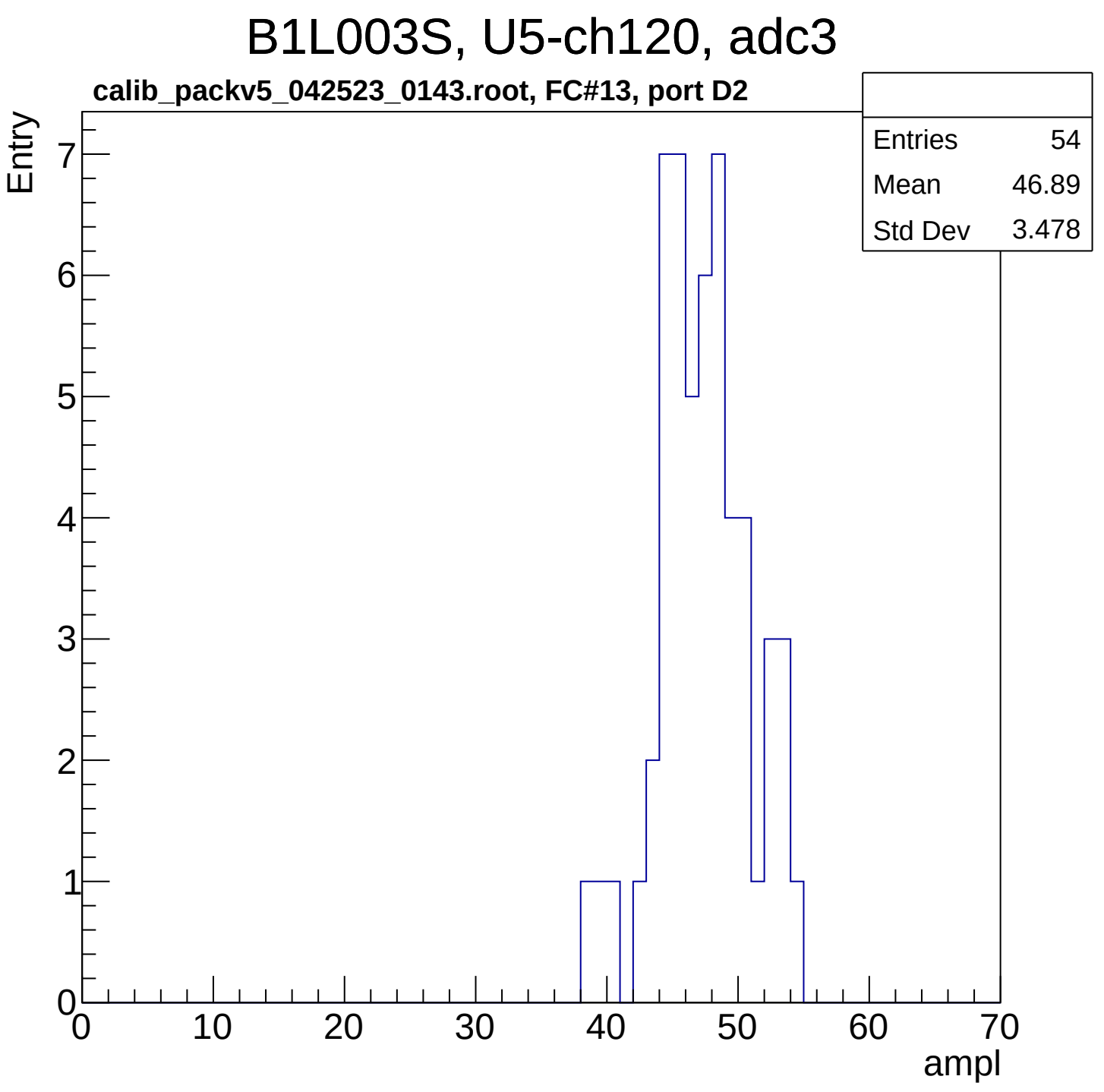
B1L003S, U5-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 46.89
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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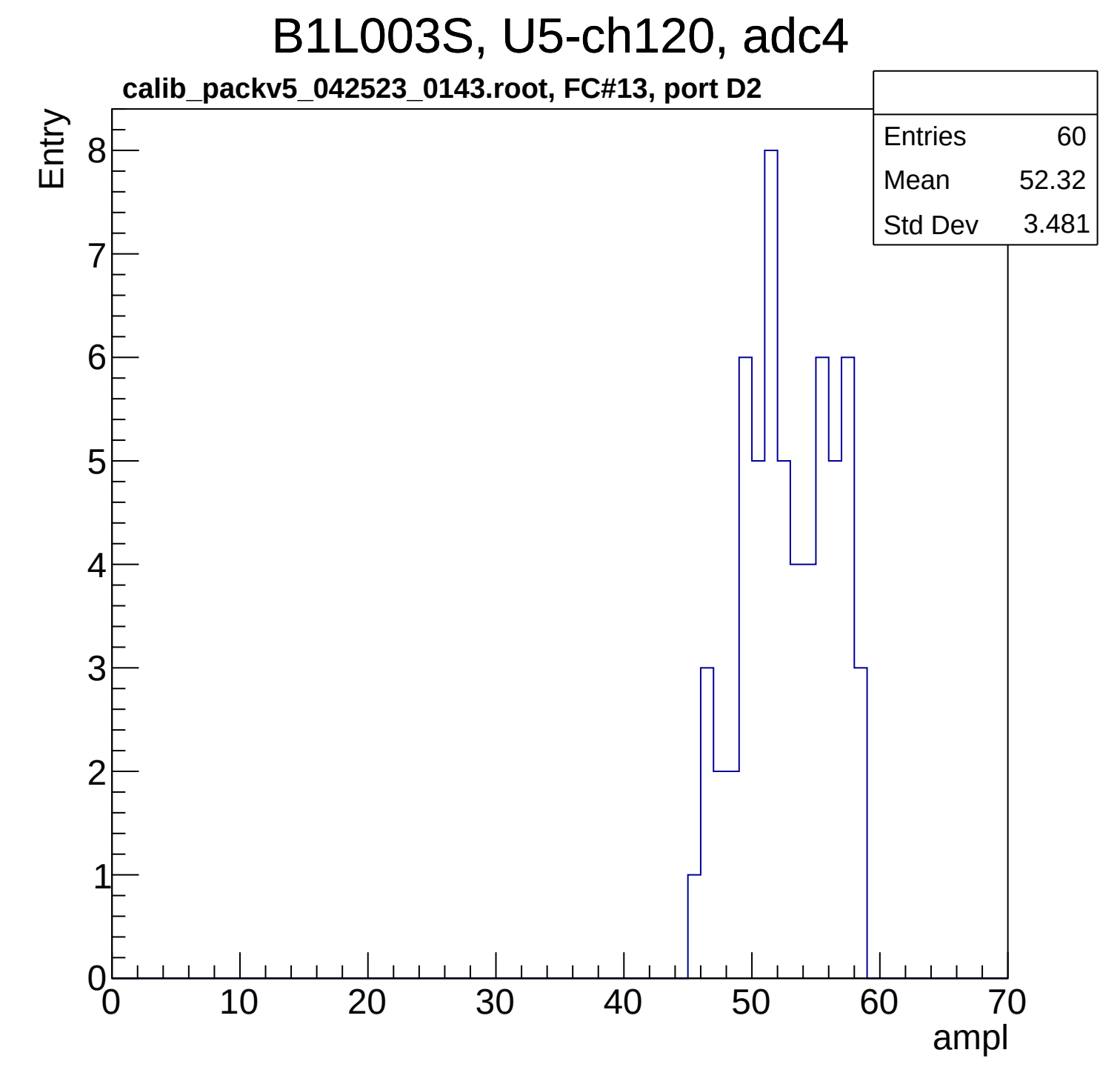
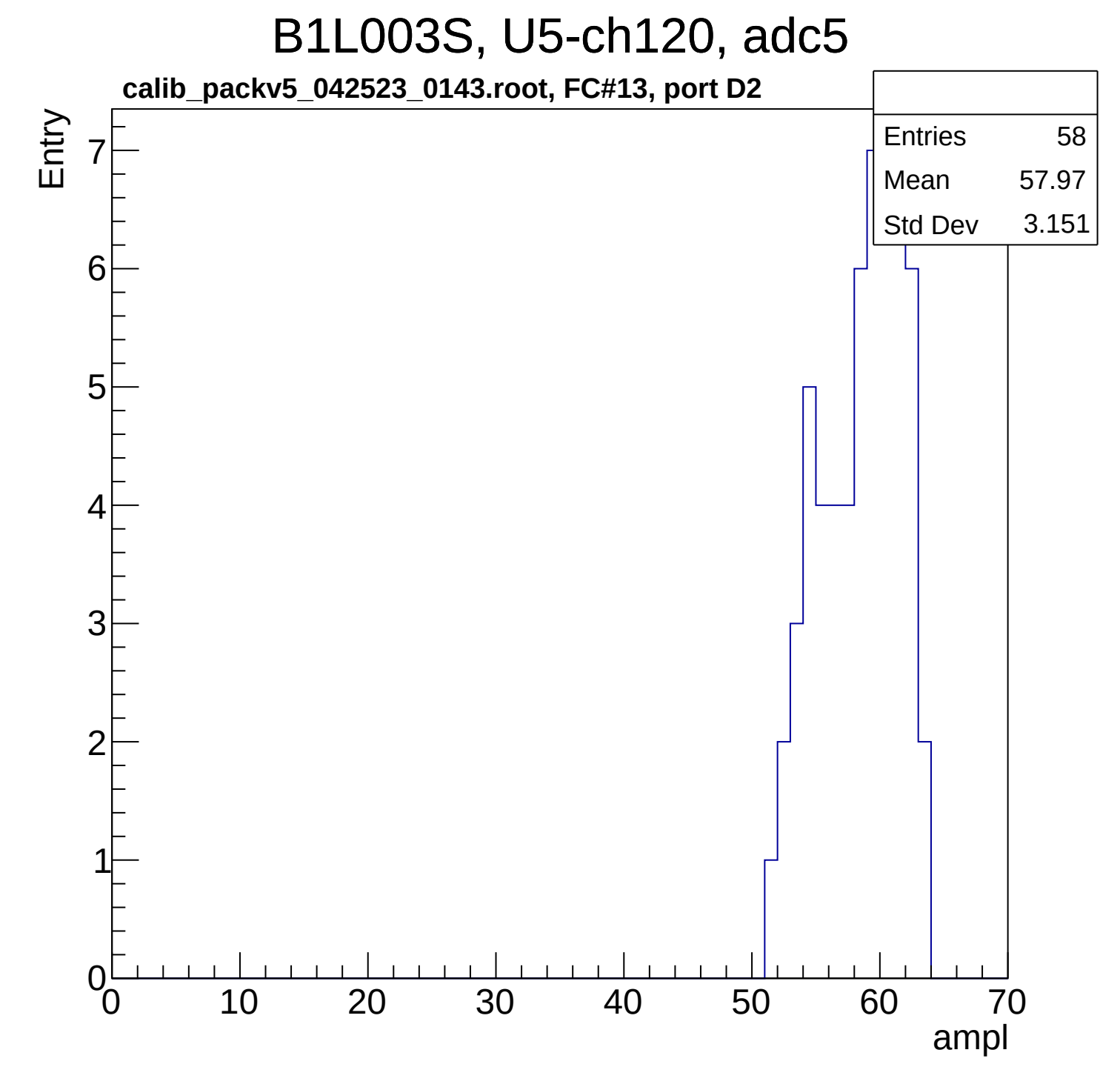

B1L003S, U5-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 52.32
Std Dev 3.481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.97
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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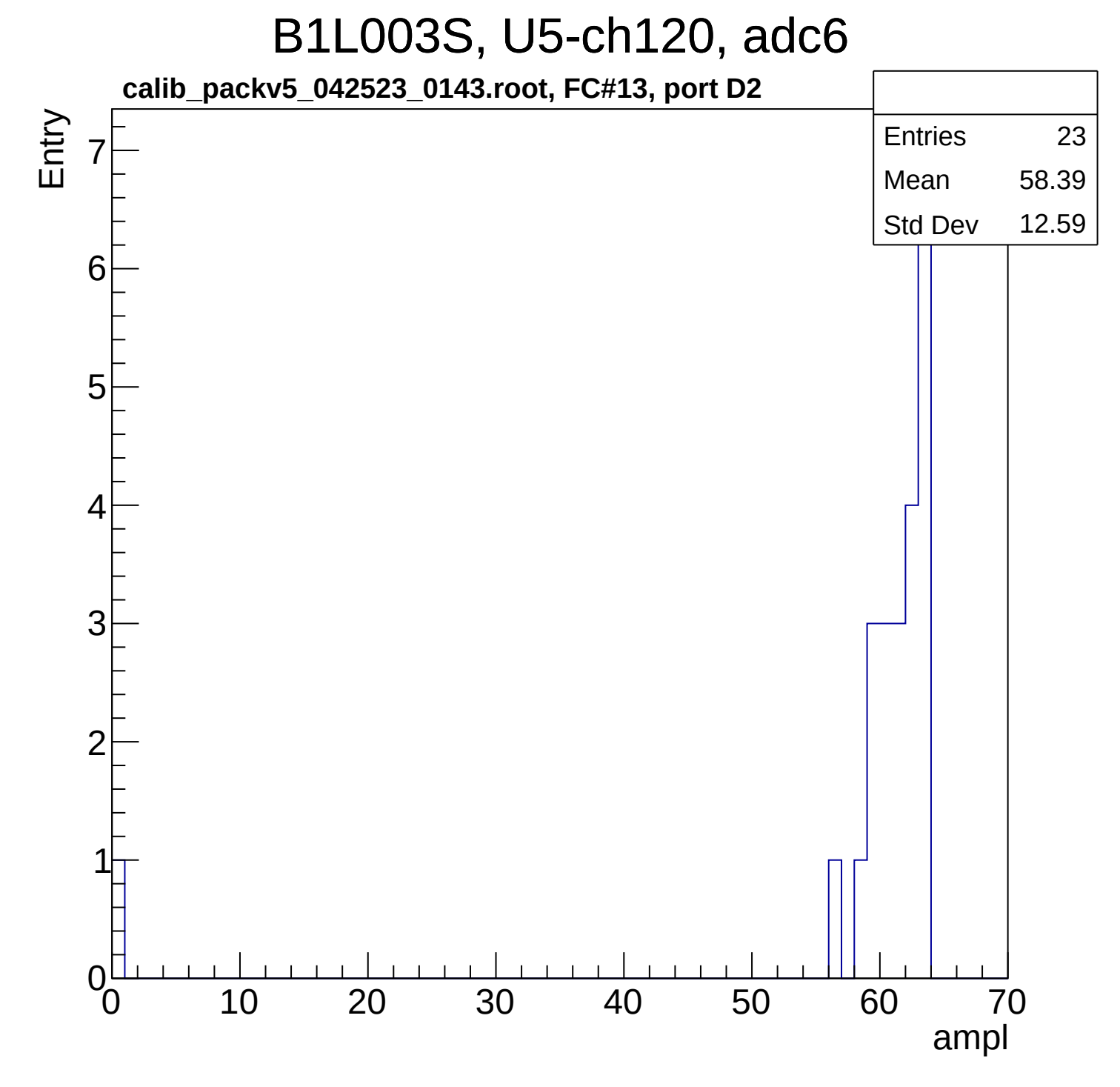
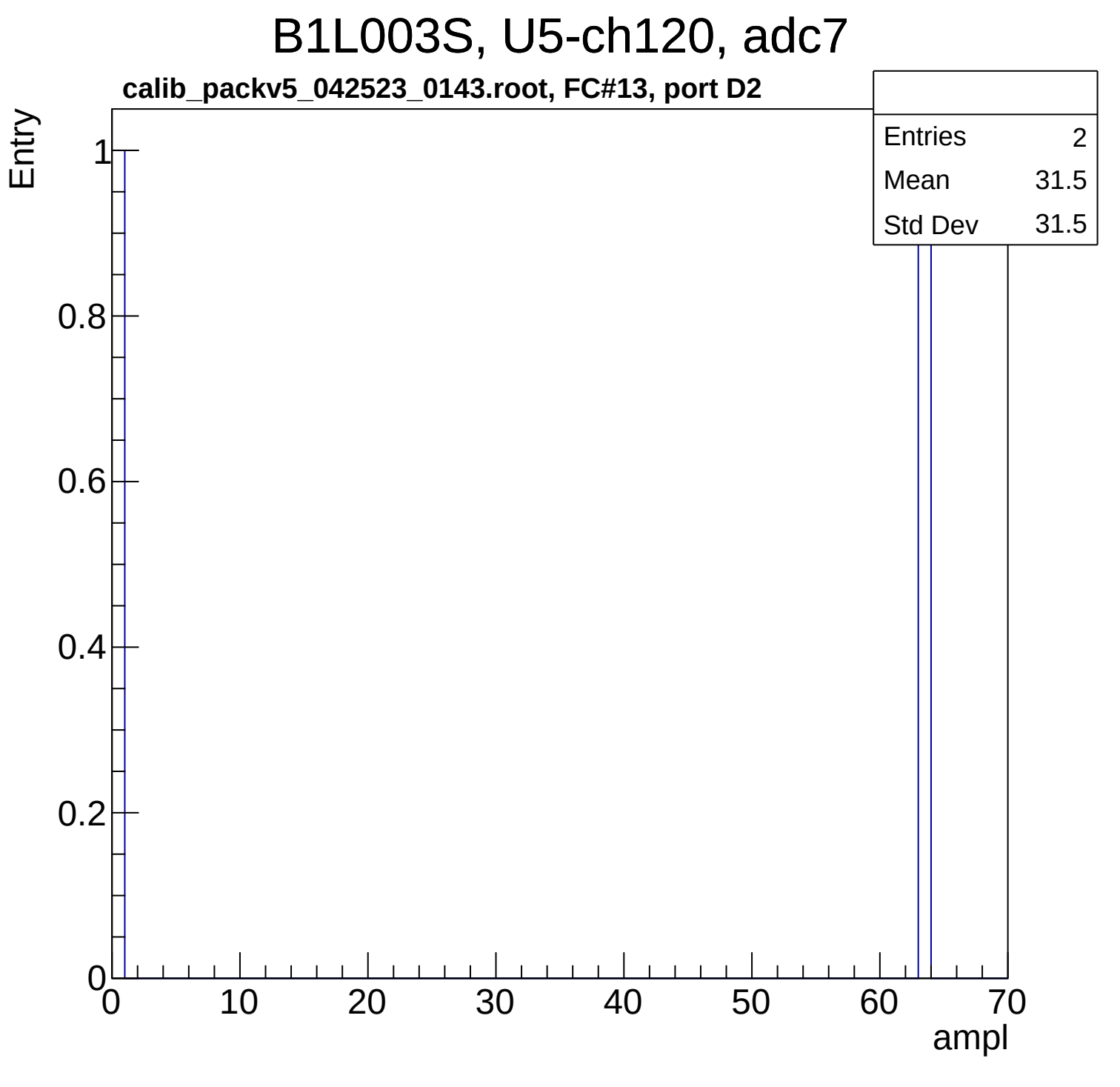
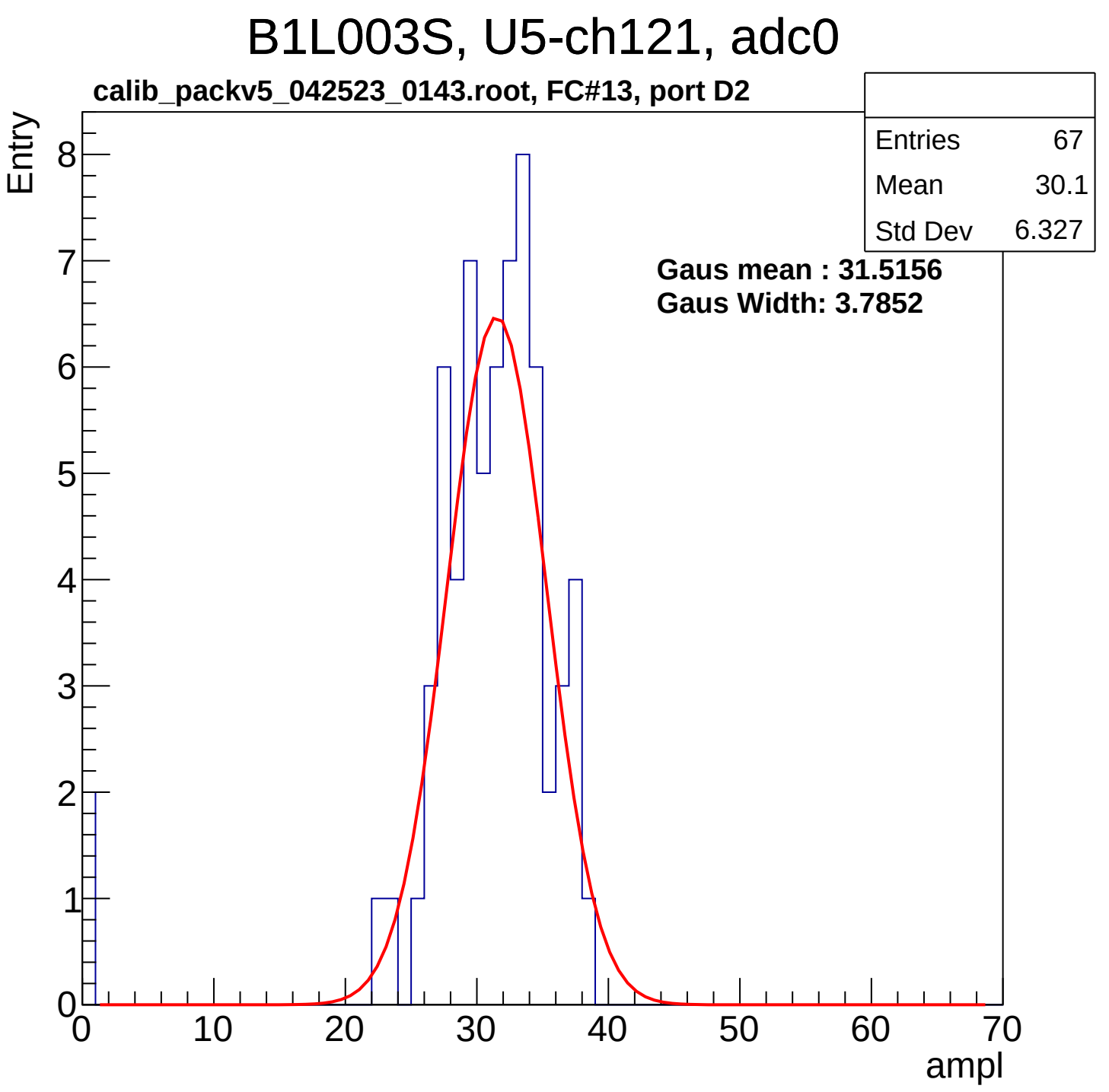

B1L003S, U5-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 58.39
Std Dev 12.59
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.1
Std Dev 6.327
Gaus mean : 31.5156
Gaus Width: 3.7852
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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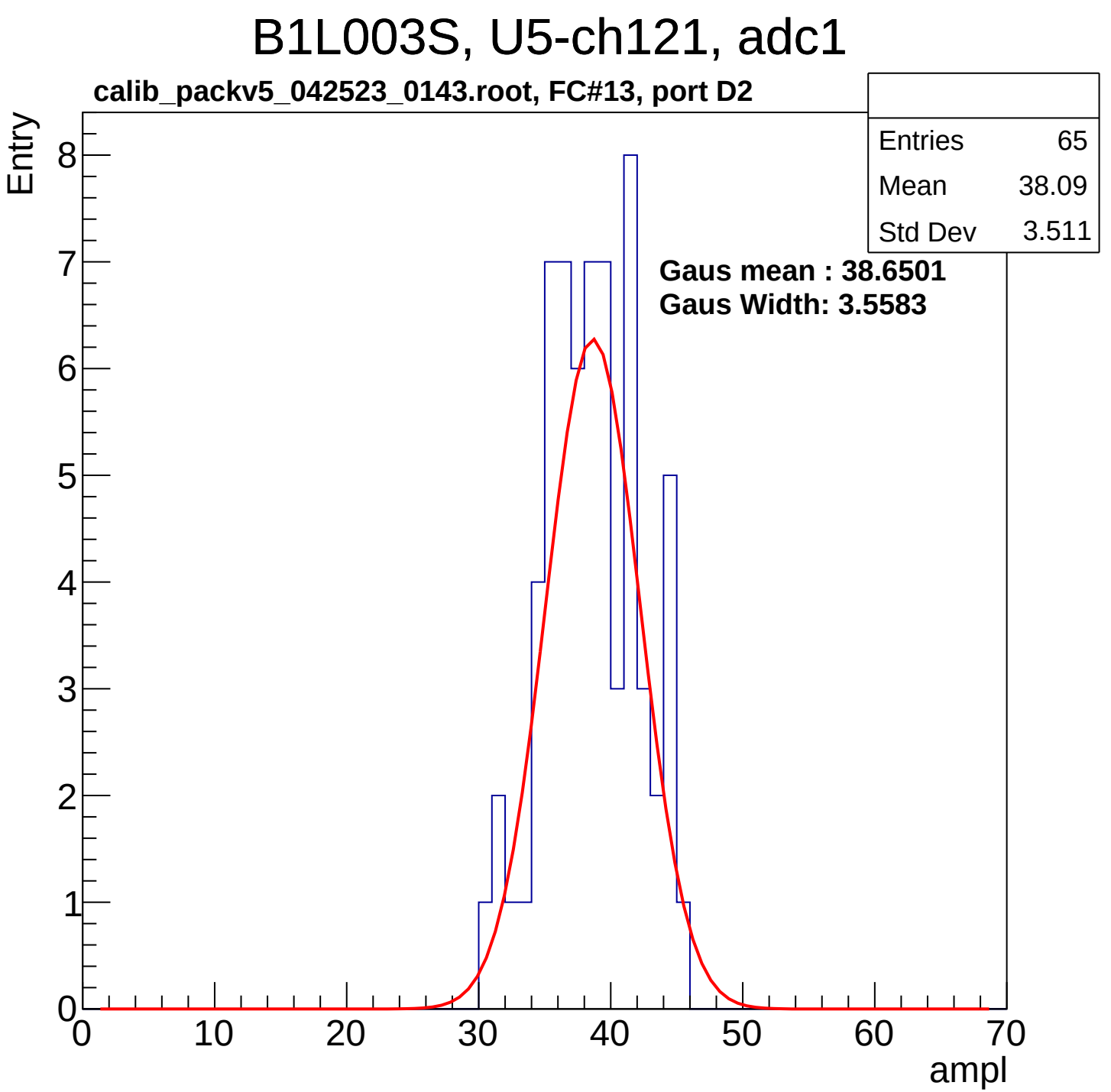
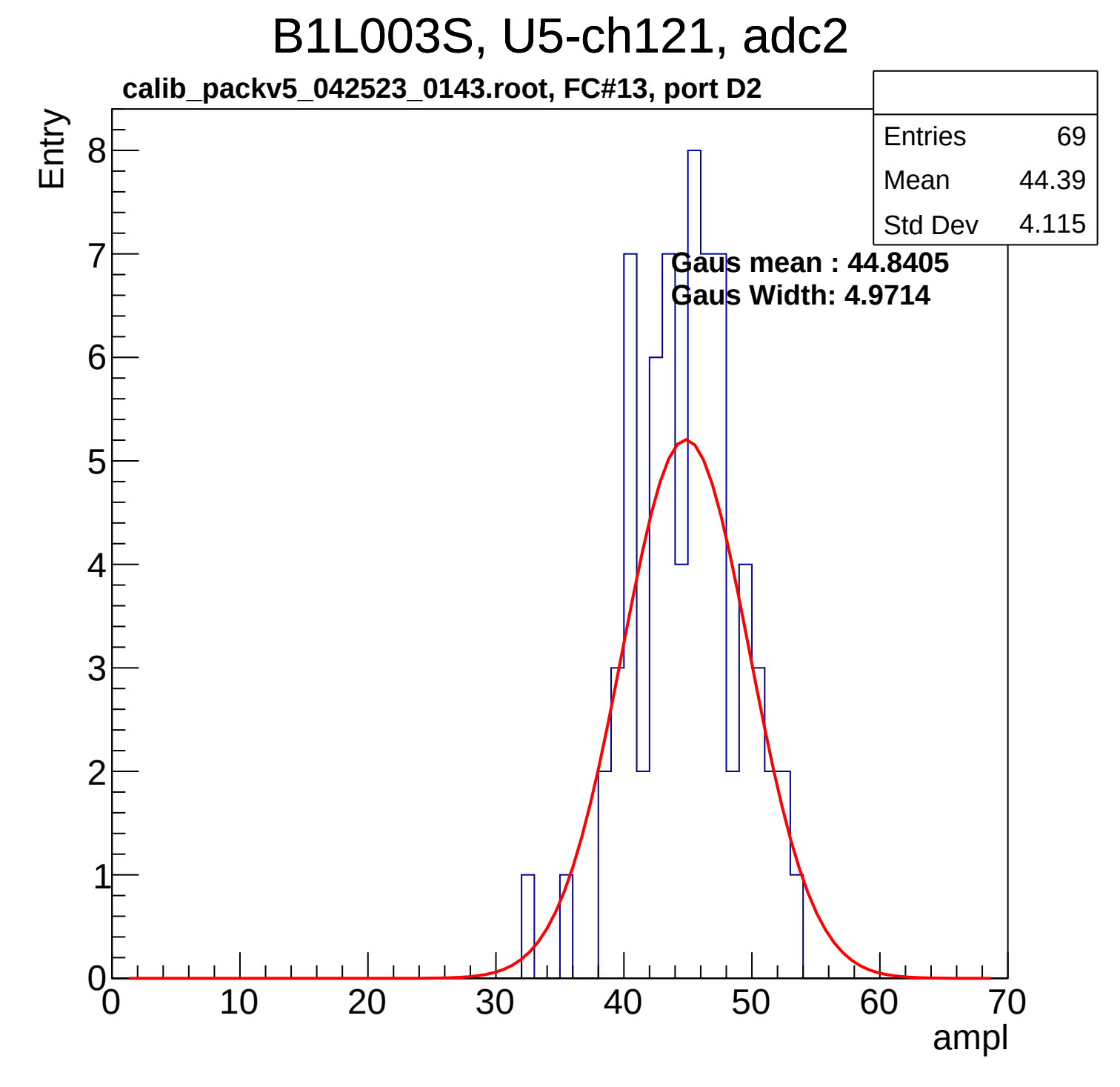
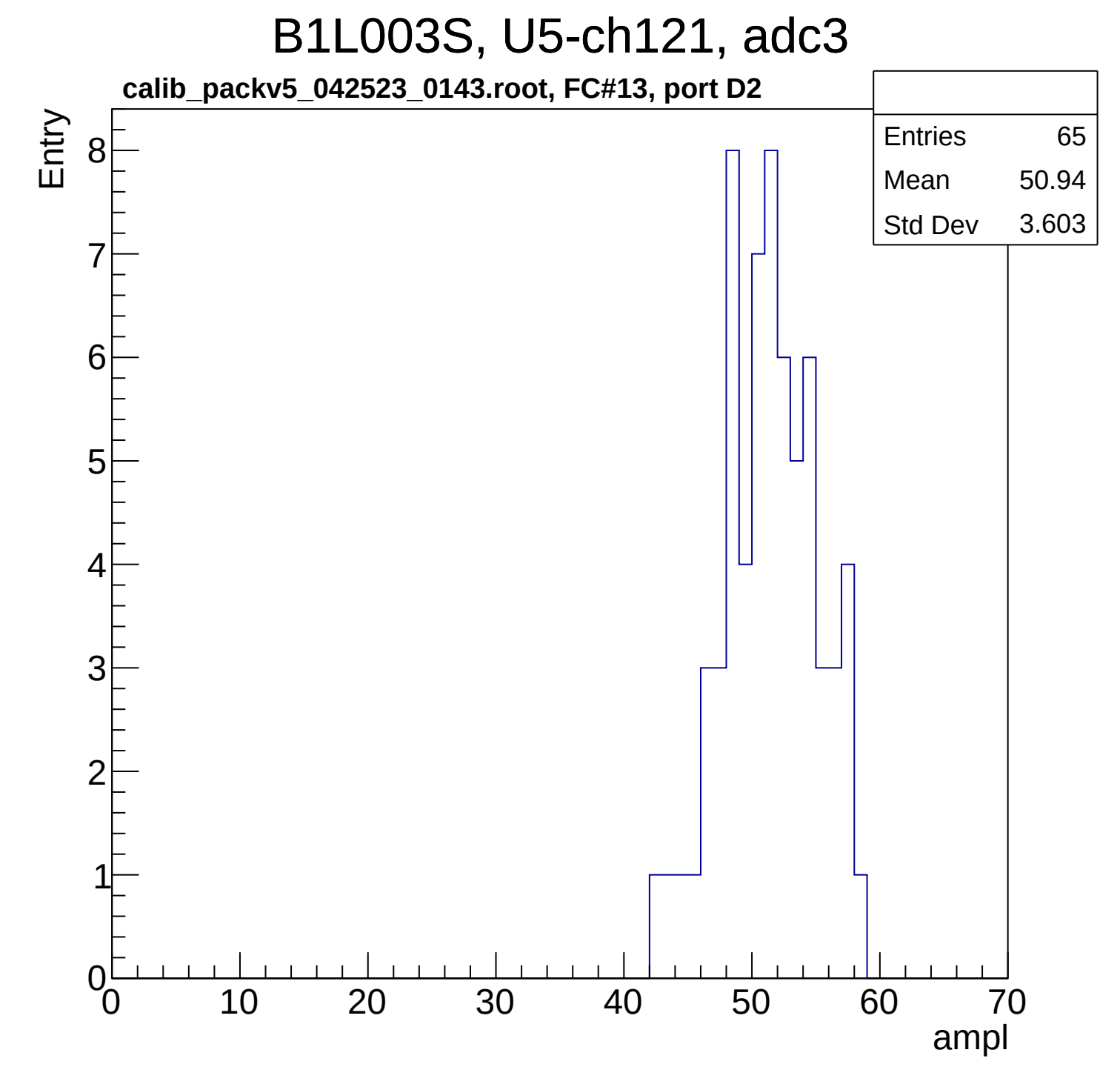
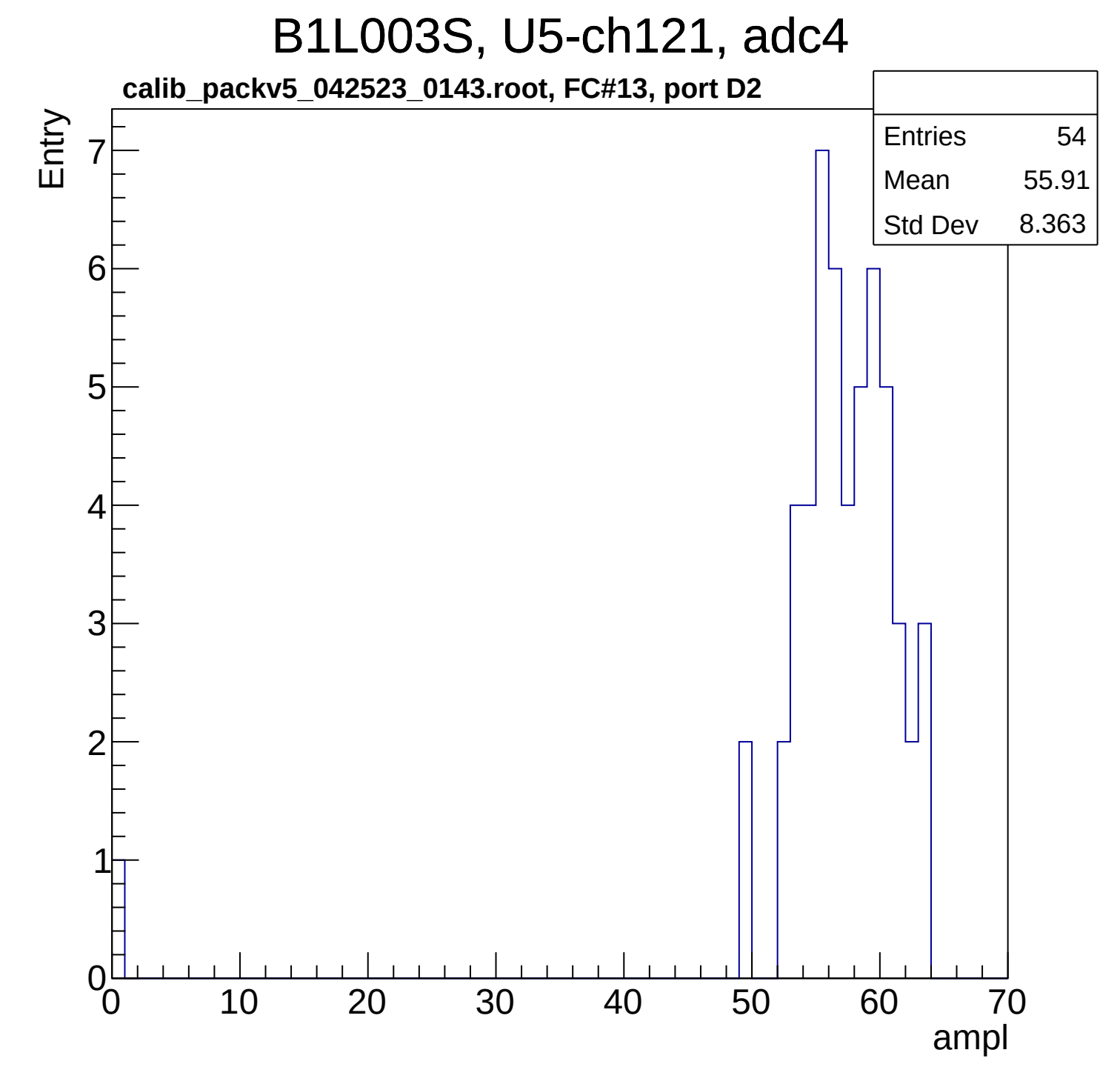
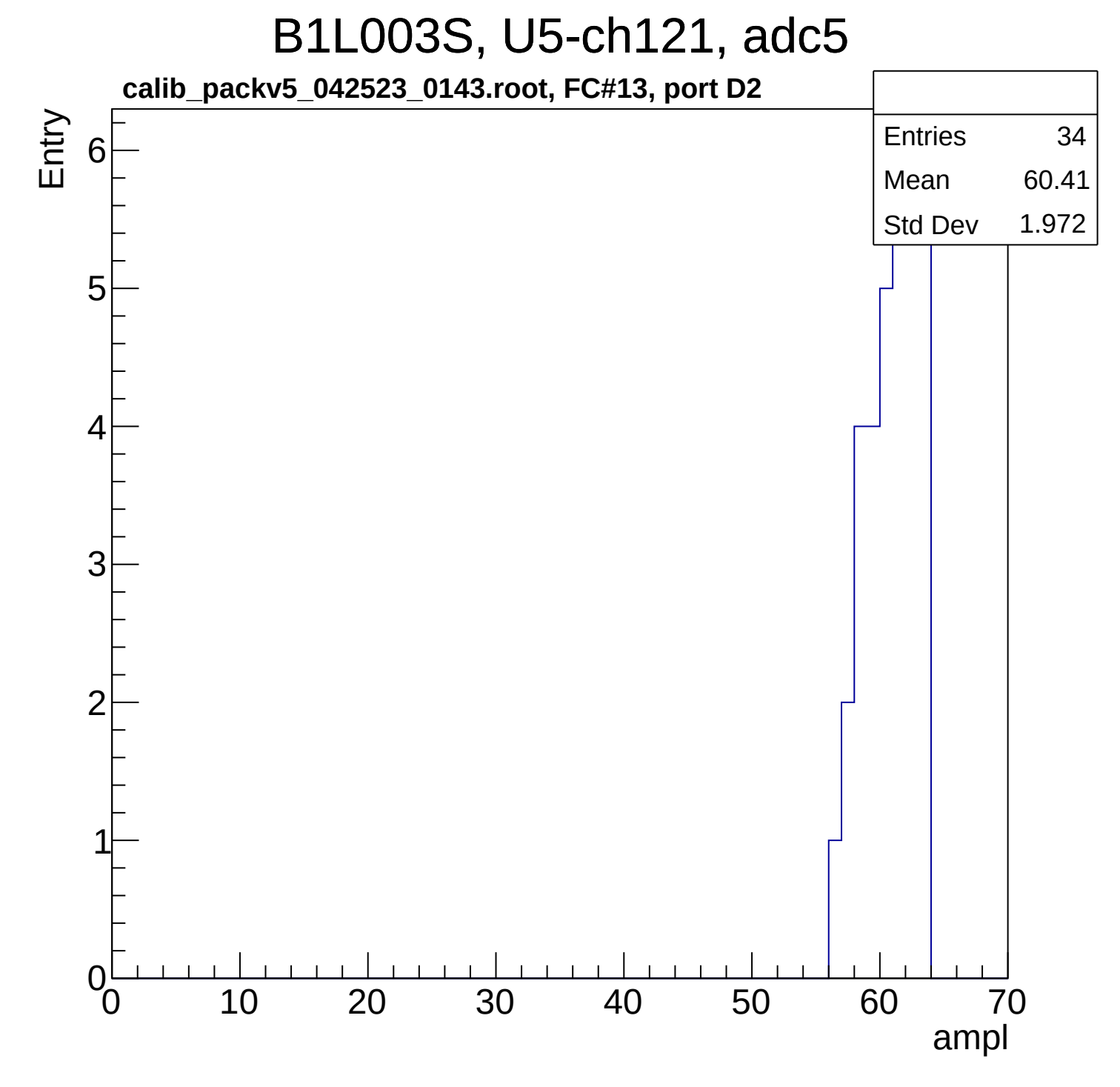
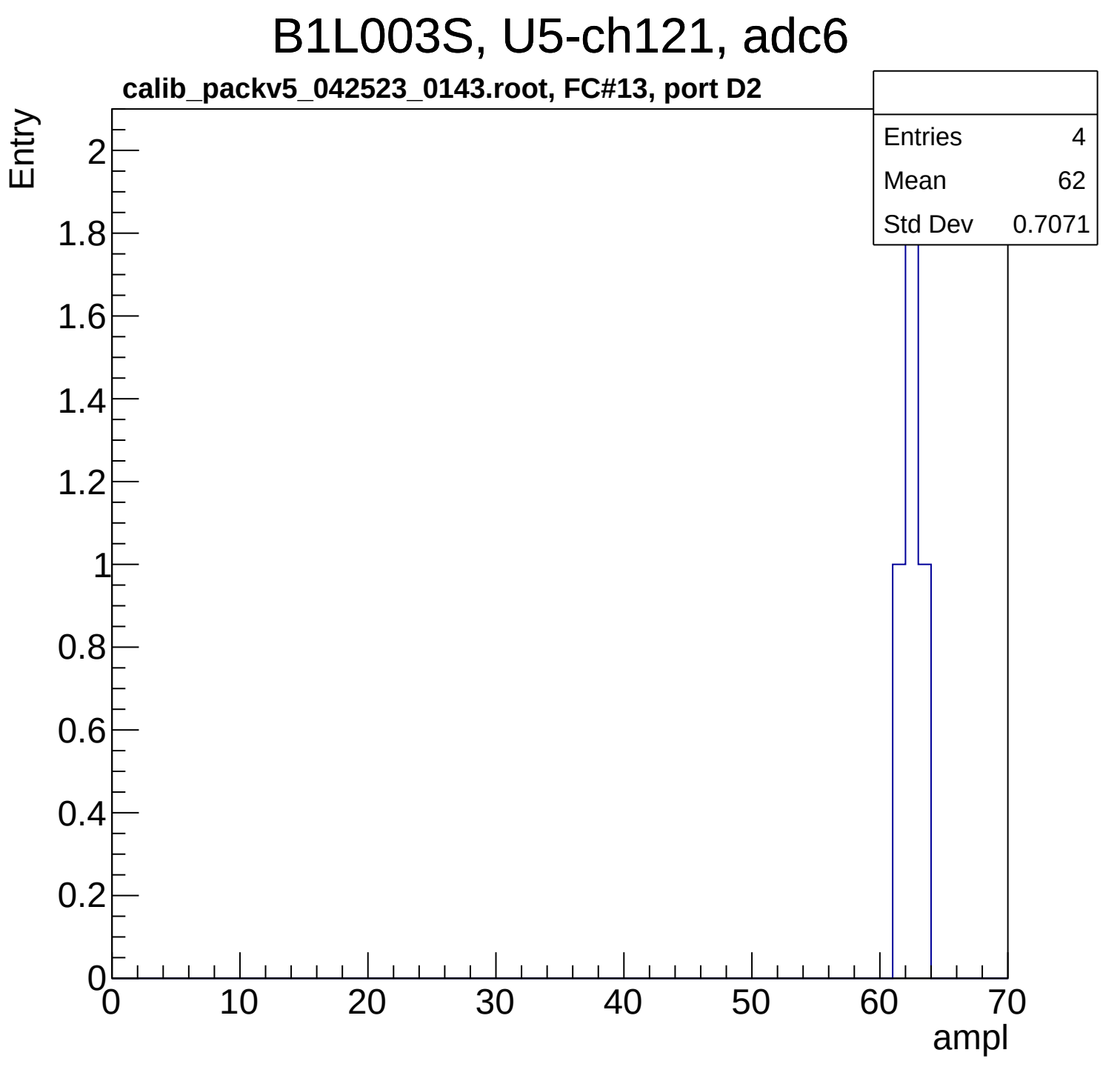

B1L003S, U5-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 38.09
Std Dev 3.511
Gaus mean : 38.6501
Gaus Width: 3.5583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

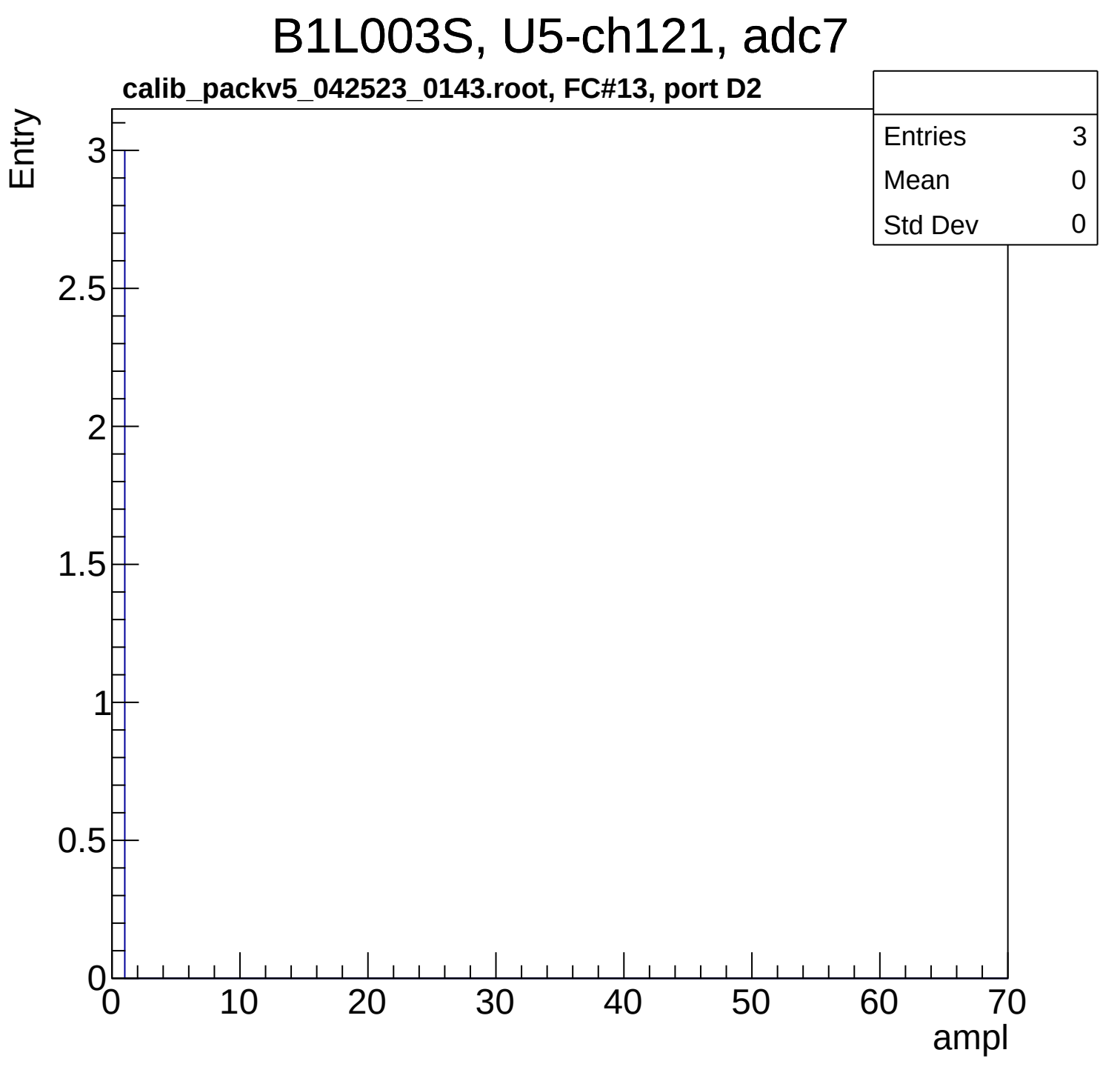

B1L003S, U5-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 44.39
Std Dev 4.115
Gaus mean : 44.8405
Gaus Width: 4.9714
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.94
Std Dev 3.603
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U5-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.91
Std Dev 8.363
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 60.41
Std Dev 1.972
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U5-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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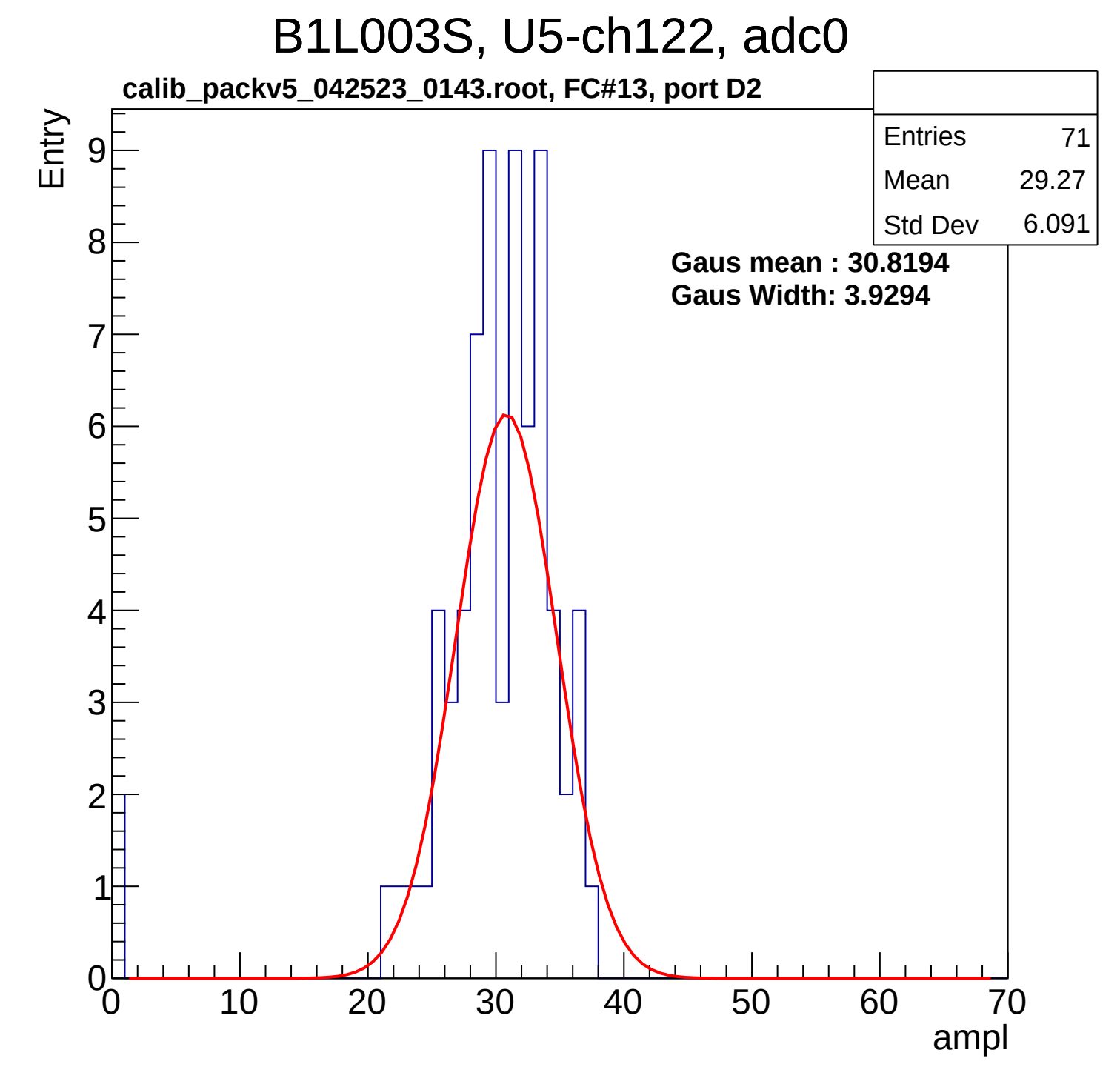
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.27
Std Dev 6.091
Gaus mean : 30.8194
Gaus Width: 3.9294
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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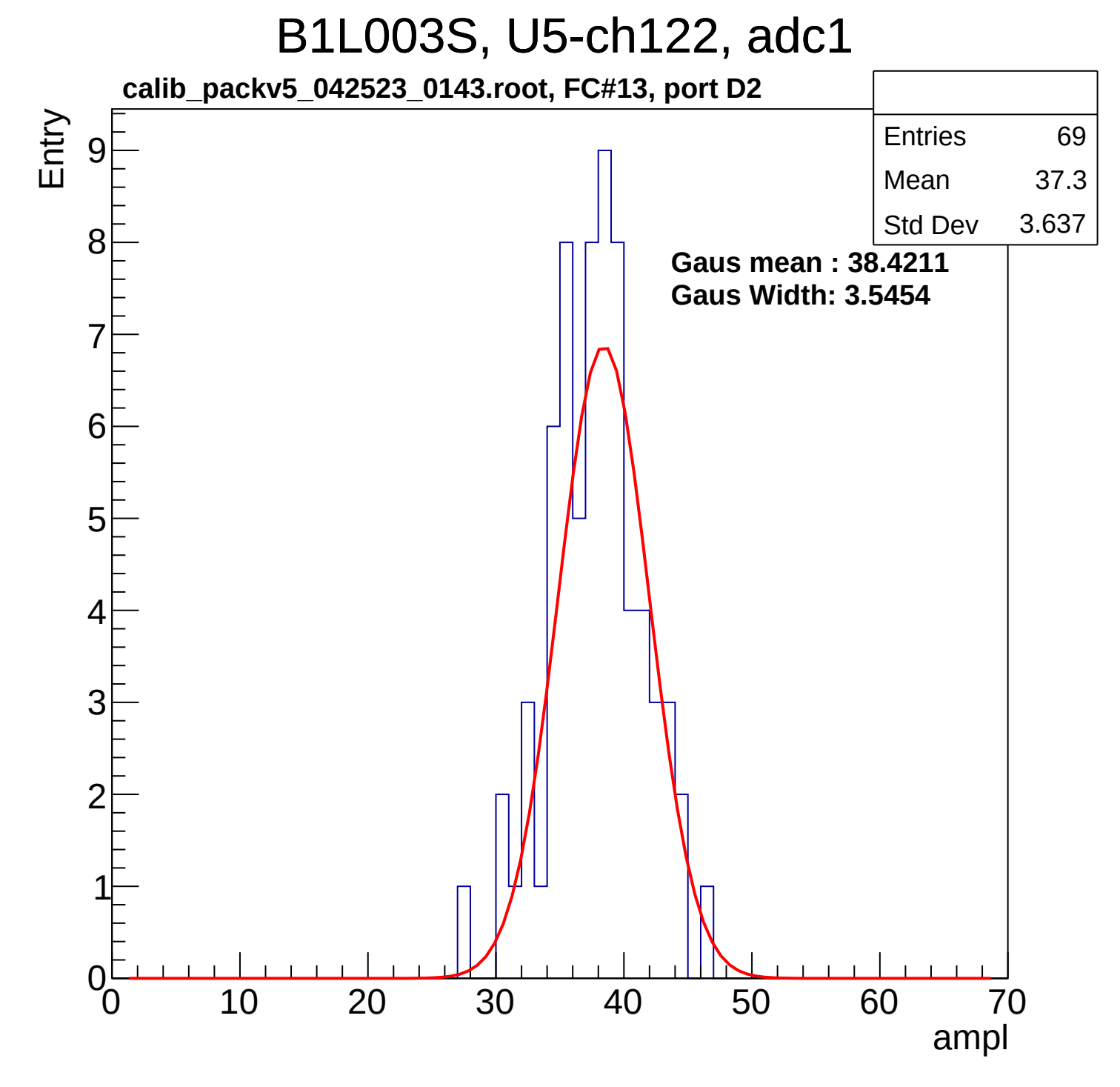
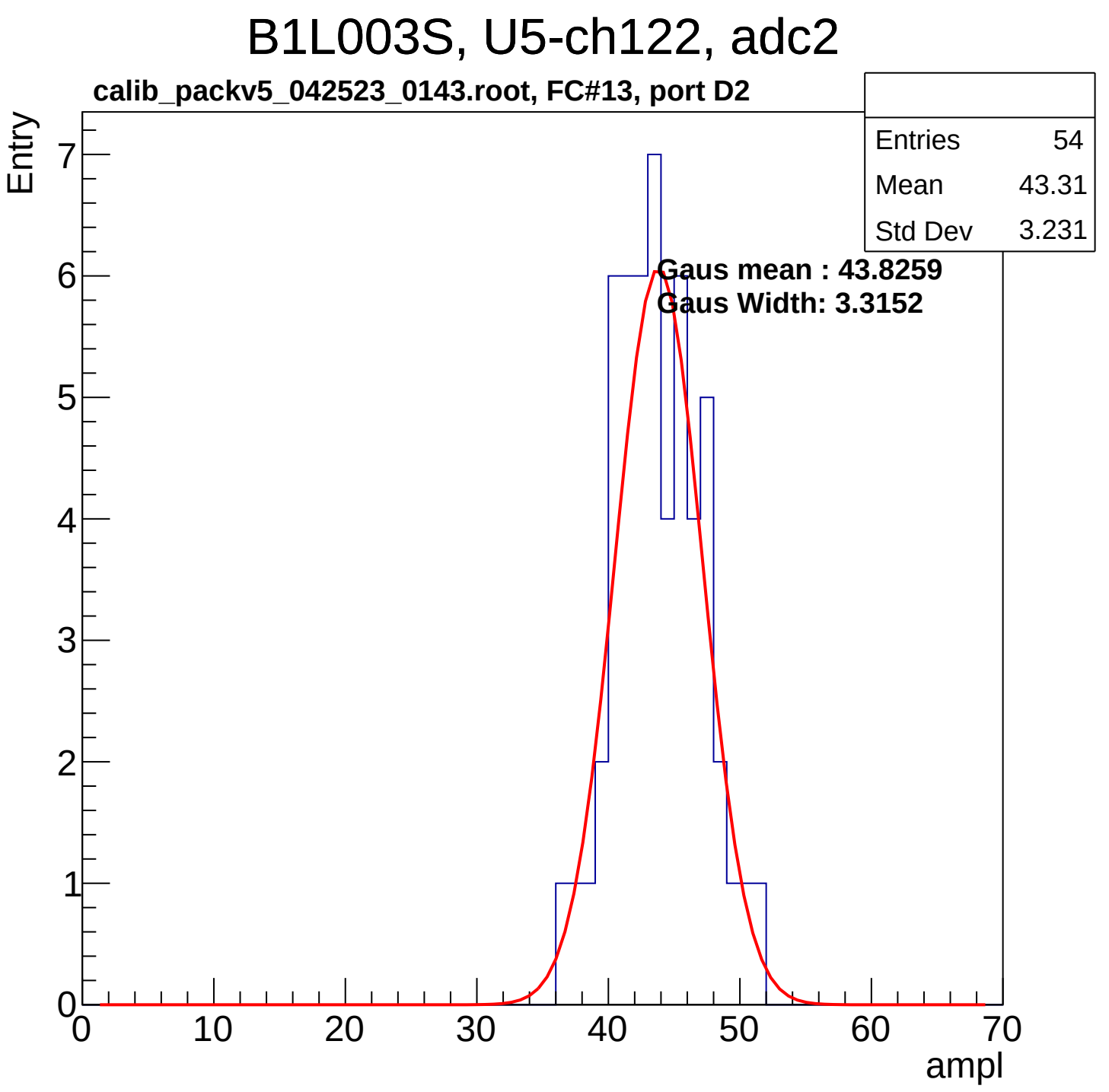
20
30
40
50
60
70


B1L003S, U5-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 37.3
Std Dev 3.637
Gaus mean : 38.4211
Gaus Width: 3.5454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.31
Std Dev 3.231
Gaus mean : 43.8259
Gaus Width: 3.3152
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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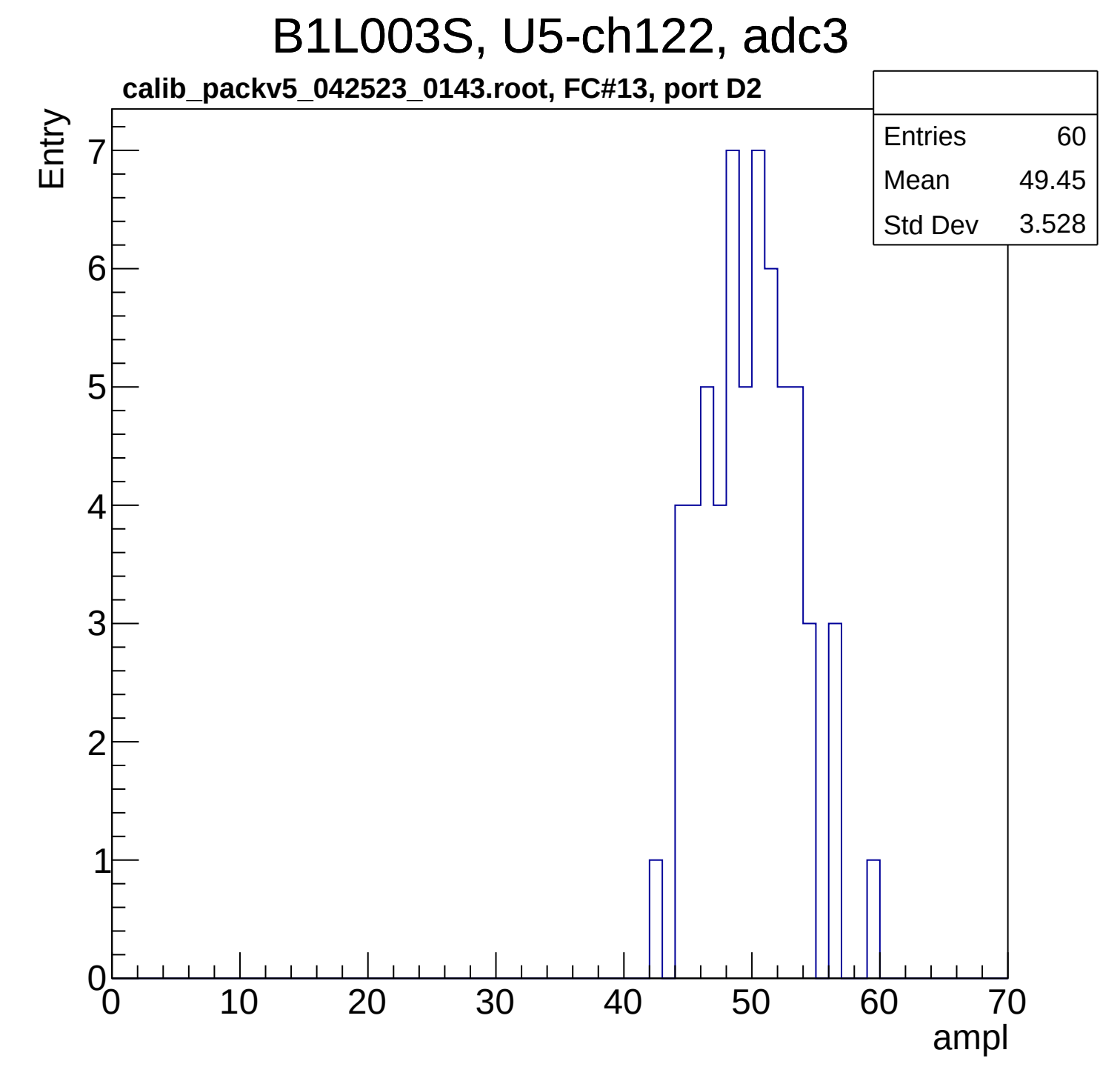
B1L003S, U5-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.45
Std Dev 3.528
Entry
ampl
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0
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50
60
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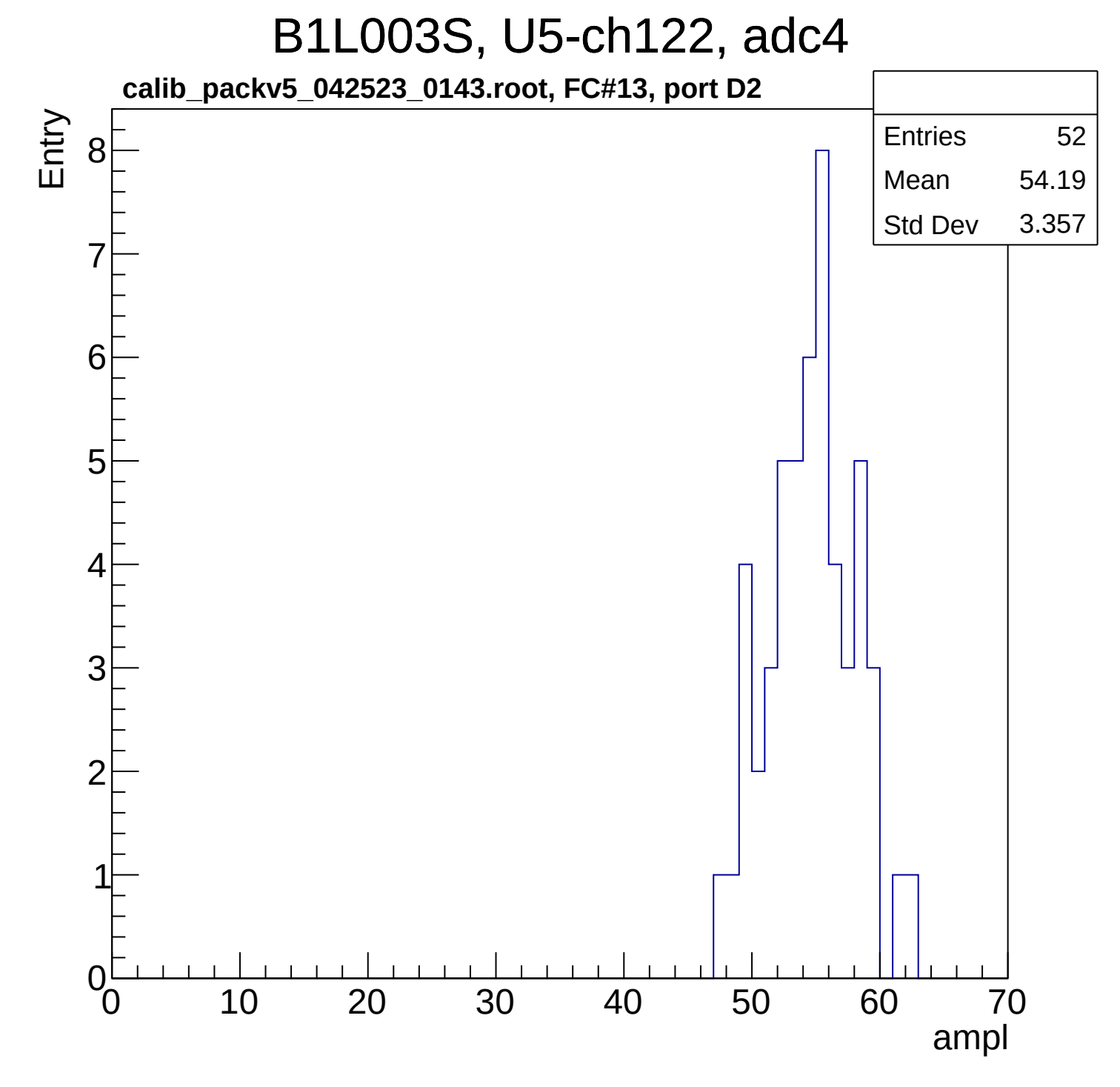
B1L003S, U5-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.19
Std Dev 3.357
Entry
ampl
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1
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3
4
5
6
7
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0
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20
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40
50
60
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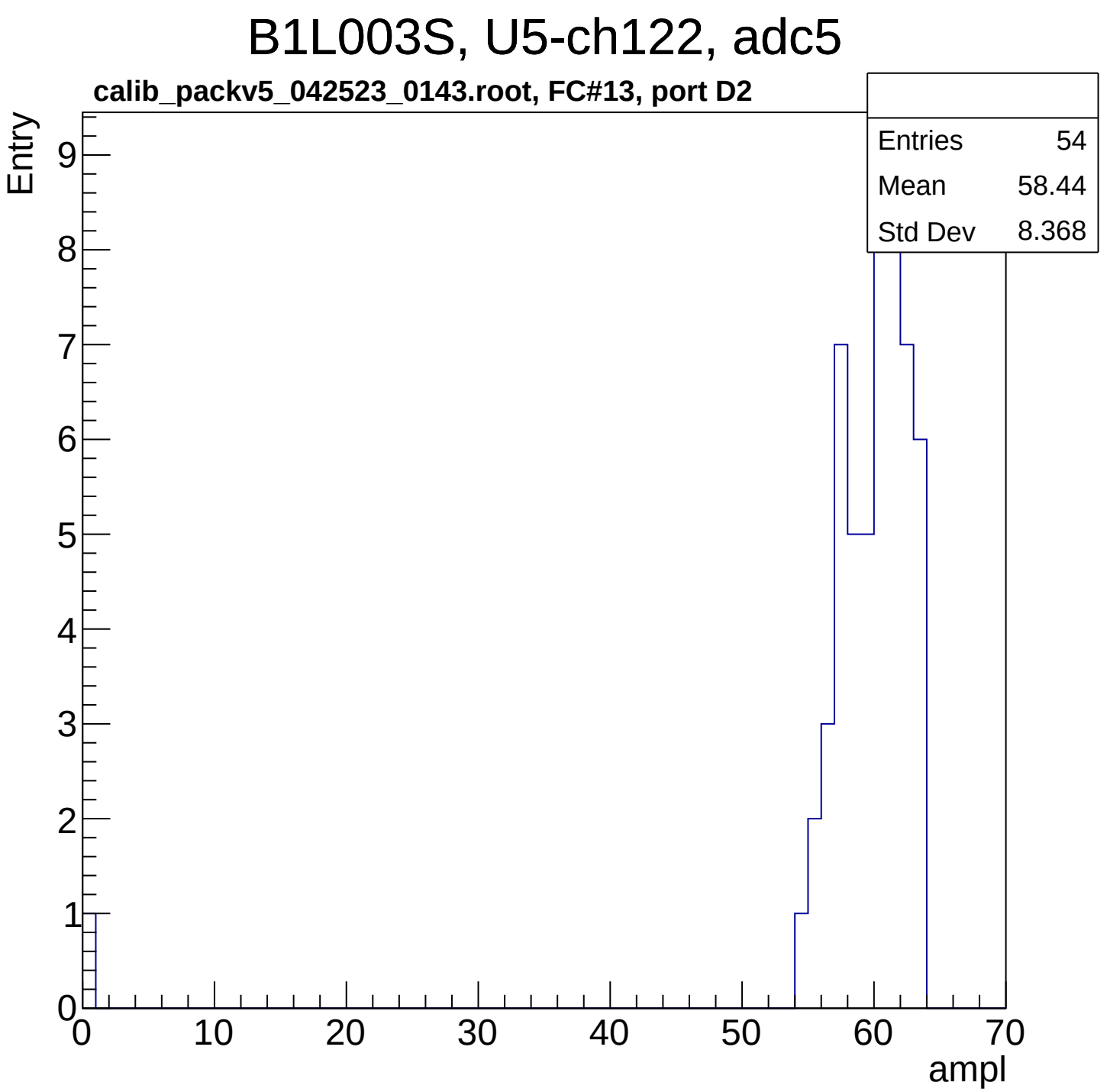
B1L003S, U5-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.44
Std Dev 8.368
Entry
ampl
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3
4
5
6
7
8
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0
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60
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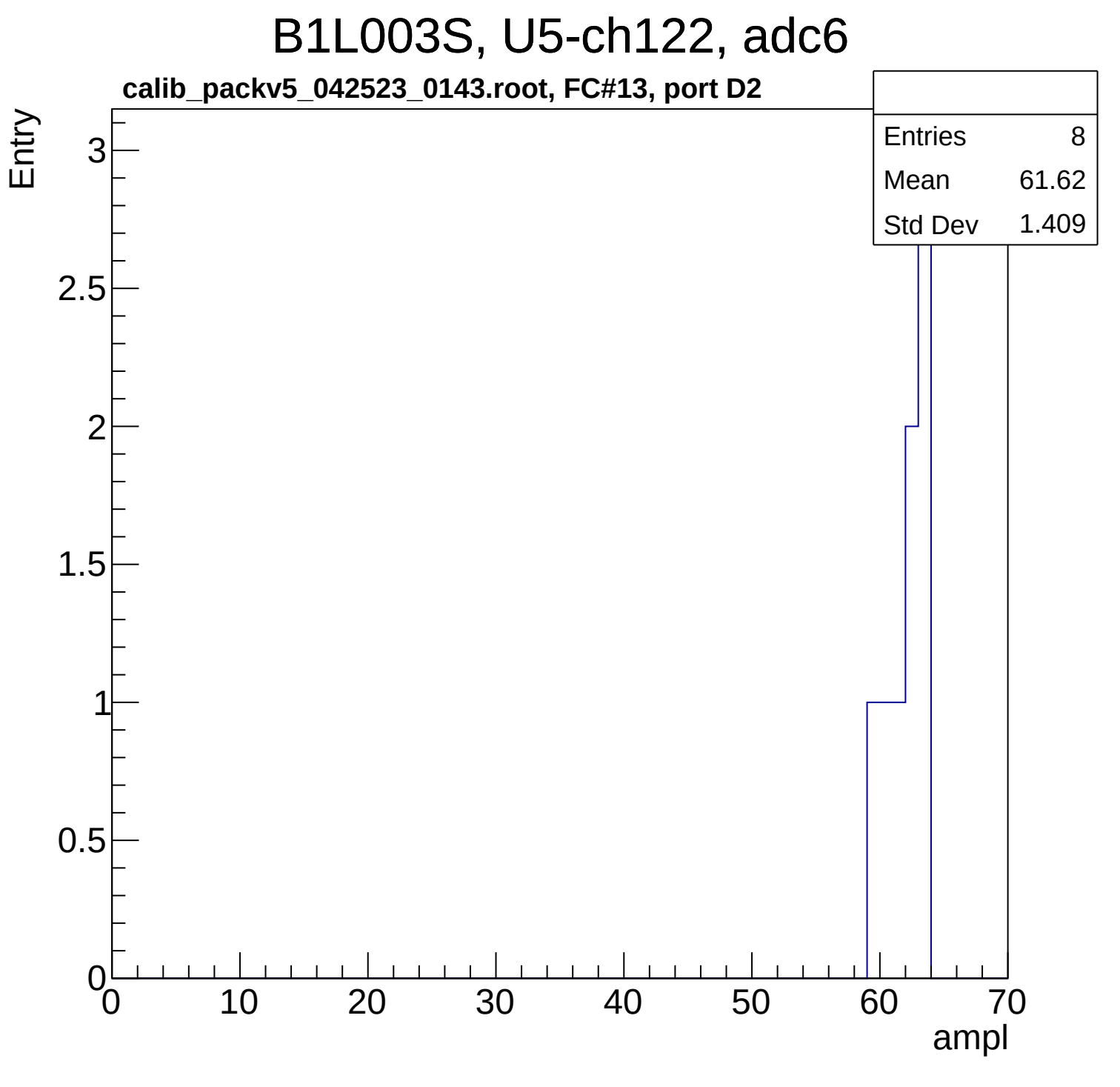
B1L003S, U5-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.62
Std Dev 1.409
Entry
ampl
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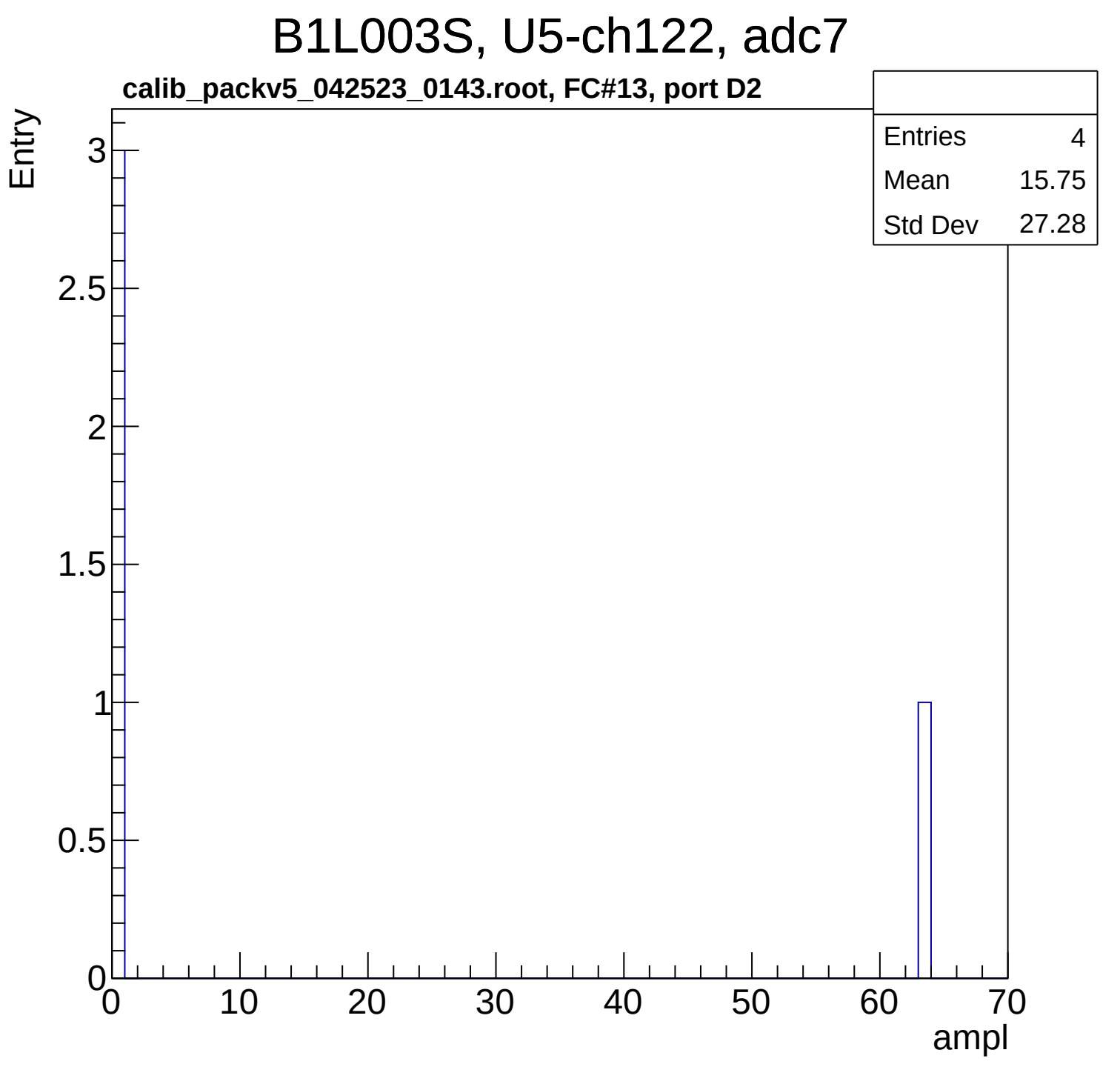
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U5-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
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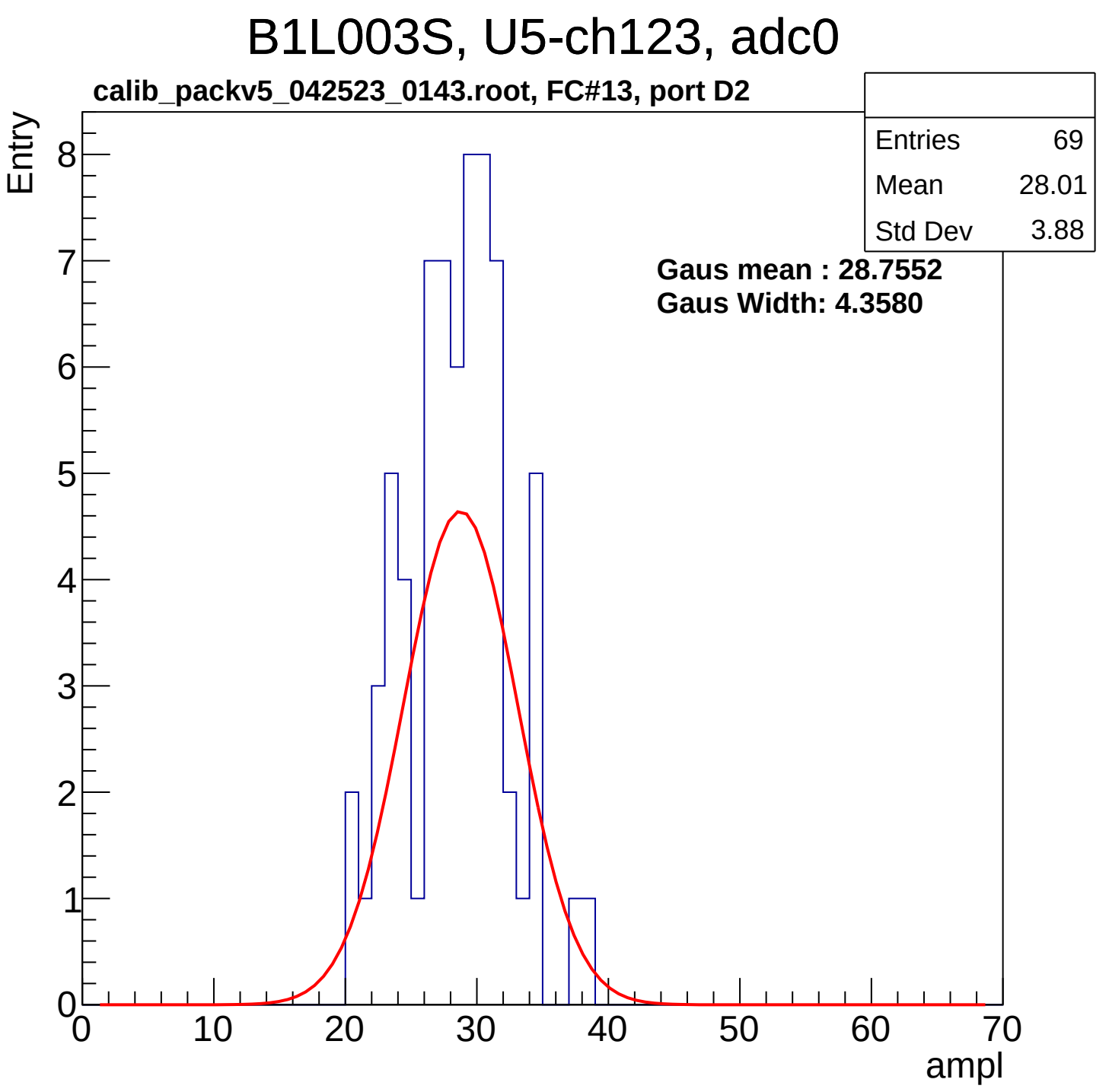
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.01
Std Dev 3.88
Gaus mean : 28.7552
Gaus Width: 4.3580
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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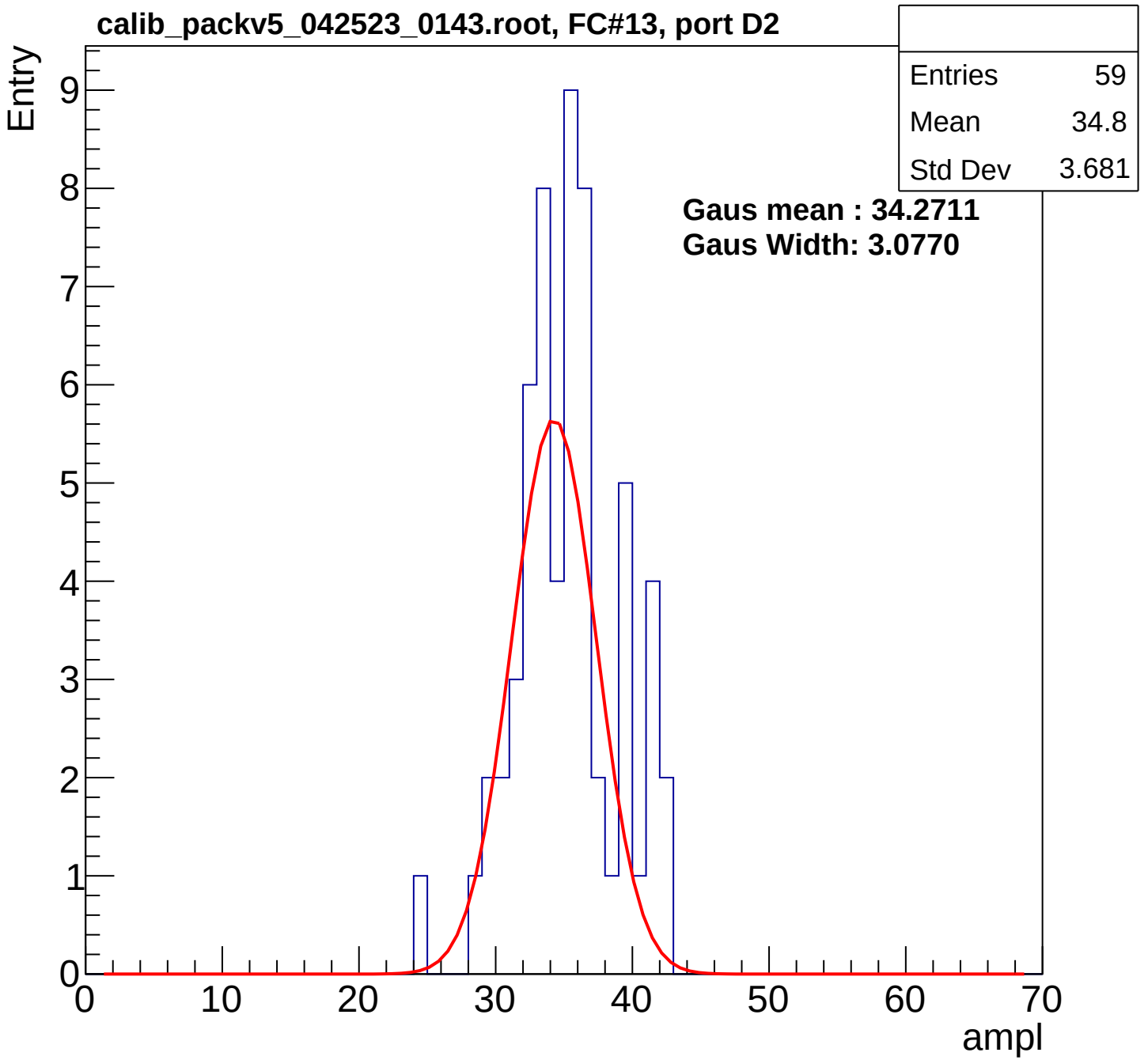
B1L003S, U5-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 34.8
Std Dev 3.681
Gaus mean : 34.2711
Gaus Width: 3.0770
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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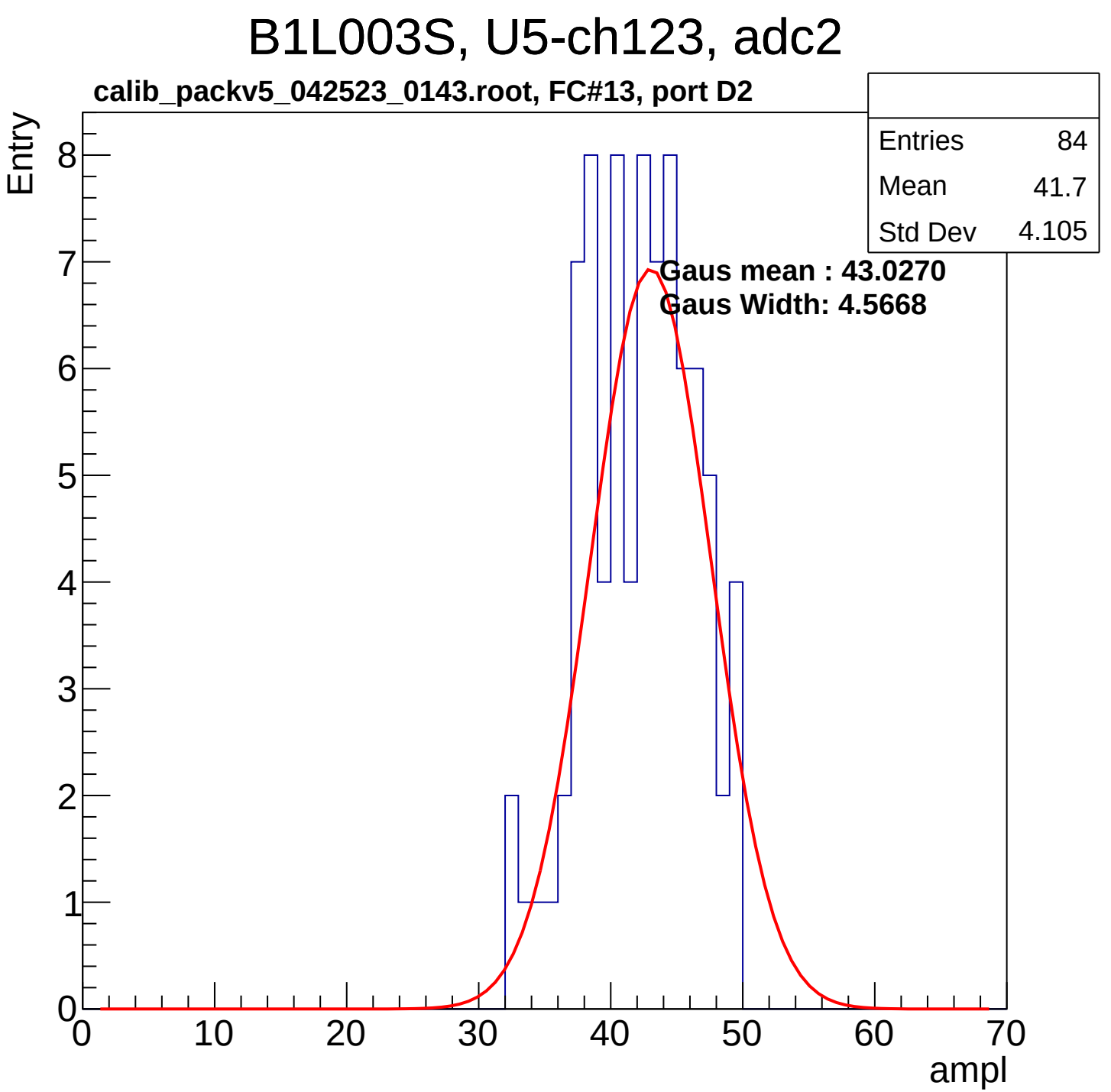
B1L003S, U5-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 41.7
Std Dev 4.105
Gaus mean : 43.0270
Gaus Width: 4.5668
Entry
ampl
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2
3
4
5
6
7
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0
10
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60
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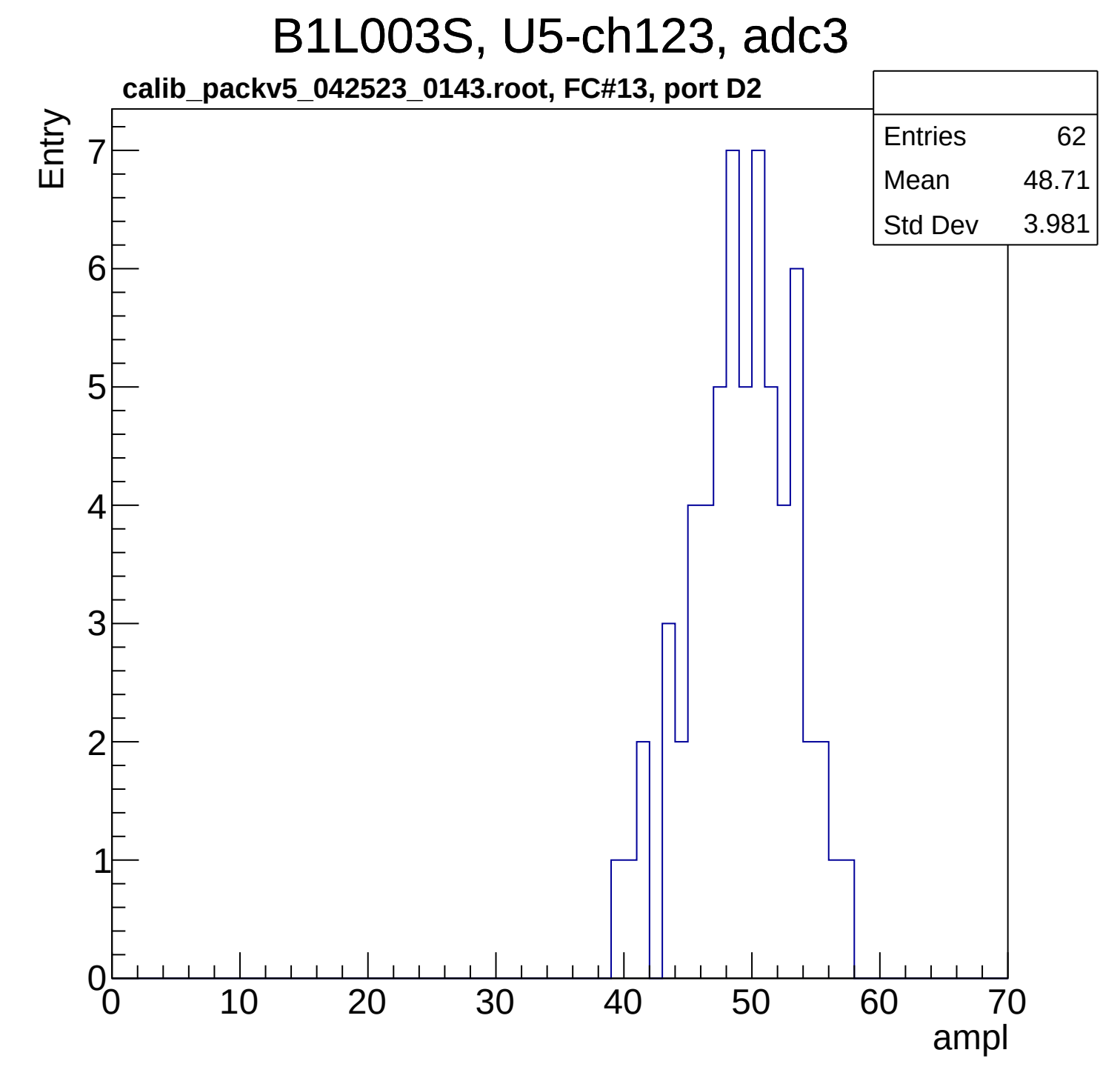
B1L003S, U5-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.71
Std Dev 3.981
Entry
ampl
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60
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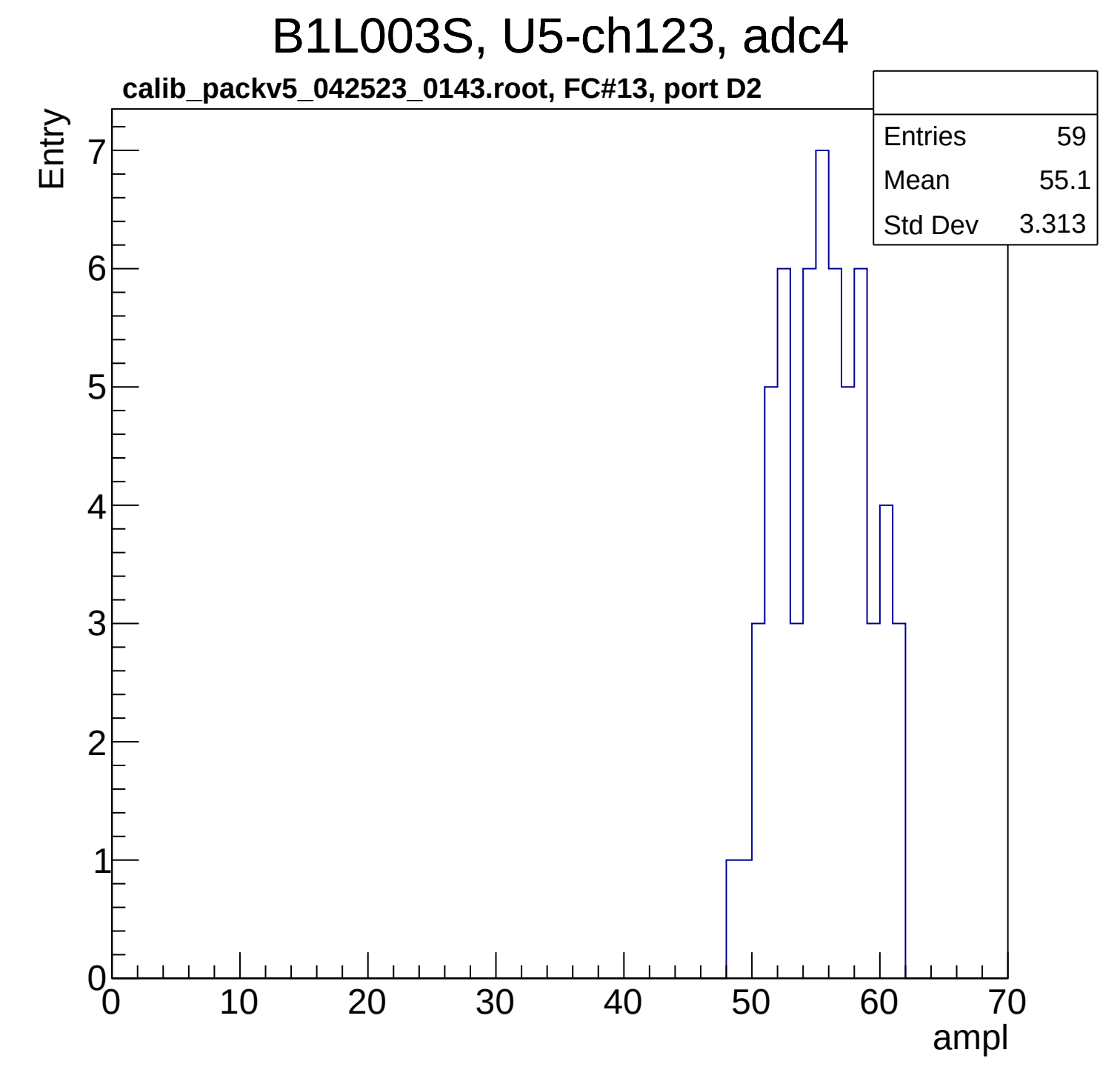
B1L003S, U5-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.1
Std Dev 3.313
Entry
ampl
0
1
2
3
4
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6
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0
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60
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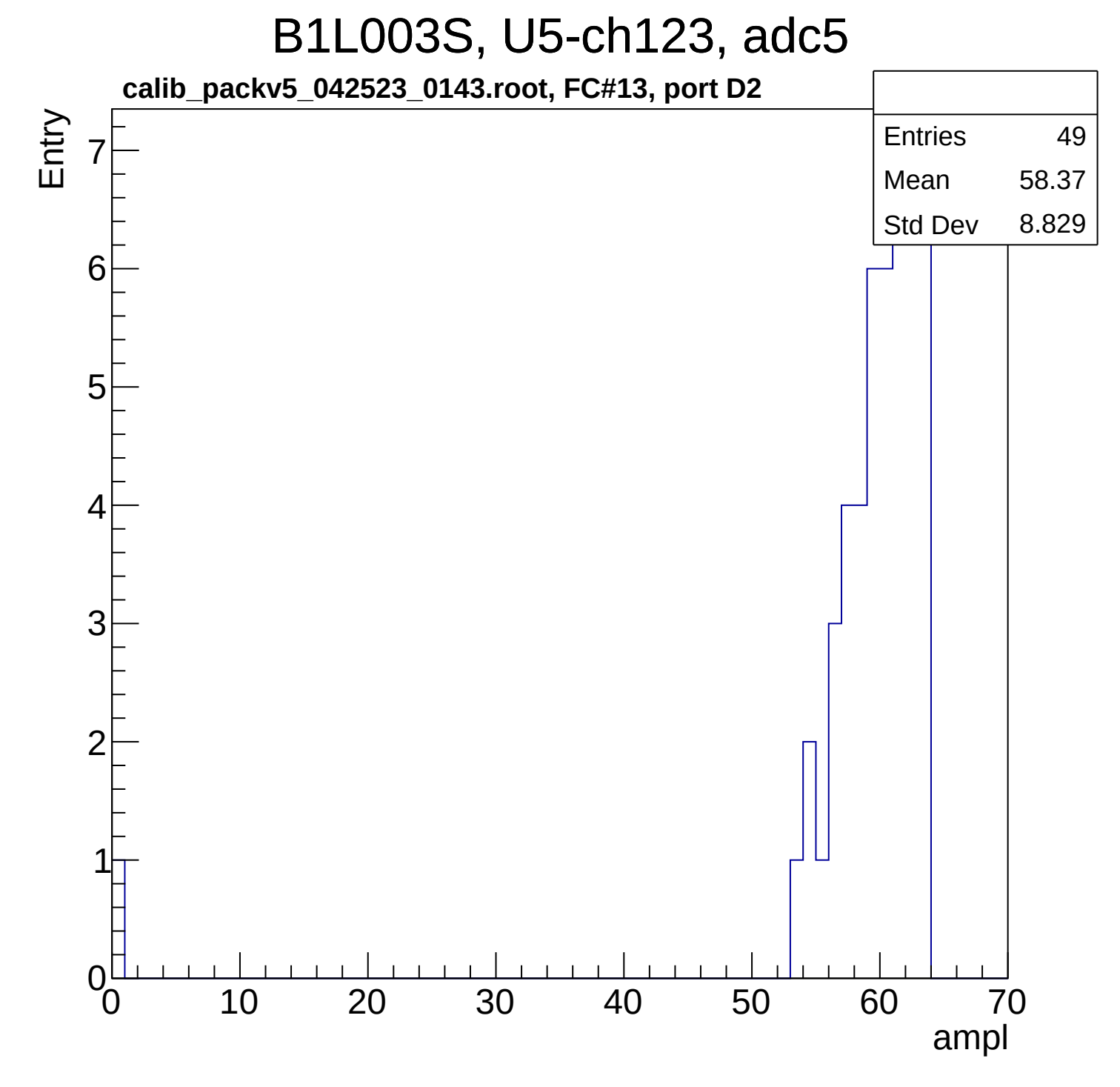
B1L003S, U5-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.37
Std Dev 8.829
Entry
ampl
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1
2
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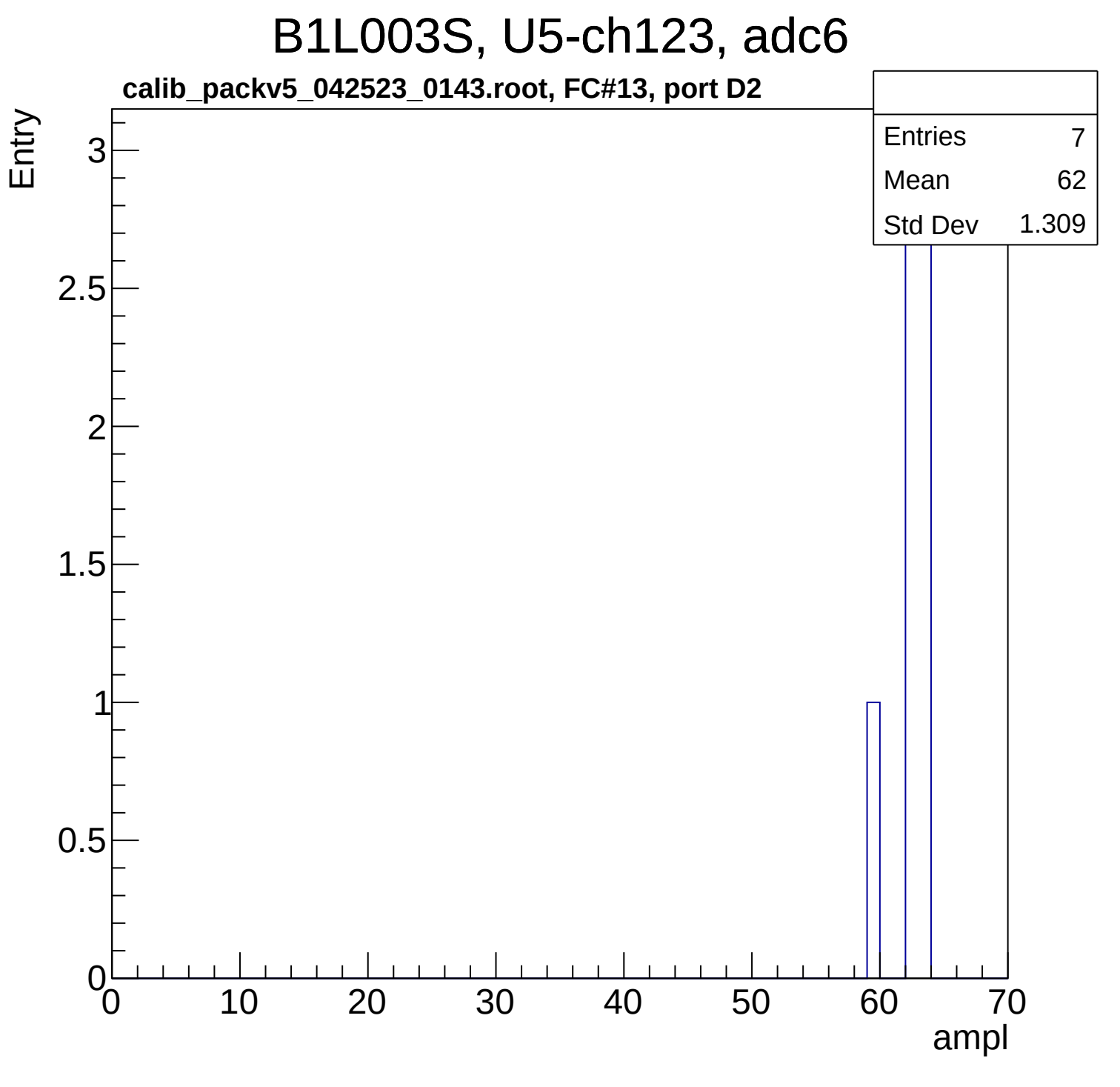
B1L003S, U5-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 1.309
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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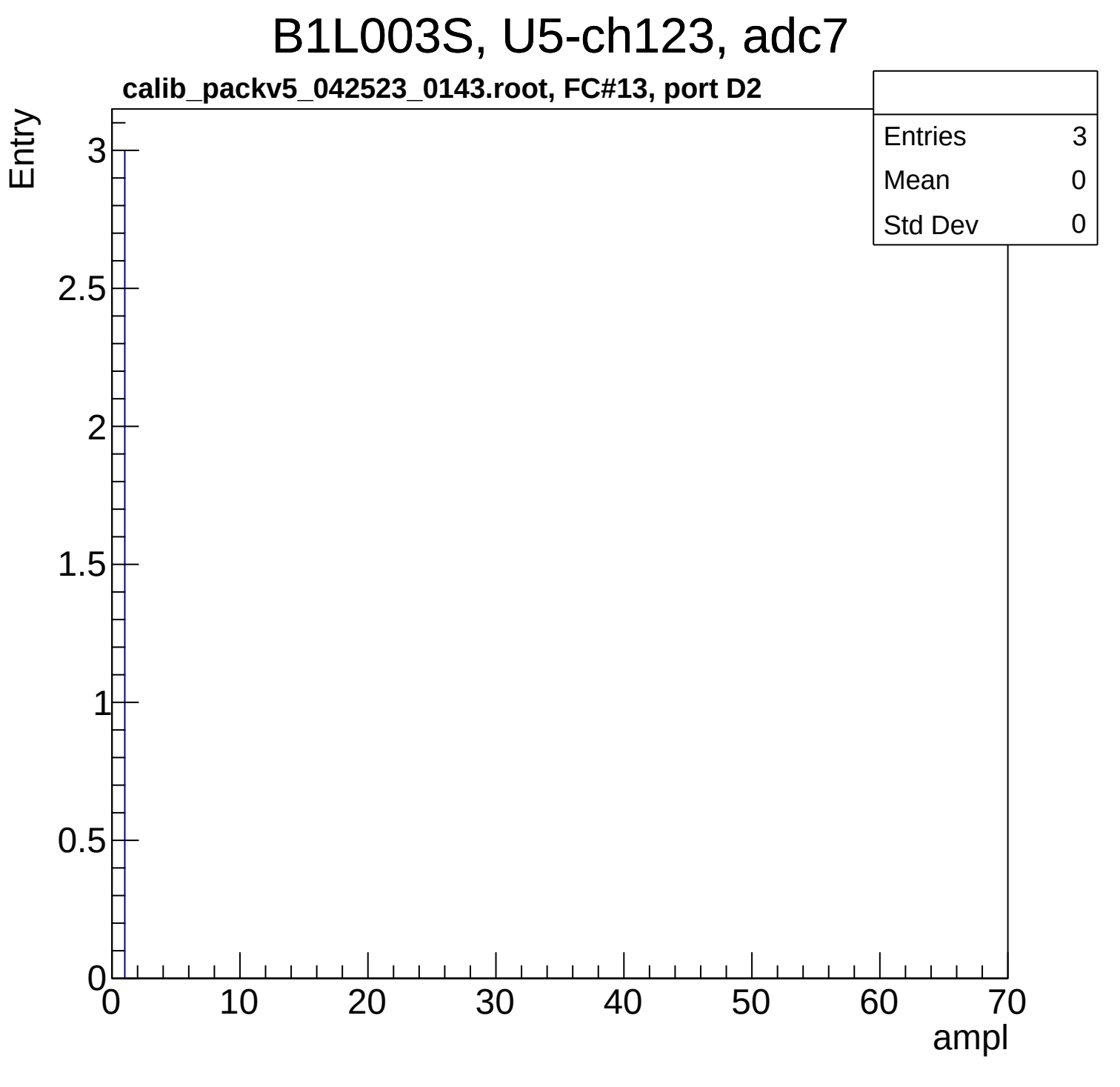
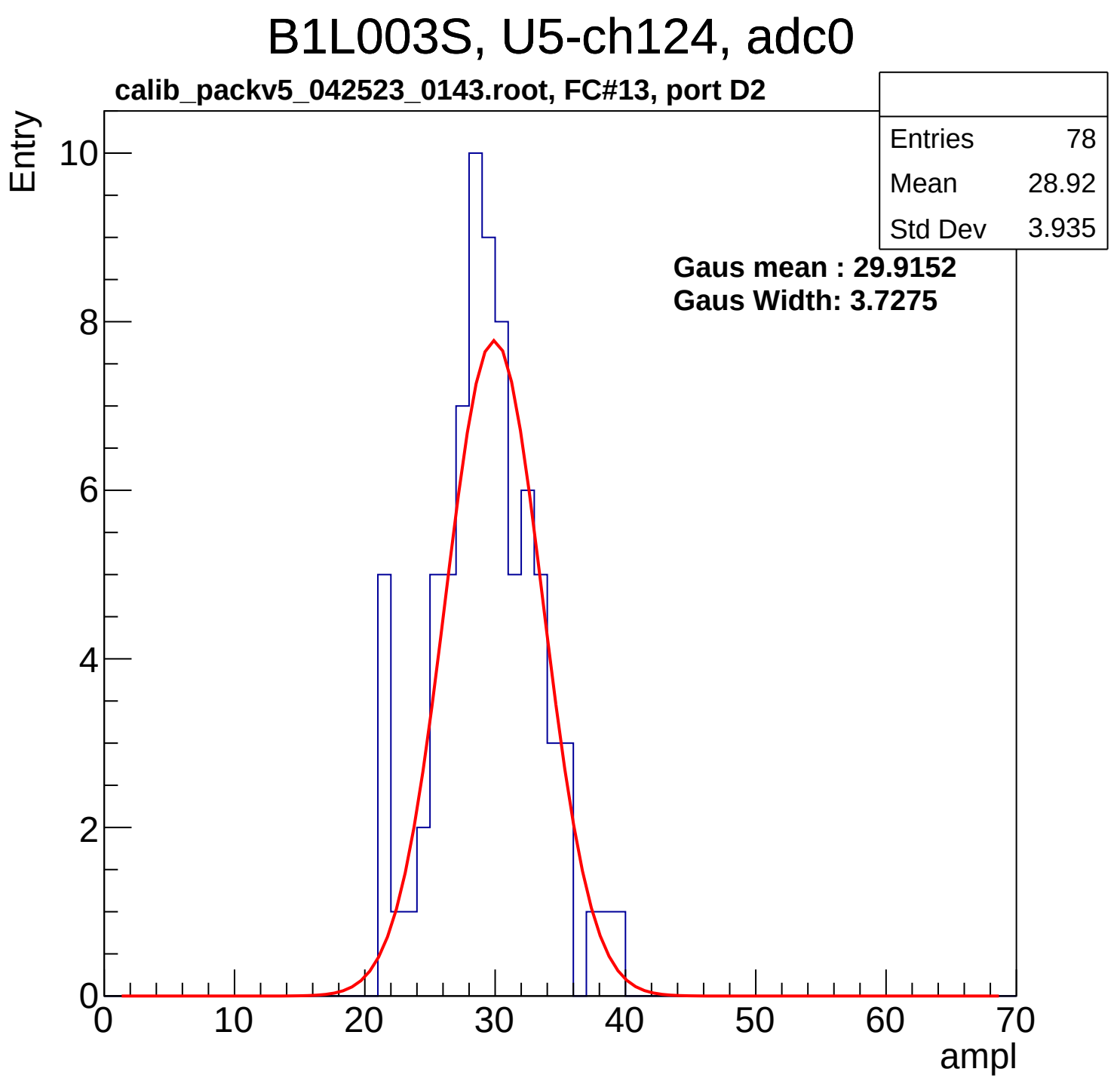

B1L003S, U5-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 28.92
Std Dev 3.935
Gaus mean : 29.9152
Gaus Width: 3.7275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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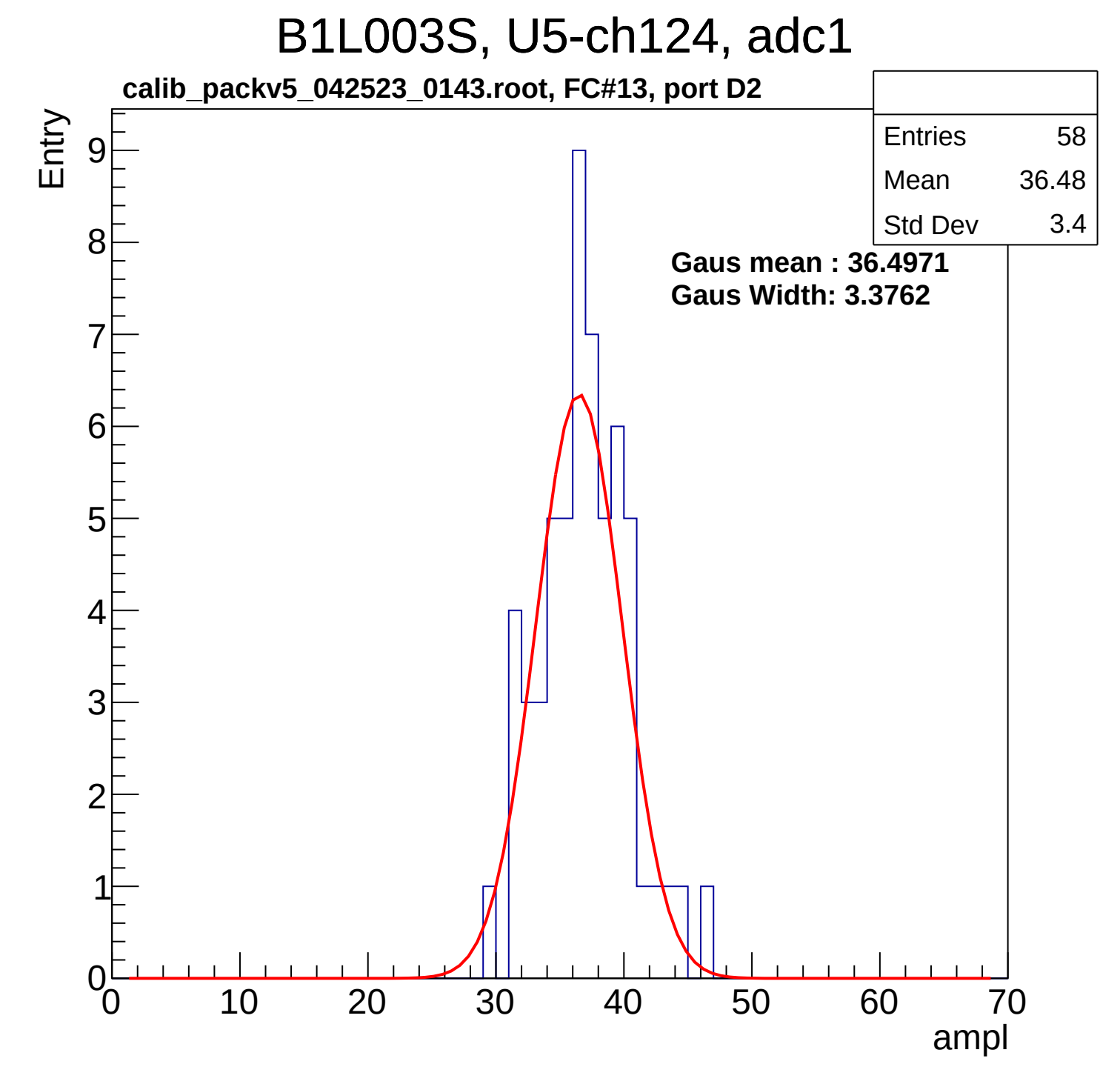
B1L003S, U5-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.48
Std Dev 3.4
Gaus mean : 36.4971
Gaus Width: 3.3762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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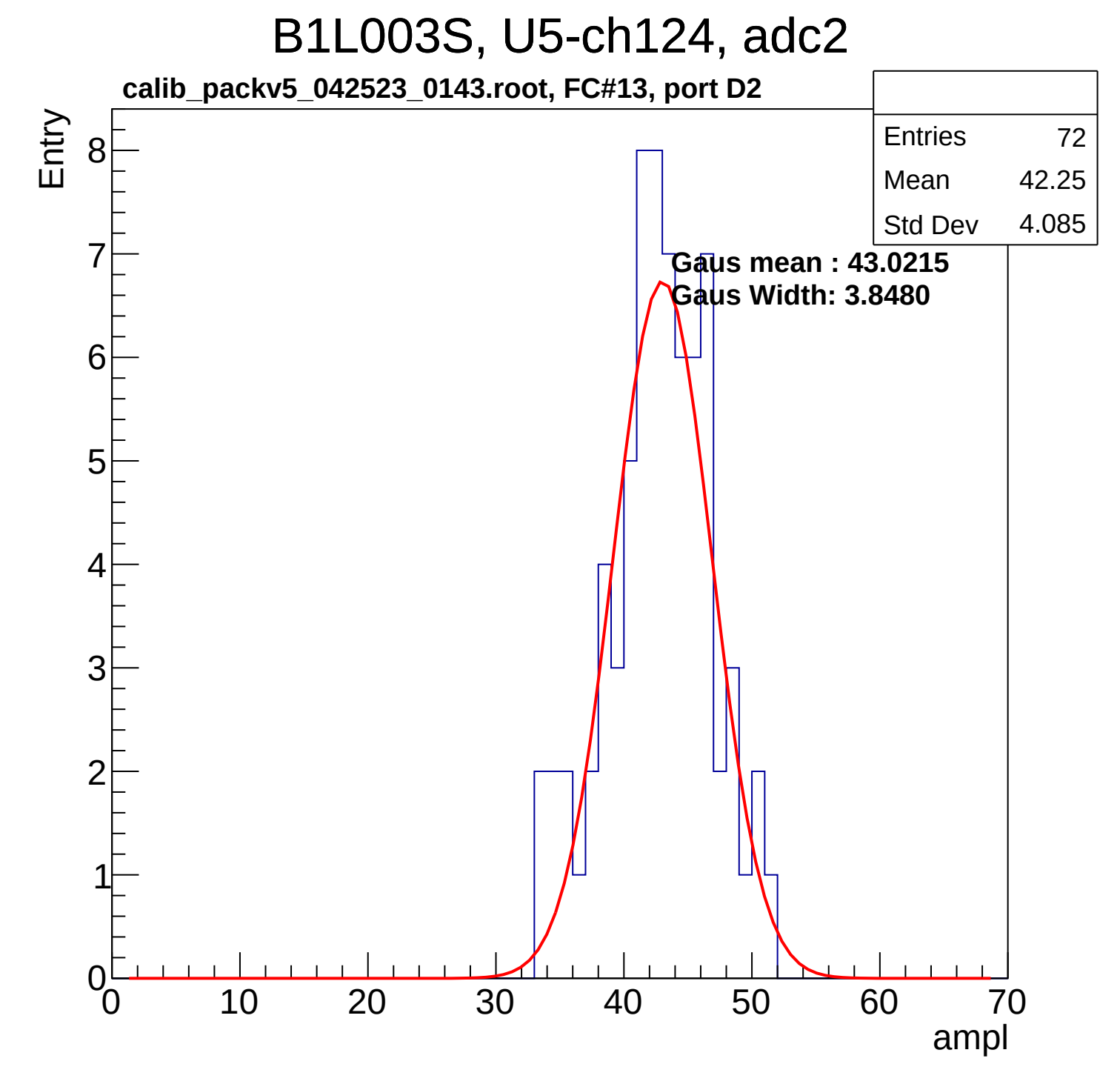
B1L003S, U5-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.25
Std Dev 4.085
Gaus mean : 43.0215
Gaus Width: 3.8480
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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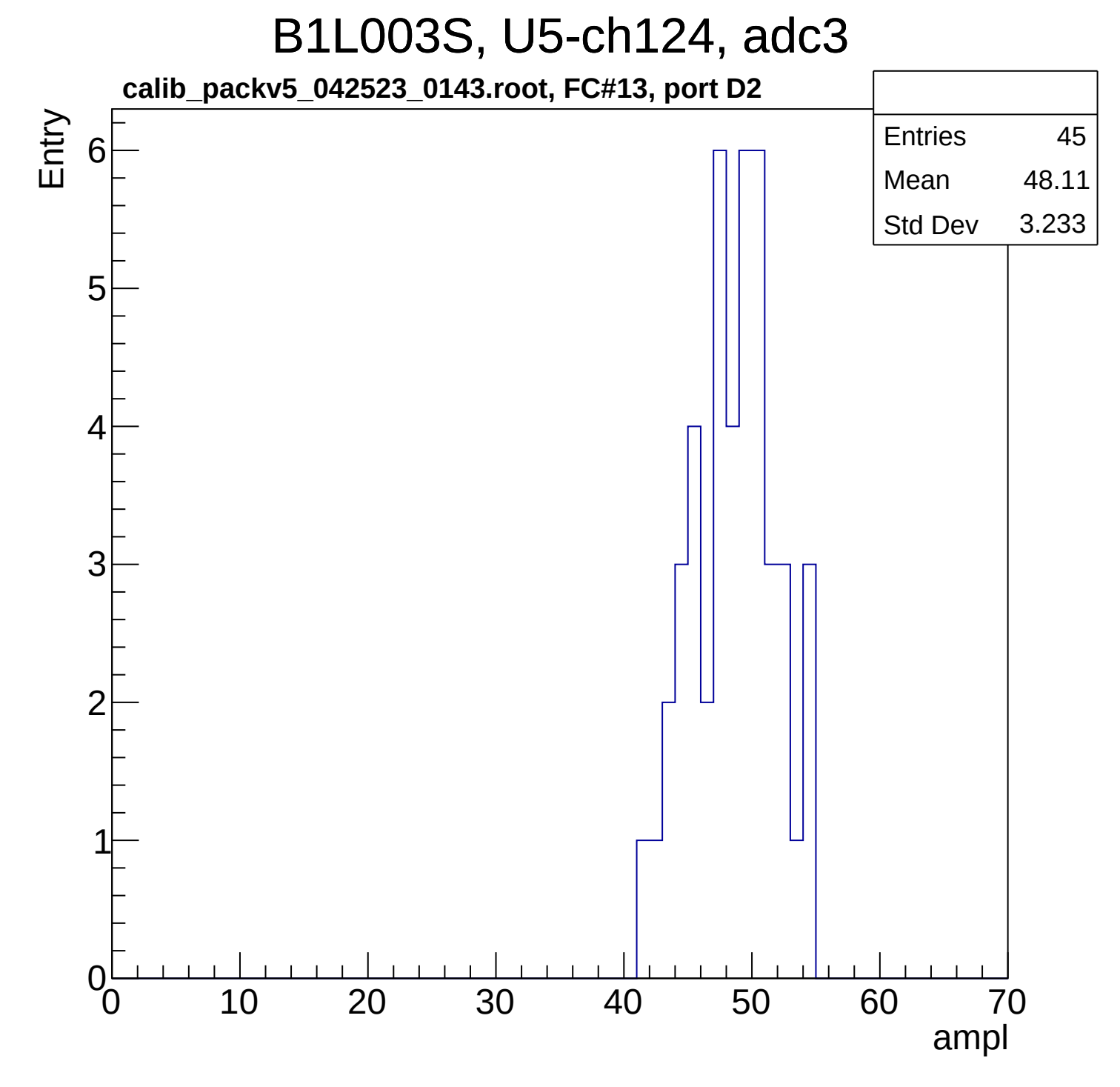
B1L003S, U5-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 48.11
Std Dev 3.233
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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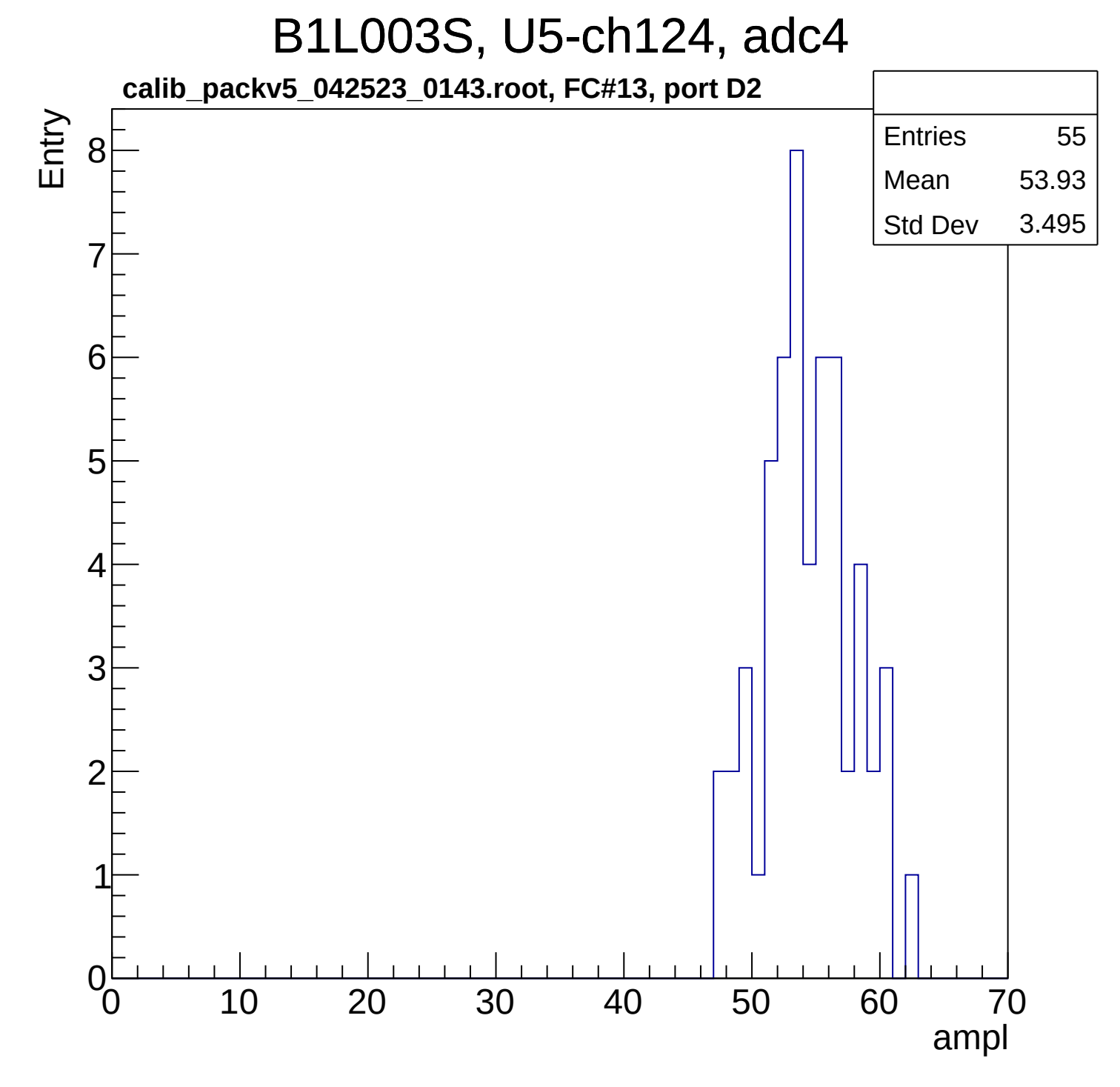
B1L003S, U5-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.93
Std Dev 3.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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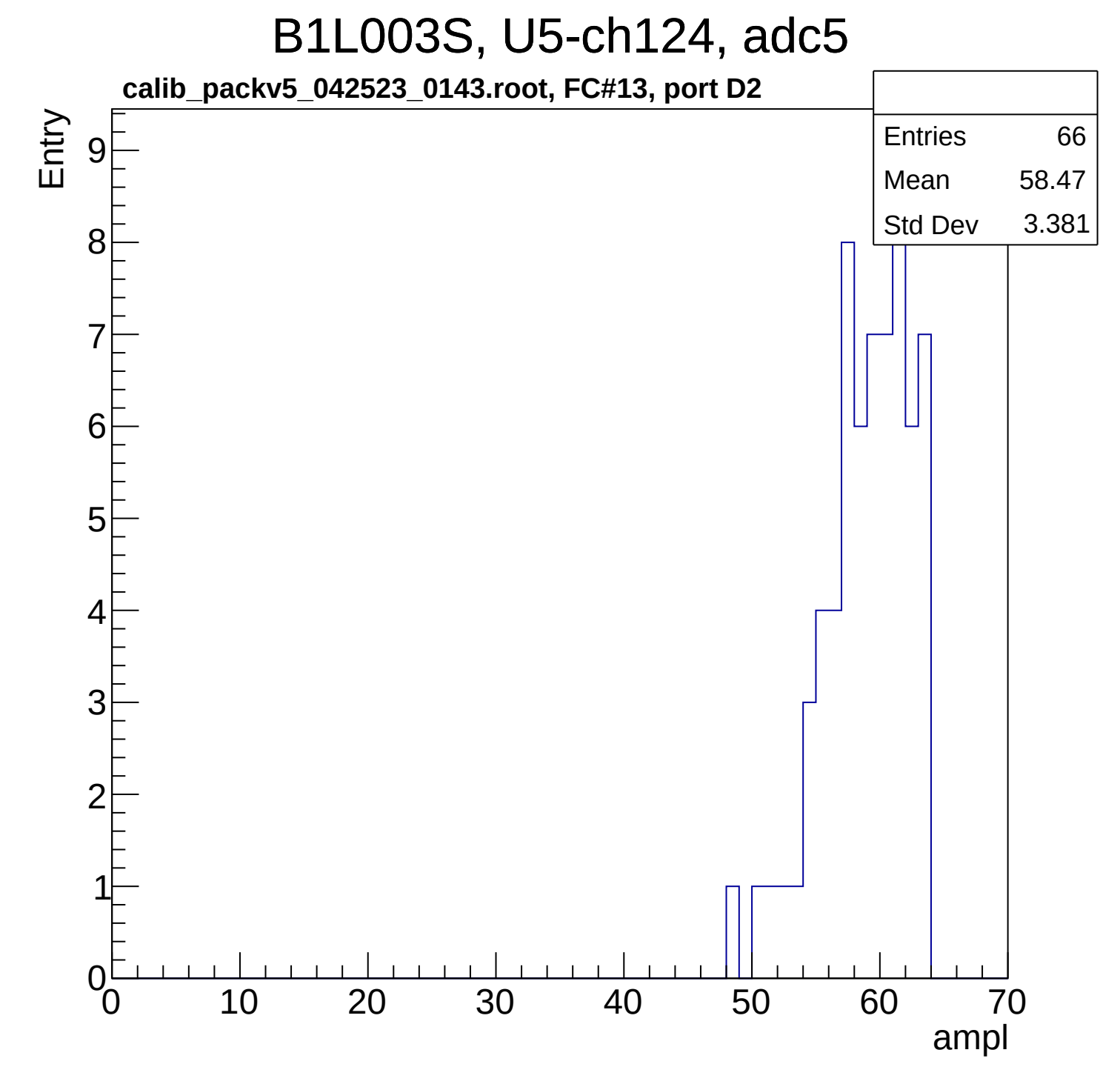
B1L003S, U5-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 58.47
Std Dev 3.381
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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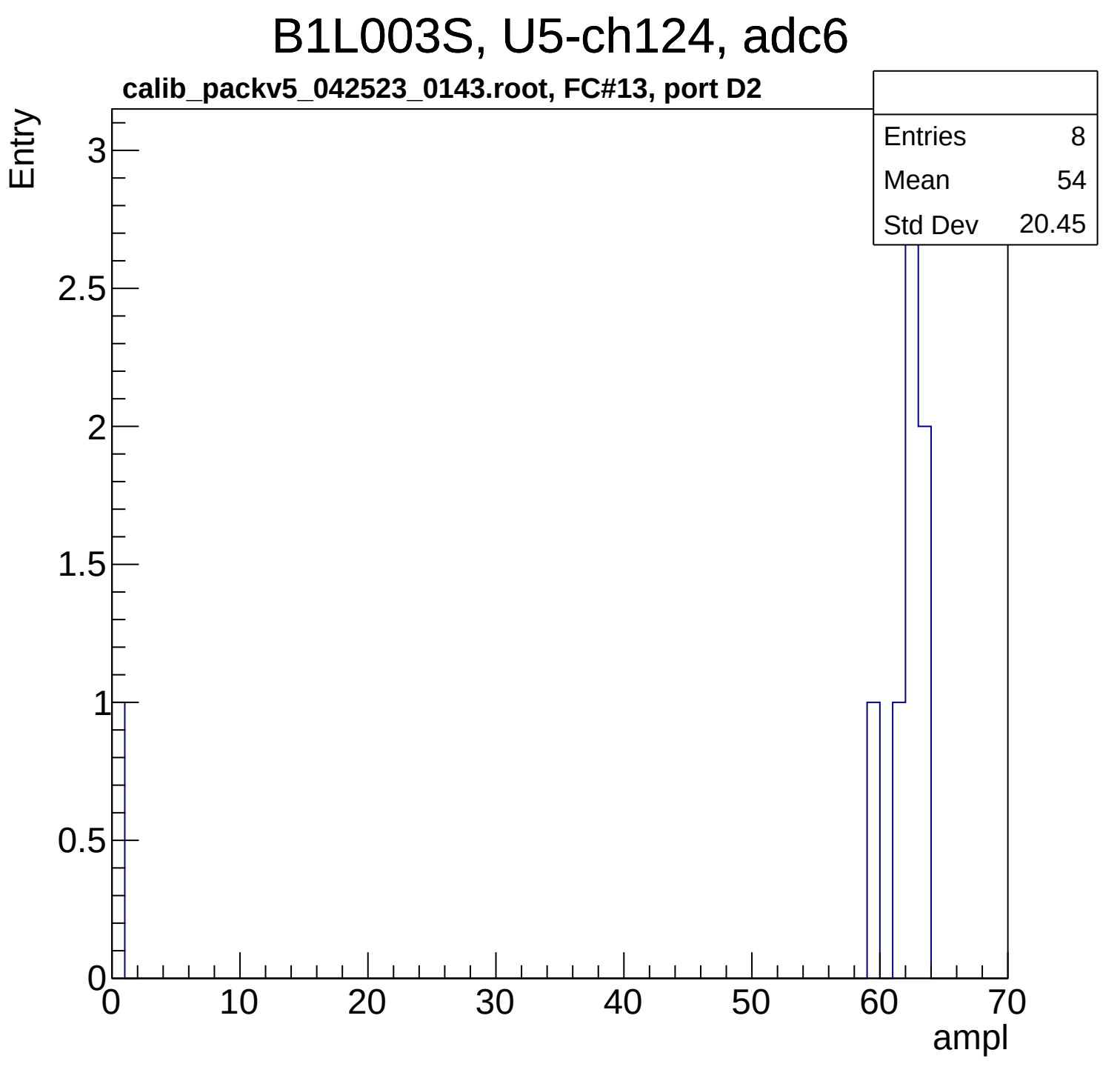
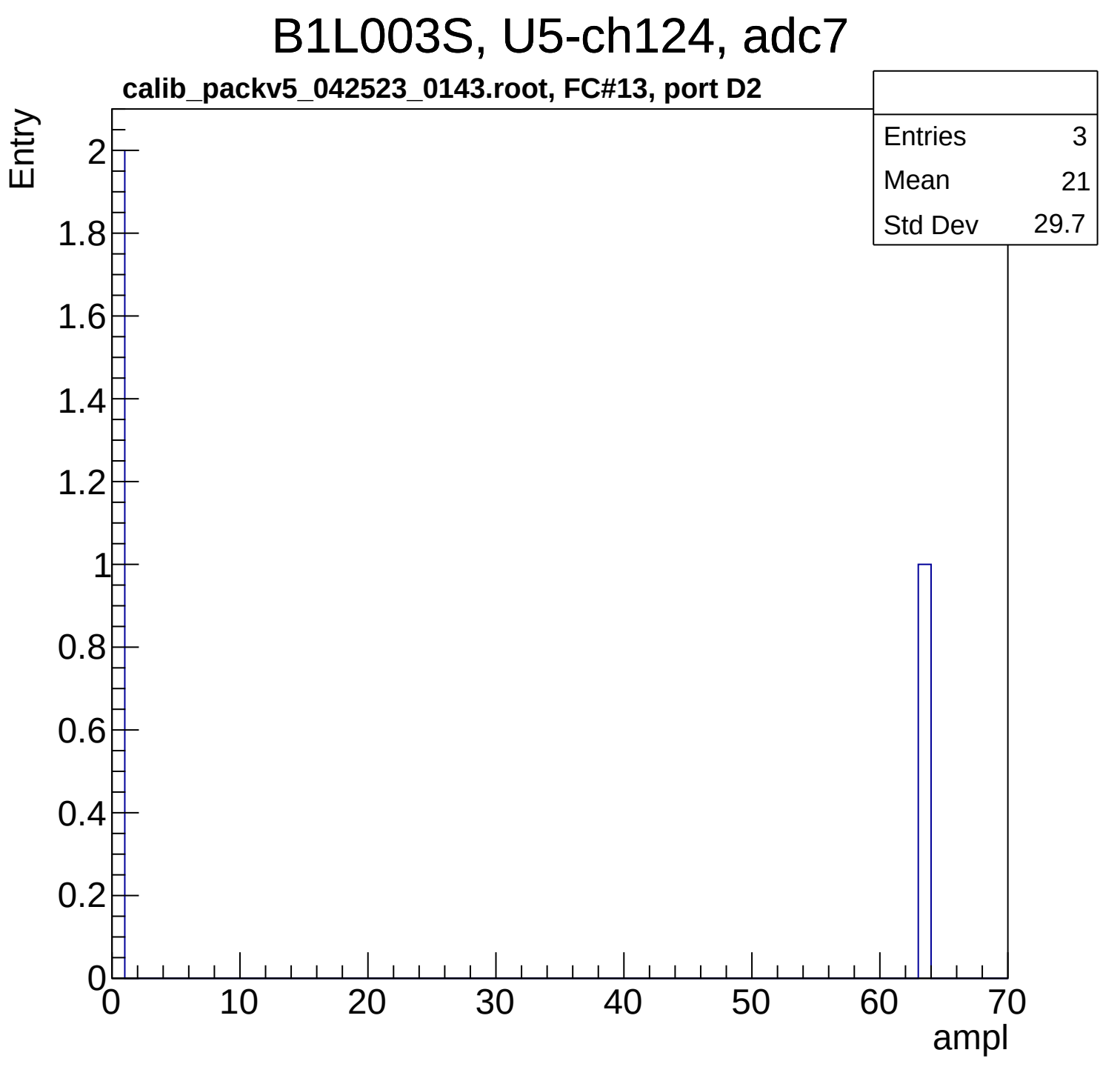

B1L003S, U5-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54
Std Dev 20.45
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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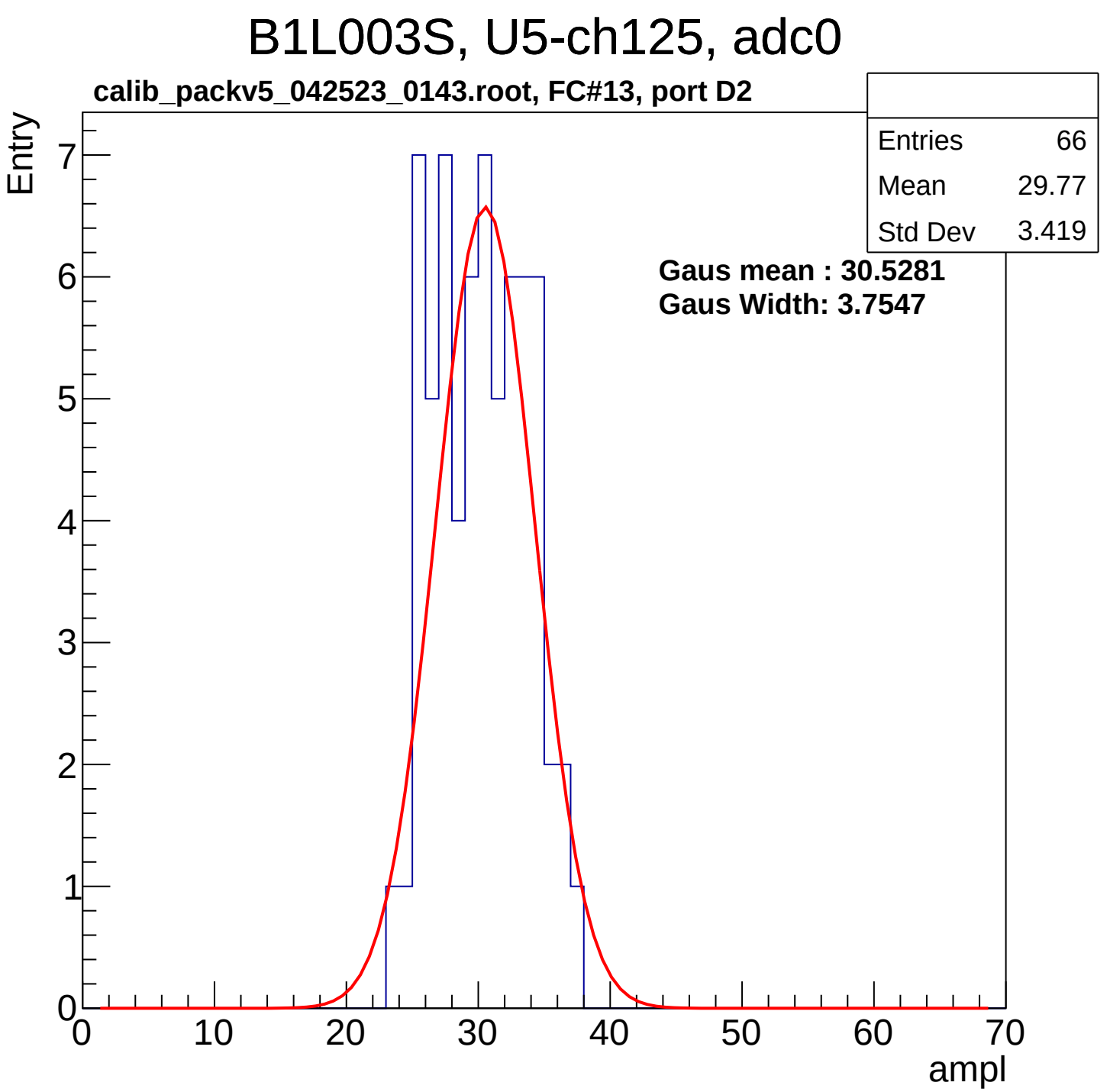
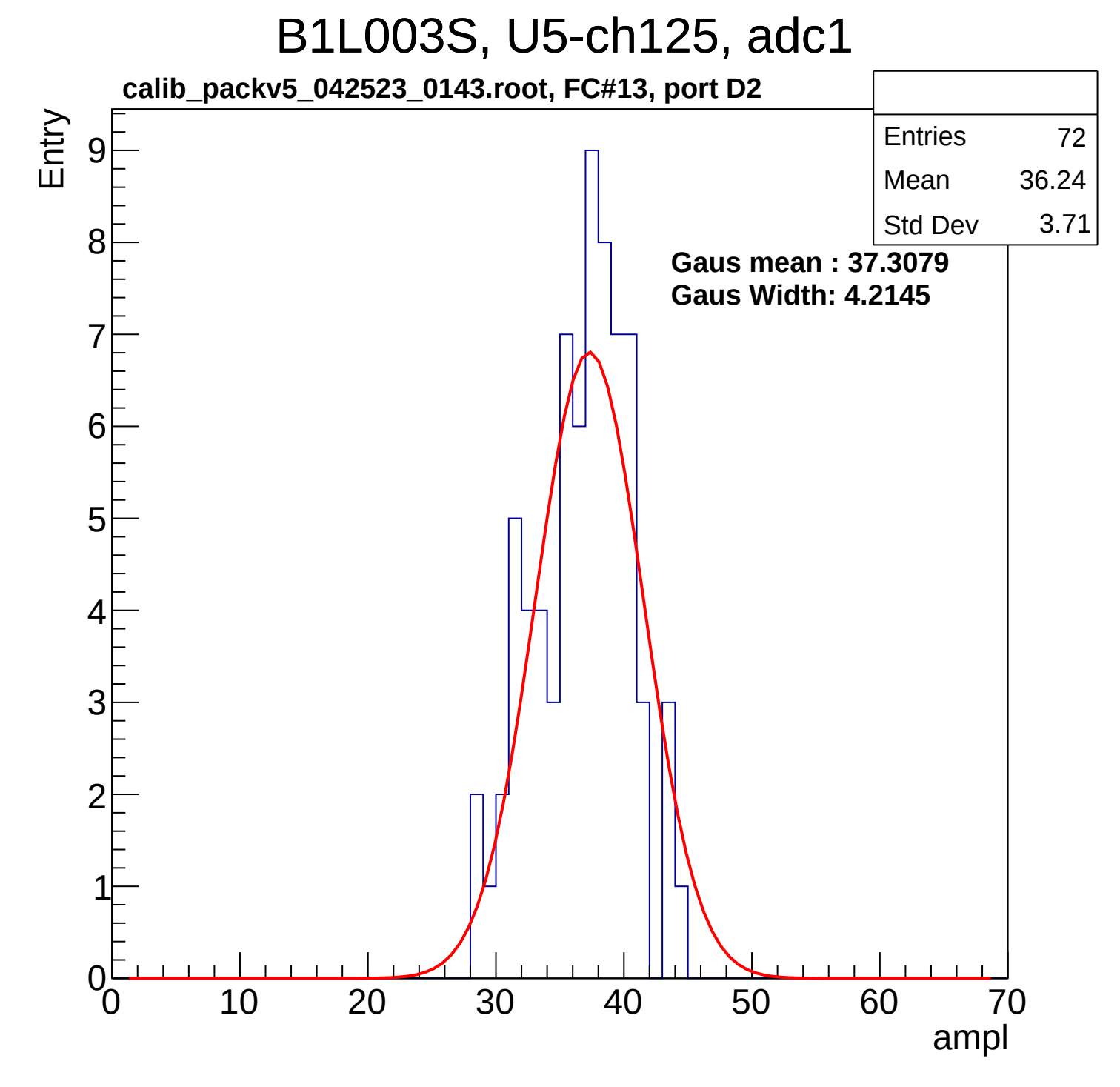
20
30
40
50
60
70


B1L003S, U5-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.77
Std Dev 3.419
Gaus mean : 30.5281
Gaus Width: 3.7547
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.24
Std Dev 3.71
Gaus mean : 37.3079
Gaus Width: 4.2145
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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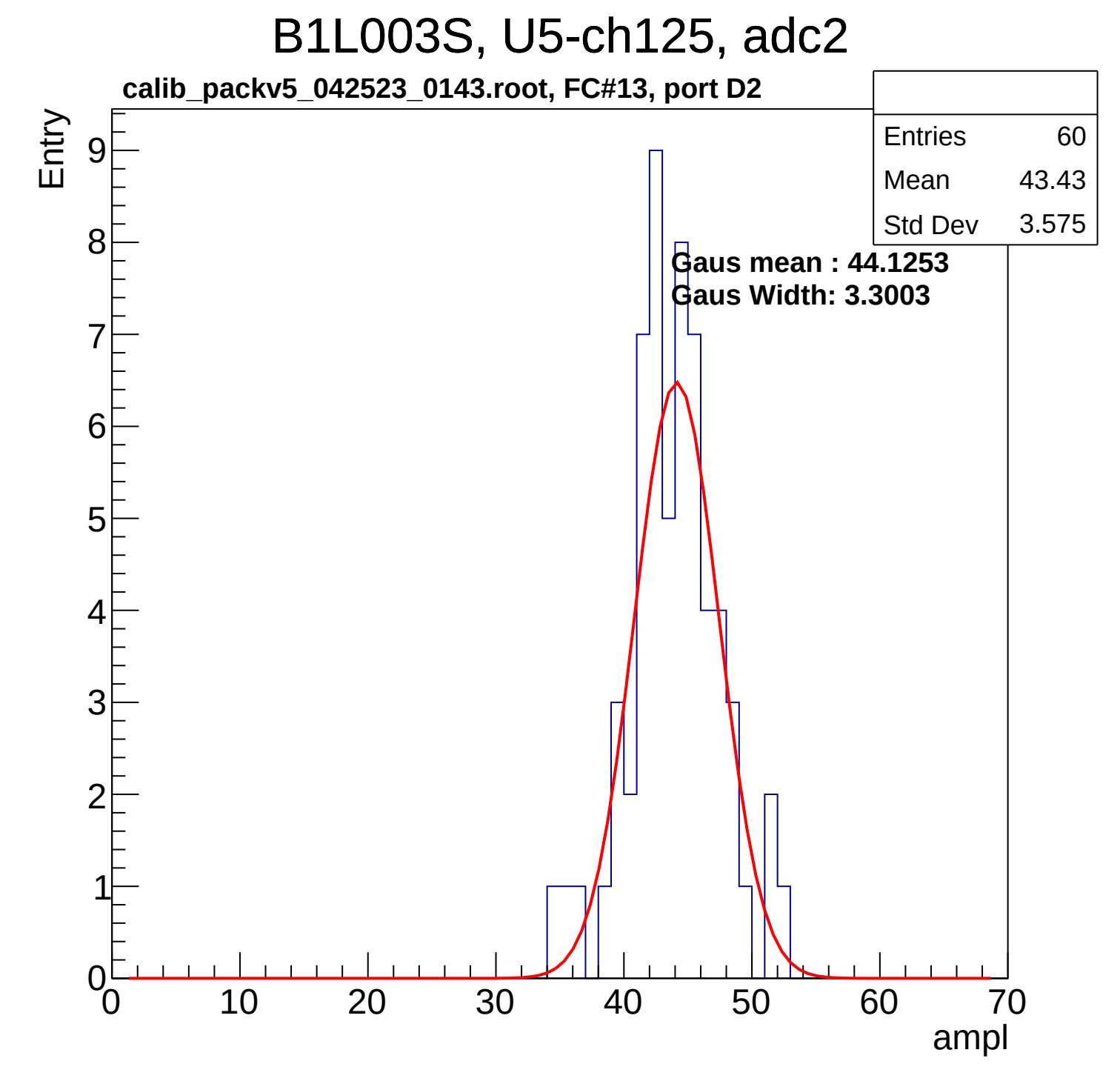
B1L003S, U5-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.43
Std Dev 3.575
Gaus mean : 44.1253
Gaus Width: 3.3003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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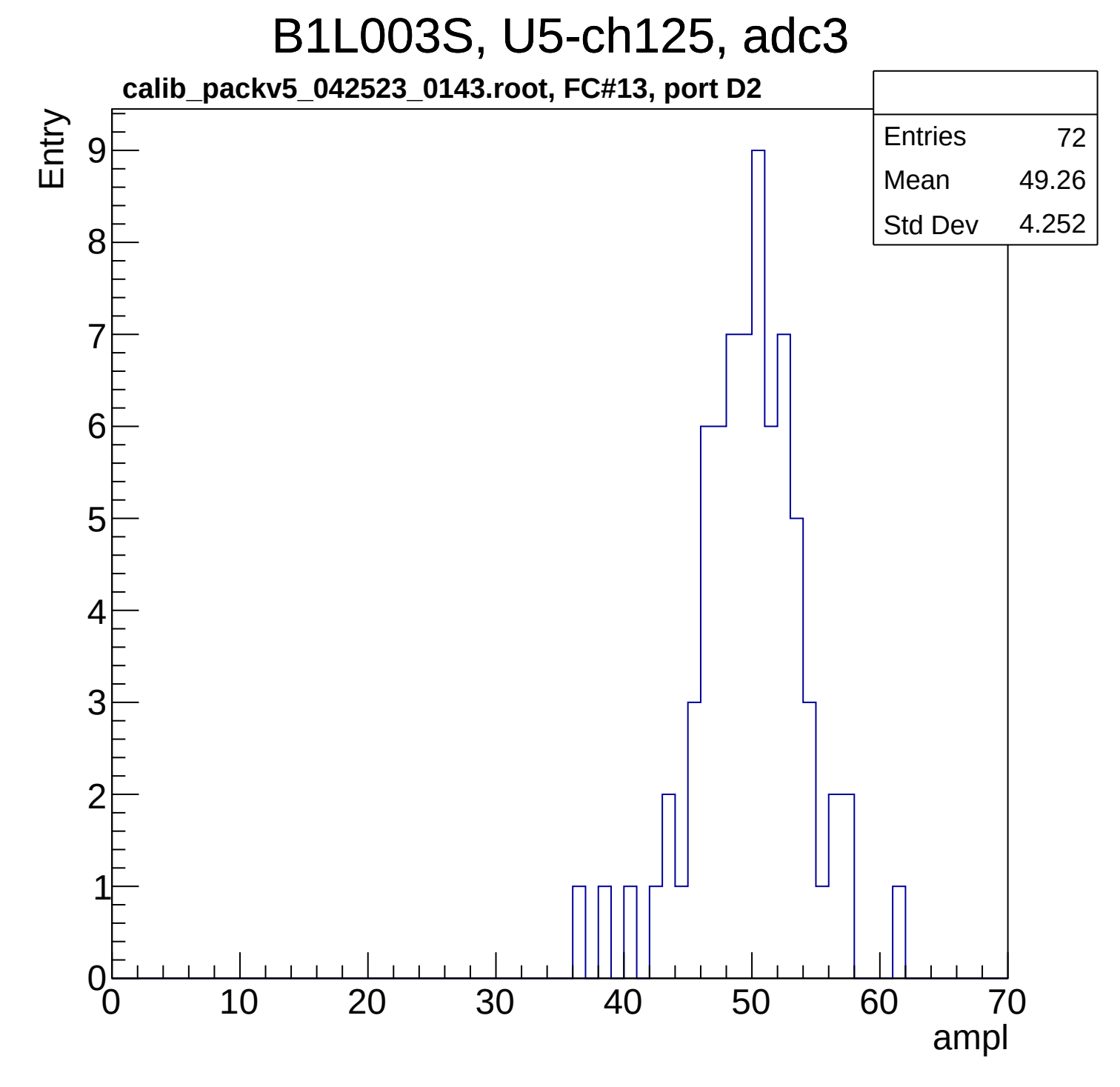
B1L003S, U5-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.26
Std Dev 4.252
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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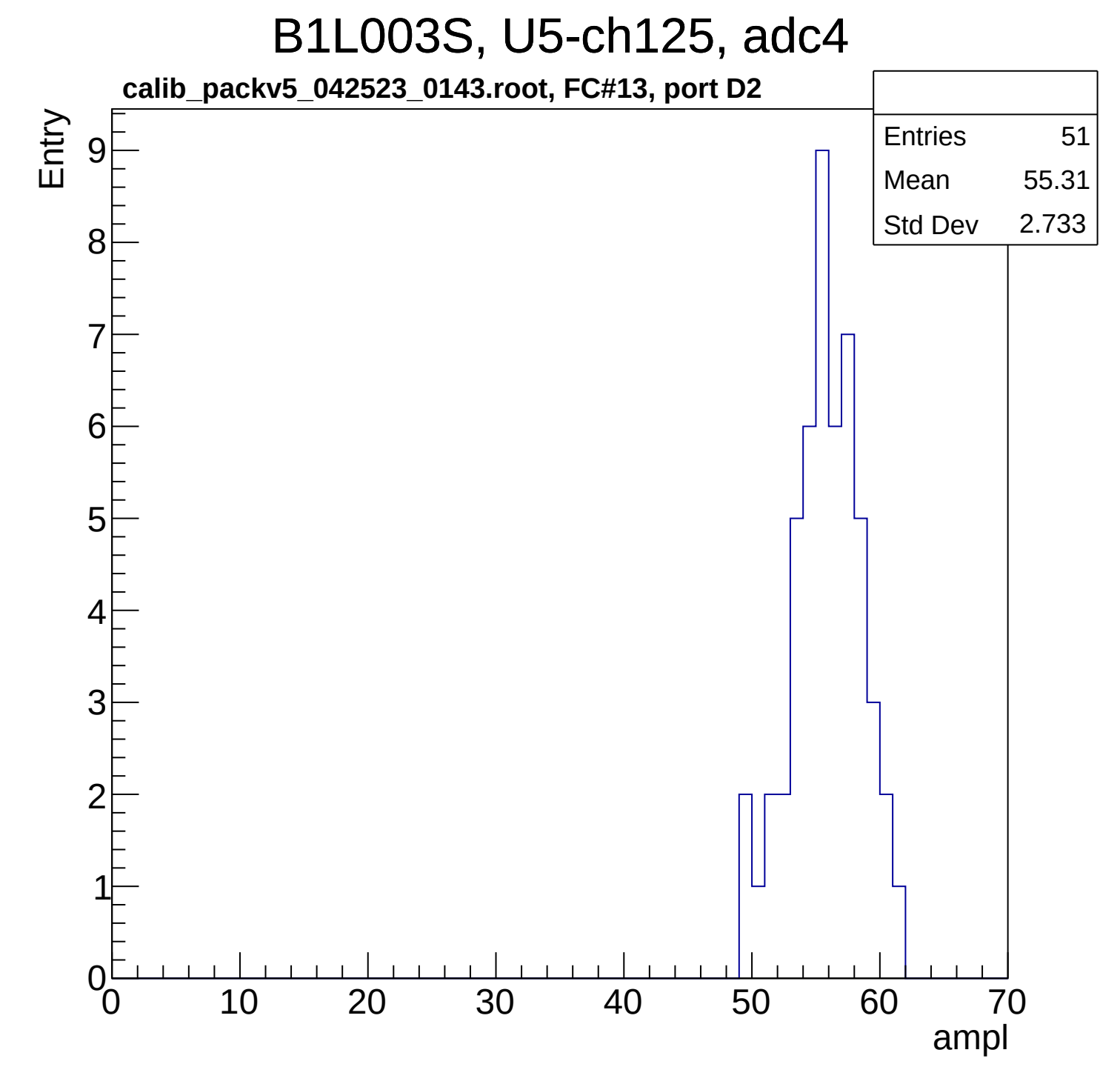
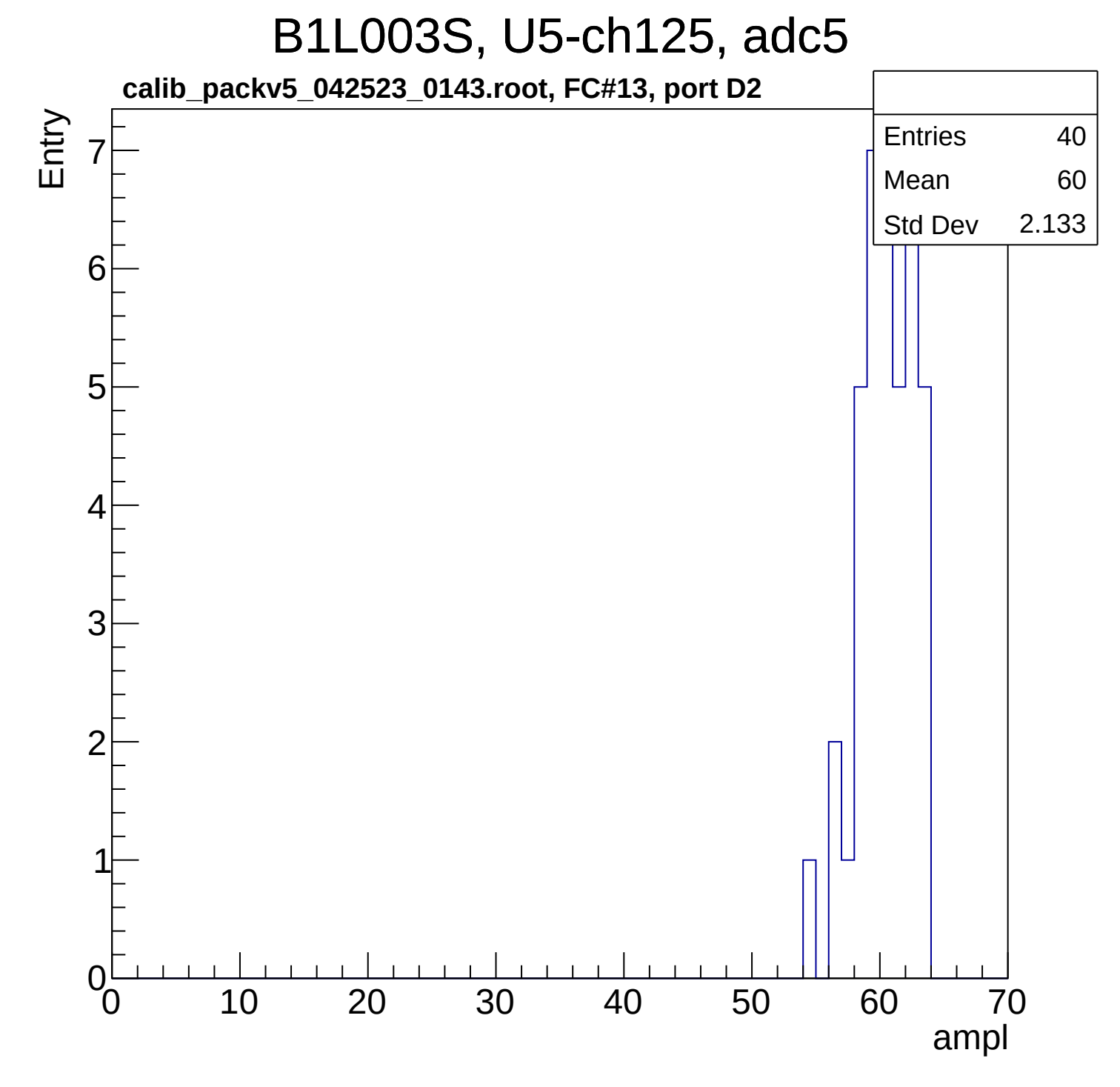
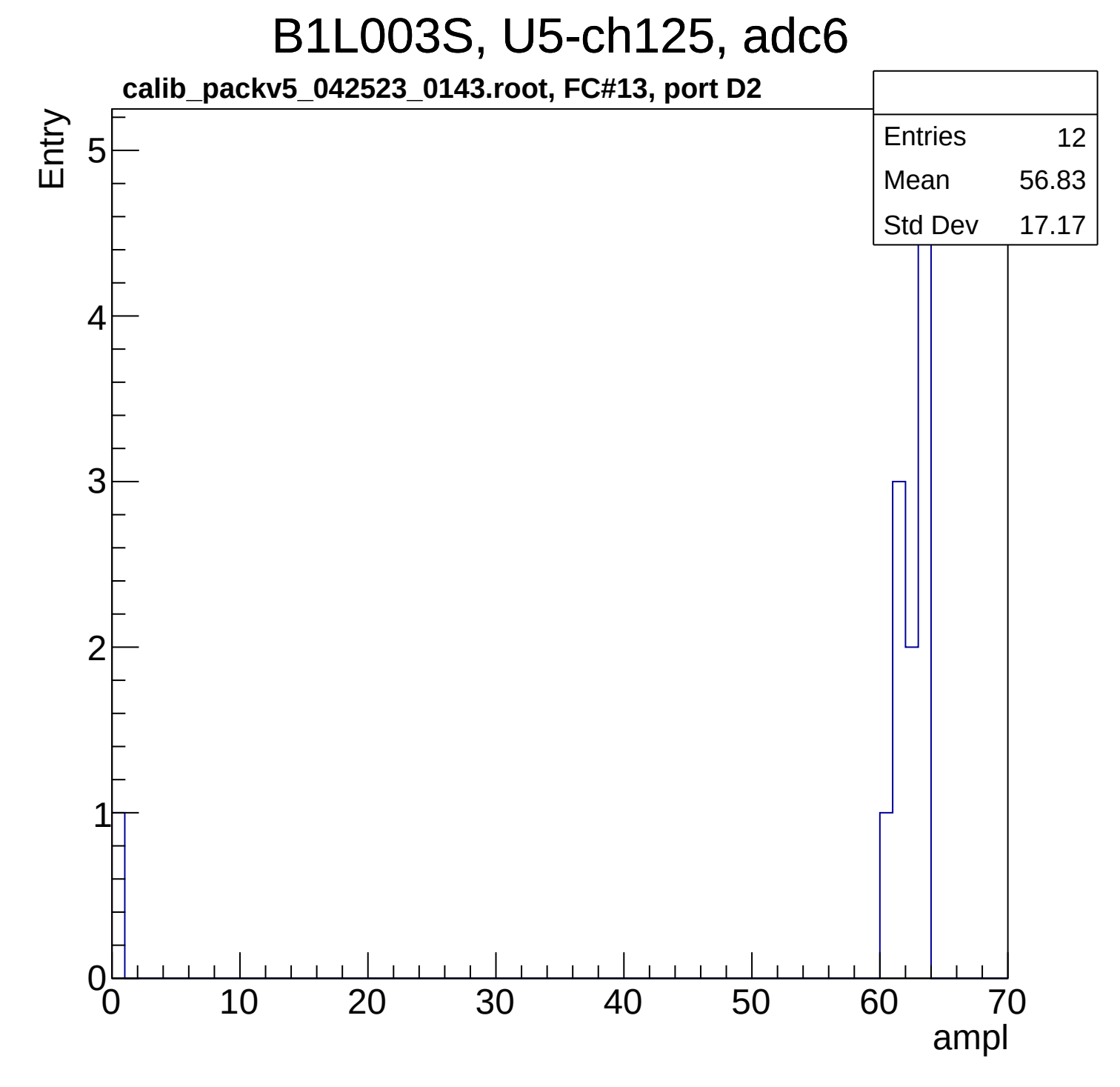
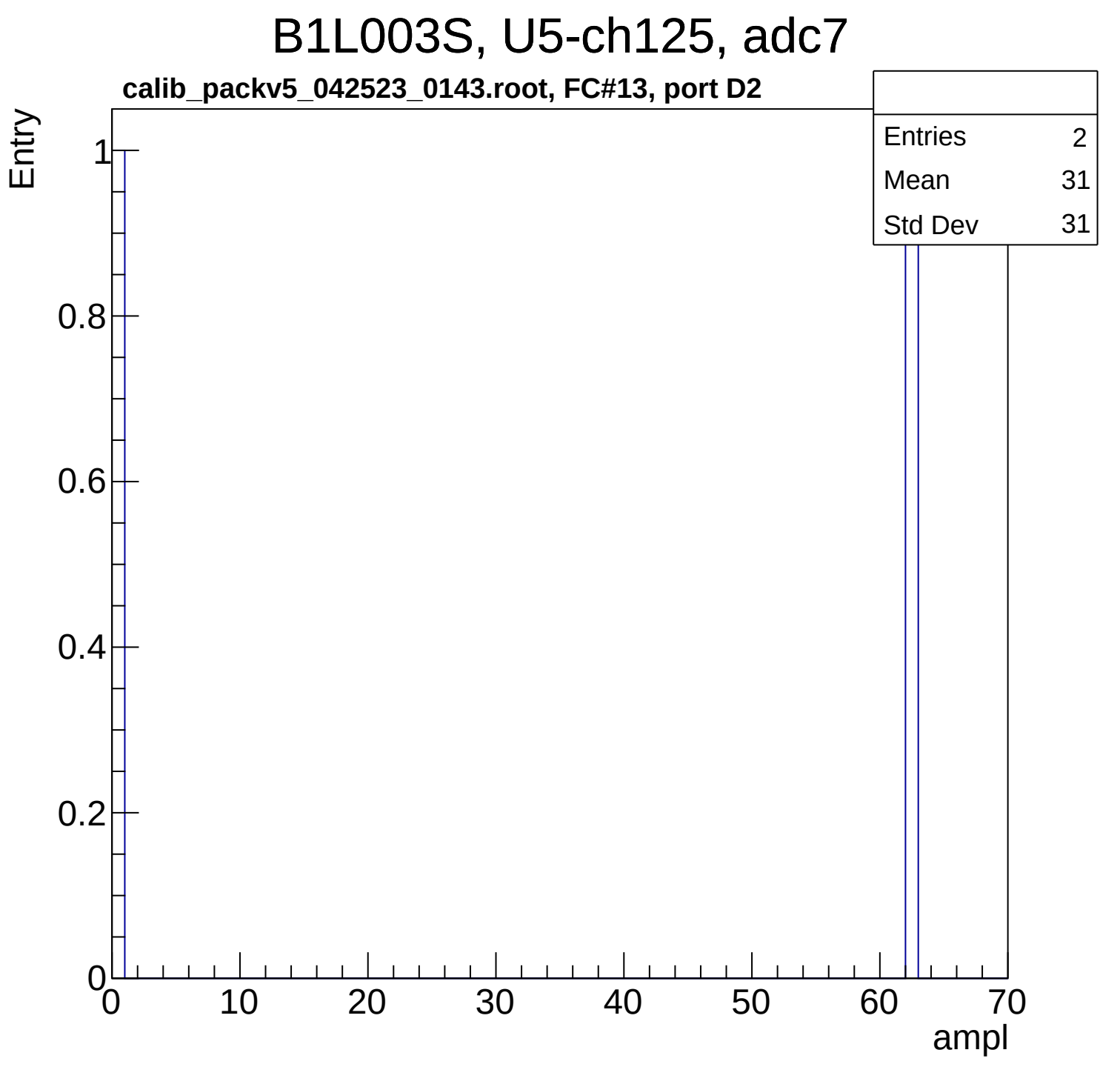

B1L003S, U5-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.31
Std Dev 2.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 60
Std Dev 2.133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U5-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
0.2
0
0
10
20
30
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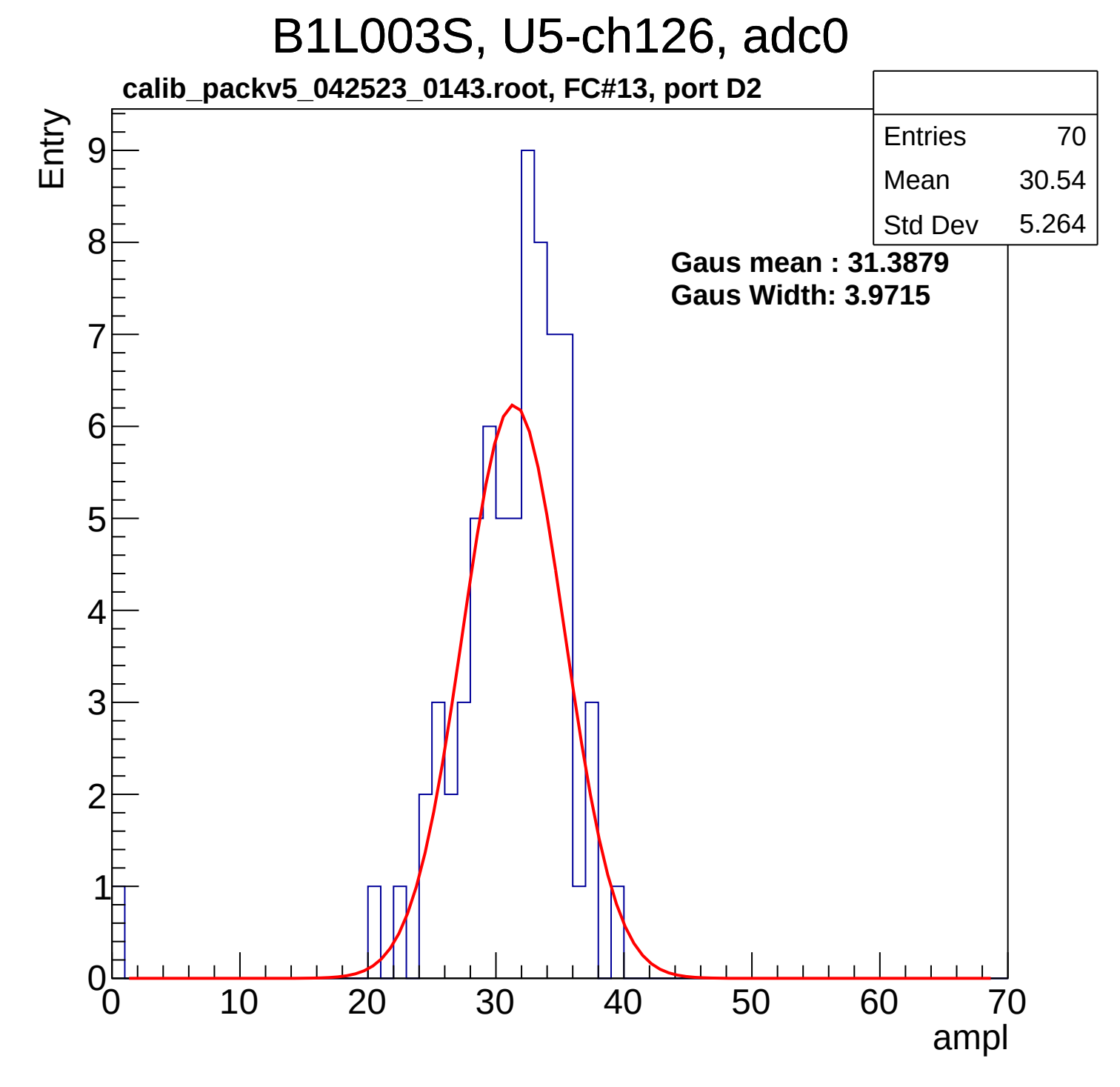
50
60
70
ampl


B1L003S, U5-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 30.54
Std Dev 5.264
Gaus mean : 31.3879
Gaus Width: 3.9715
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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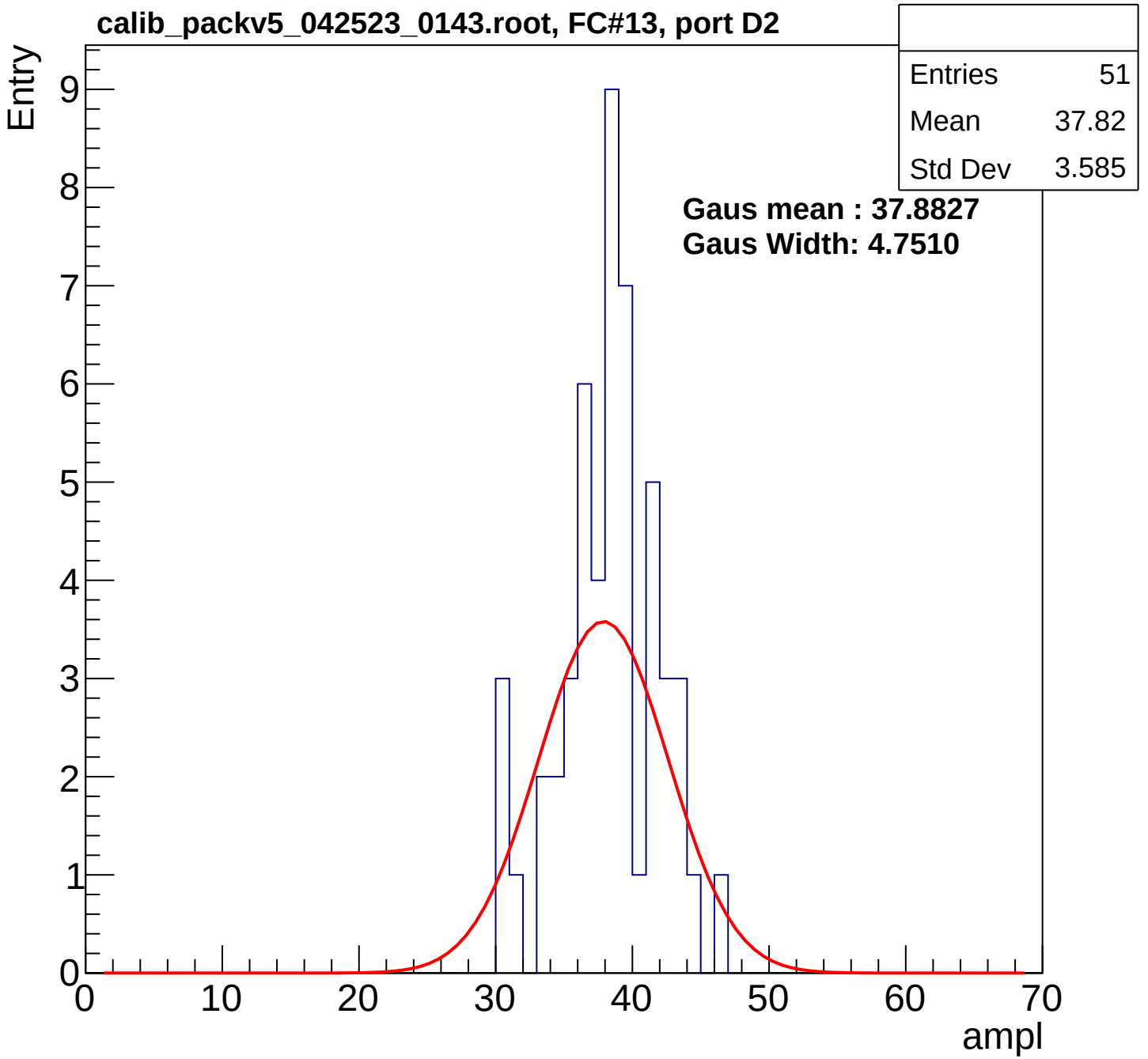
B1L003S, U5-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 37.82
Std Dev 3.585
Gaus mean : 37.8827
Gaus Width: 4.7510
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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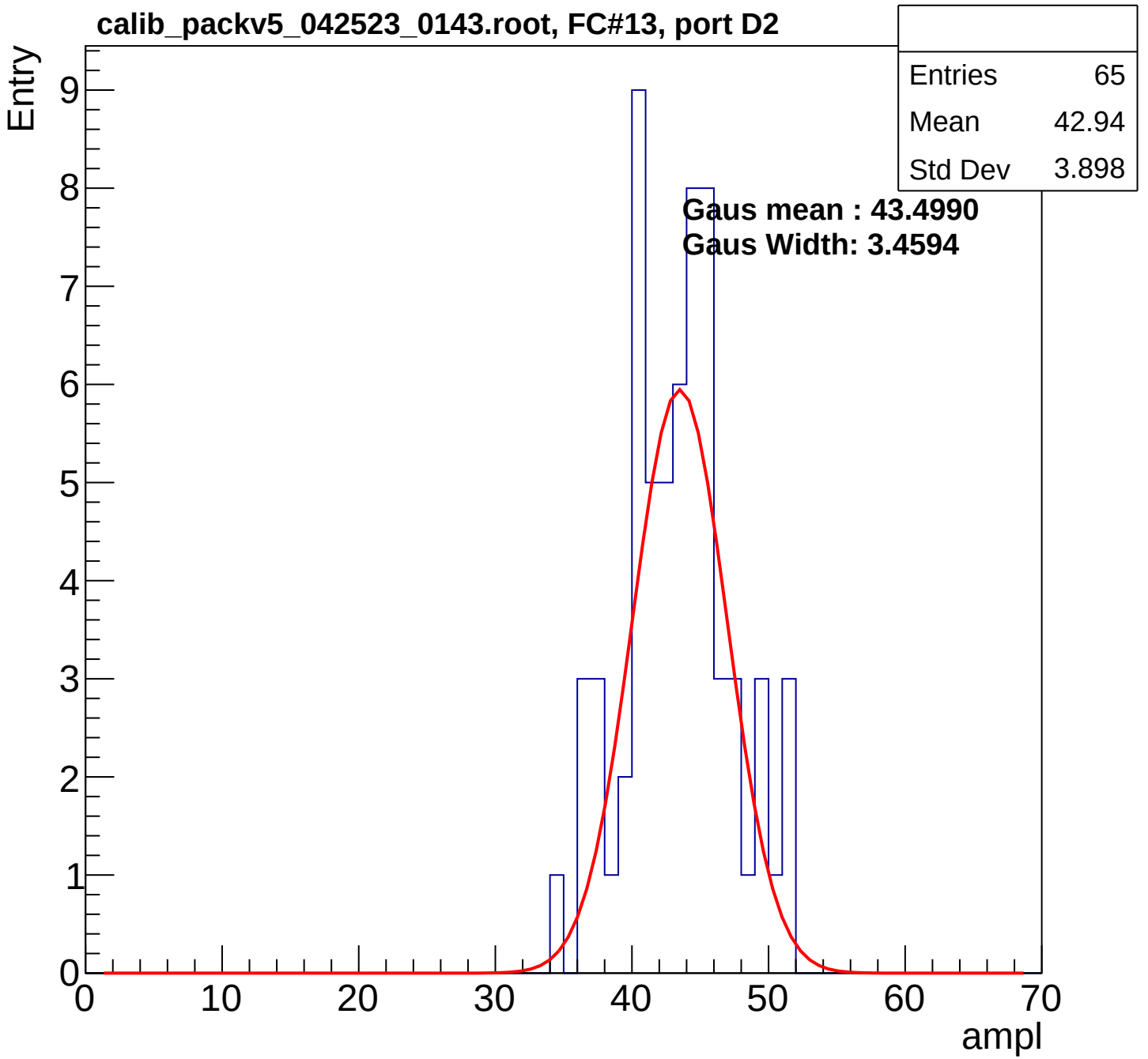
B1L003S, U5-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.94
Std Dev 3.898
Gaus mean : 43.4990
Gaus Width: 3.4594
Entry
ampl
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20
30
40
50
60
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1
2
3
4
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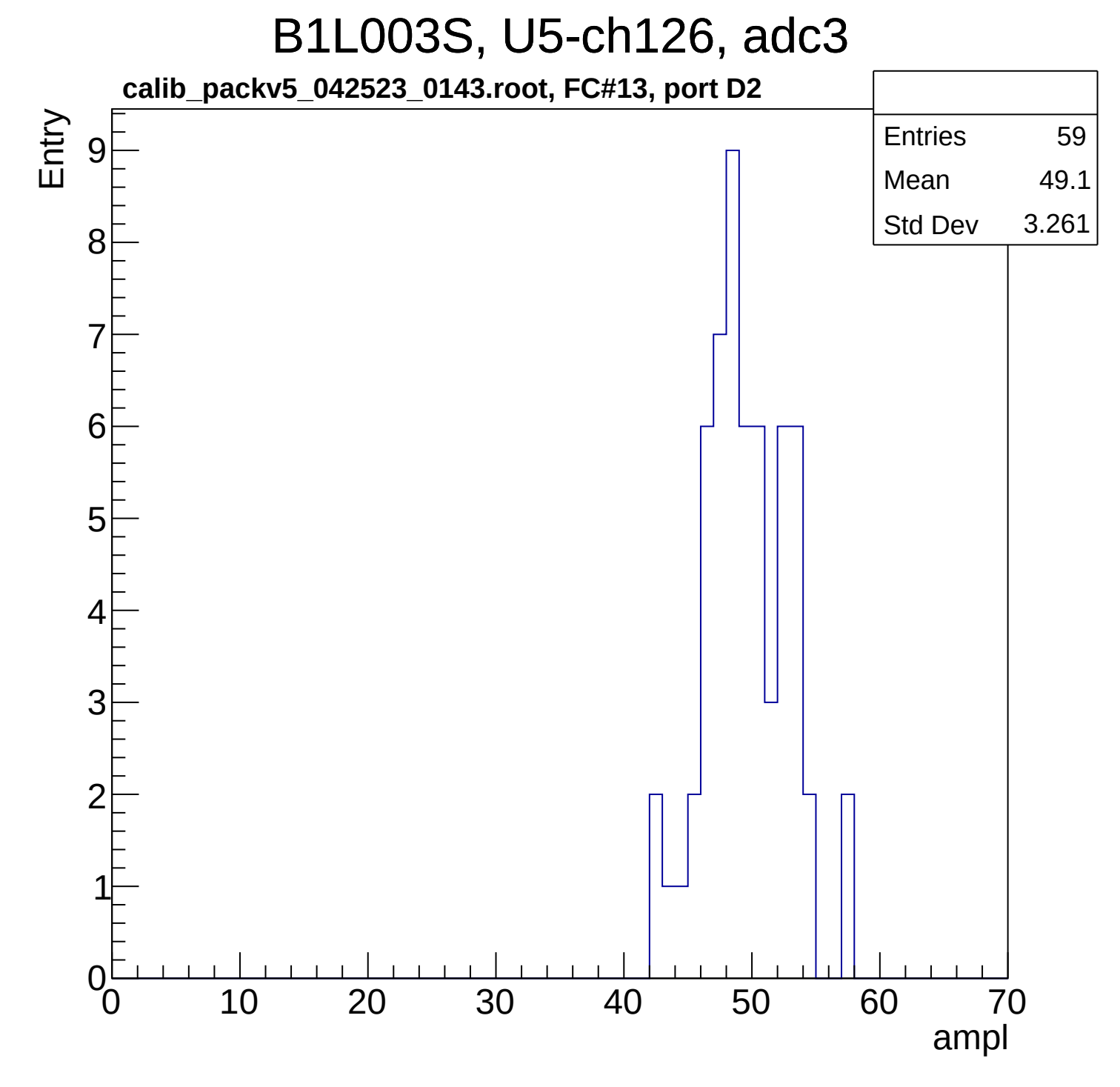
6
7
8
9


B1L003S, U5-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.1
Std Dev 3.261
Entry
ampl
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4
5
6
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0
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60
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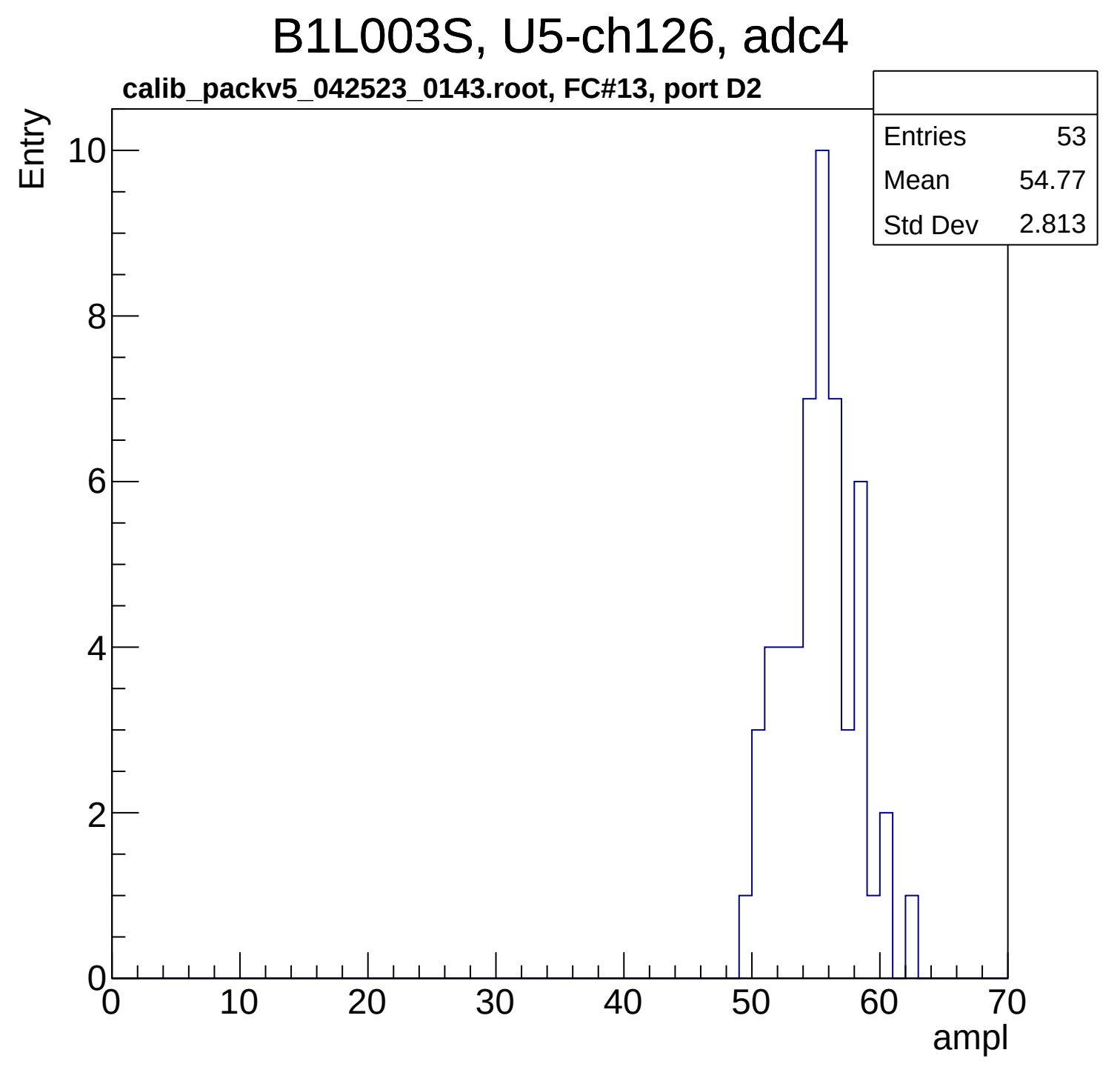
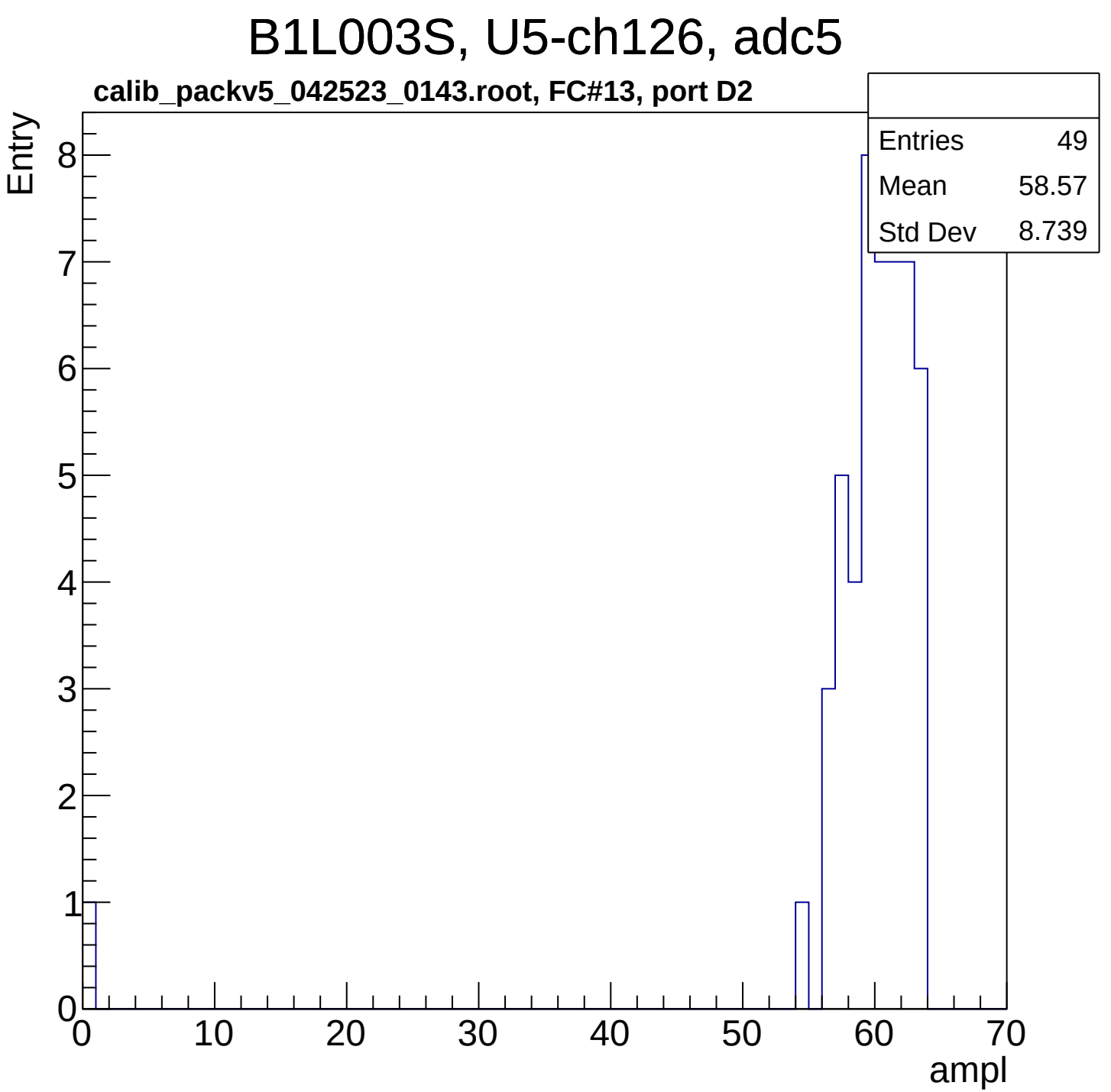

B1L003S, U5-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.77
Std Dev 2.813
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.57
Std Dev 8.739
Entry
ampl
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0
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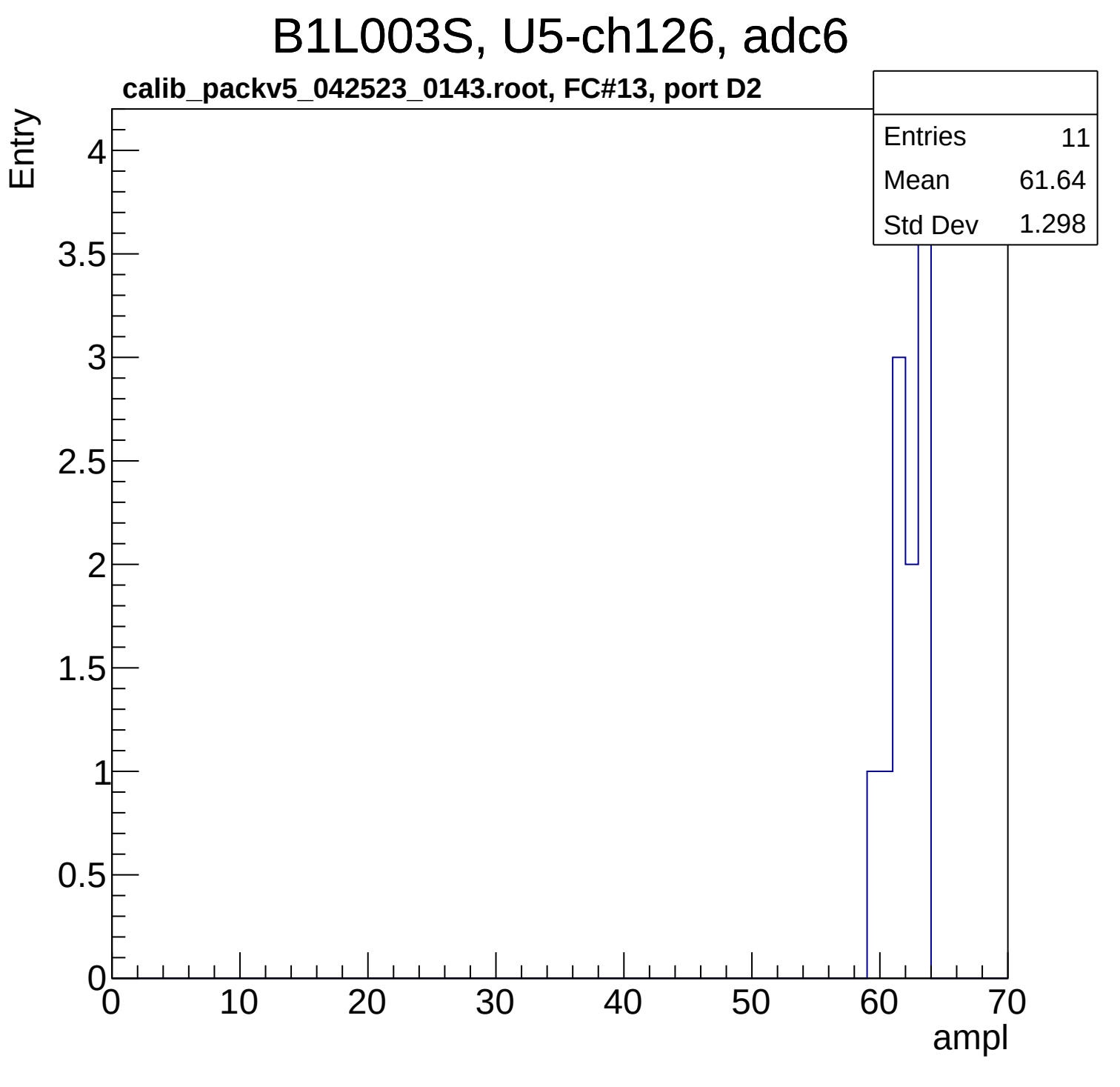
B1L003S, U5-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.64
Std Dev 1.298
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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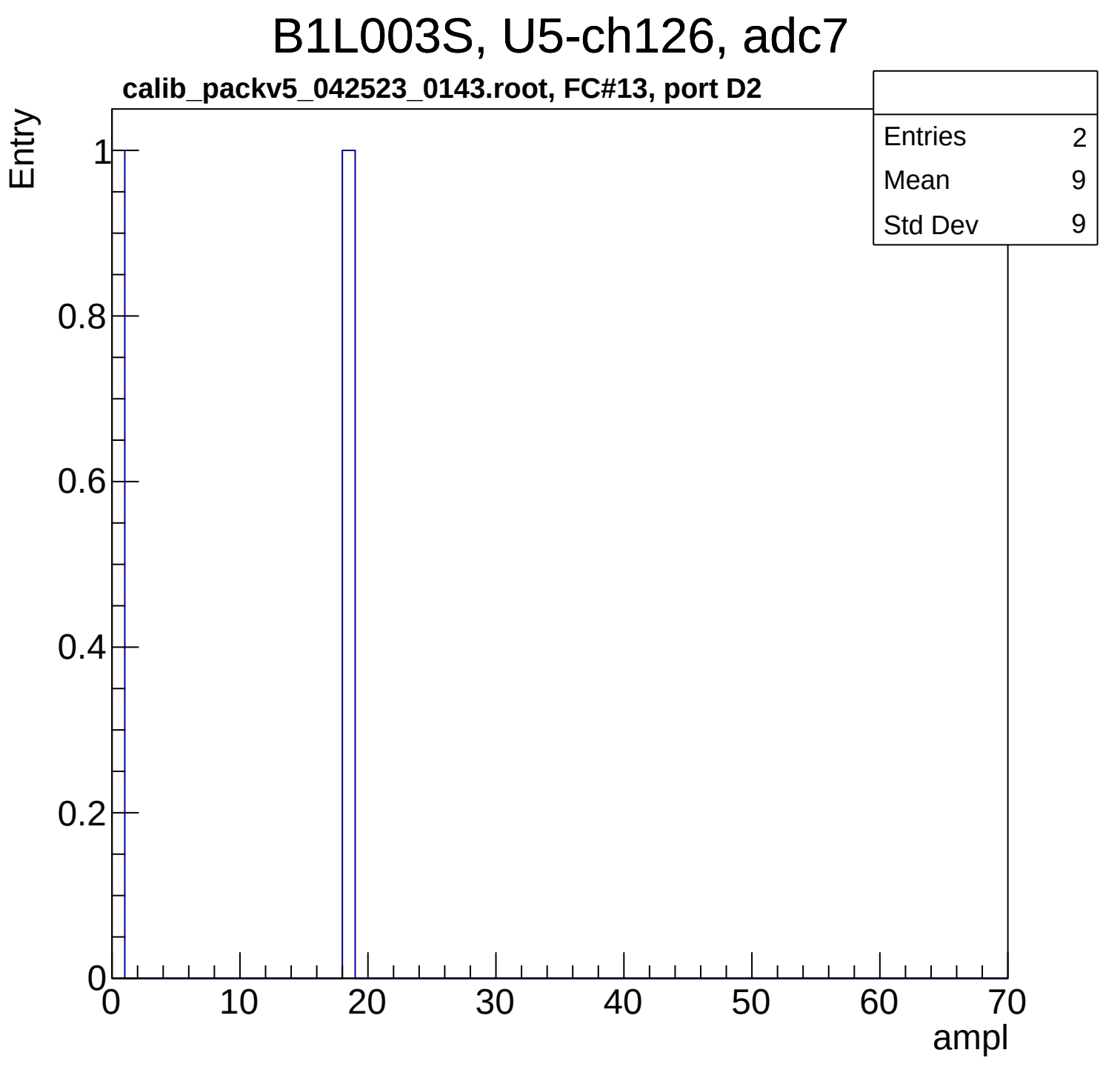
B1L003S, U5-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 9
Std Dev 9
Entry
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0.6
0.4
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0
0
10
20
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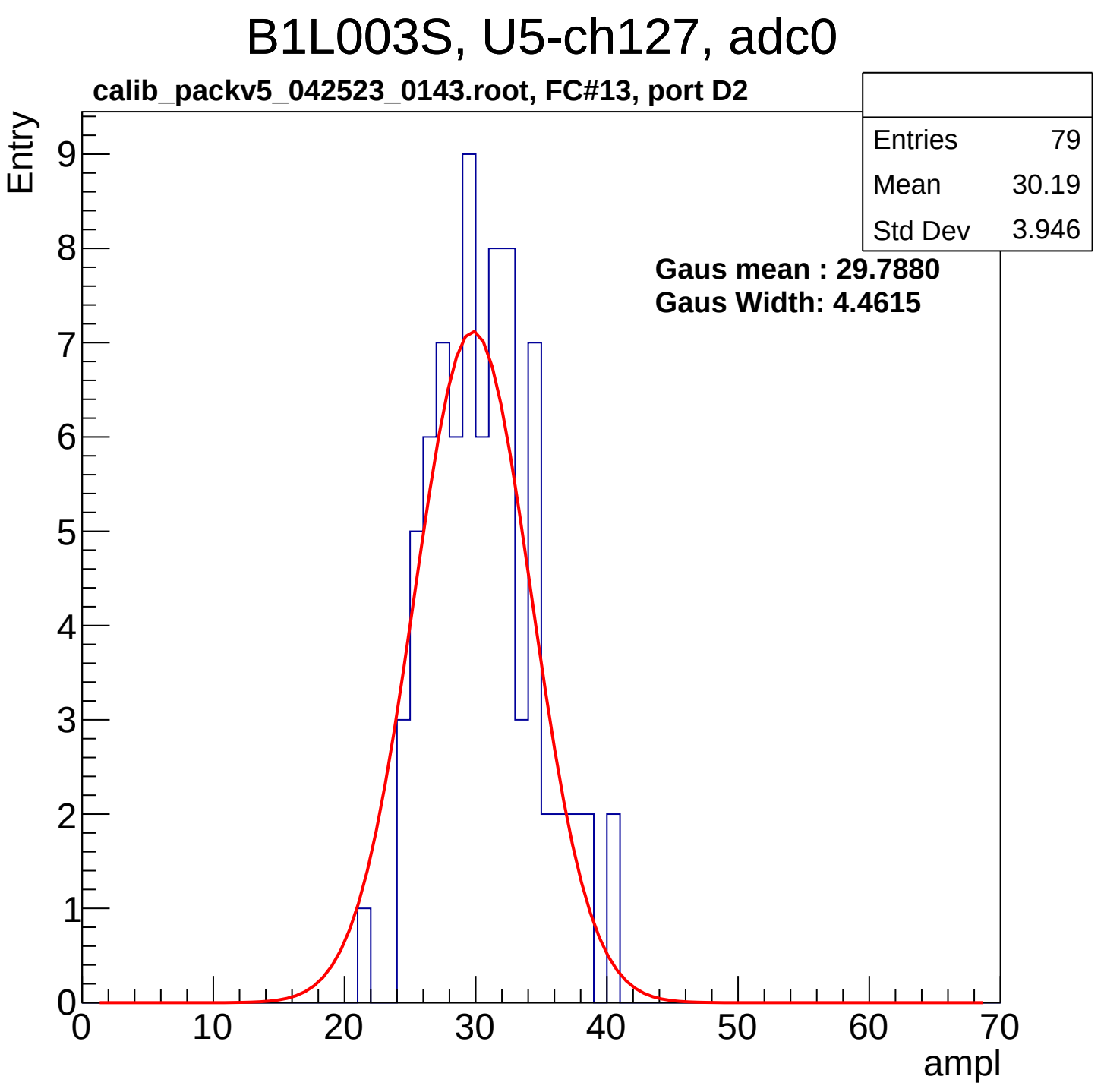
40
50
60
70
ampl


B1L003S, U5-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 30.19
Std Dev 3.946
Gaus mean : 29.7880
Gaus Width: 4.4615
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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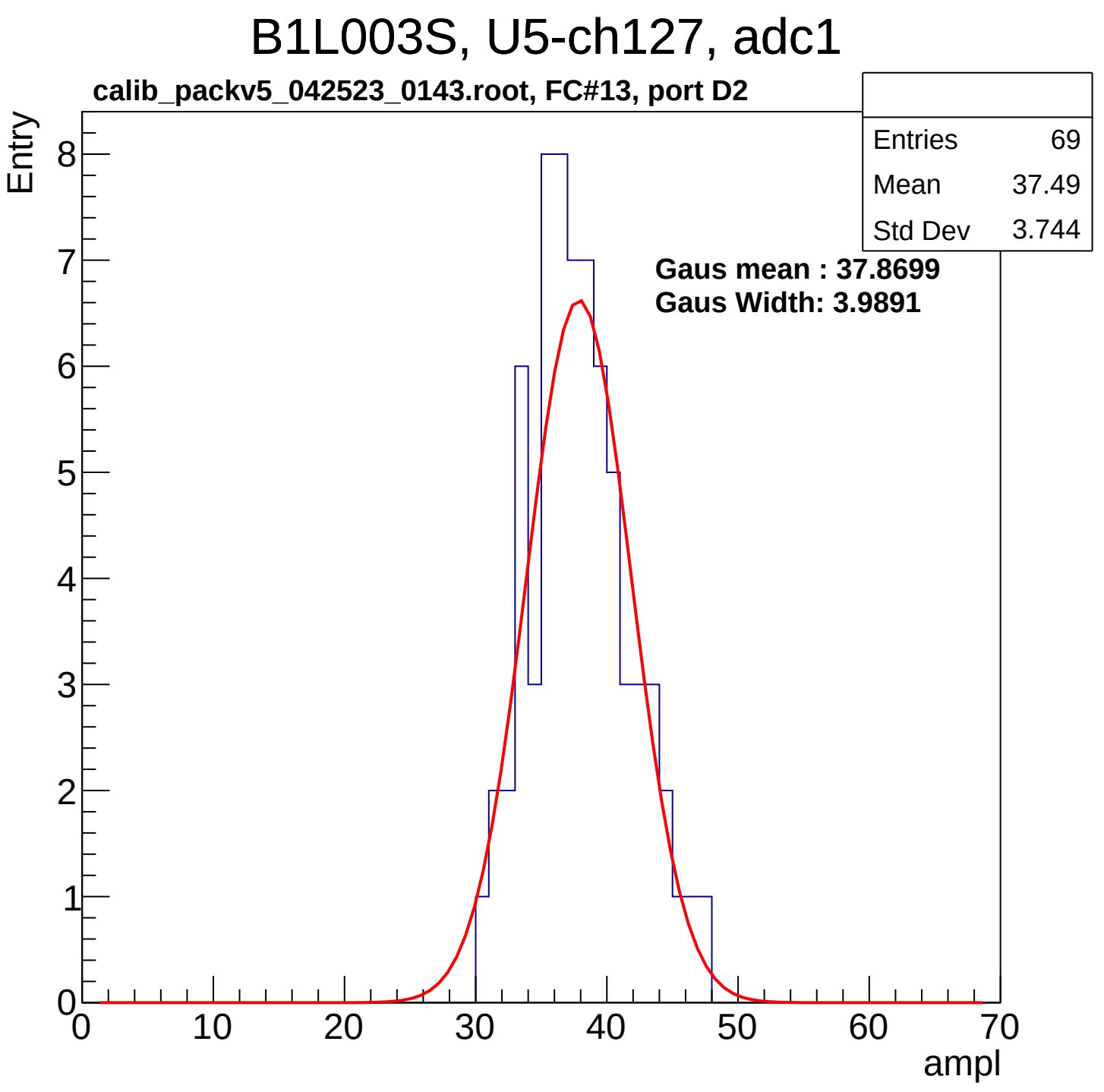
B1L003S, U5-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 37.49
Std Dev 3.744
Gaus mean : 37.8699
Gaus Width: 3.9891
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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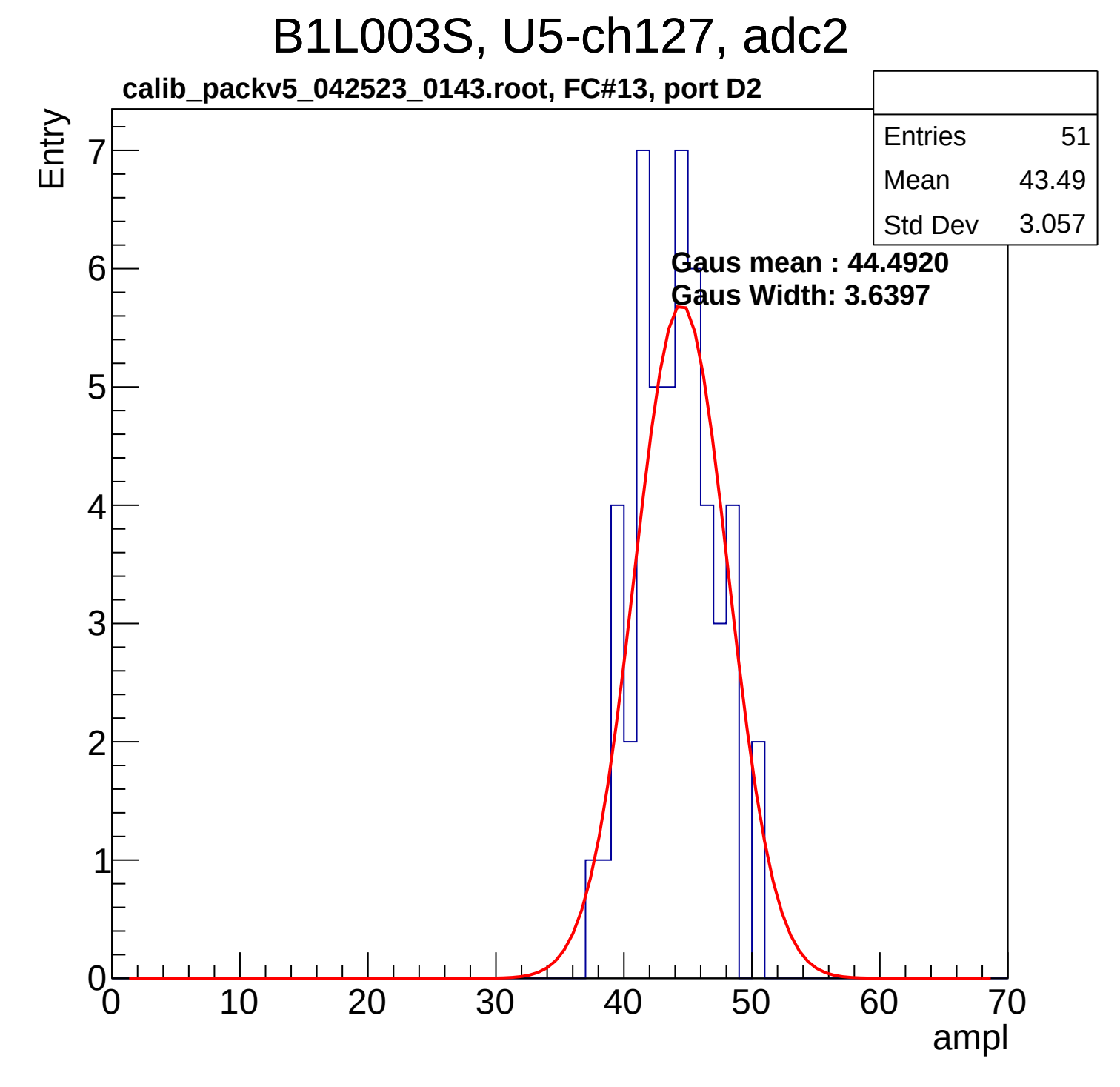
B1L003S, U5-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.49
Std Dev 3.057
Gaus mean : 44.4920
Gaus Width: 3.6397
Entry
ampl
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1
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3
4
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0
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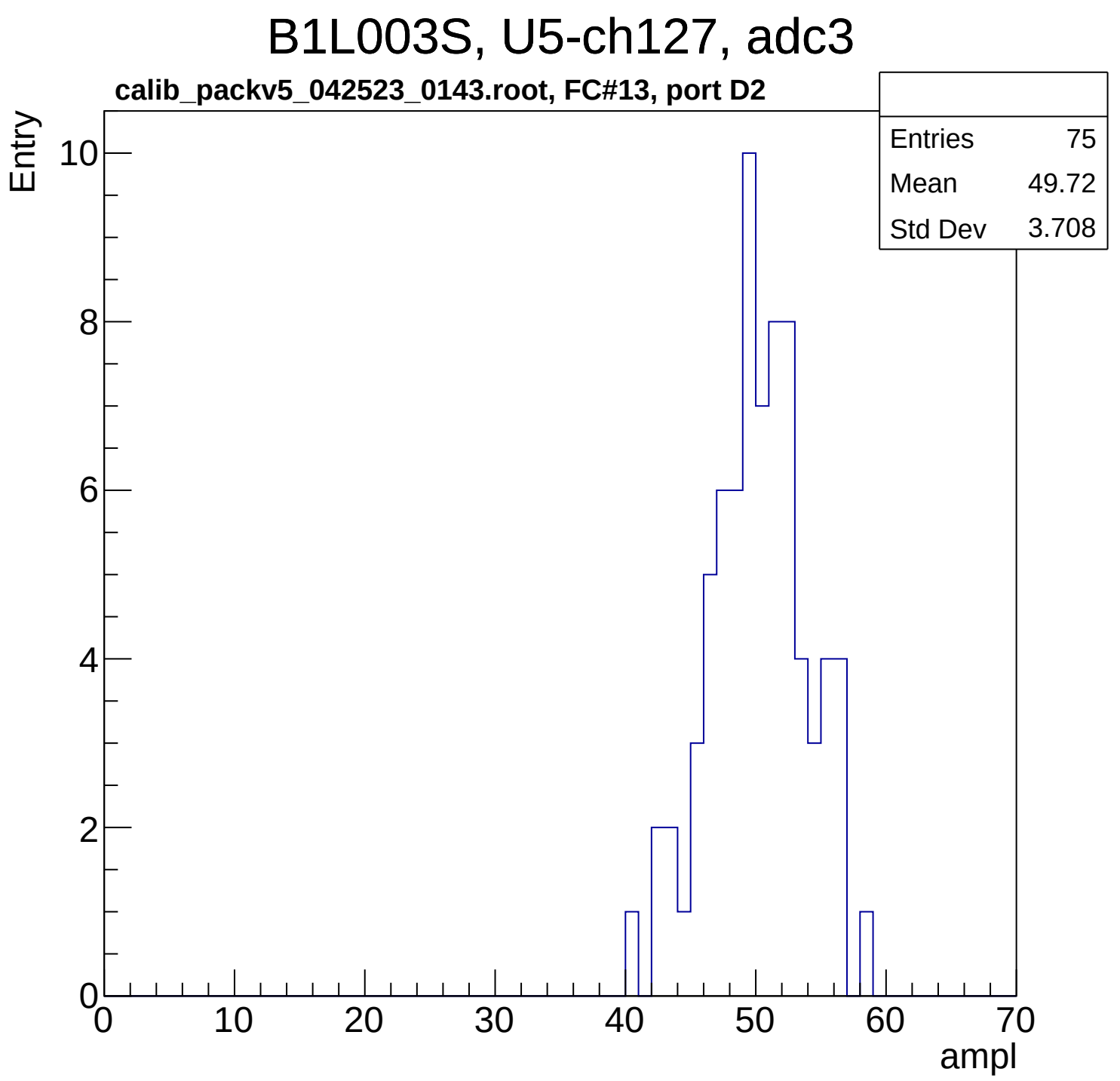
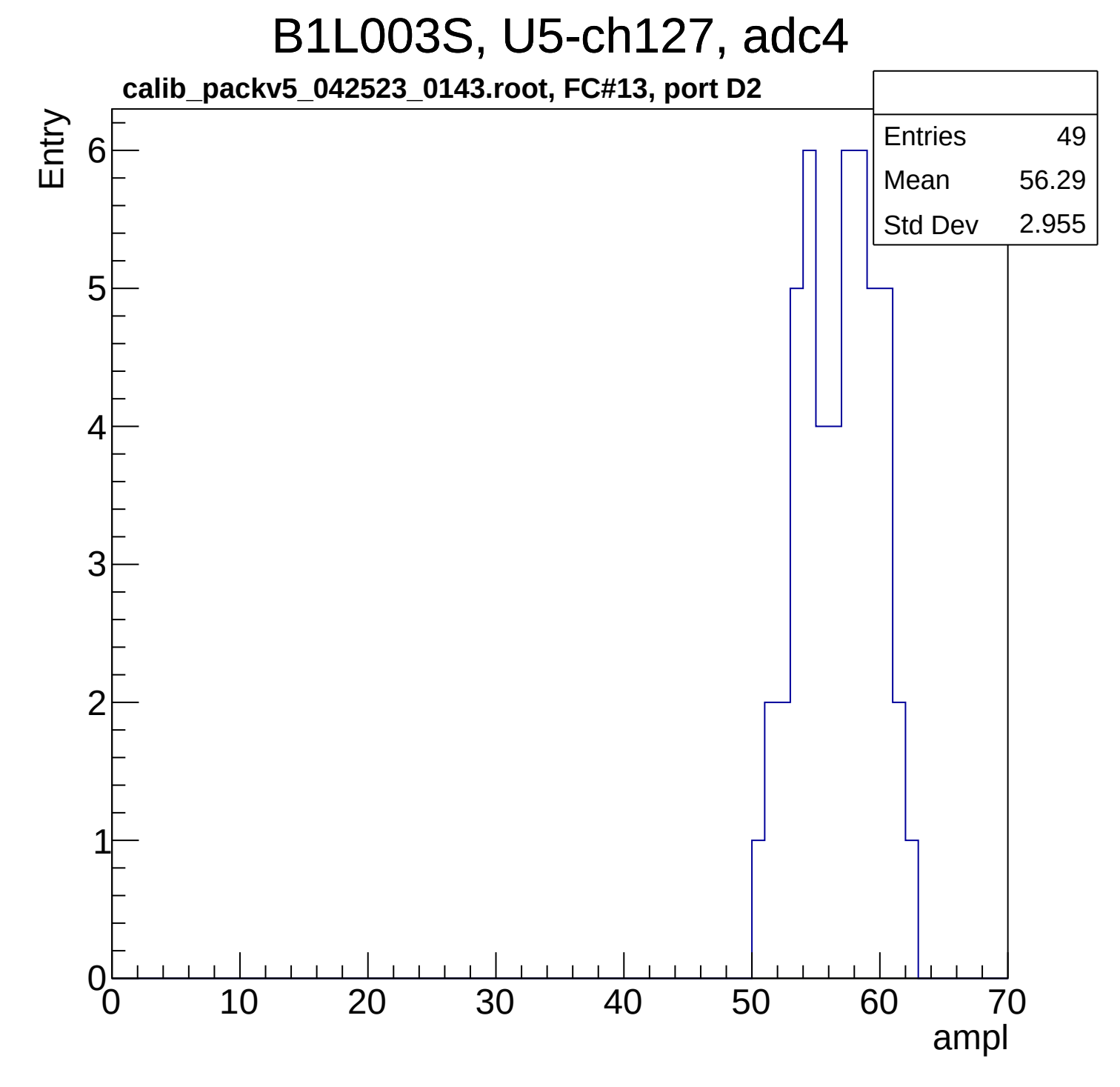
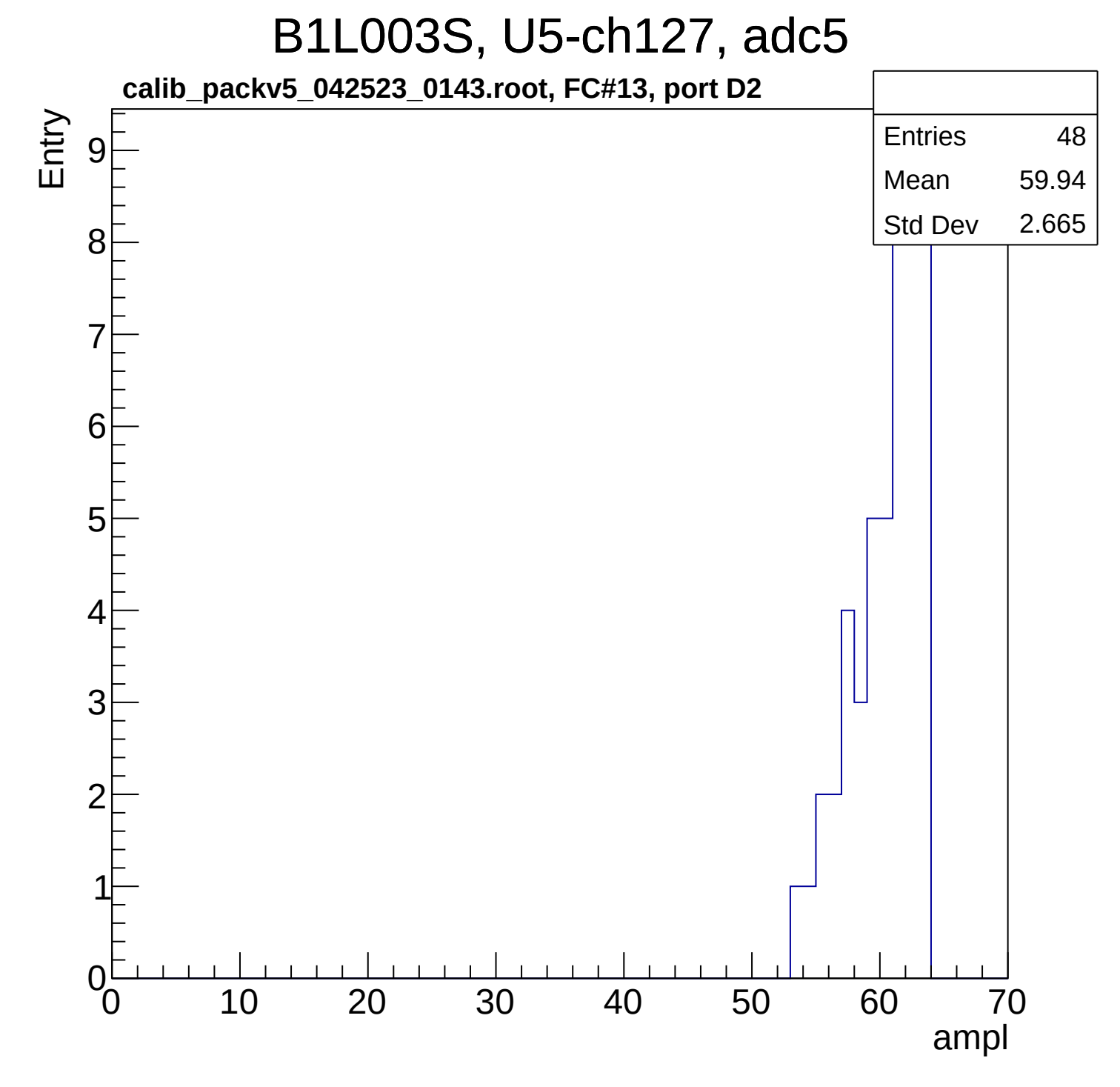
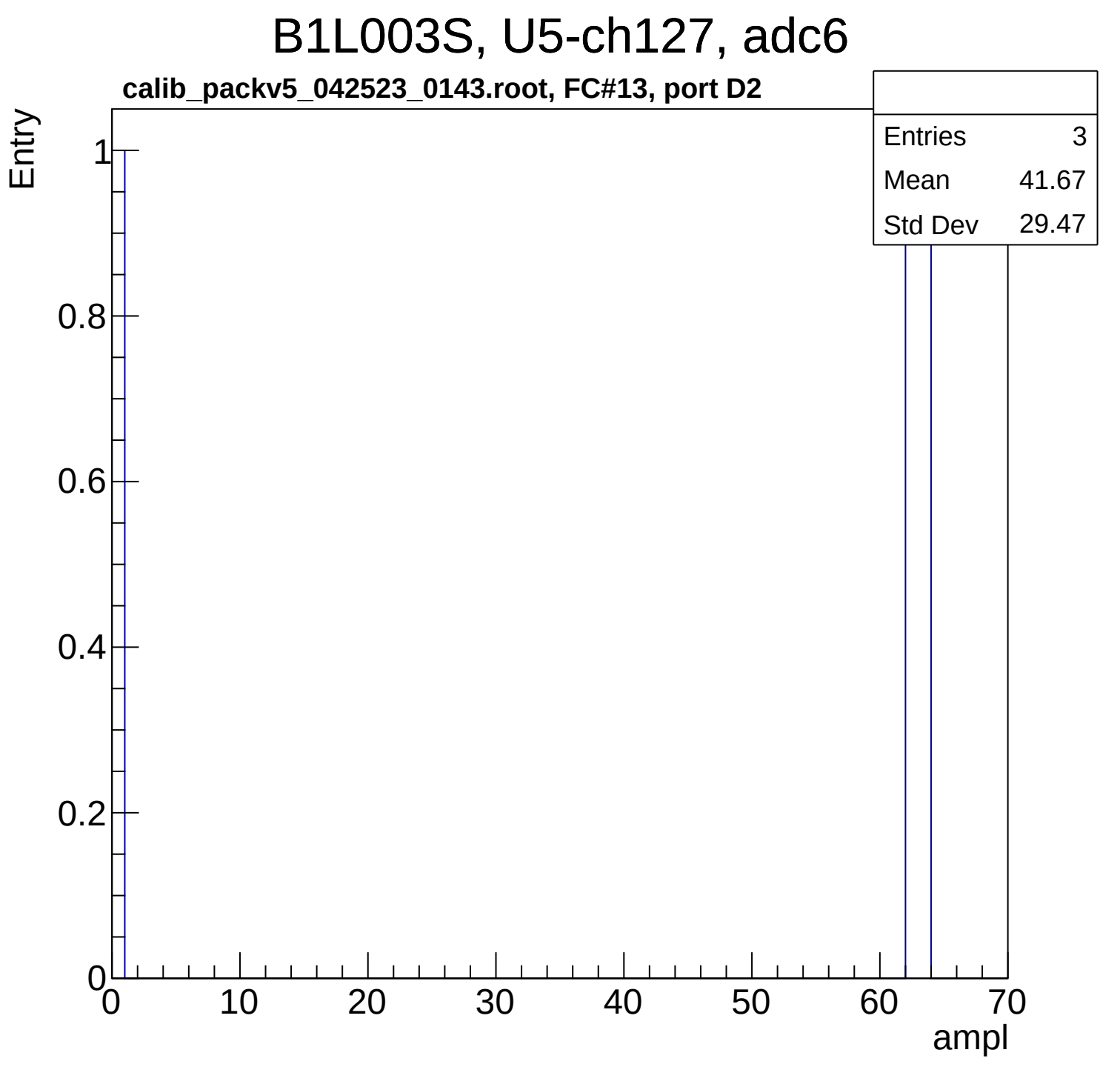
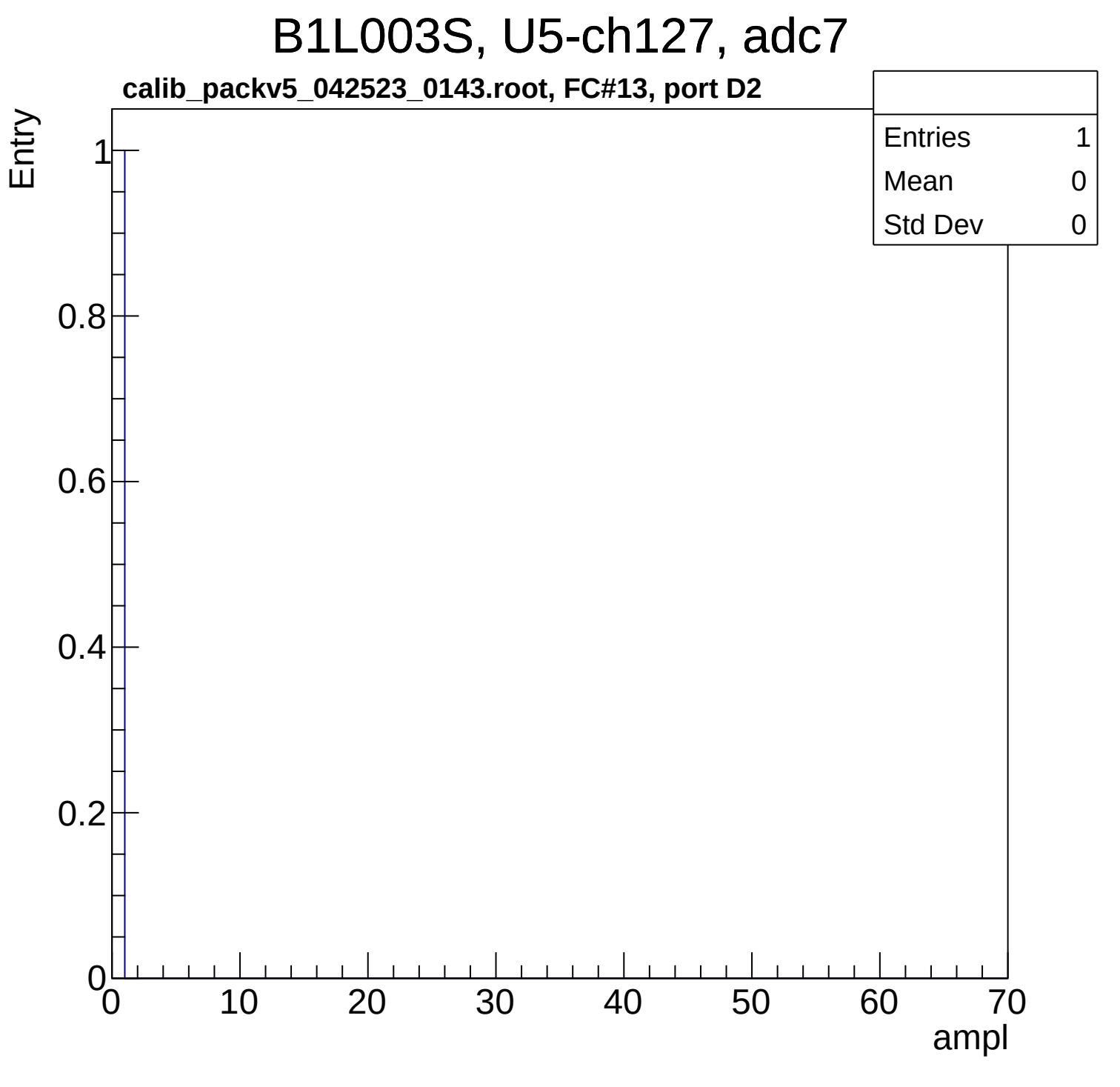
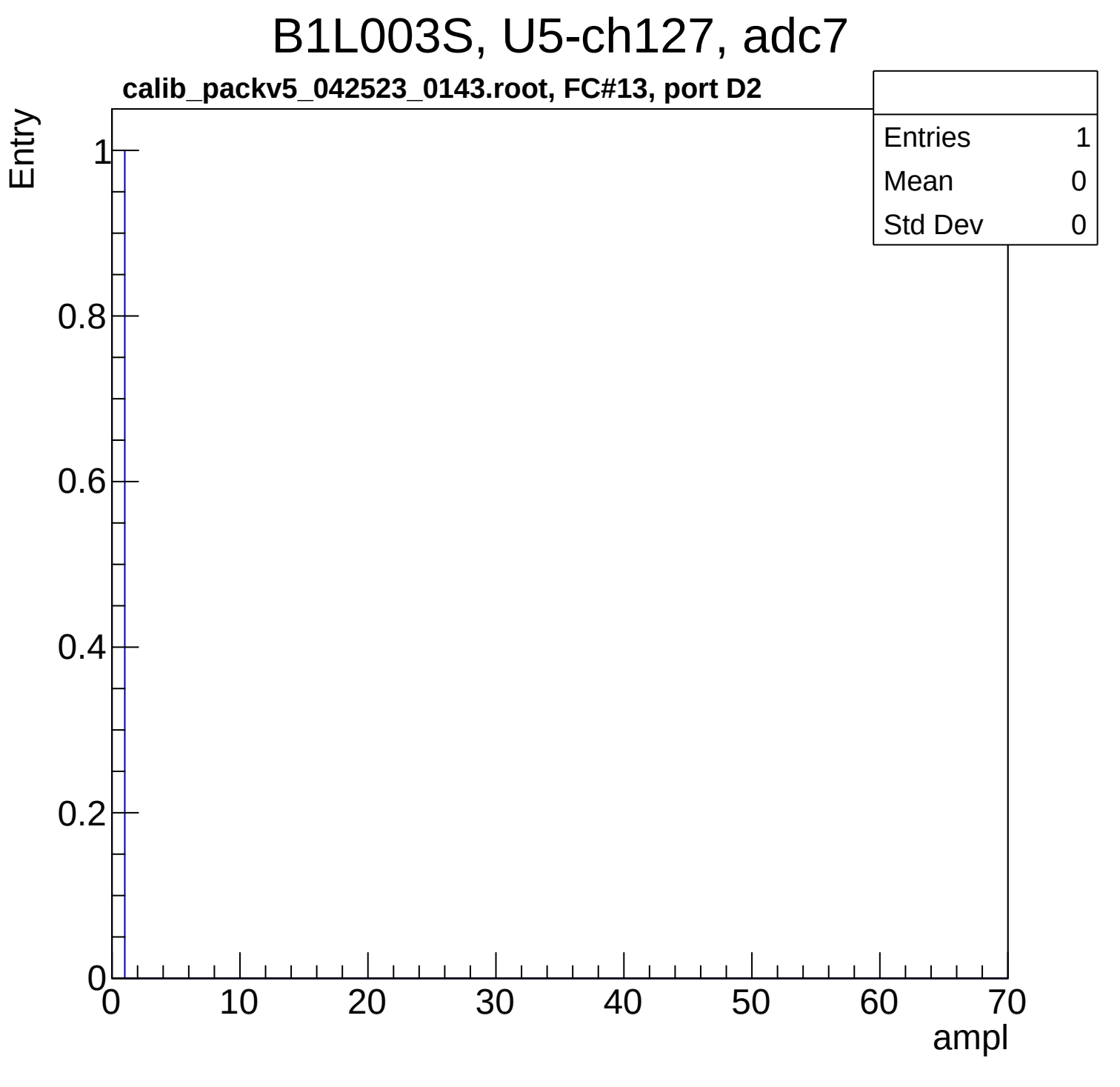

B1L003S, U5-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.72
Std Dev 3.708
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U5-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 56.29
Std Dev 2.955
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.94
Std Dev 2.665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U5-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl