

B1L002S, U3-ch0

calib_packv5_042523_0143.root, FC#10, port B3

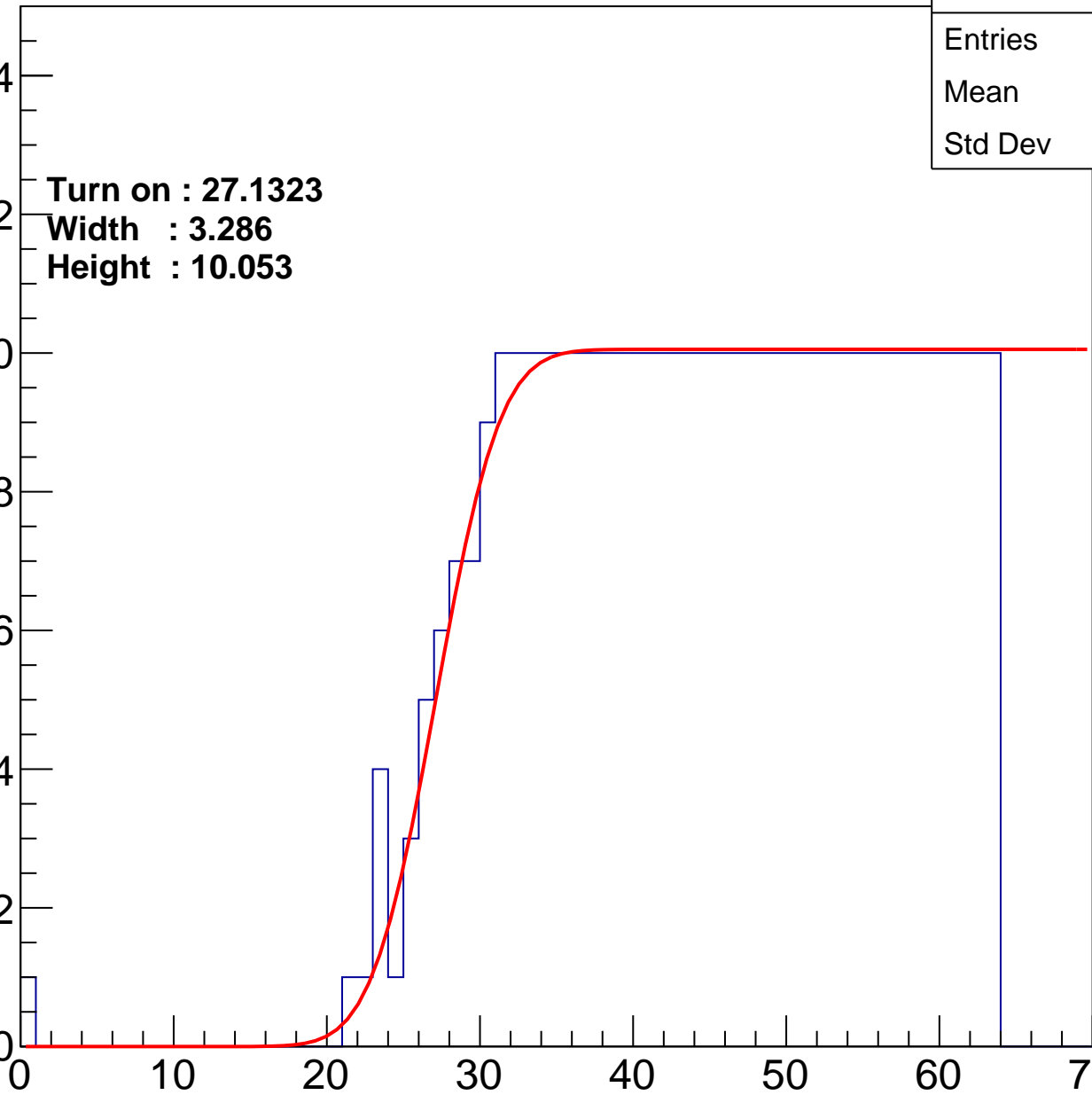
Entry

14
12
10
8
6
4
2
0

Turn on : 27.1323
Width : 3.286
Height : 10.053

Entries	375
Mean	44.55
Std Dev	11.25

ampl



B1L002S, U3-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.33
Std Dev	11.7

Turn on : 27.1980

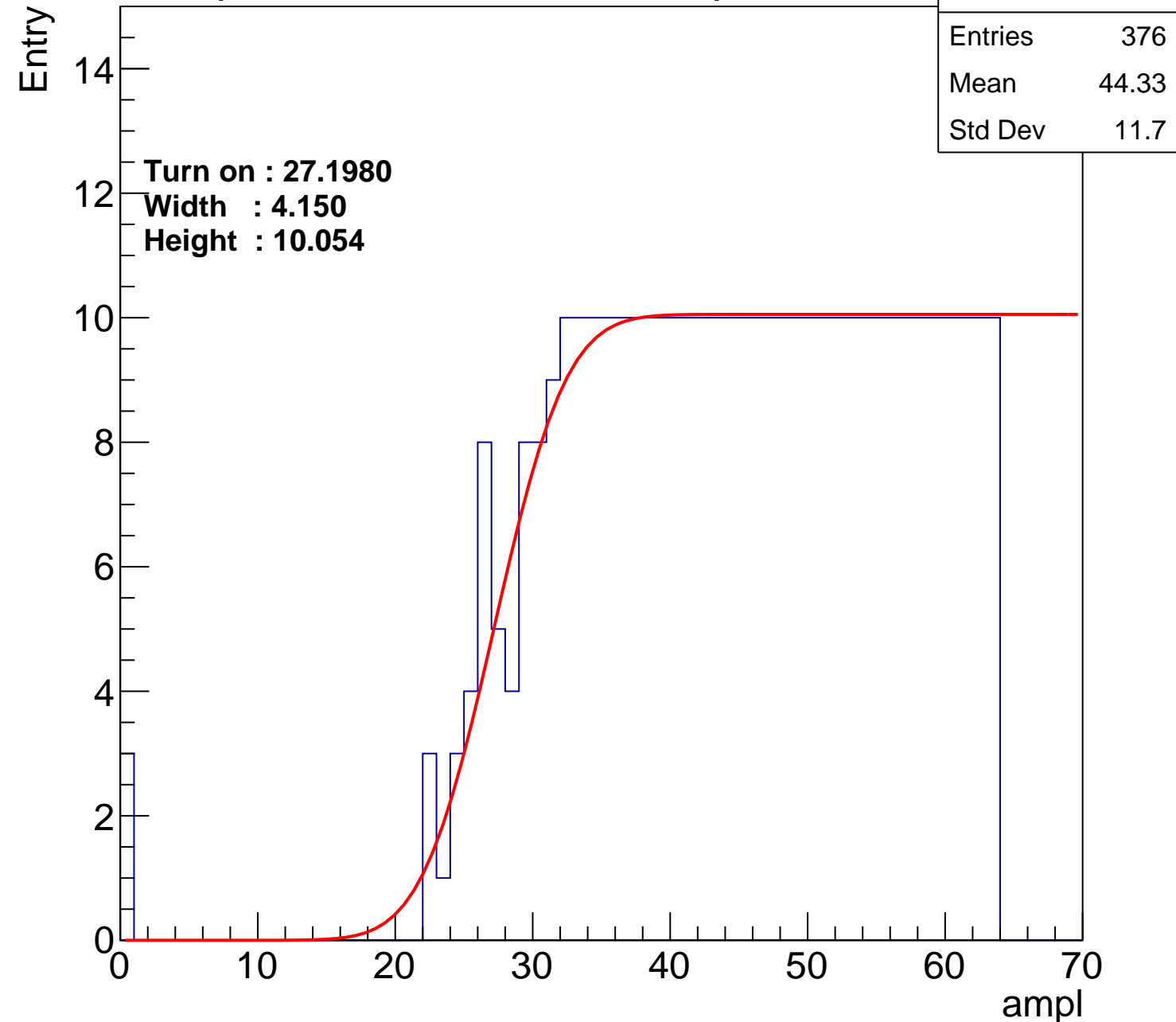
Width : 4.150

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.12
Std Dev	12.08

Turn on : 23.9147

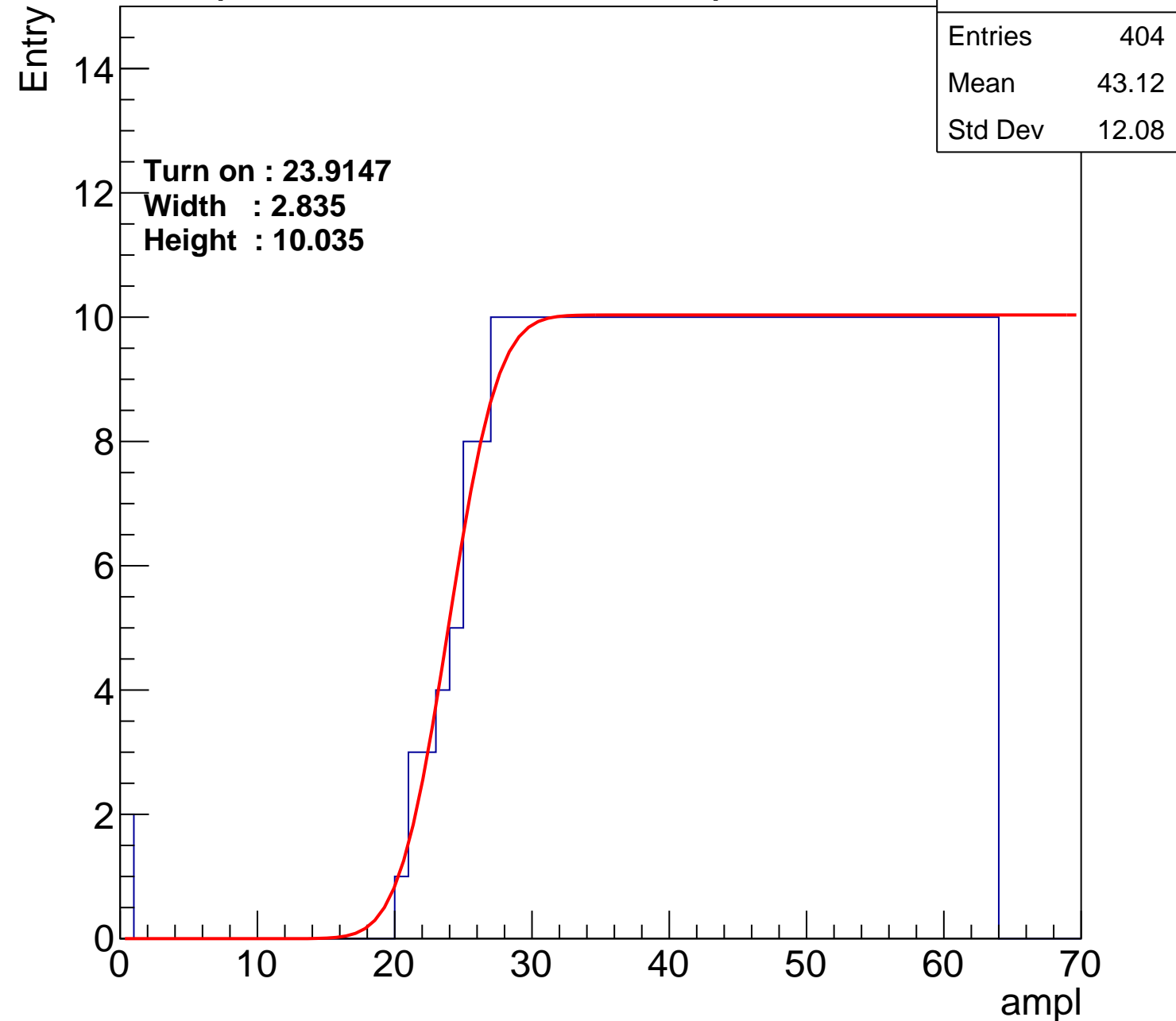
Width : 2.835

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.26
Std Dev	12.28

Turn on : 24.6367

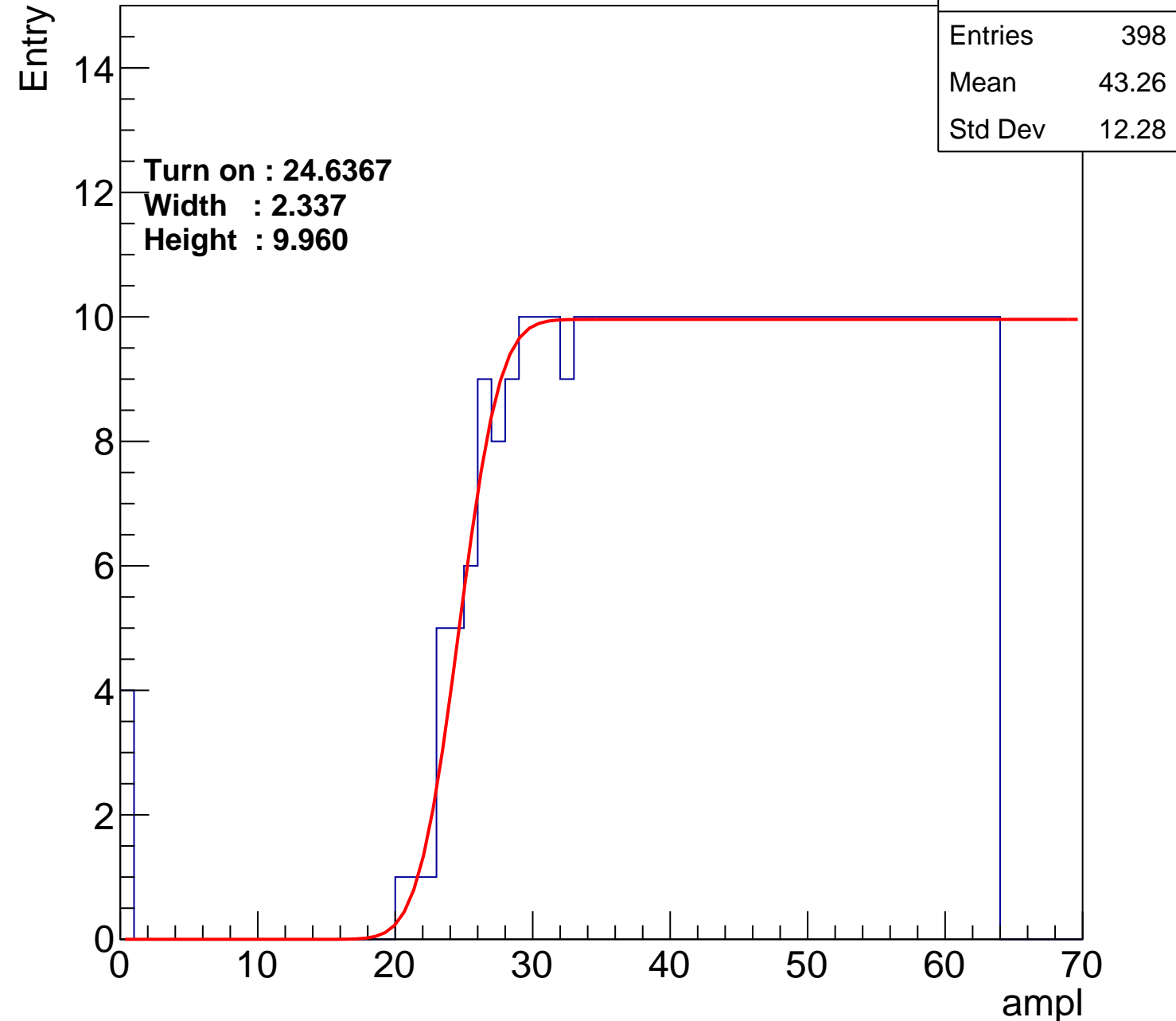
Width : 2.337

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch4

calib_packv5_042523_0143.root, FC#10, port B3

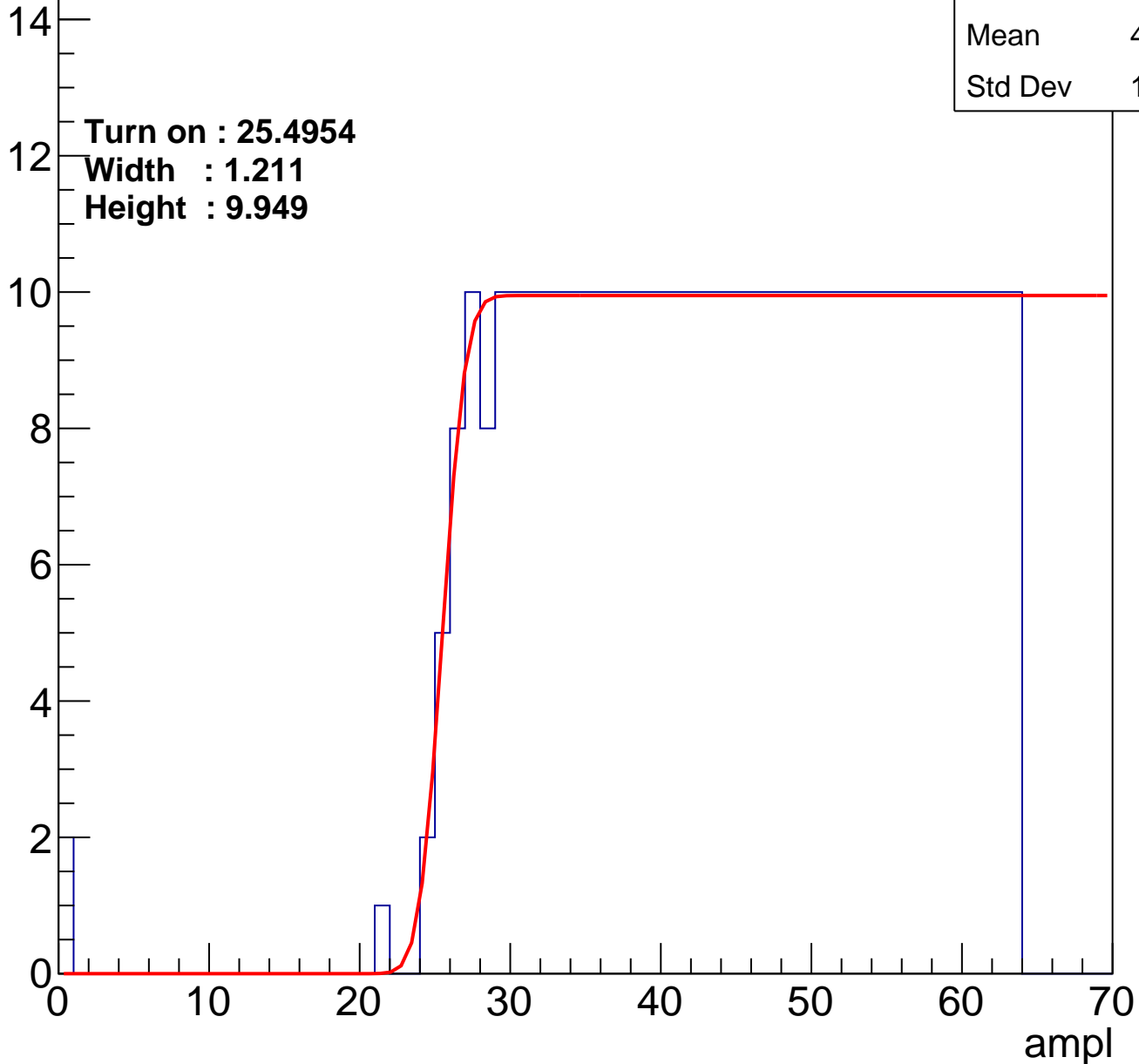
Entries	386
Mean	44.03
Std Dev	11.57

Turn on : 25.4954

Width : 1.211

Height : 9.949

Entry



B1L002S, U3-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.5
Std Dev	12.12

Turn on : 24.8170

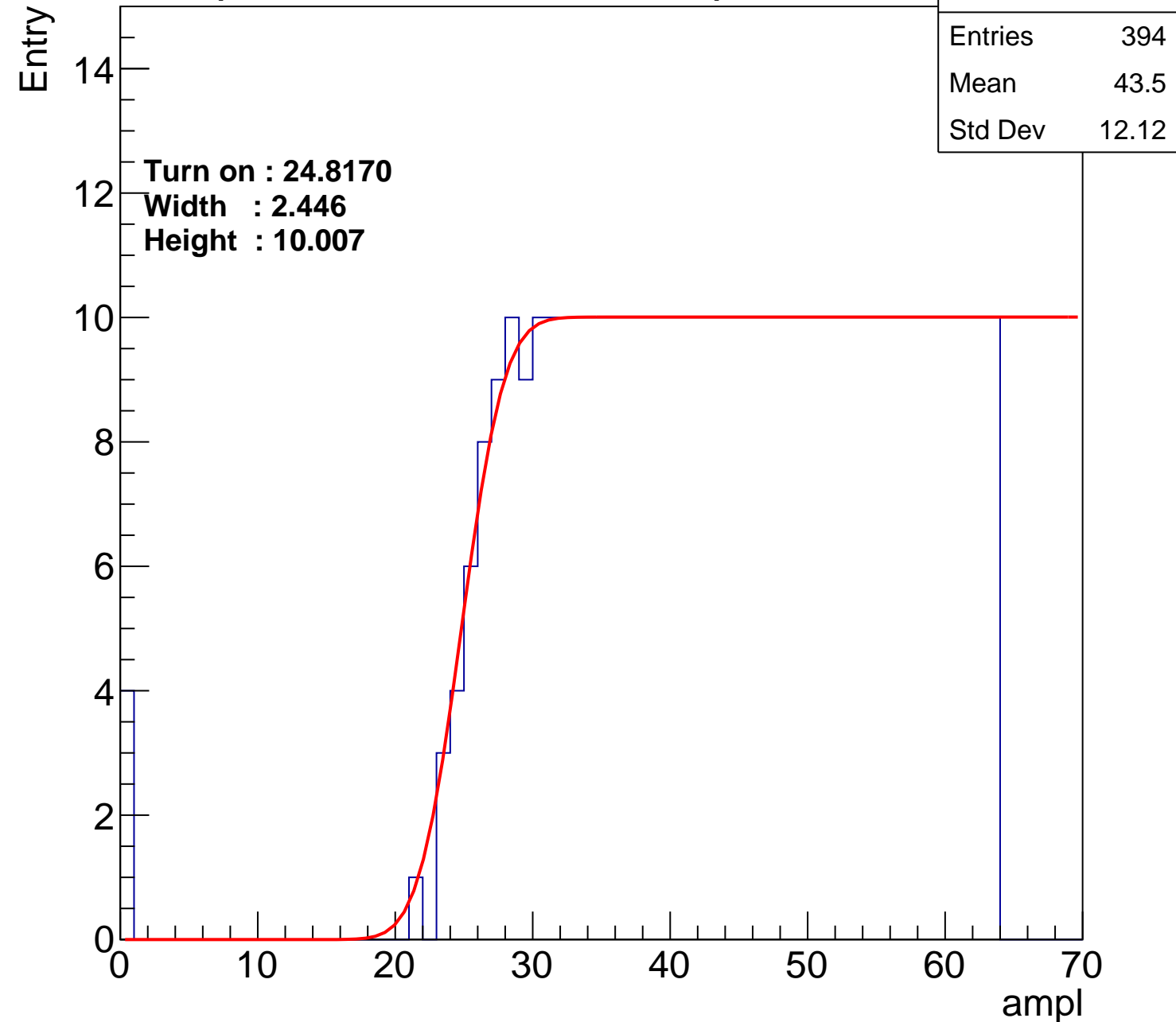
Width : 2.446

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch6

calib_packv5_042523_0143.root, FC#10, port B3

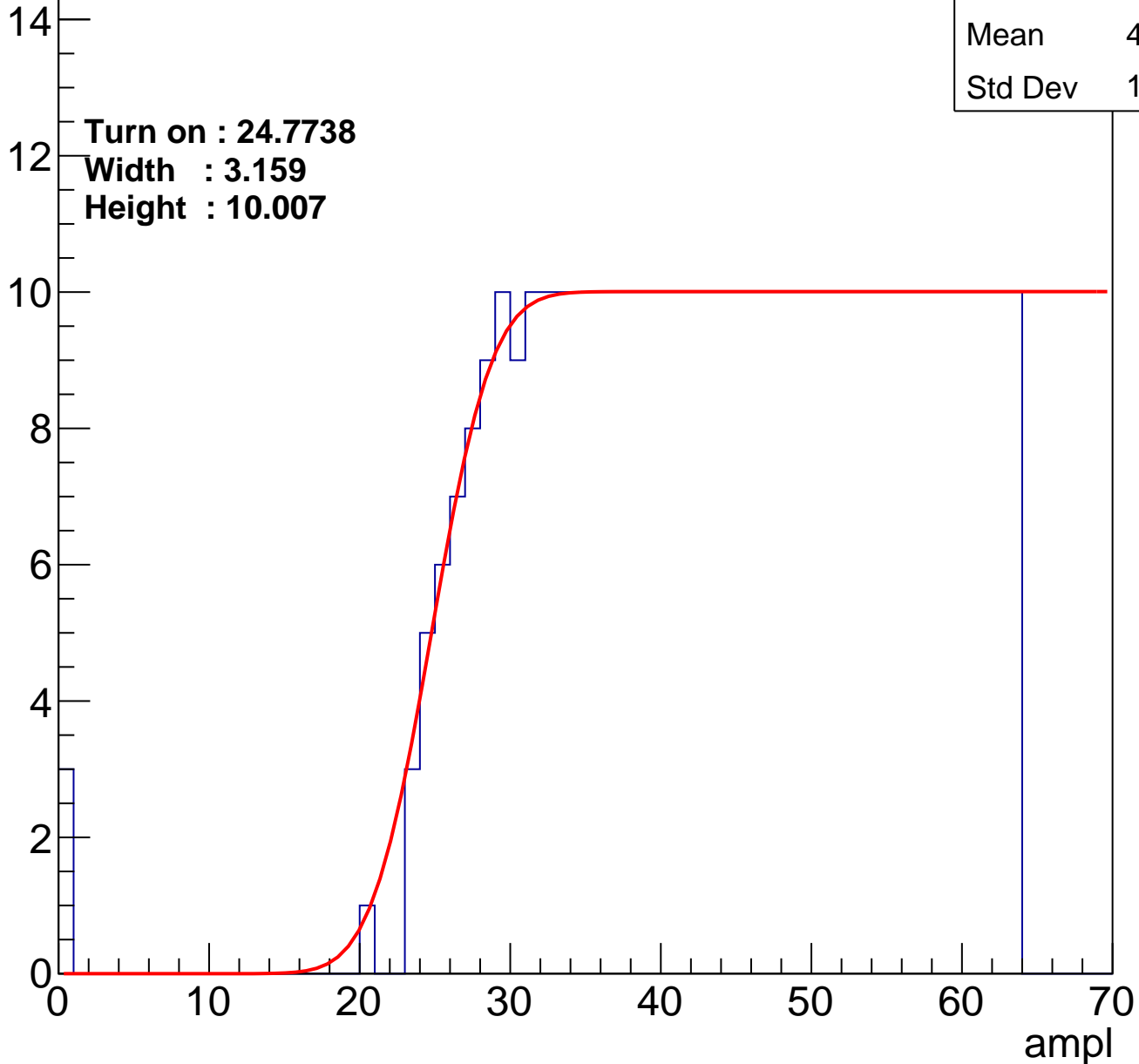
Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 24.7738

Width : 3.159

Height : 10.007

Entry



B1L002S, U3-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.63
Std Dev	11.83

Turn on : 25.0561

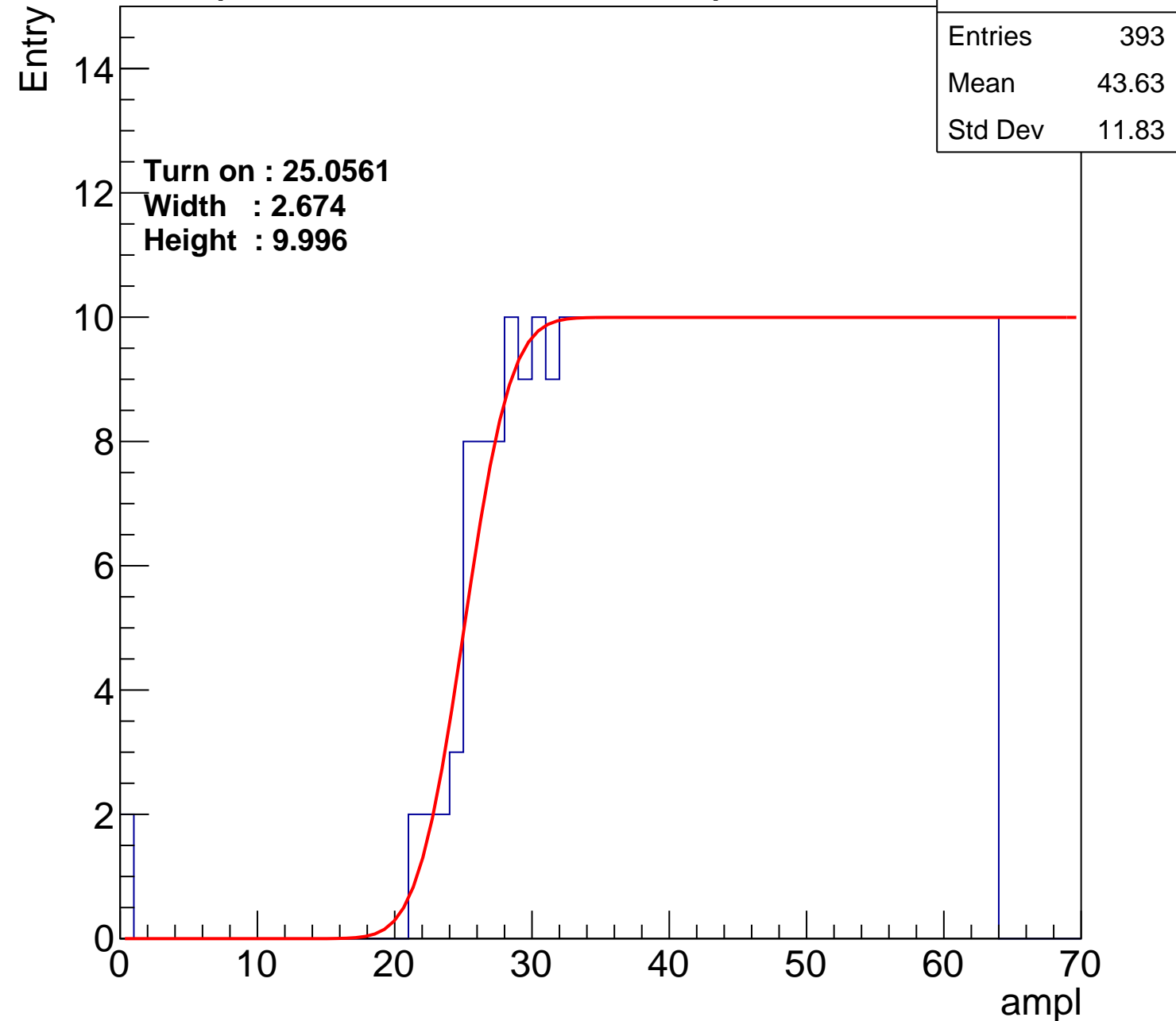
Width : 2.674

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.1463

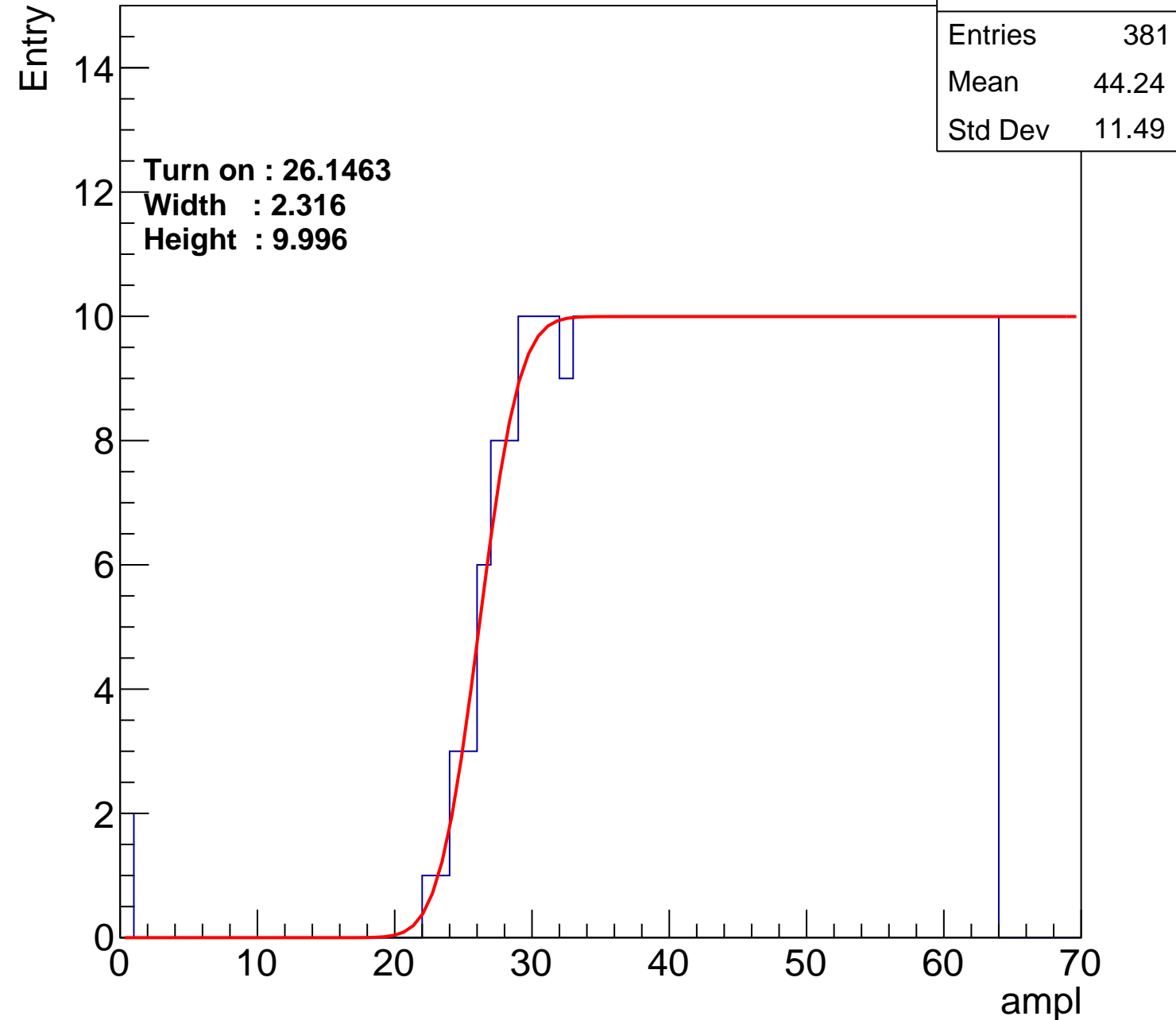
Width : 2.316

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch9

calib_packv5_042523_0143.root, FC#10, port B3

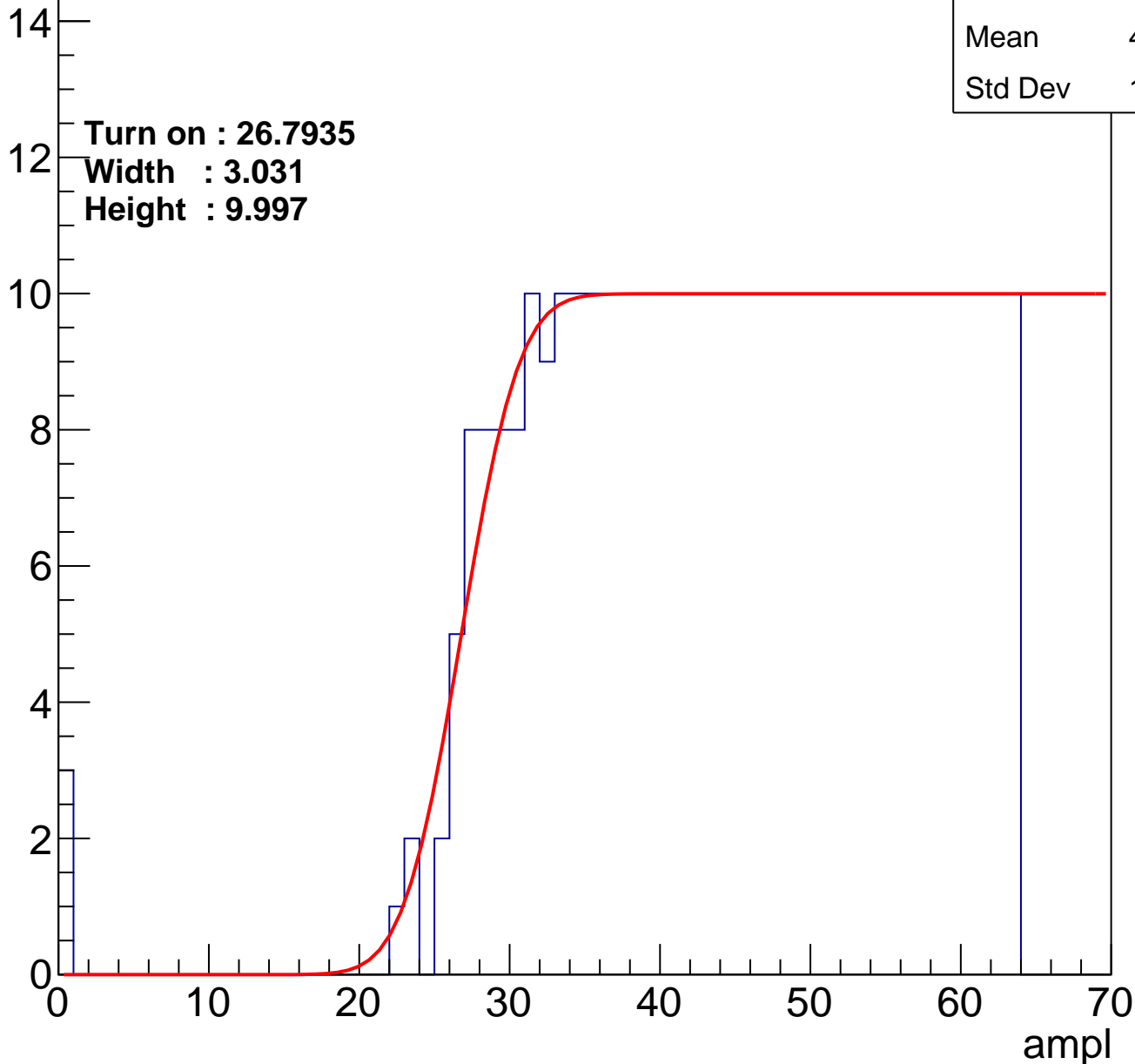
Entry

Entries	374
Mean	44.49
Std Dev	11.55

Turn on : 26.7935

Width : 3.031

Height : 9.997



B1L002S, U3-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.28
Std Dev	11.62

Turn on : 26.4886

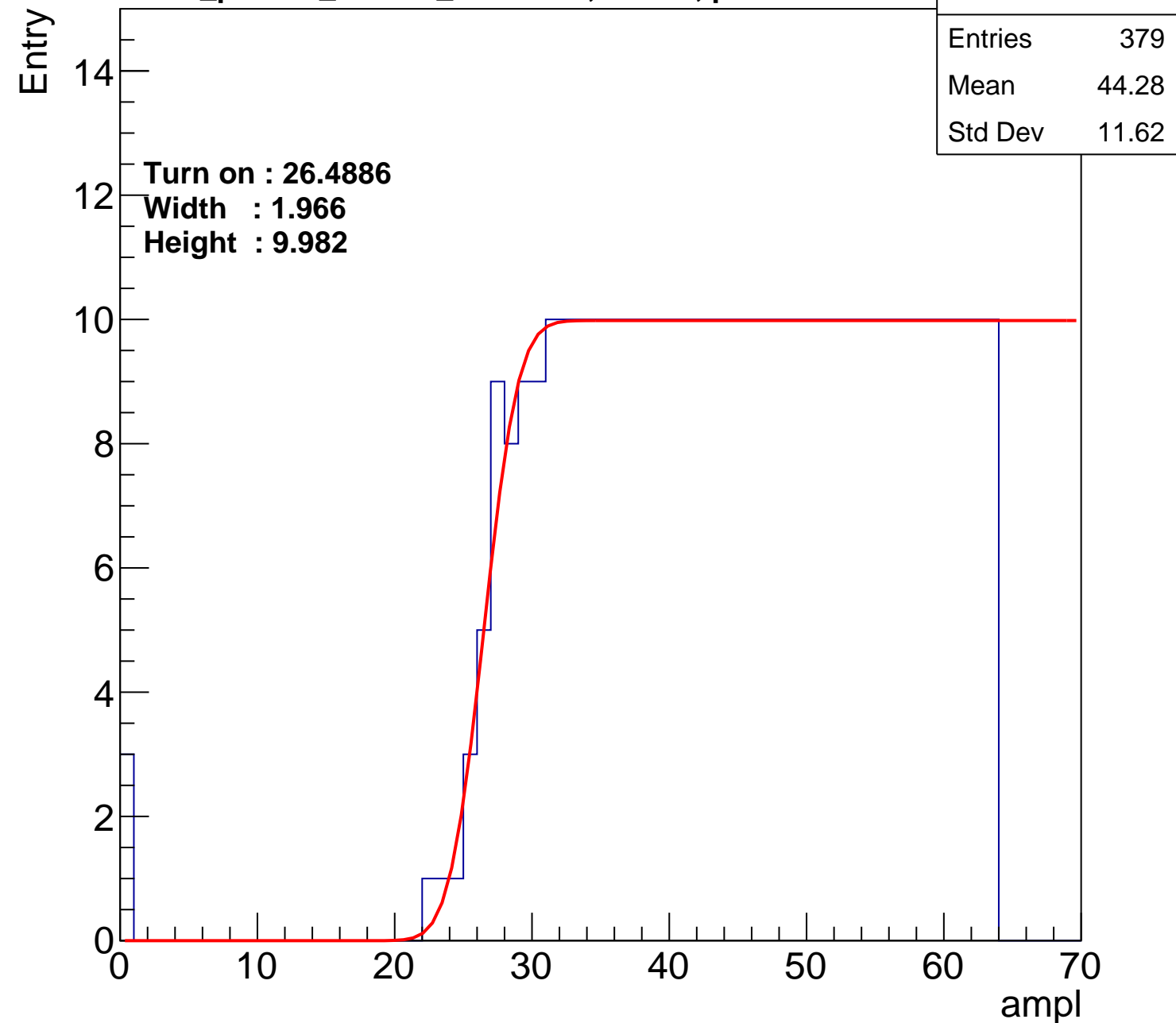
Width : 1.966

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.5448

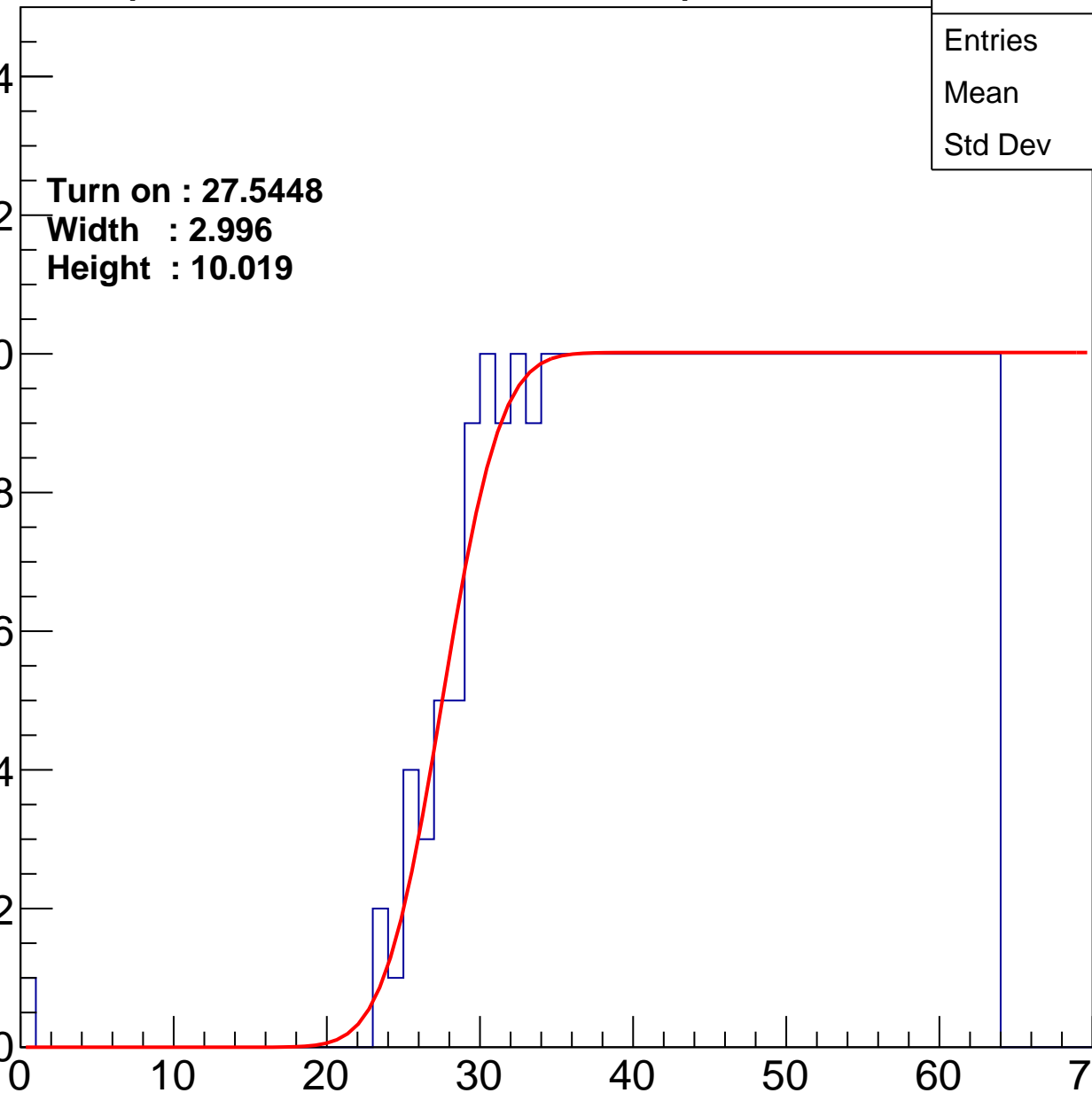
Width : 2.996

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.7351

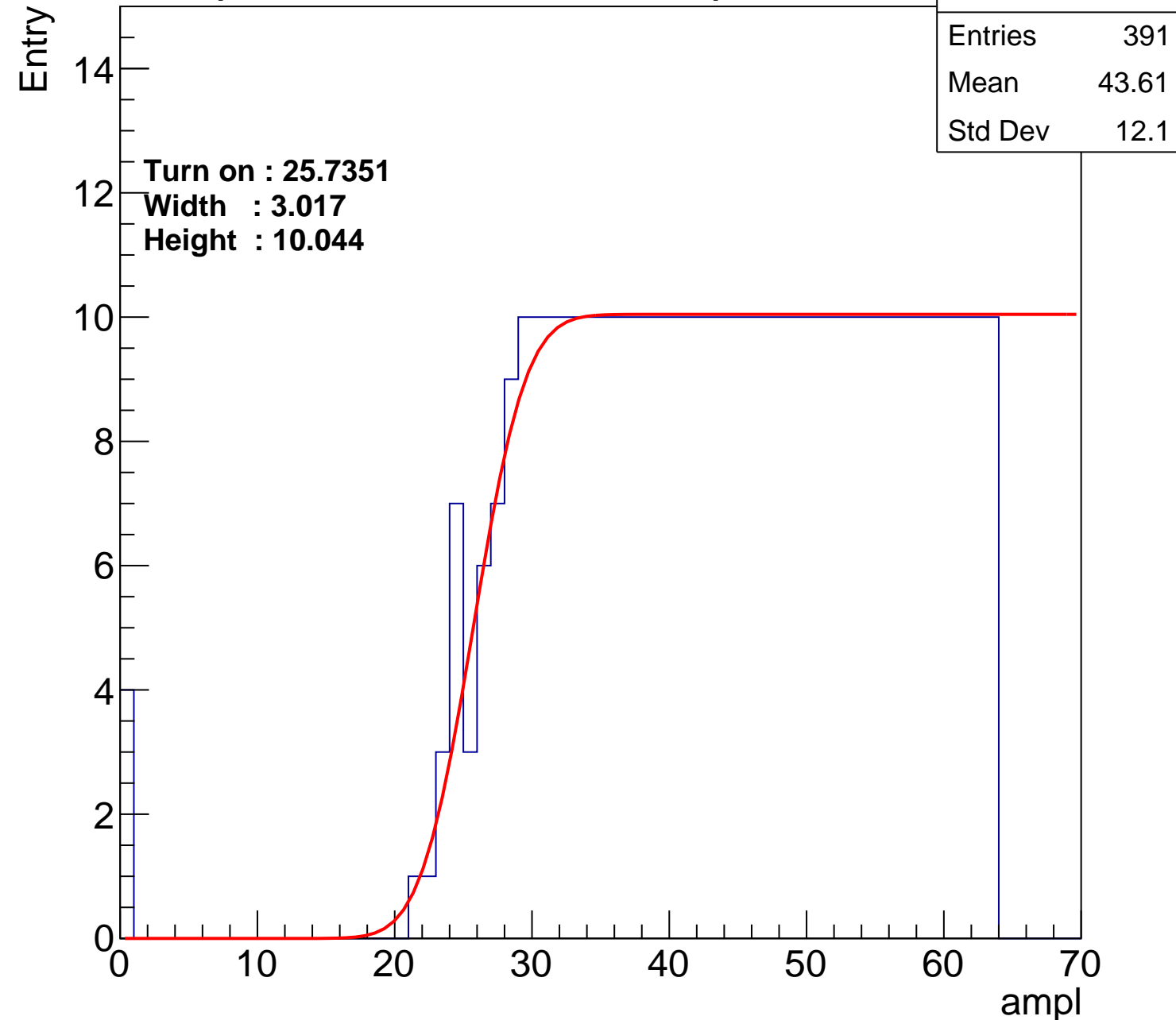
Width : 3.017

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.74
Std Dev	11.6

Turn on : 25.0102

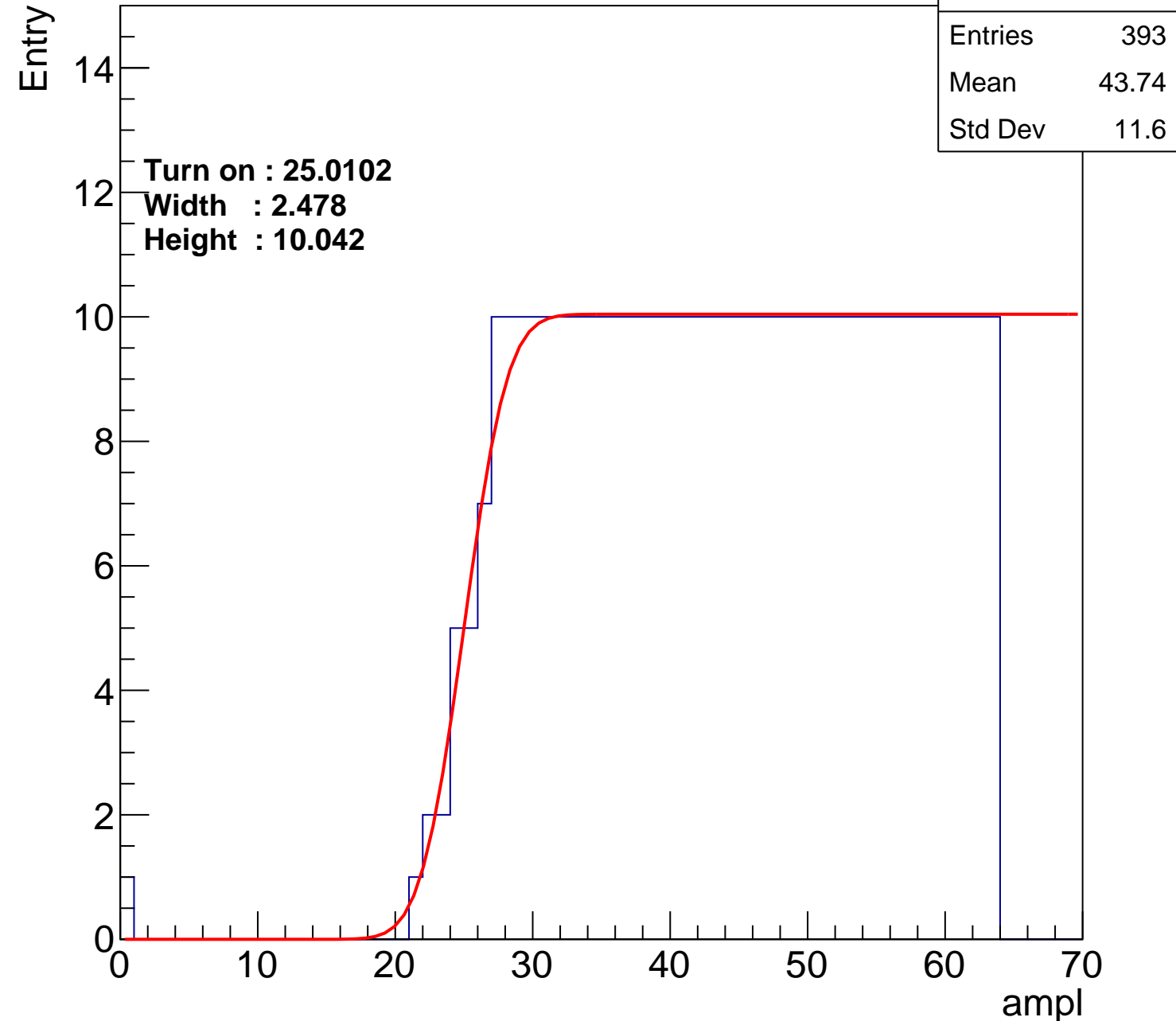
Width : 2.478

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 26.3233

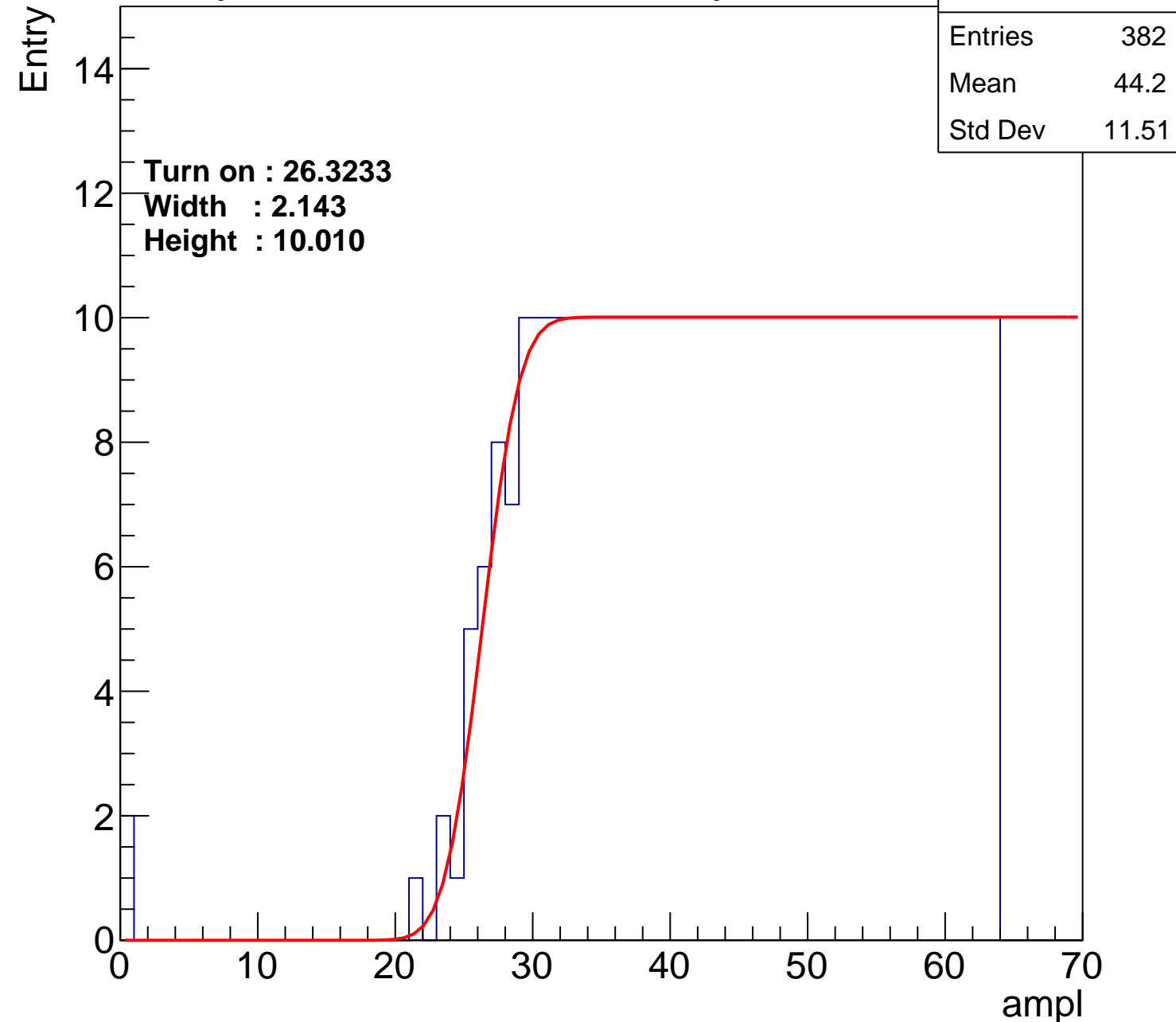
Width : 2.143

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.57

Turn on : 26.2022

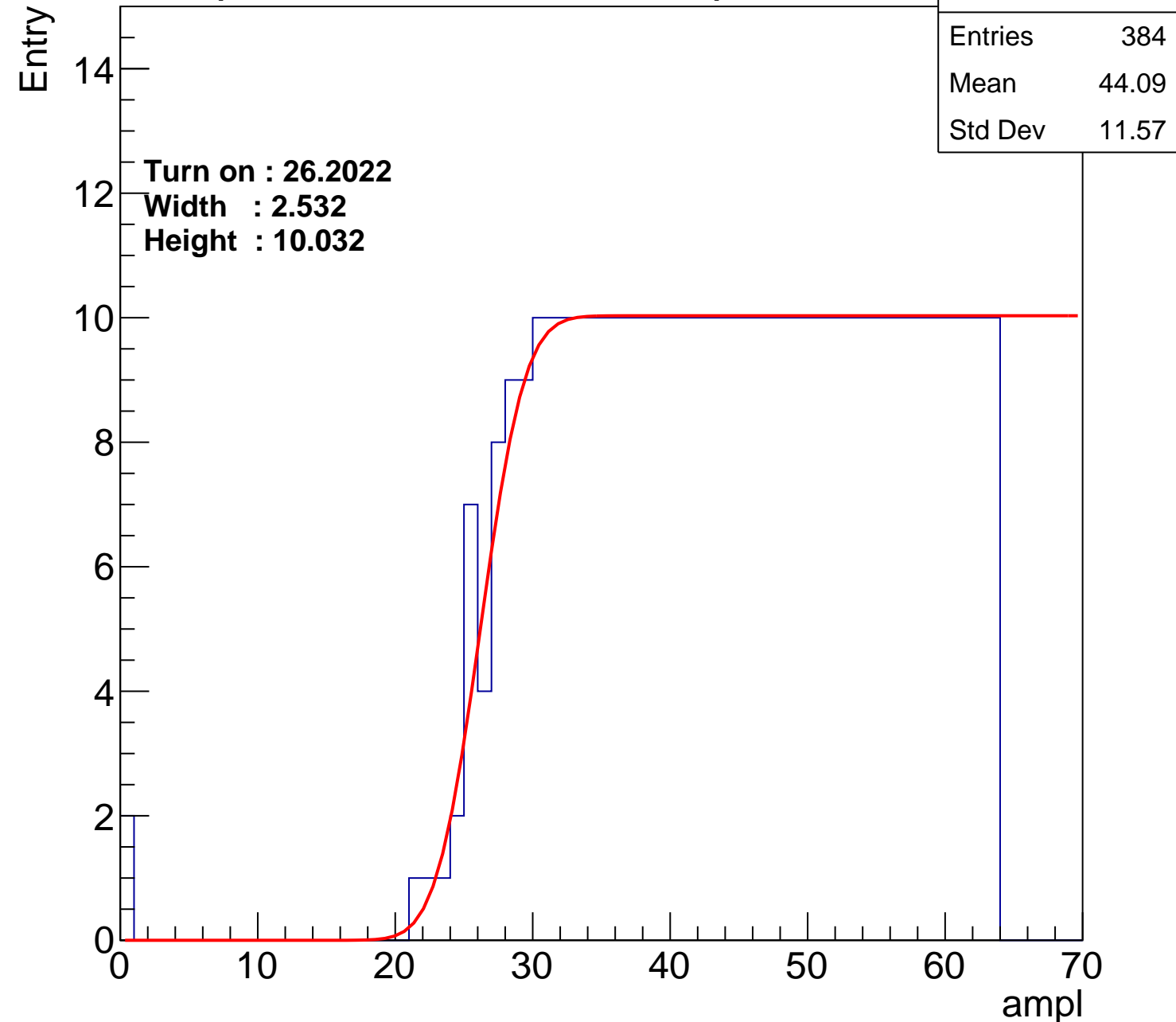
Width : 2.532

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch16

calib_packv5_042523_0143.root, FC#10, port B3

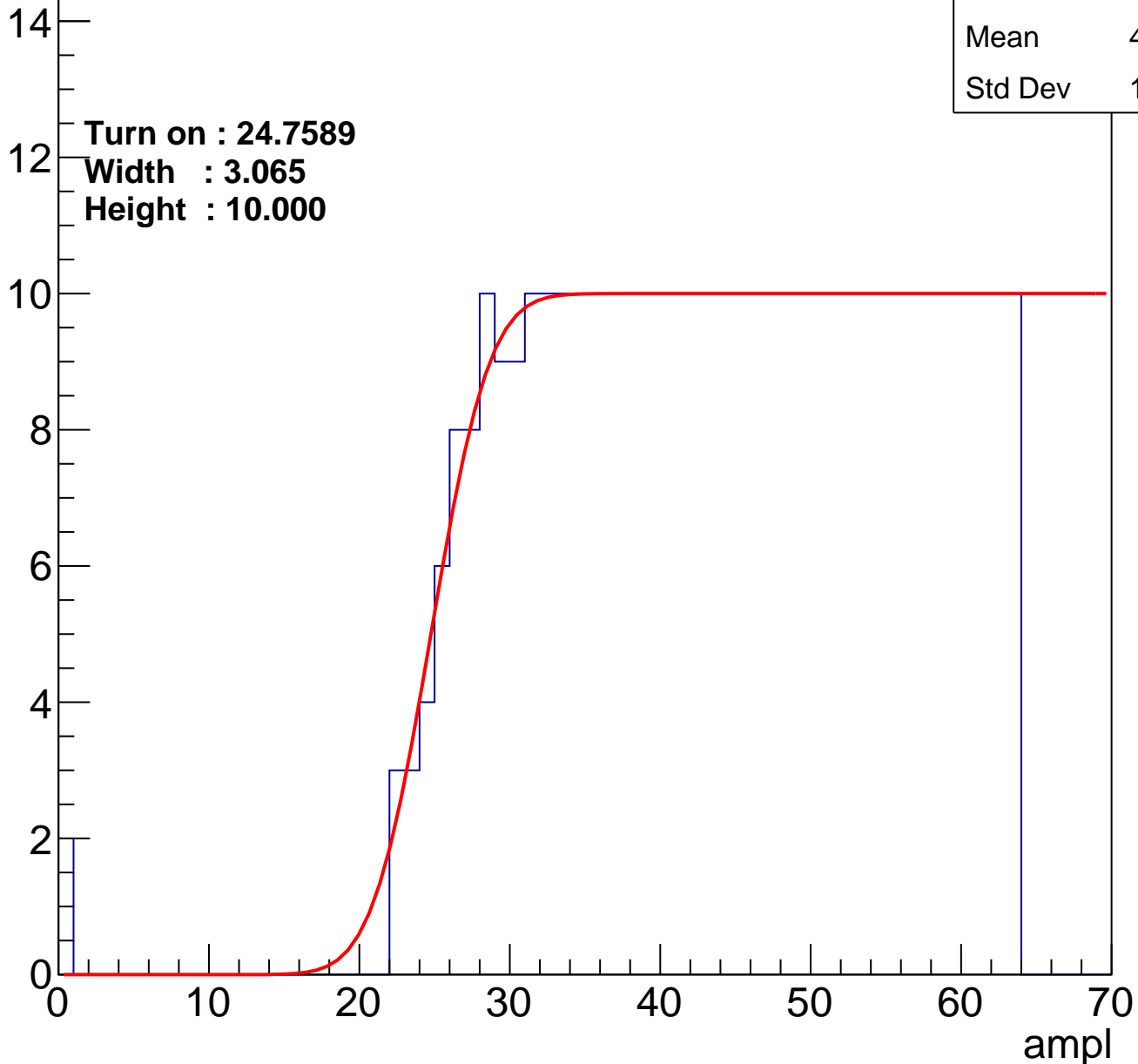
Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 24.7589

Width : 3.065

Height : 10.000

Entry



B1L002S, U3-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.7587

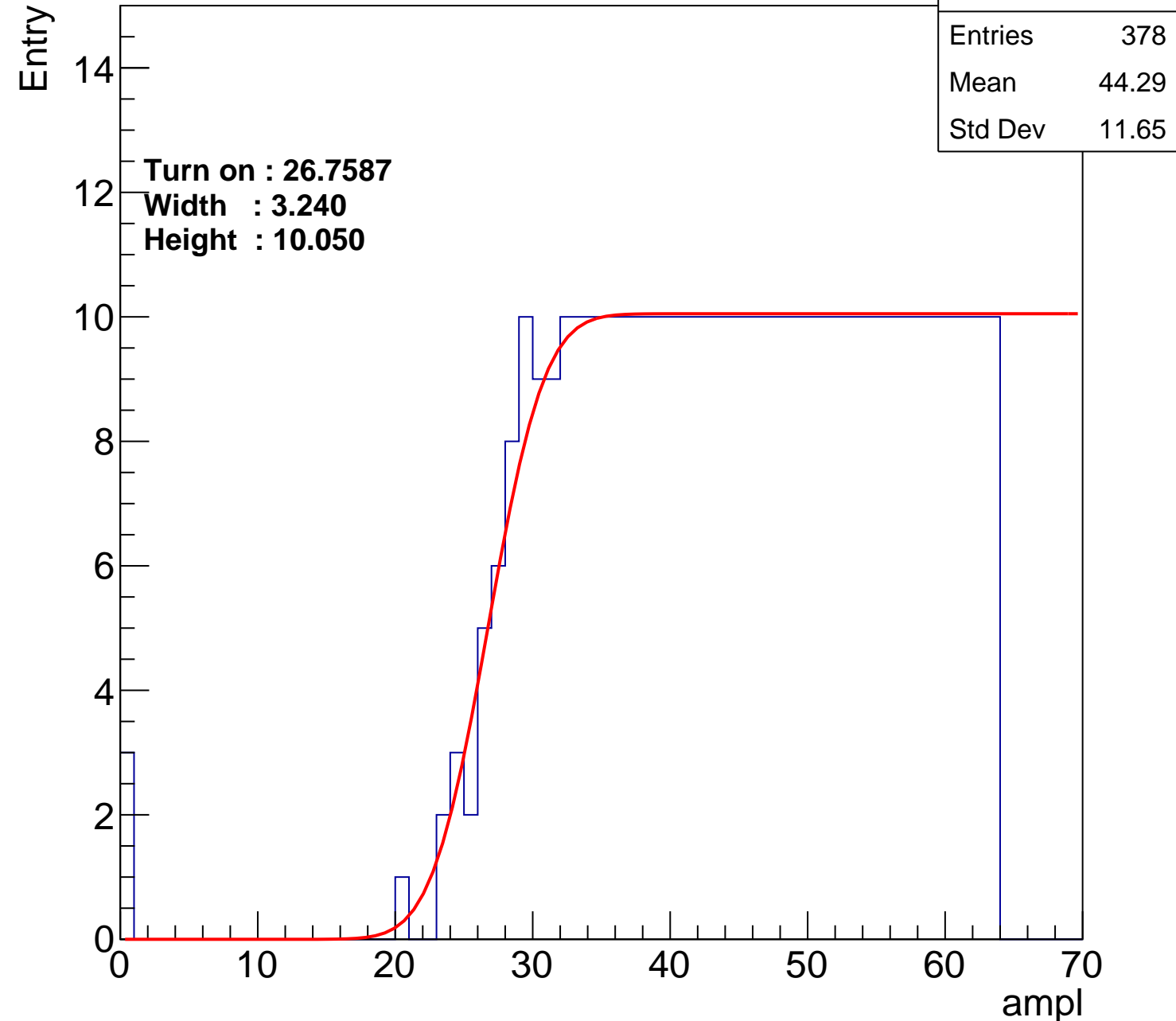
Width : 3.240

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch18

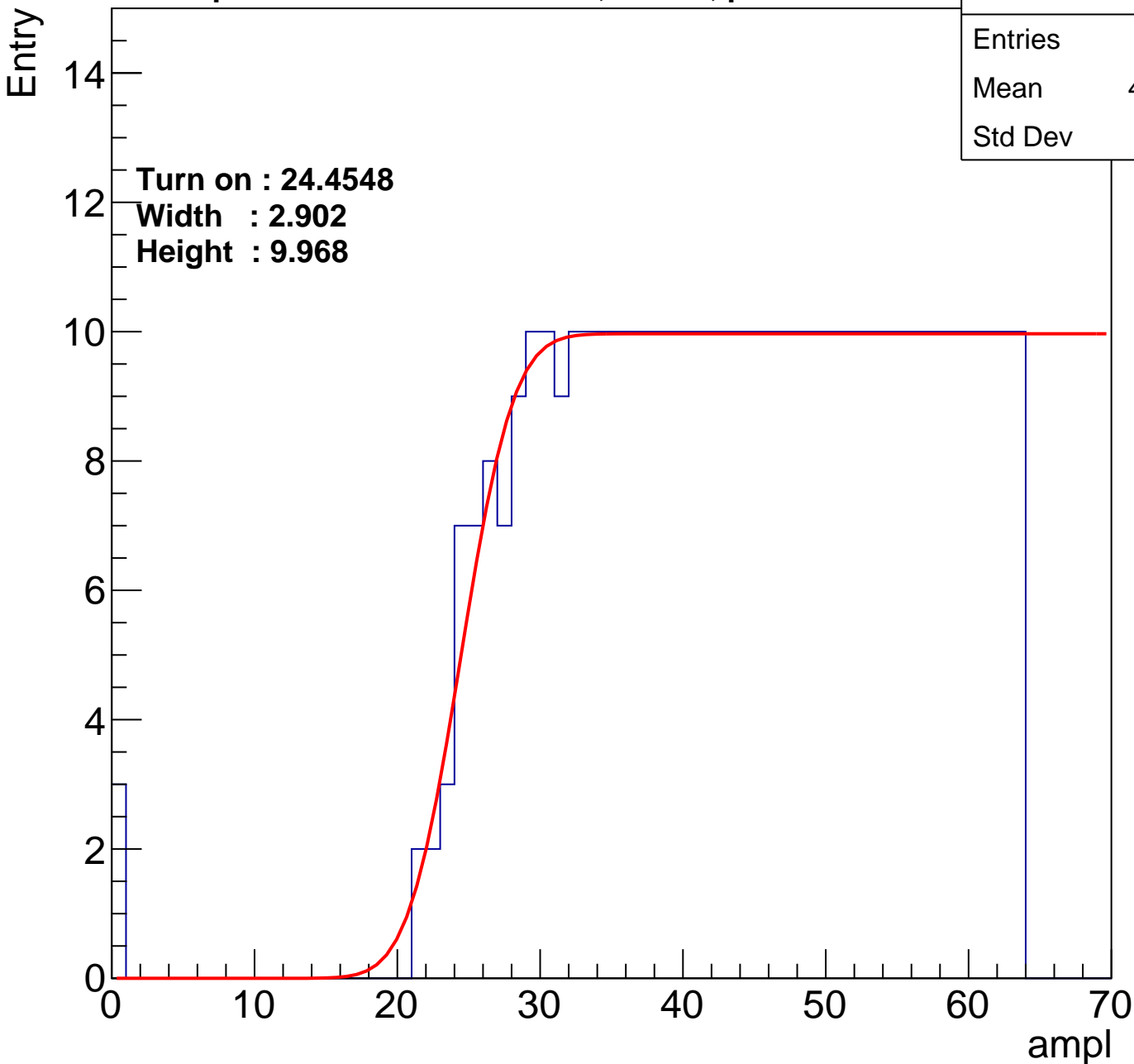
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 24.4548

Width : 2.902

Height : 9.968

Entries	397
Mean	43.37
Std Dev	12.11



B1L002S, U3-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.62
Std Dev	11.96

Turn on : 25.2068

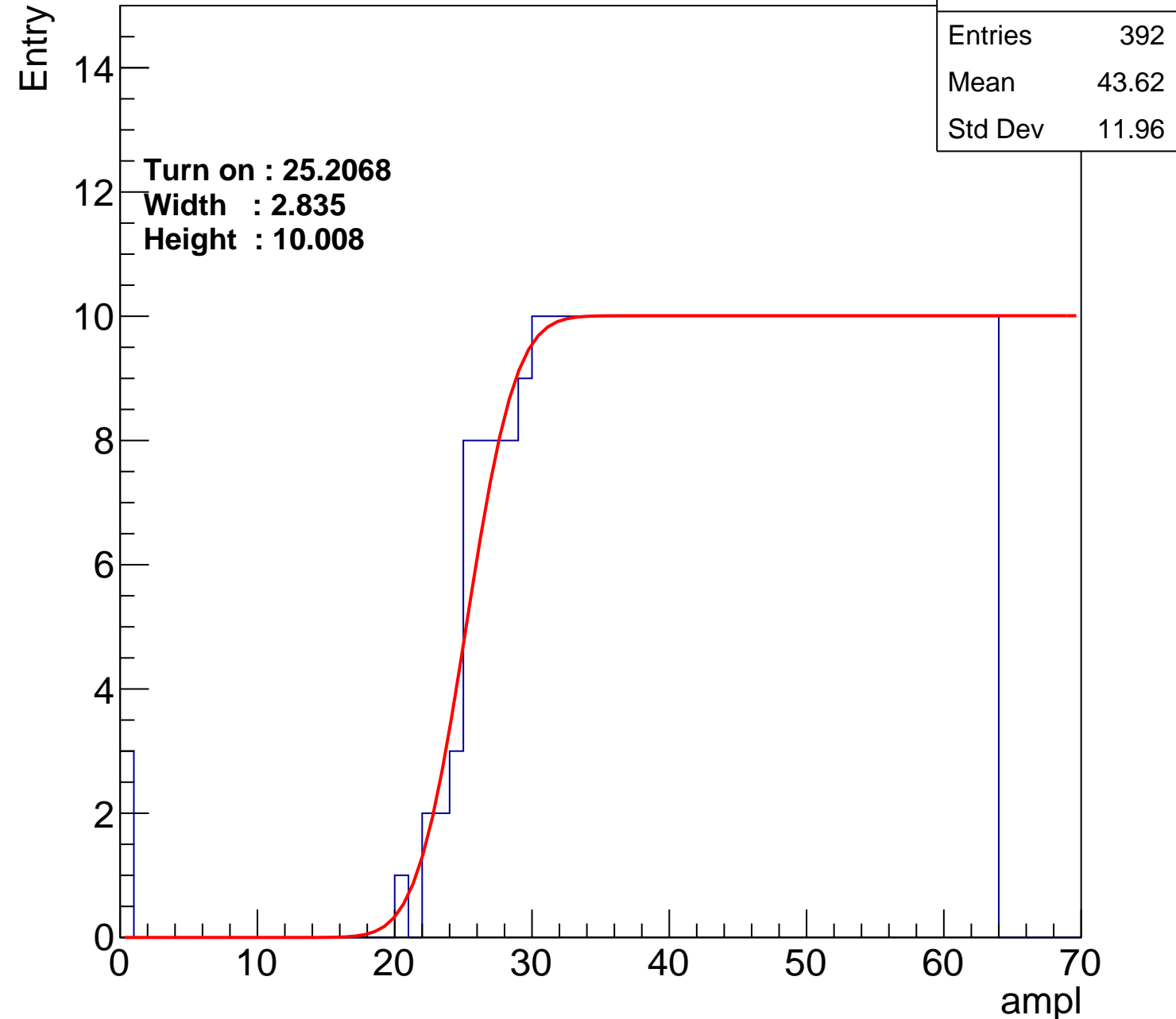
Width : 2.835

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.5339

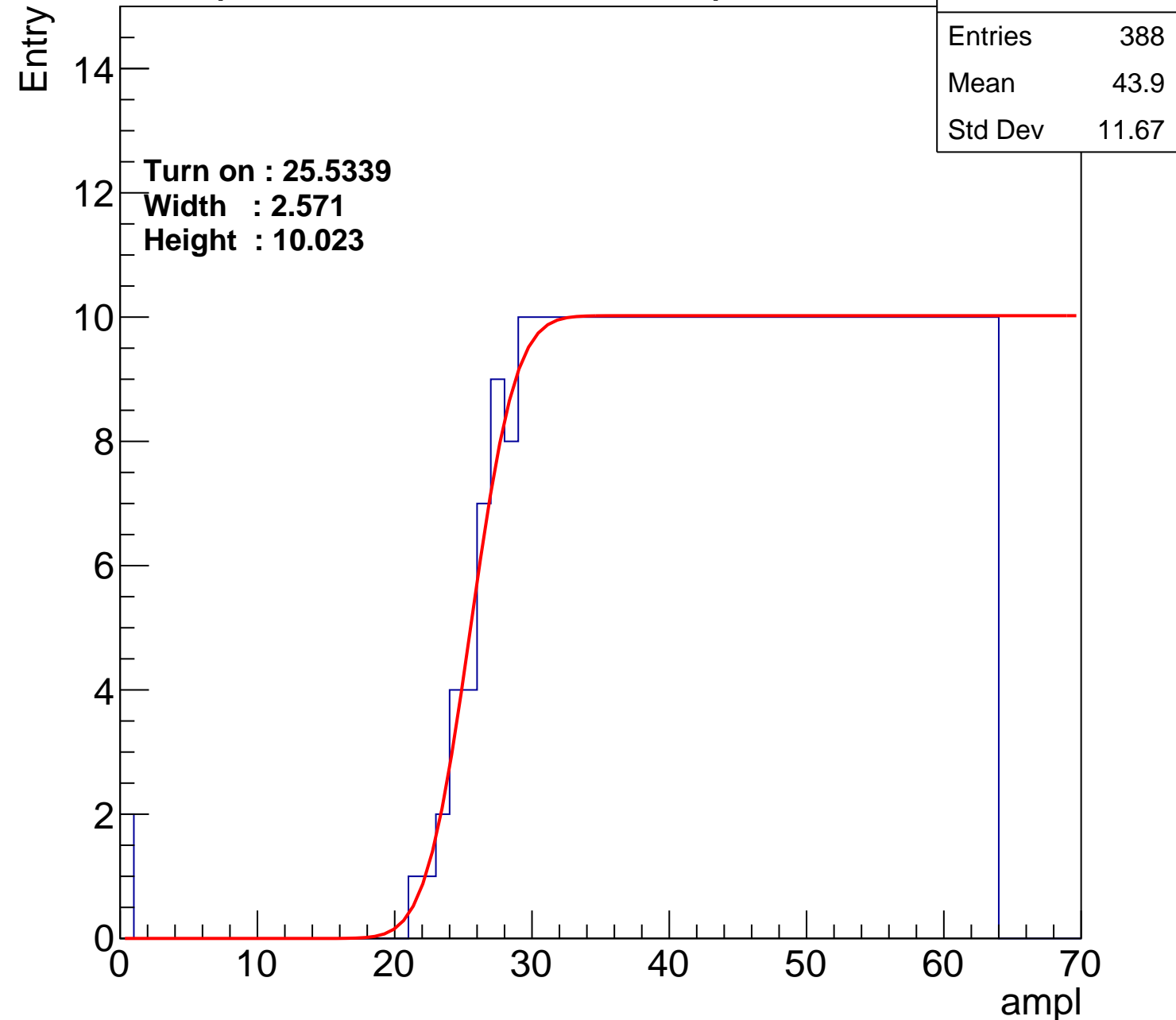
Width : 2.571

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.6981

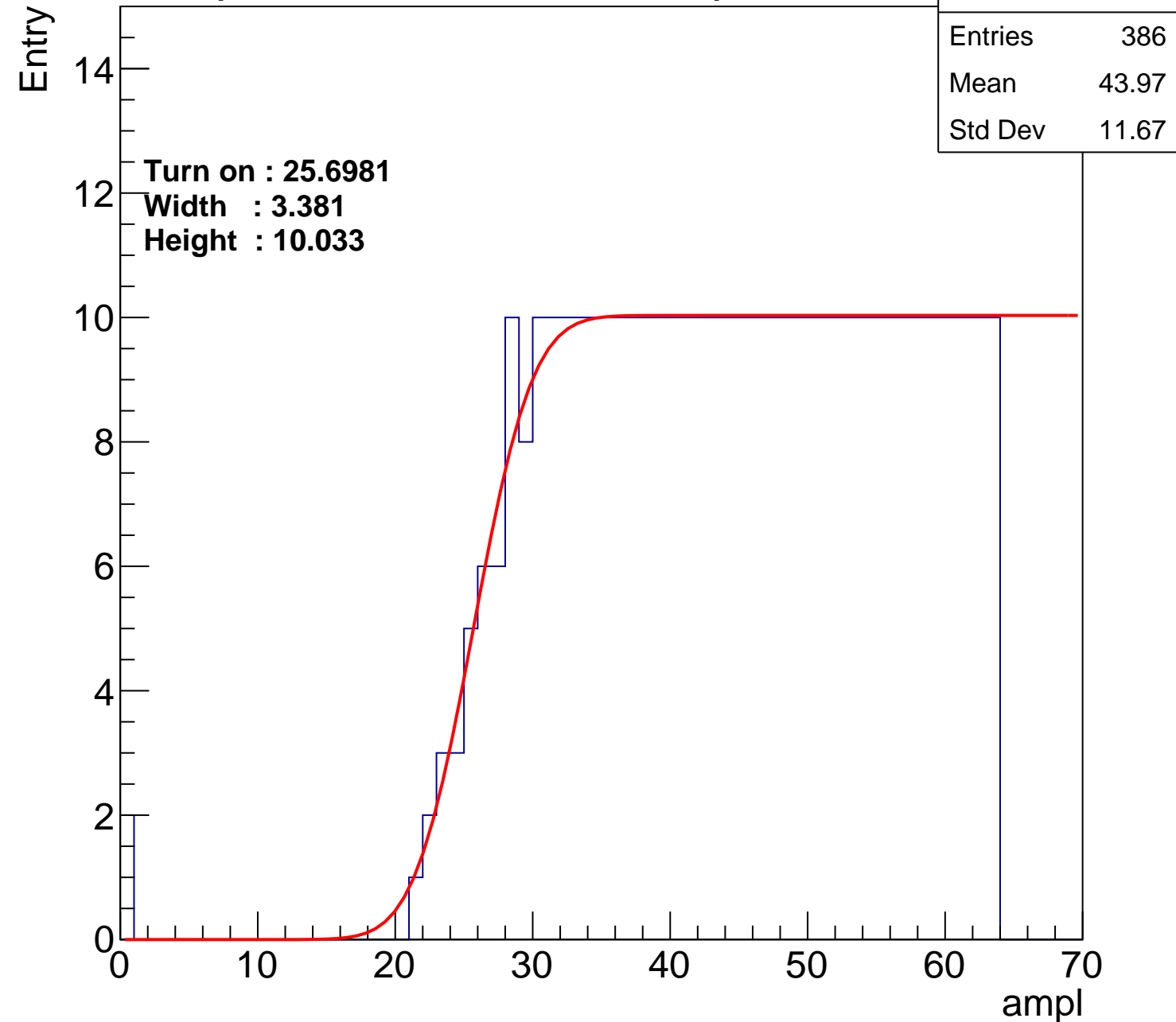
Width : 3.381

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.7570

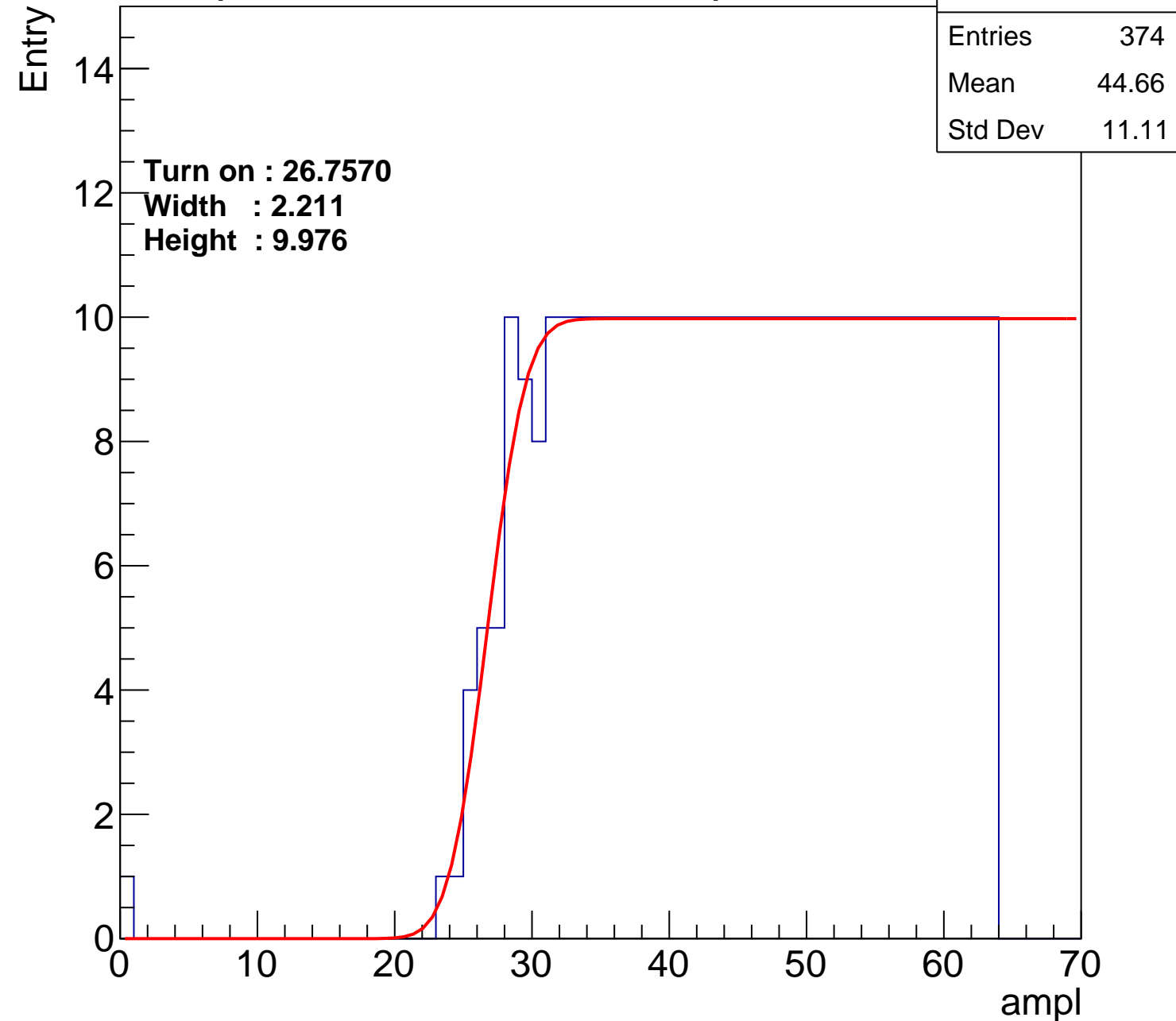
Width : 2.211

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch23

calib_packv5_042523_0143.root, FC#10, port B3

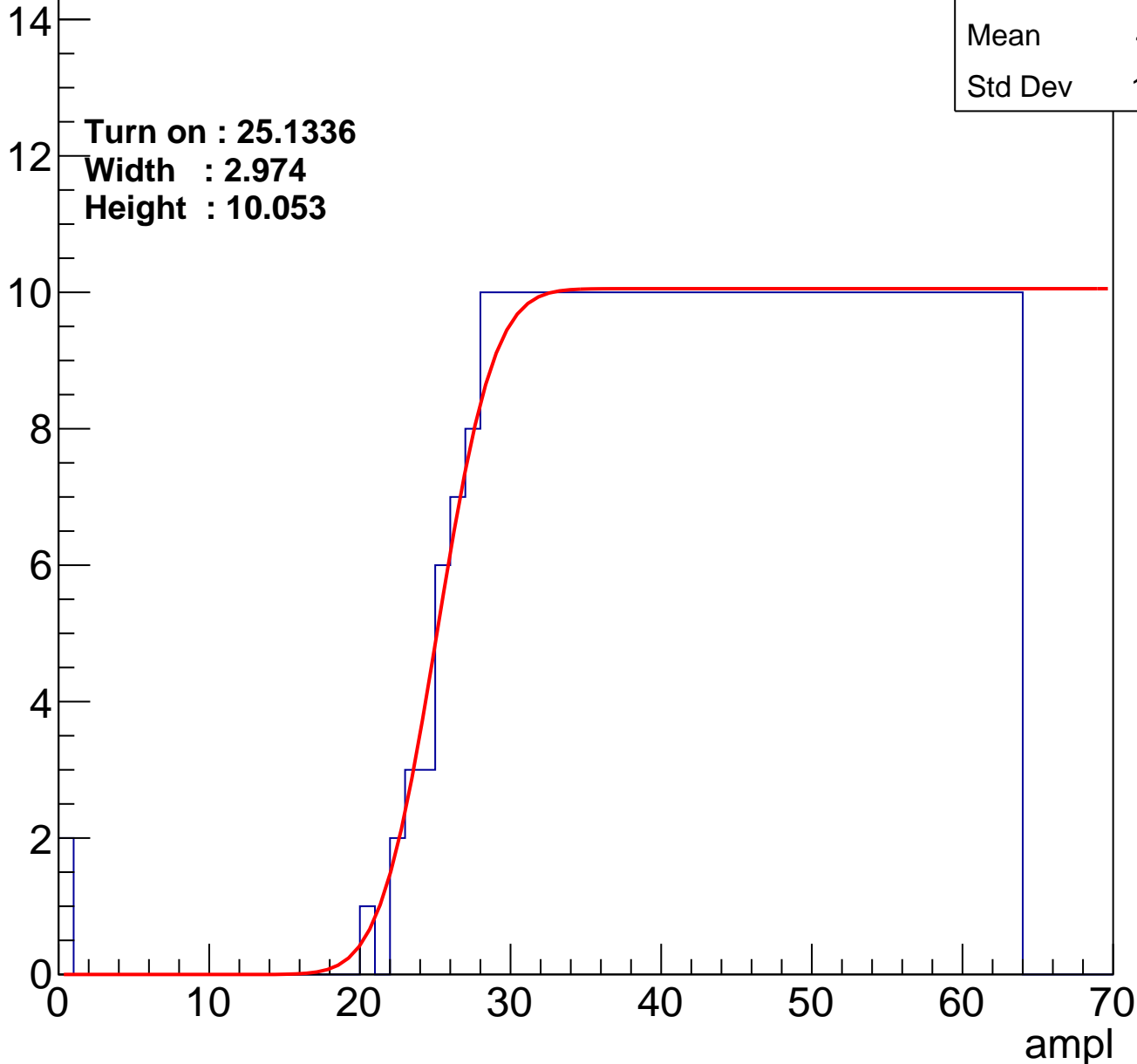
Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.1336

Width : 2.974

Height : 10.053

Entry



B1L002S, U3-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 27.0267

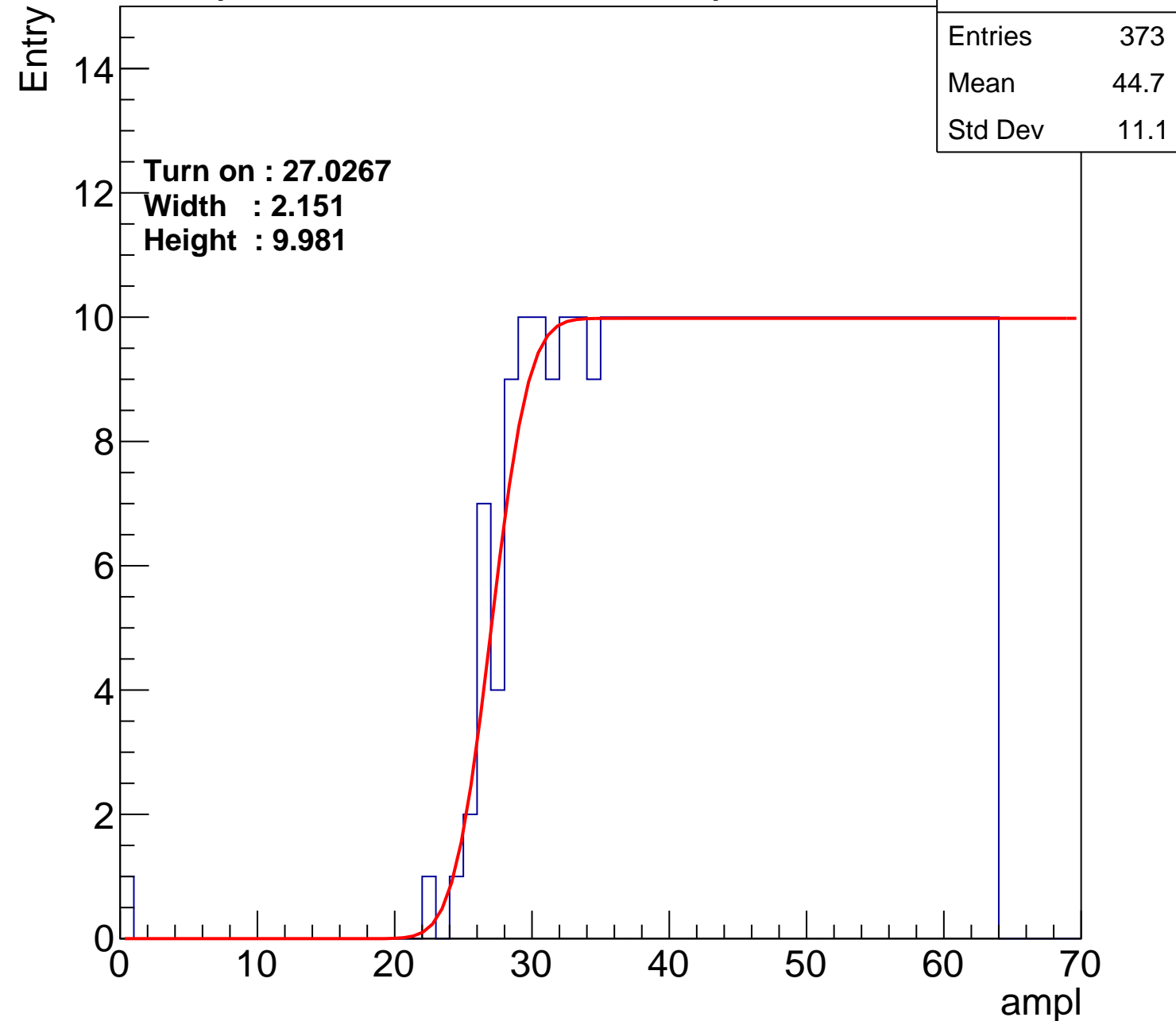
Width : 2.151

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.1184

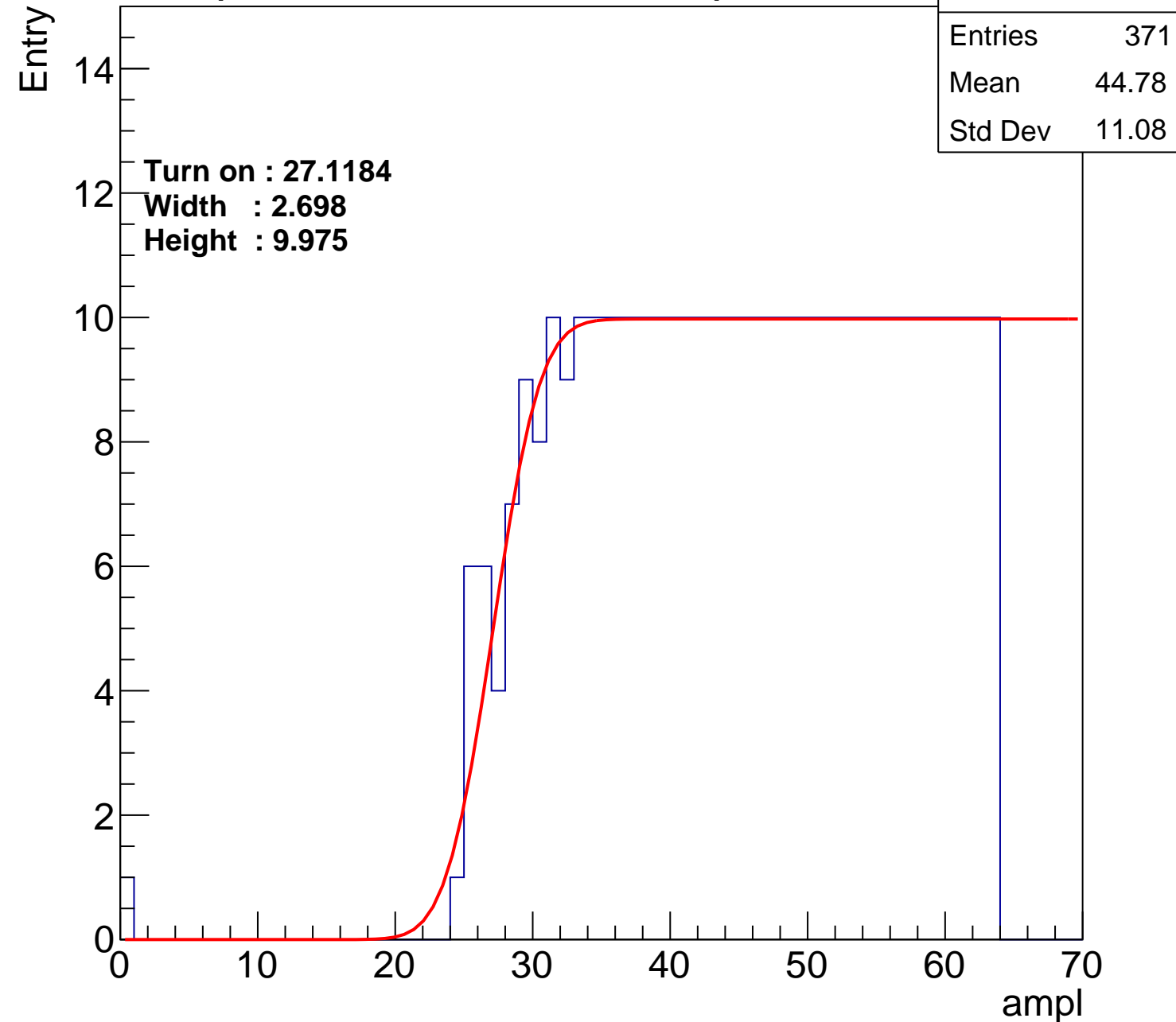
Width : 2.698

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.13
Std Dev	12.17

Turn on : 24.3064

Width : 2.353

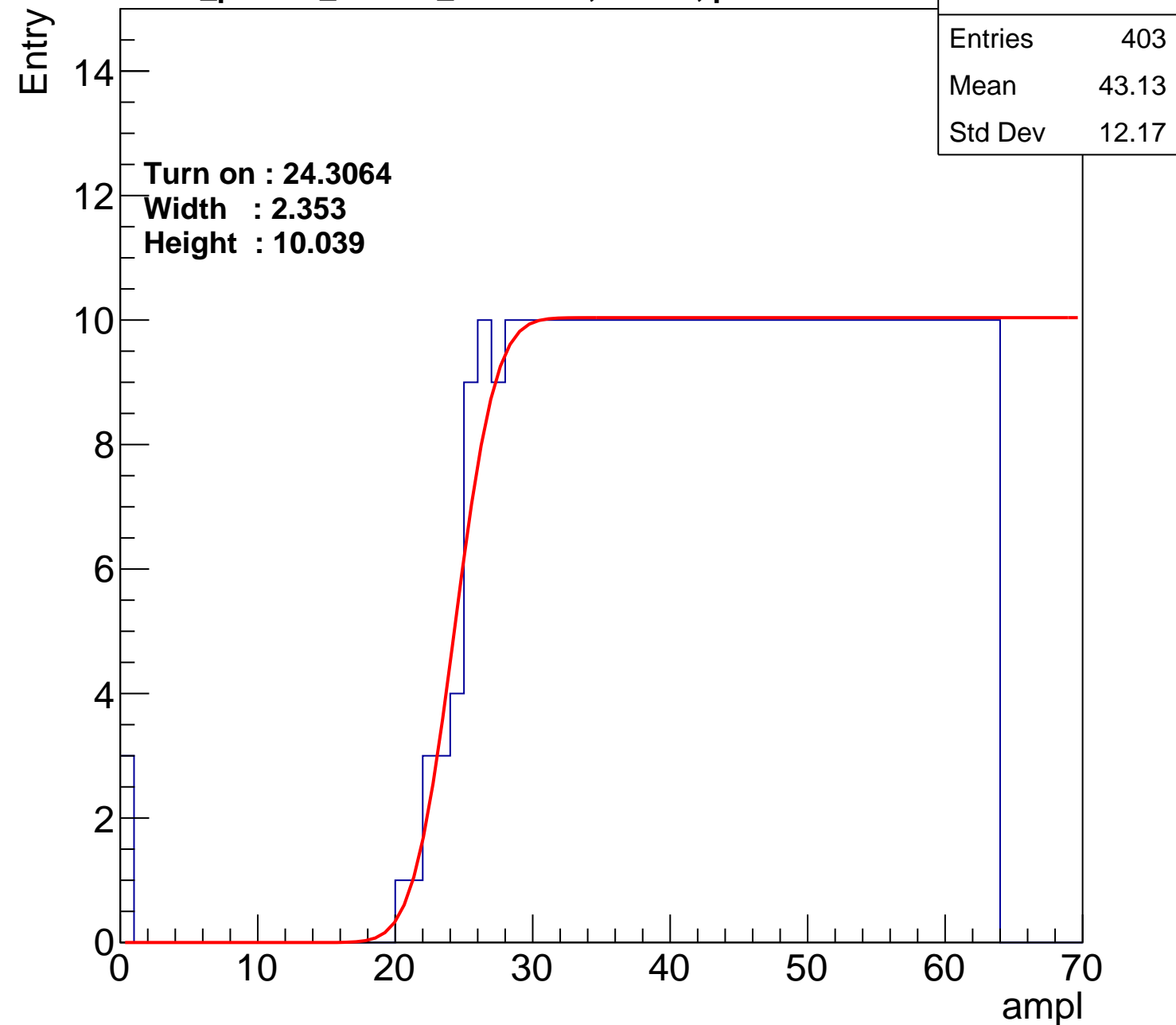
Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U3-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.68
Std Dev	11.8

Turn on : 25.1380

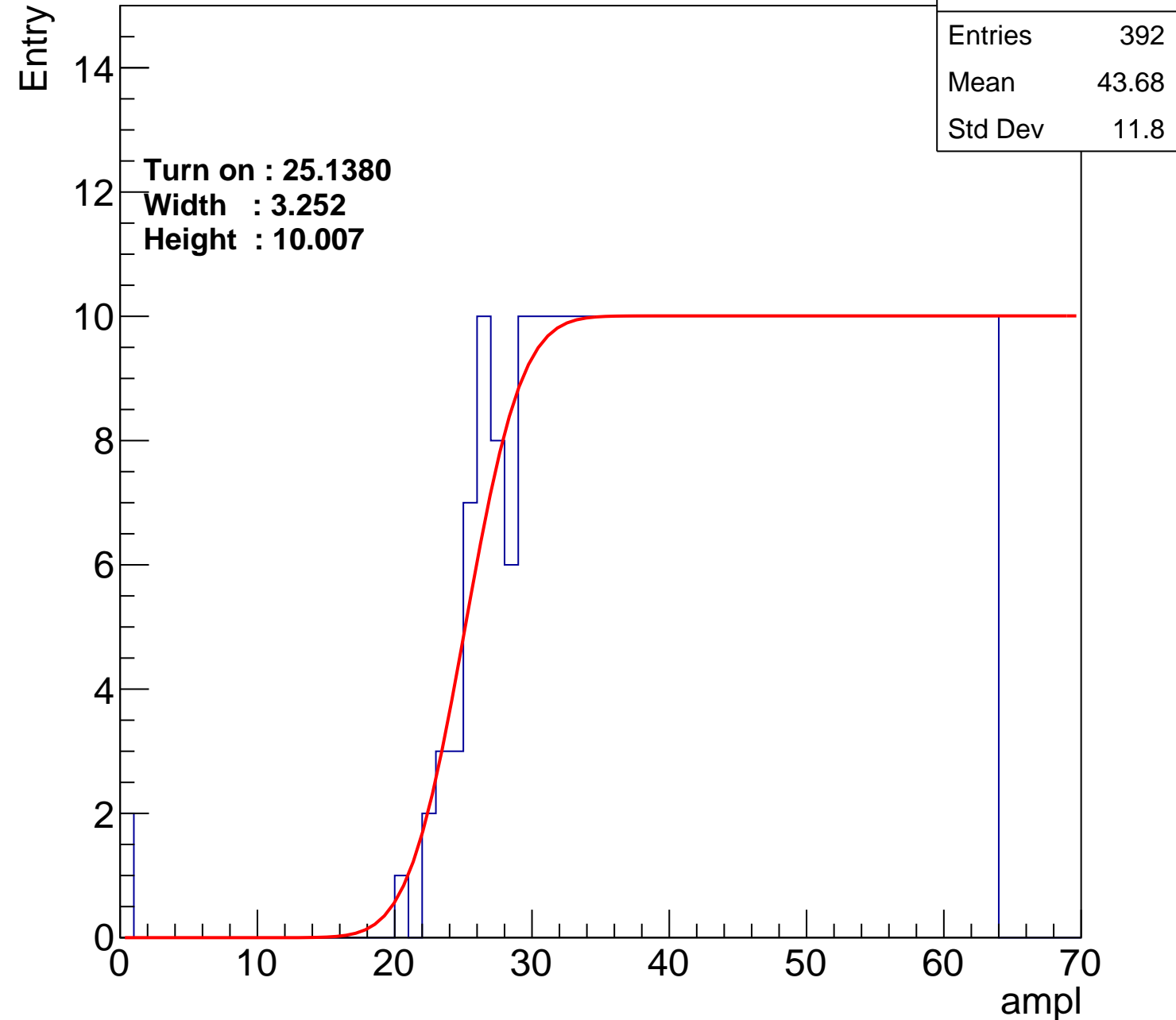
Width : 3.252

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch28

calib_packv5_042523_0143.root, FC#10, port B3

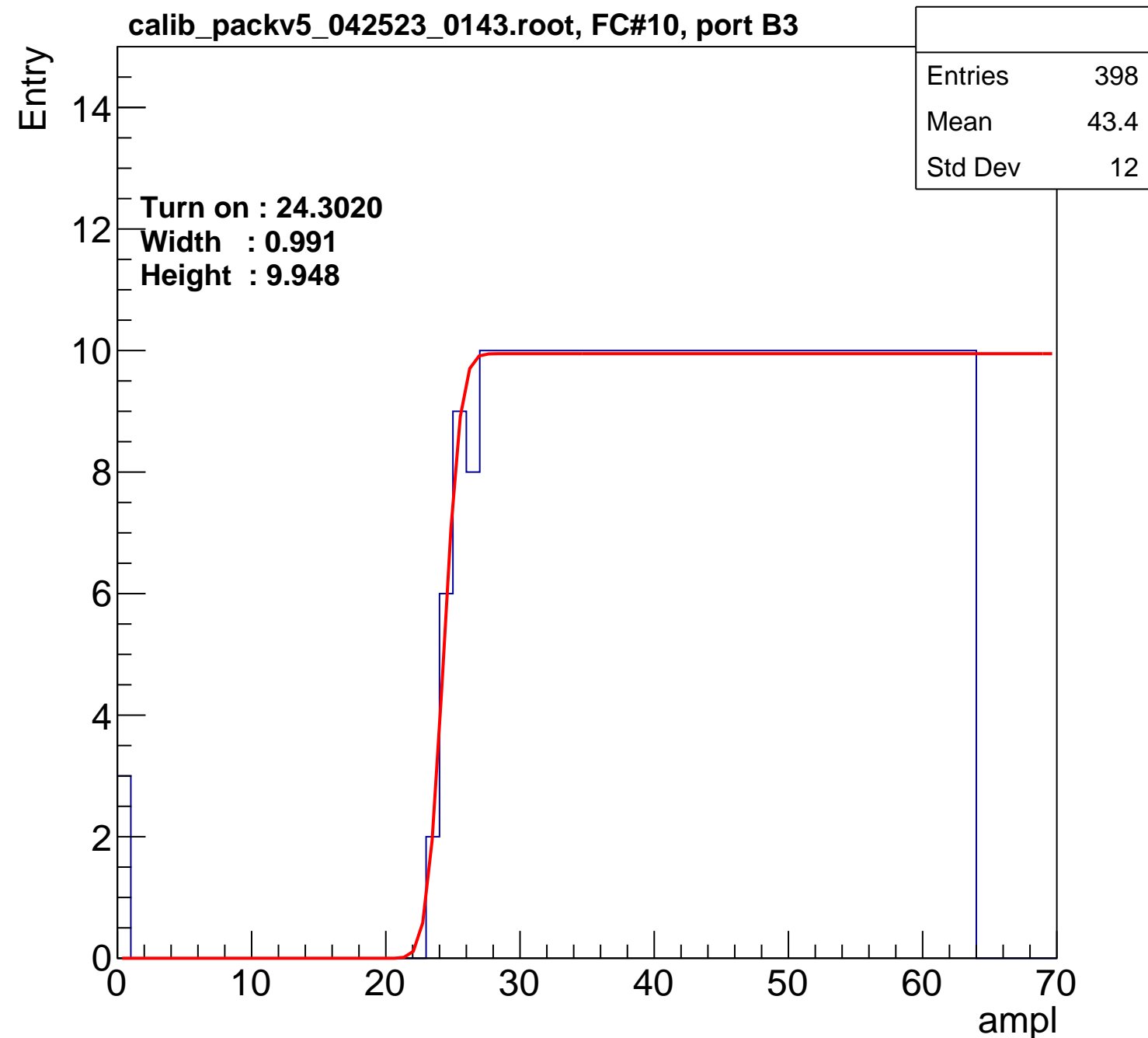
Entry

14
12
10
8
6
4
2
0

Turn on : 24.3020
Width : 0.991
Height : 9.948

Entries	398
Mean	43.4
Std Dev	12

ampl



B1L002S, U3-ch29

calib_packv5_042523_0143.root, FC#10, port B3

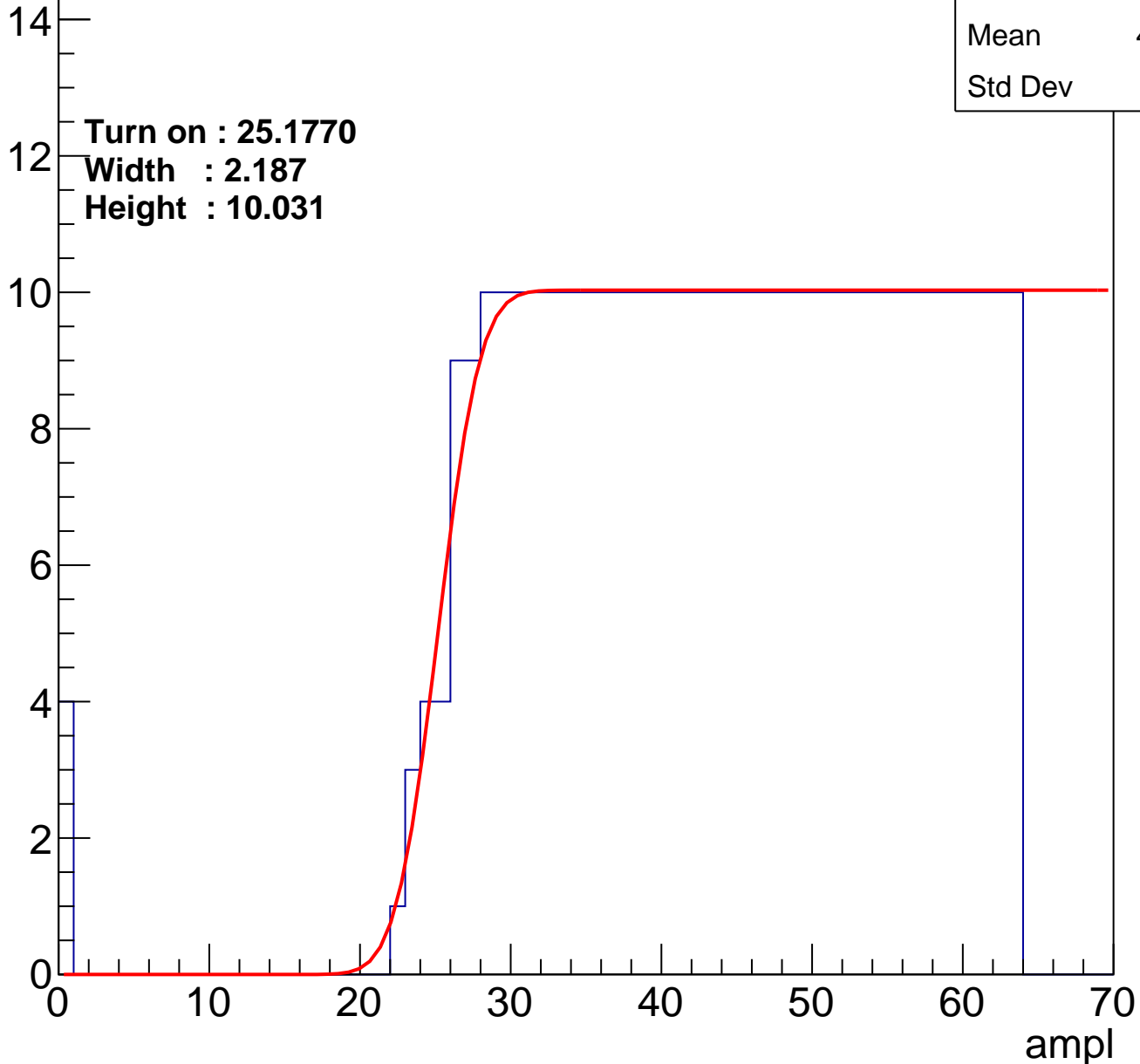
Entries	394
Mean	43.51
Std Dev	12.1

Turn on : 25.1770

Width : 2.187

Height : 10.031

Entry



B1L002S, U3-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.35
Std Dev	12.2

Turn on : 25.1765

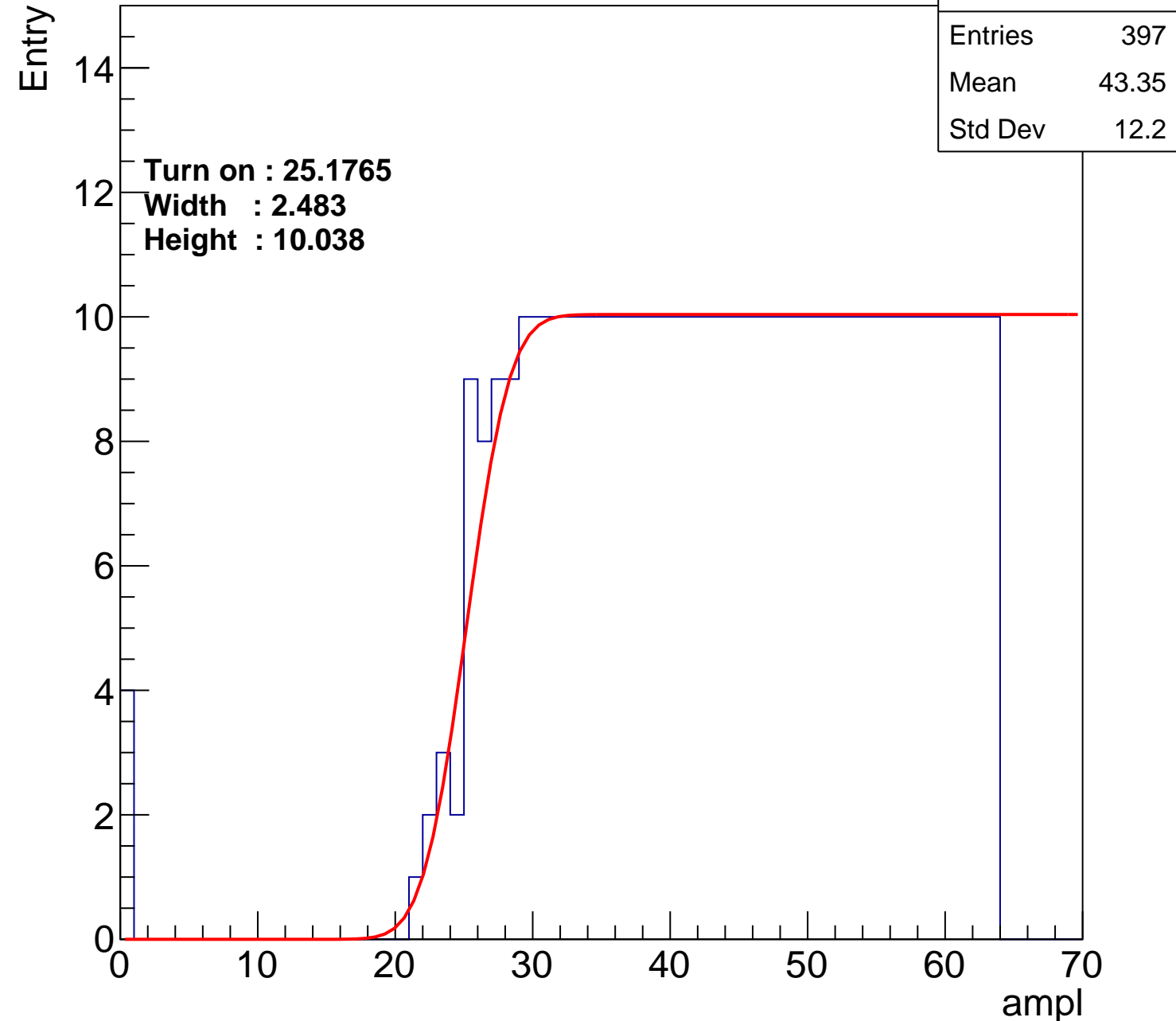
Width : 2.483

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.23
Std Dev	11.95

Turn on : 24.0115

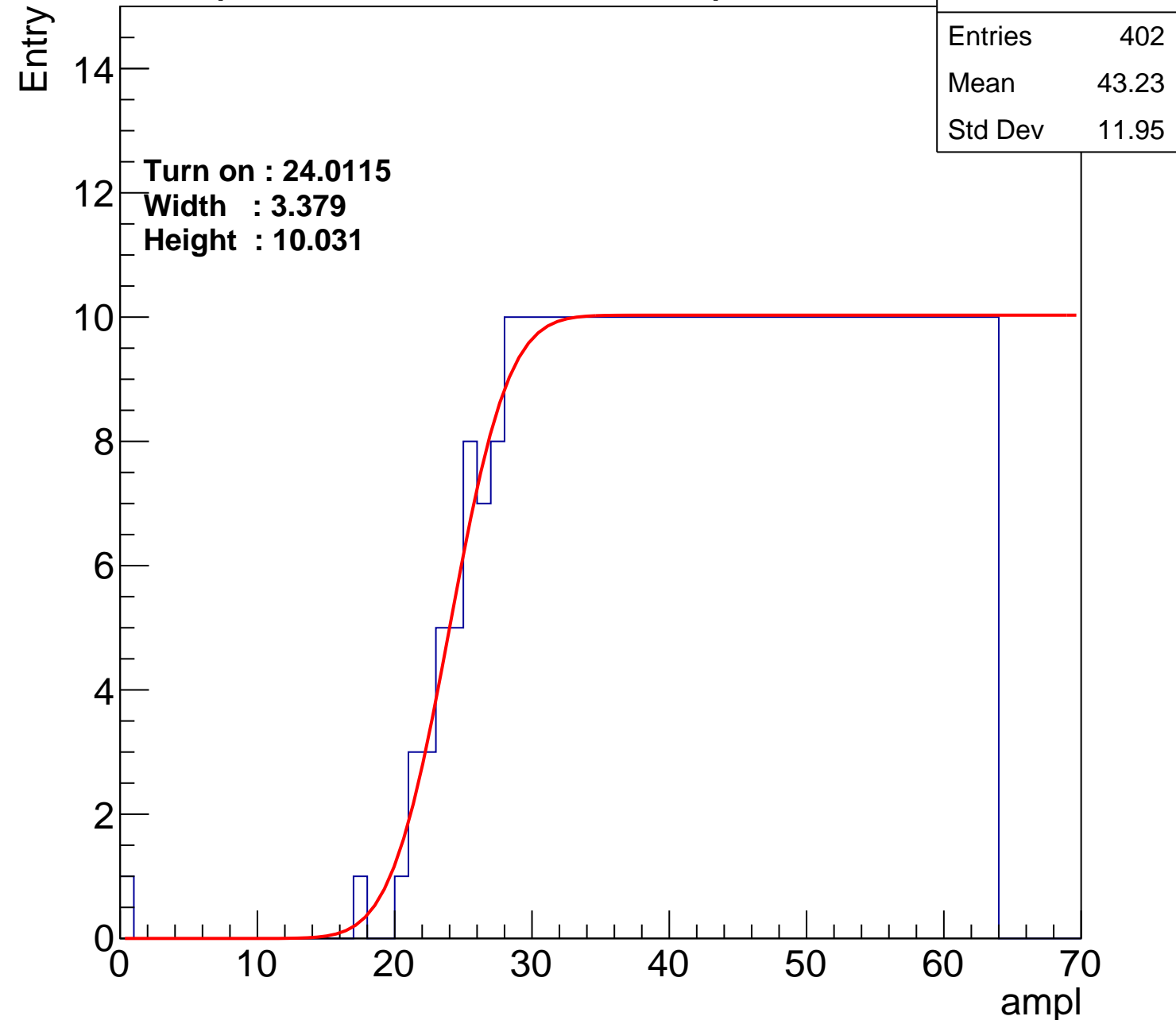
Width : 3.379

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.5463

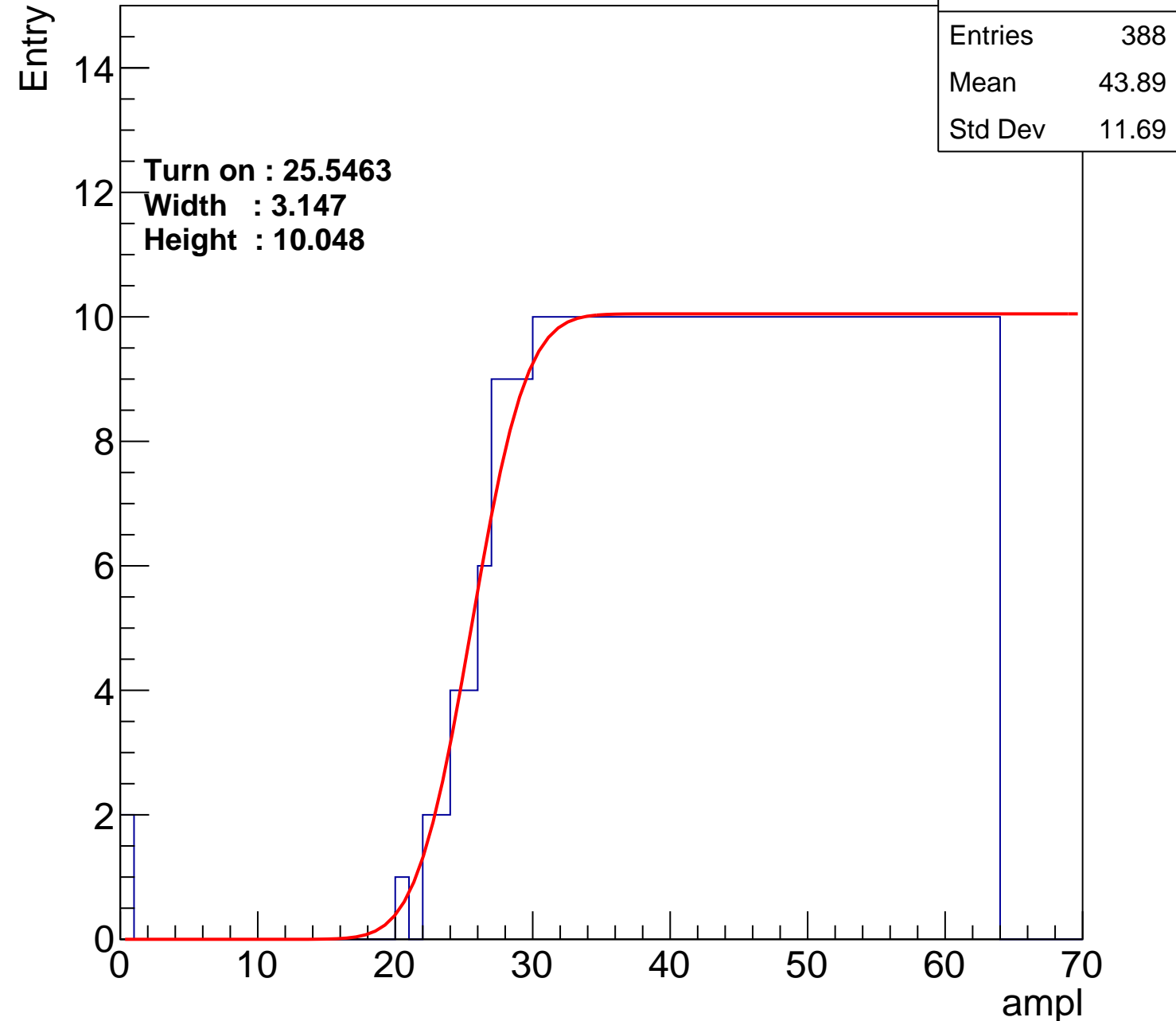
Width : 3.147

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.61
Std Dev	11.81

Turn on : 24.9195

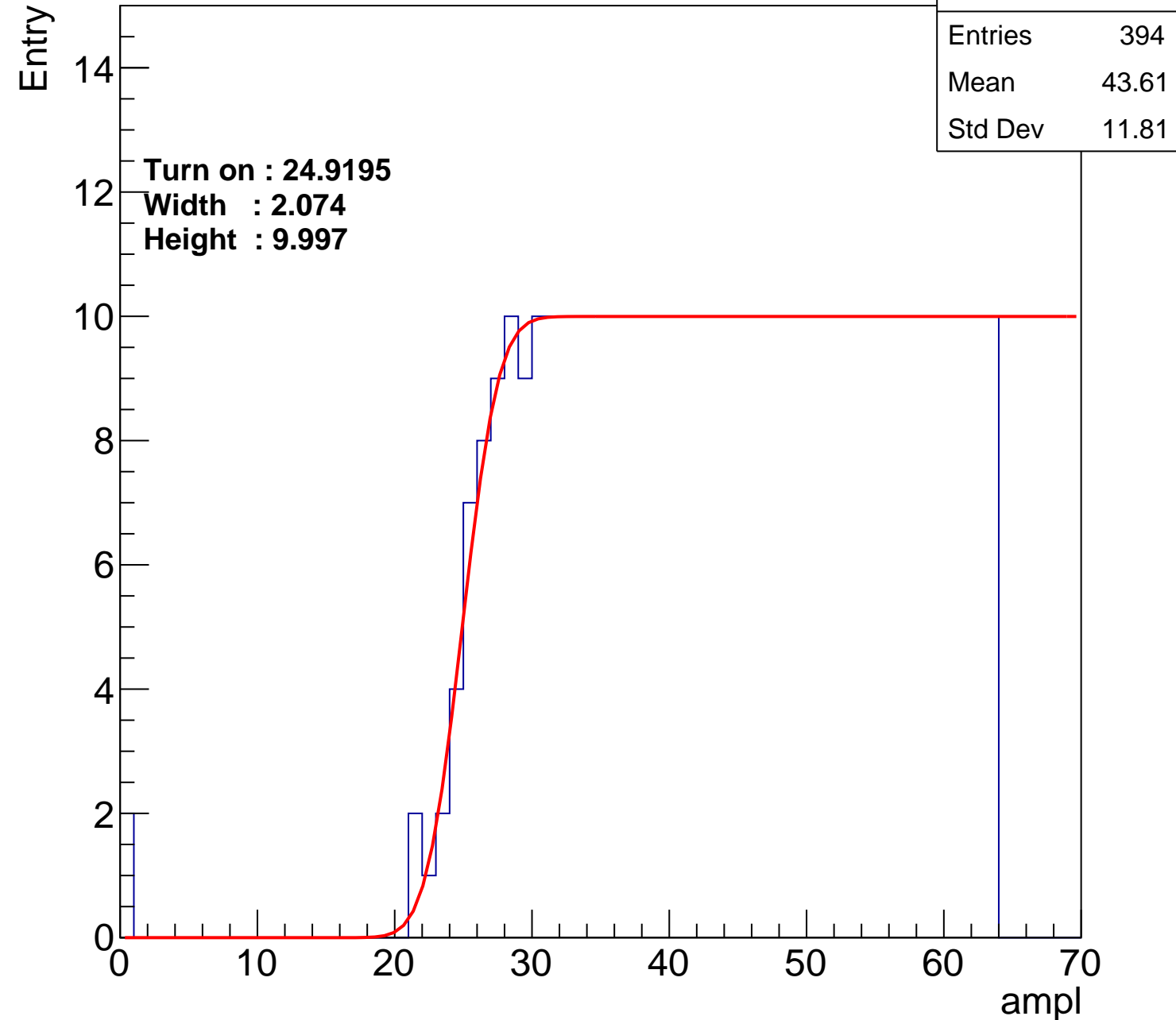
Width : 2.074

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.86
Std Dev	11.8

Turn on : 25.6863

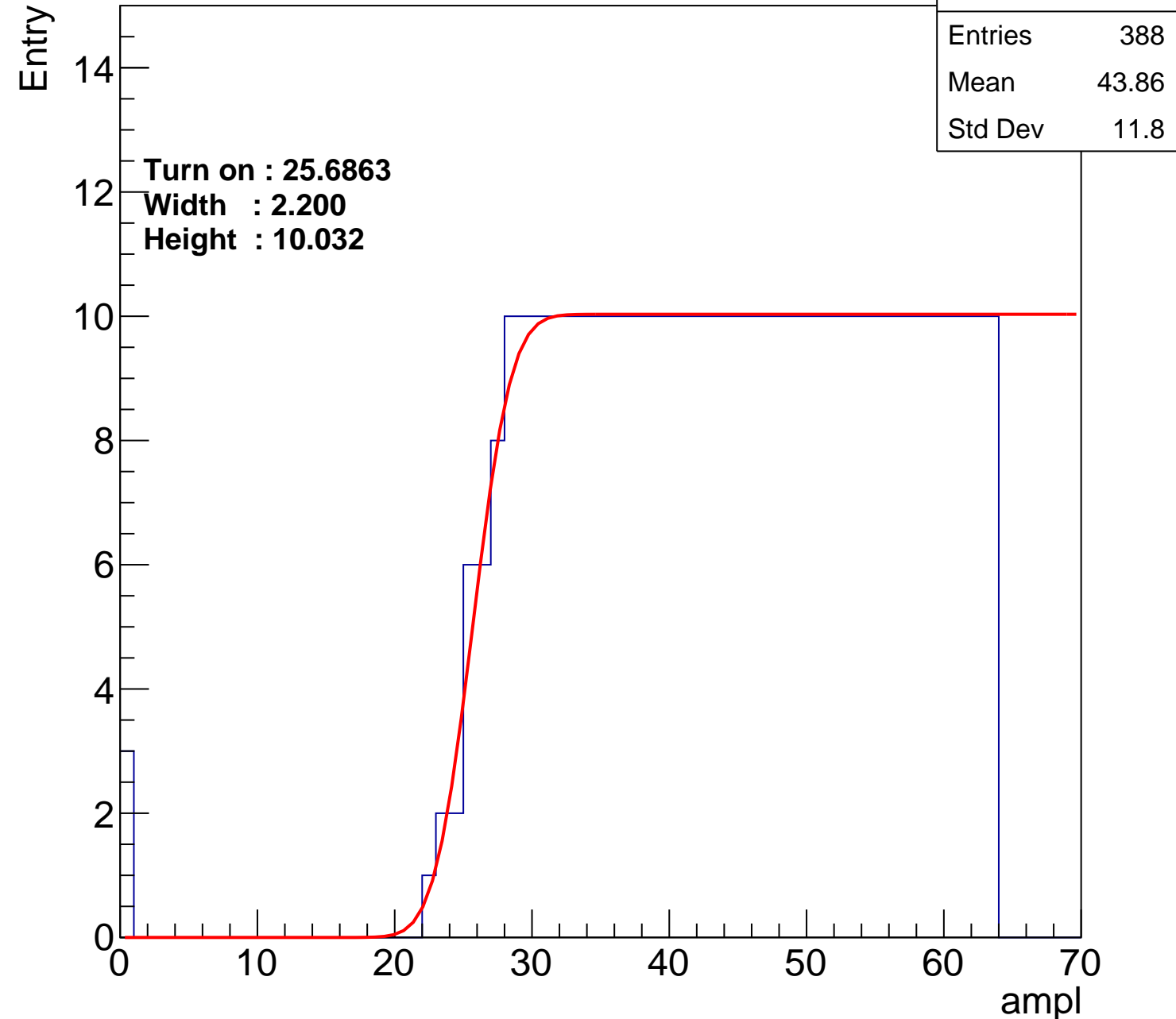
Width : 2.200

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.99
Std Dev	11.46

Turn on : 25.4151

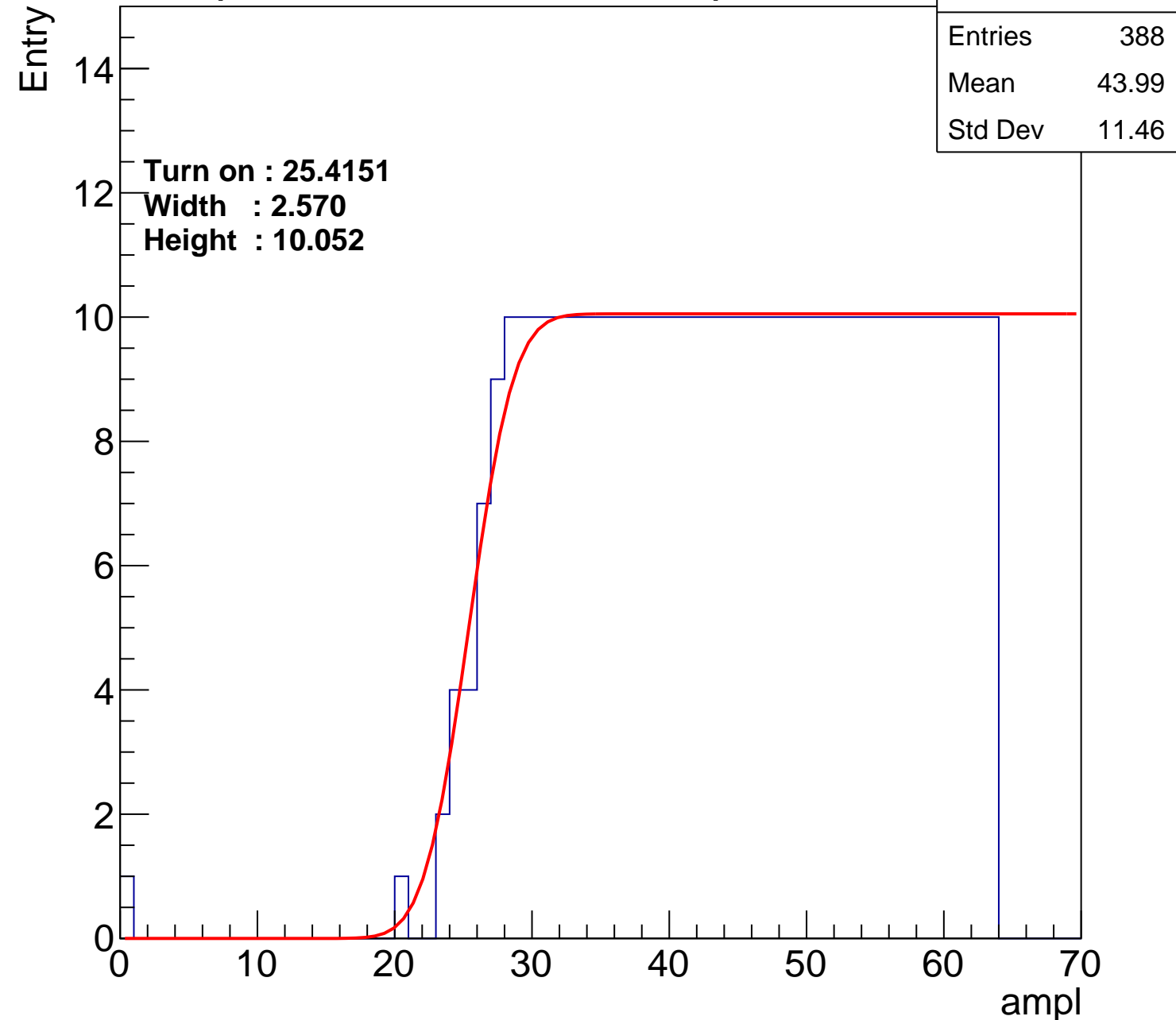
Width : 2.570

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 25.8363

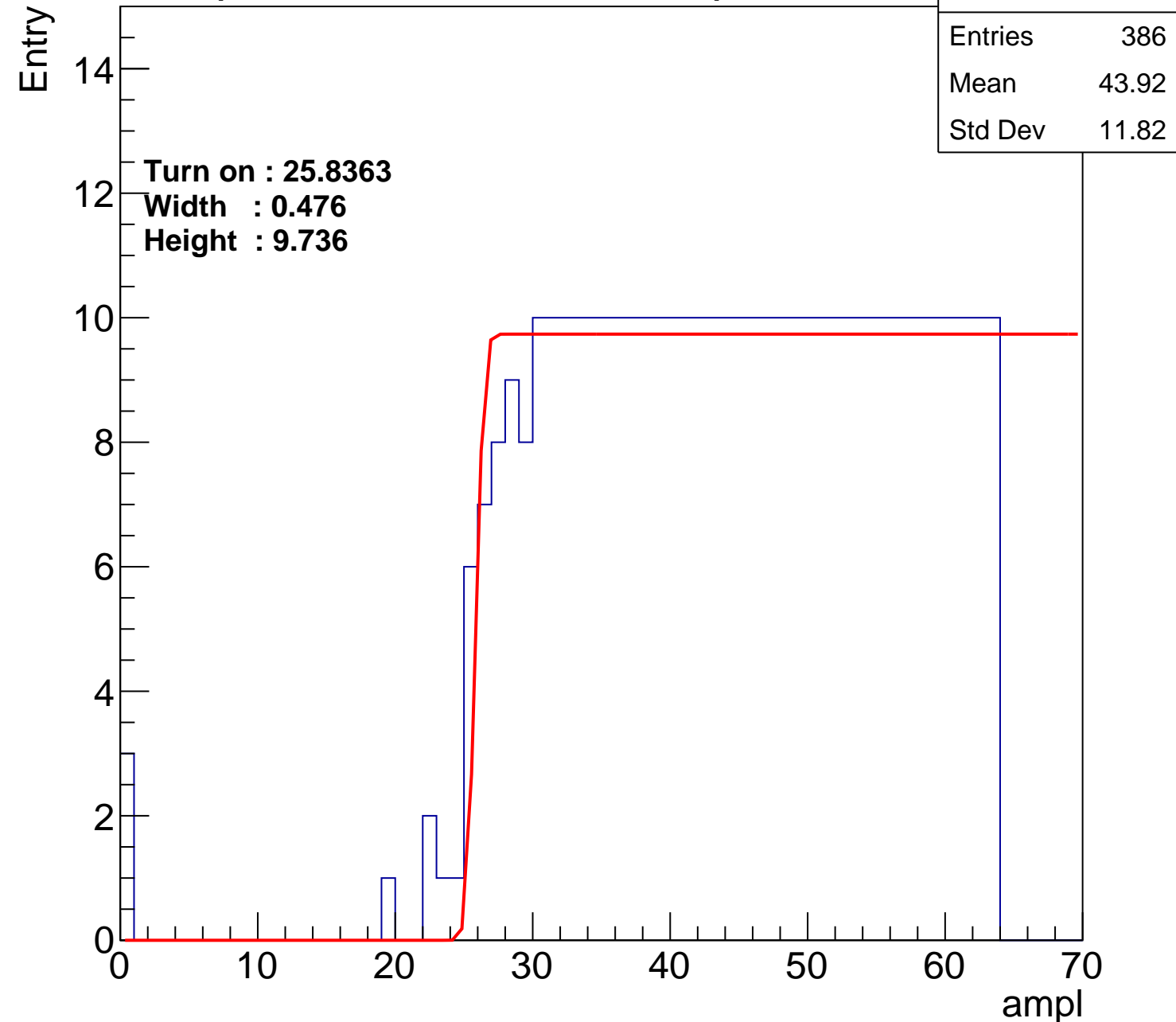
Width : 0.476

Height : 9.736

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.87
Std Dev	11.96

Turn on : 25.9906

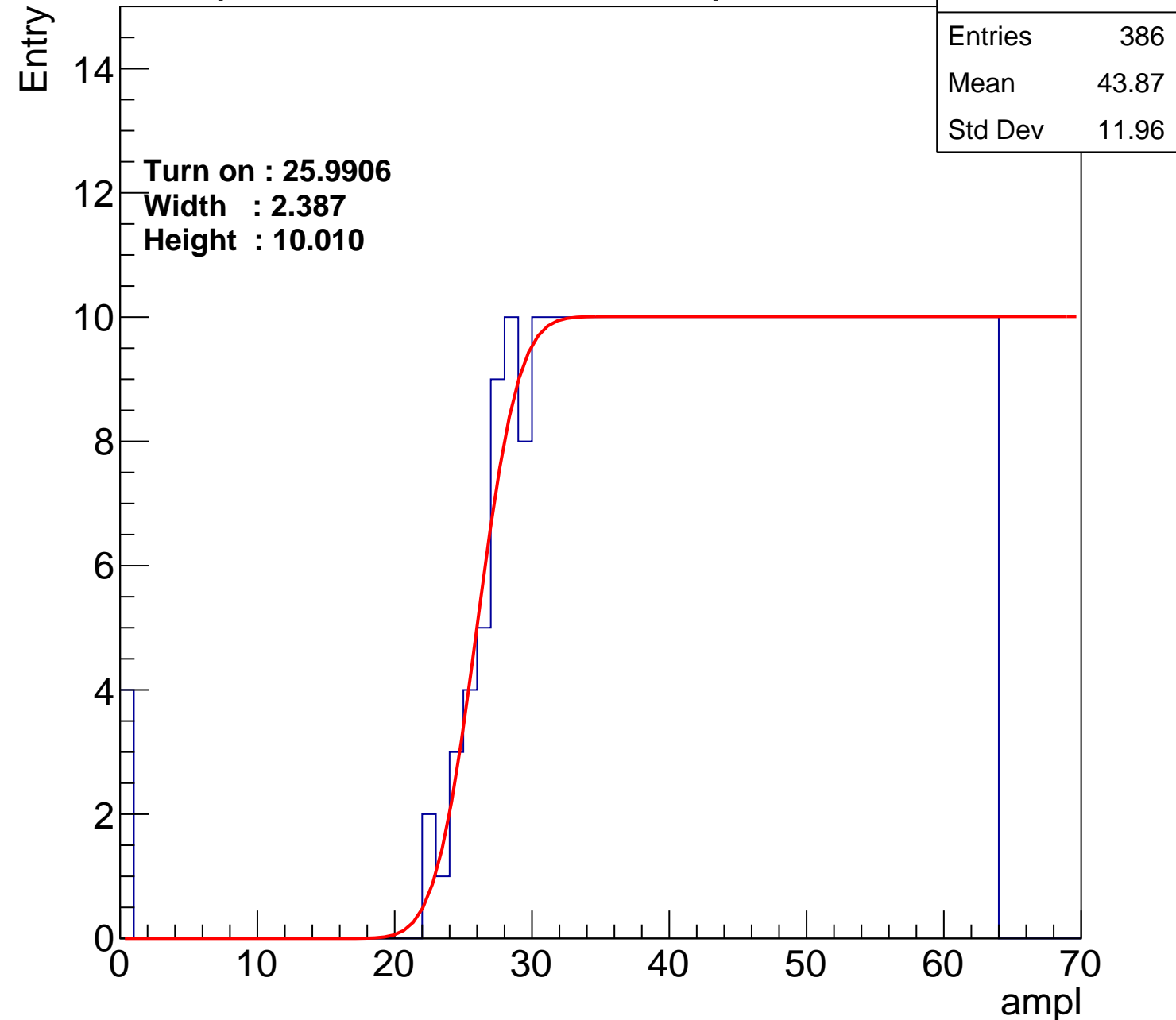
Width : 2.387

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.81
Std Dev	11.59

Turn on : 24.7024

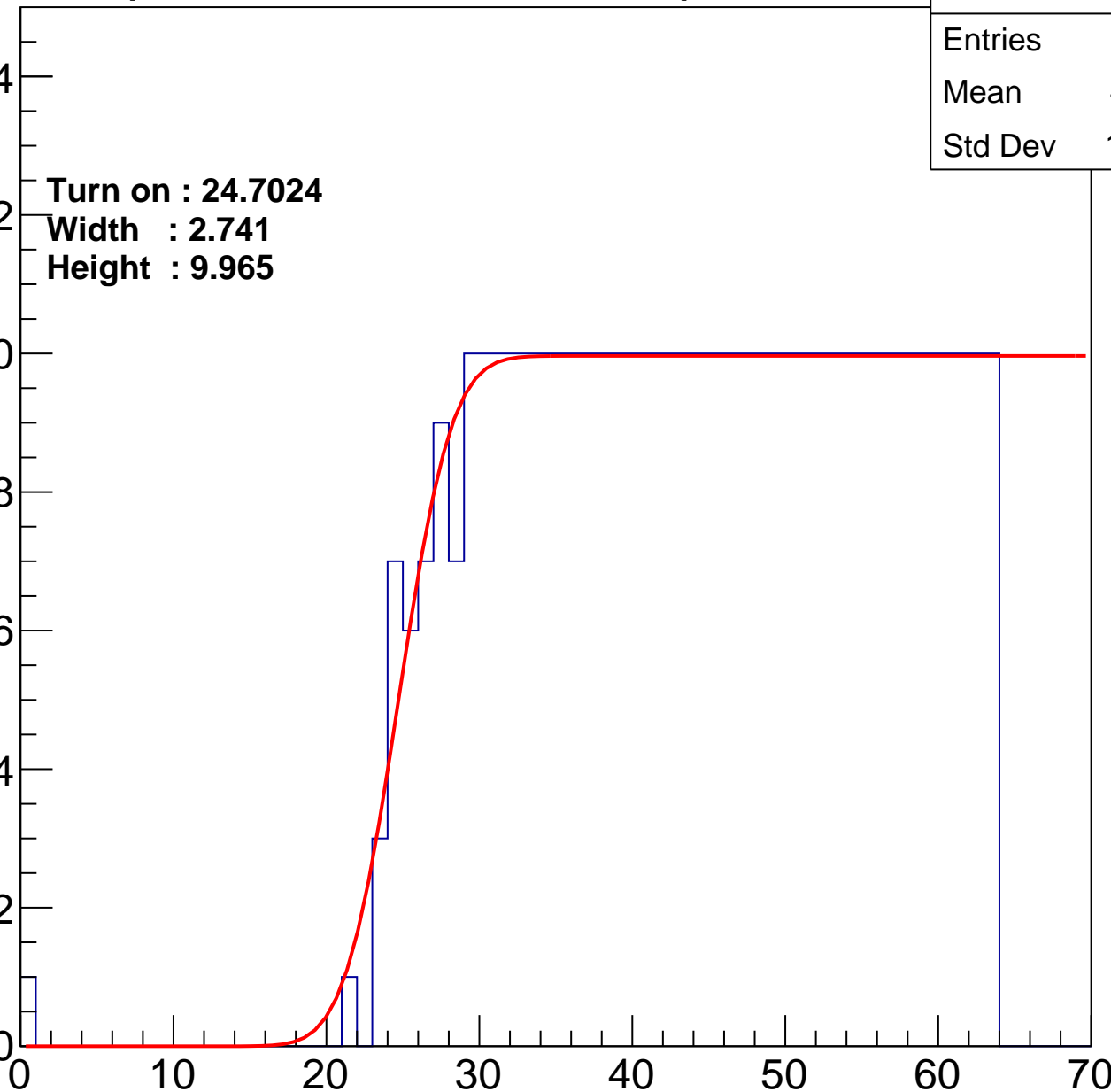
Width : 2.741

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch39

calib_packv5_042523_0143.root, FC#10, port B3

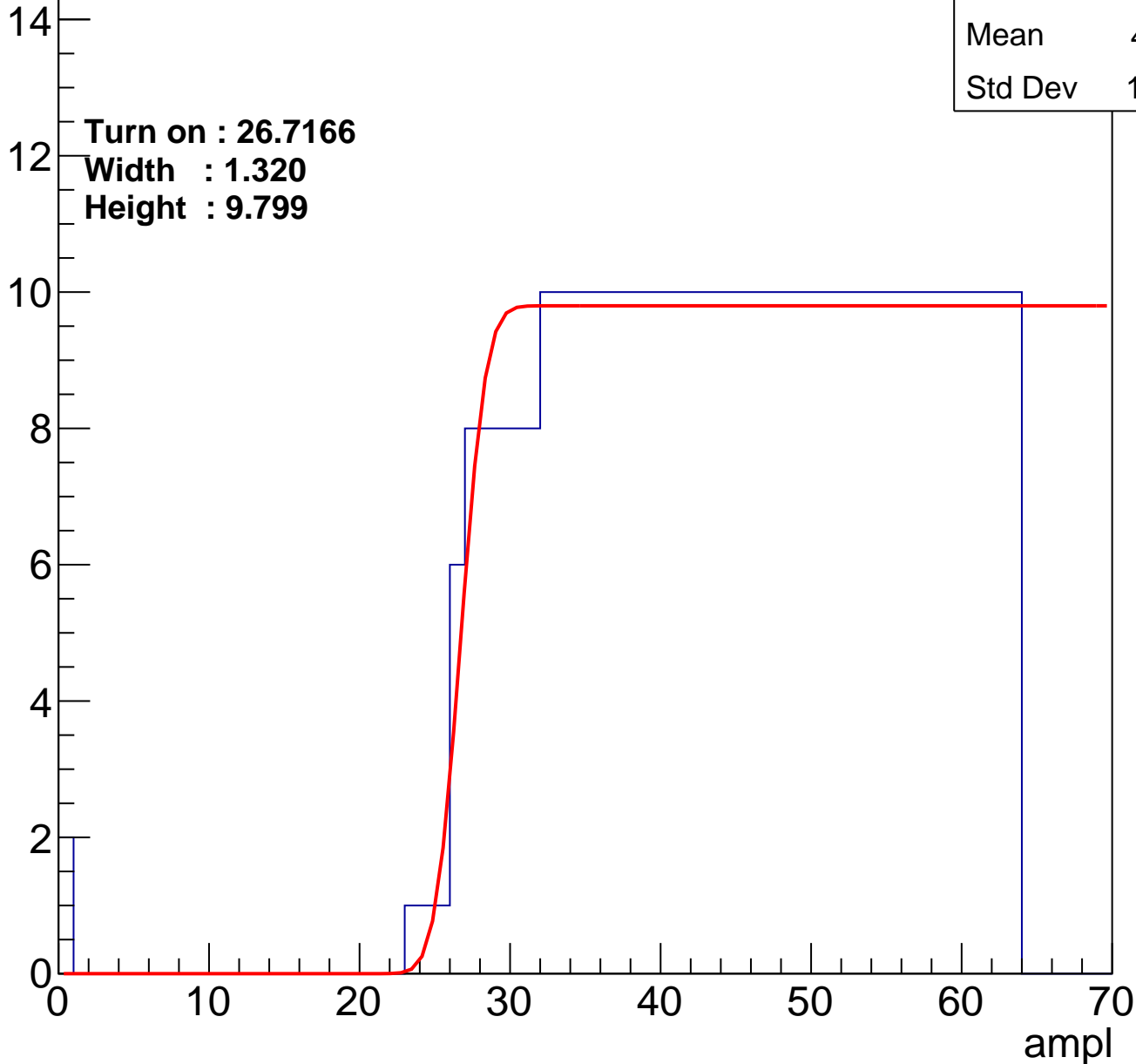
Entry

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 26.7166

Width : 1.320

Height : 9.799



B1L002S, U3-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.54

Turn on : 25.2672

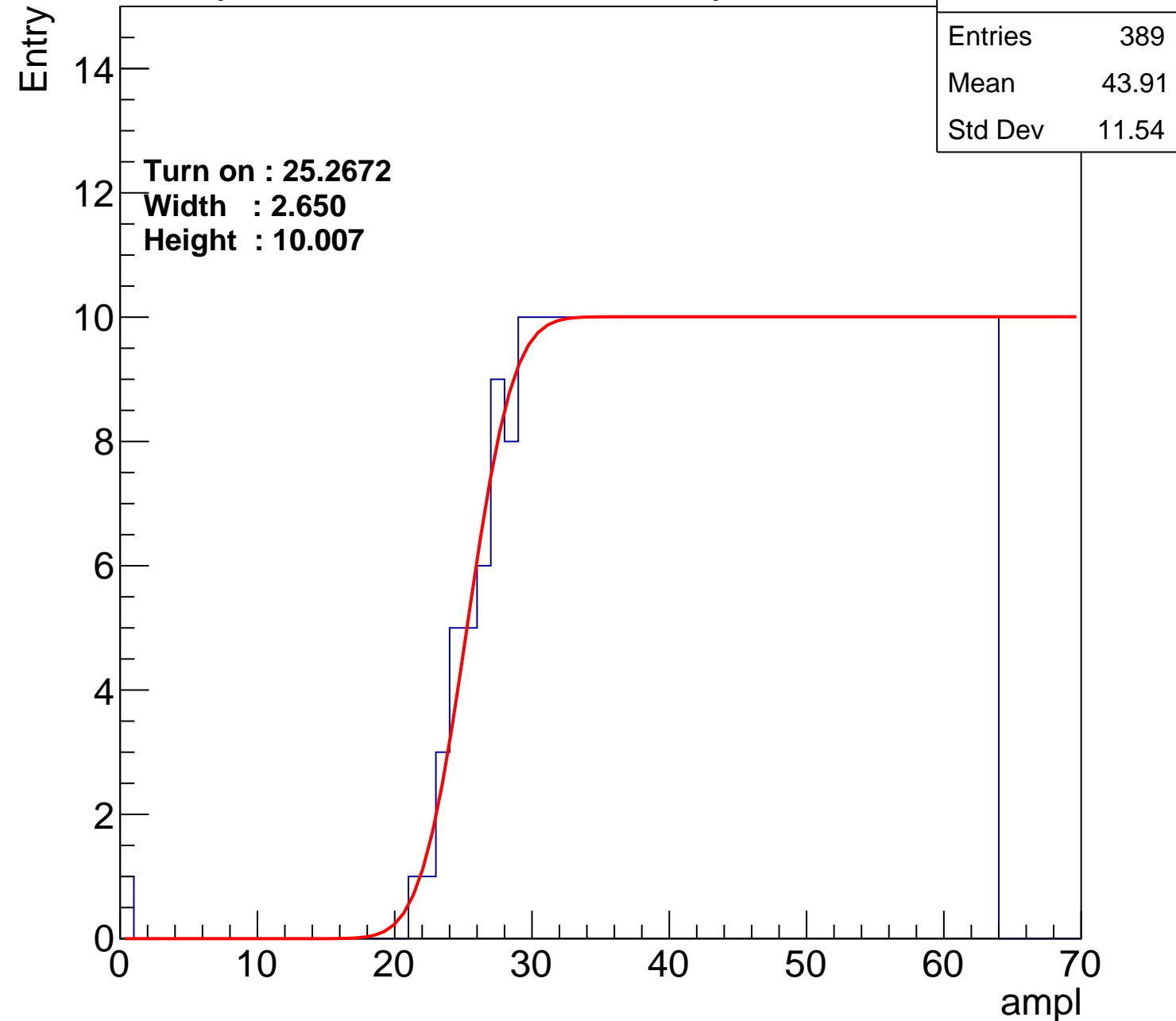
Width : 2.650

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.61
Std Dev	11.82

Turn on : 25.1868

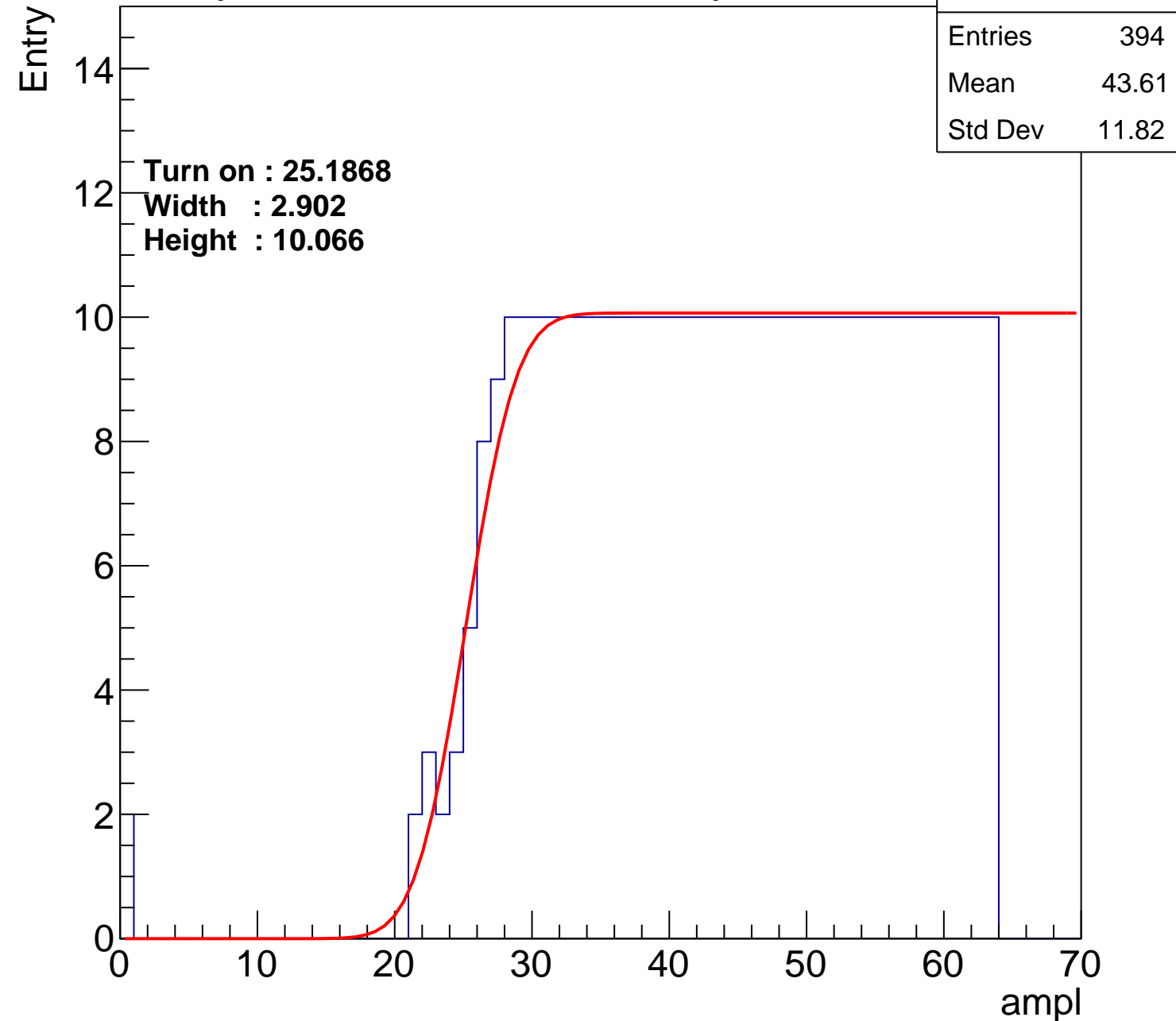
Width : 2.902

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.0978

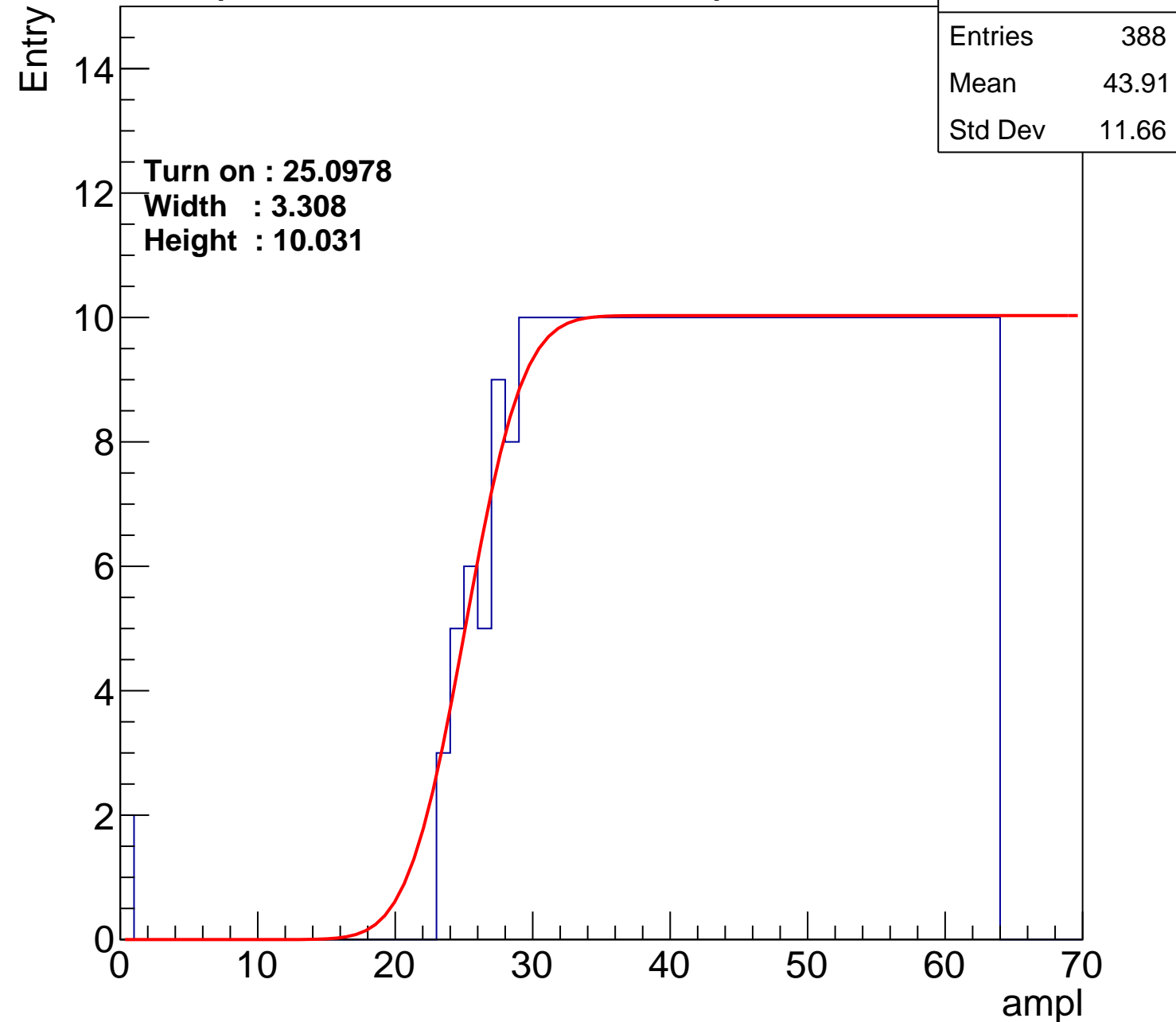
Width : 3.308

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.55
Std Dev	11.23

Turn on : 26.6852

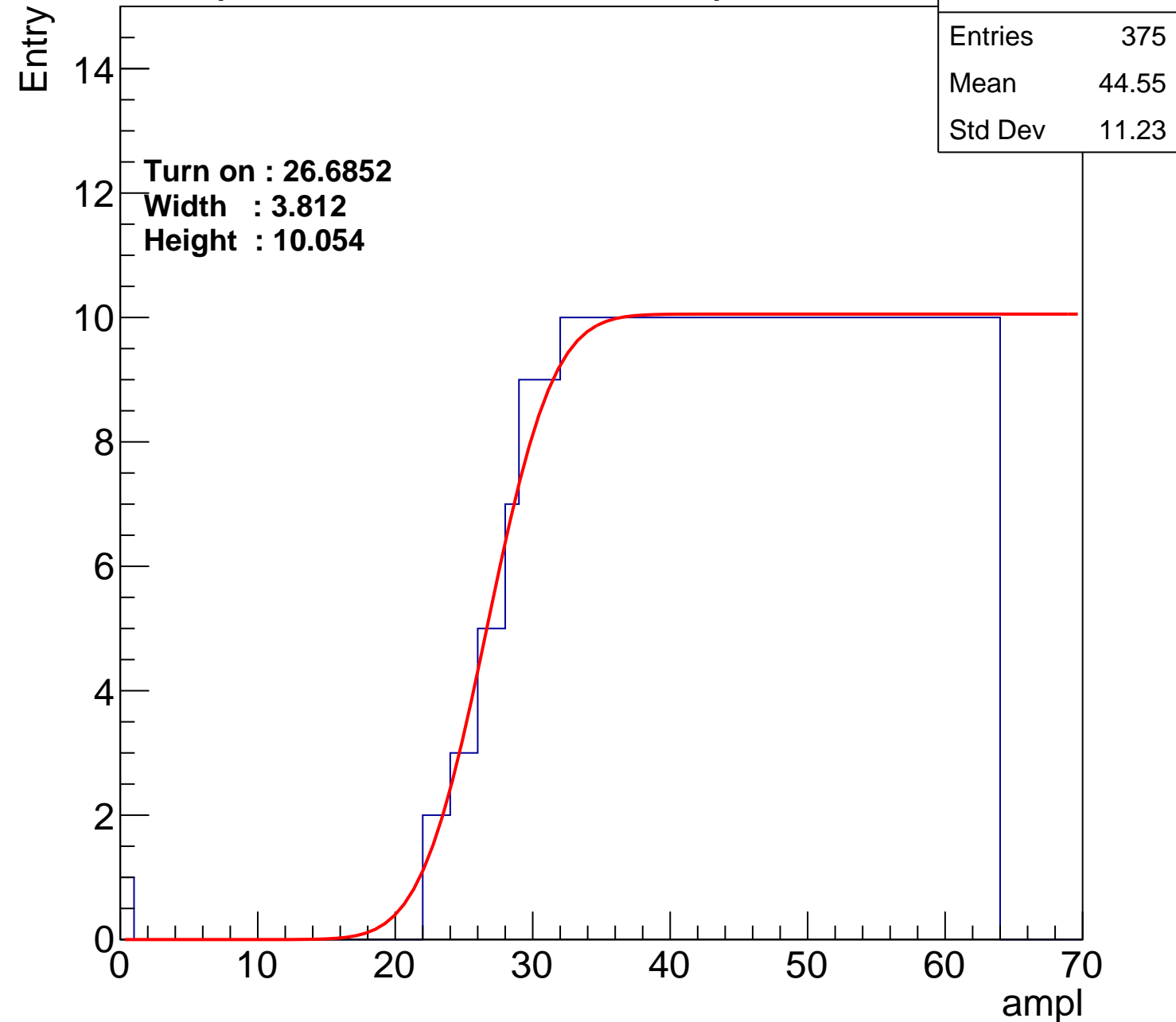
Width : 3.812

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.46
Std Dev	11.54

Turn on : 26.9907

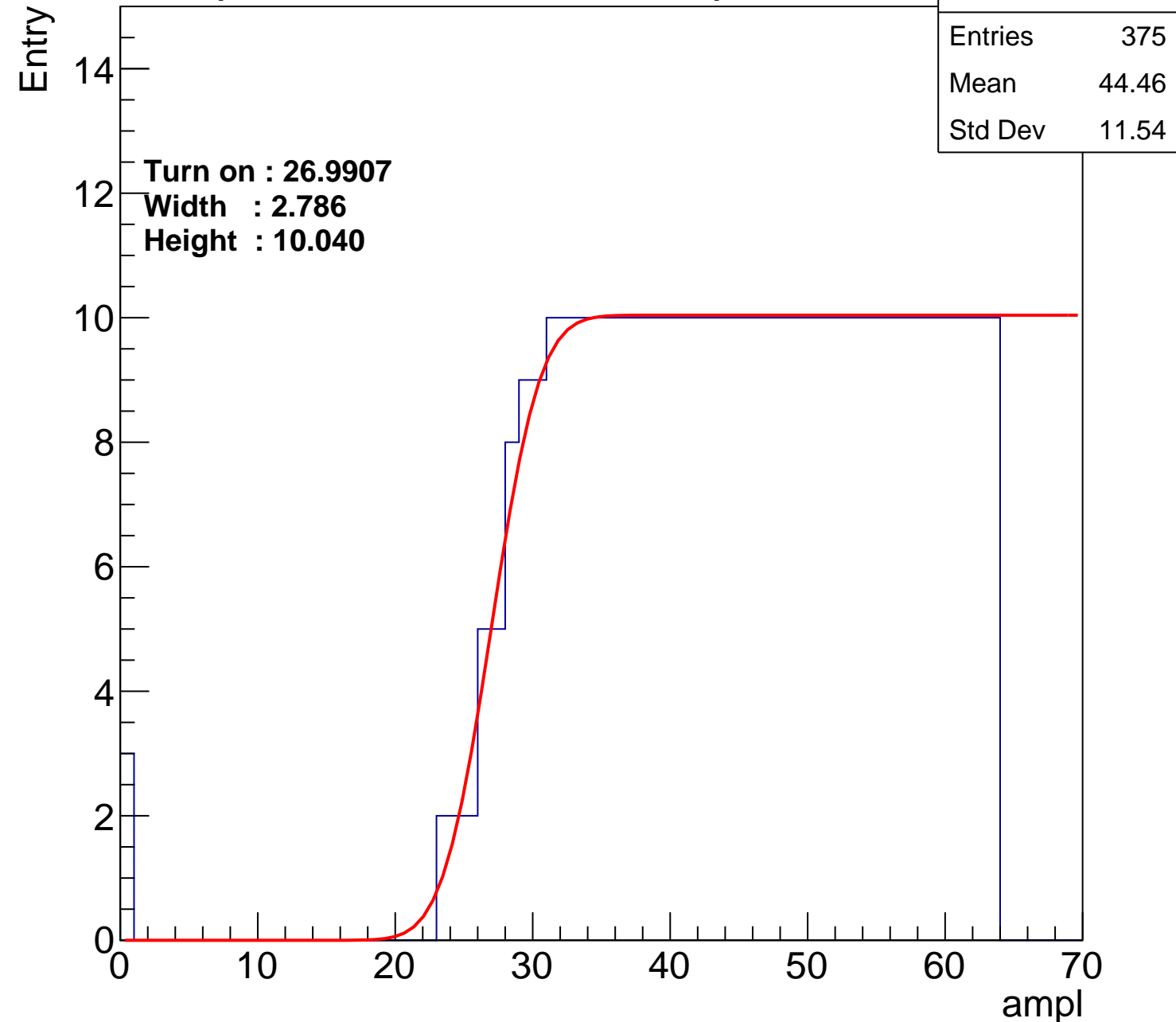
Width : 2.786

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.43
Std Dev	11.89

Turn on : 24.7237

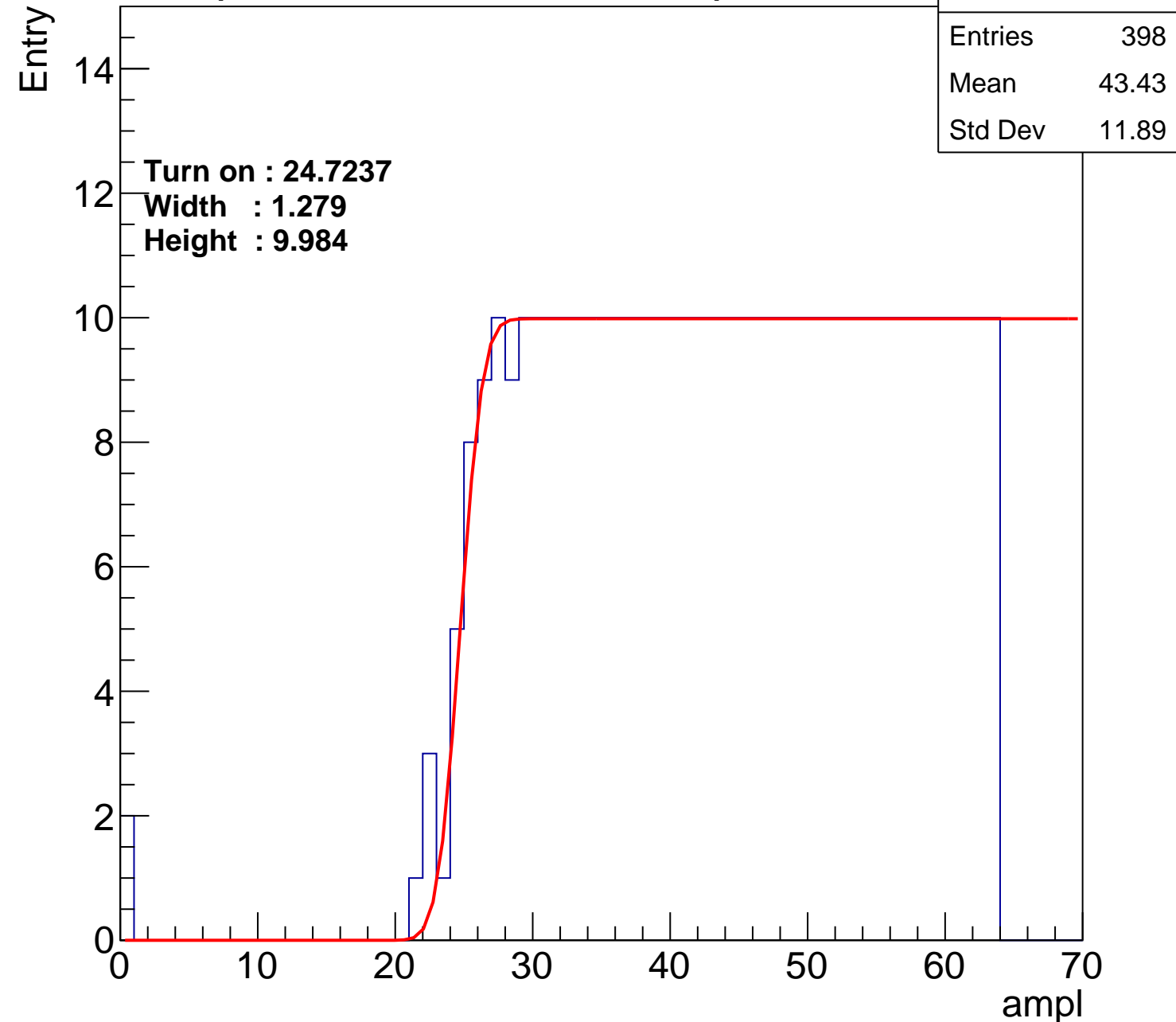
Width : 1.279

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.56

Turn on : 24.8356

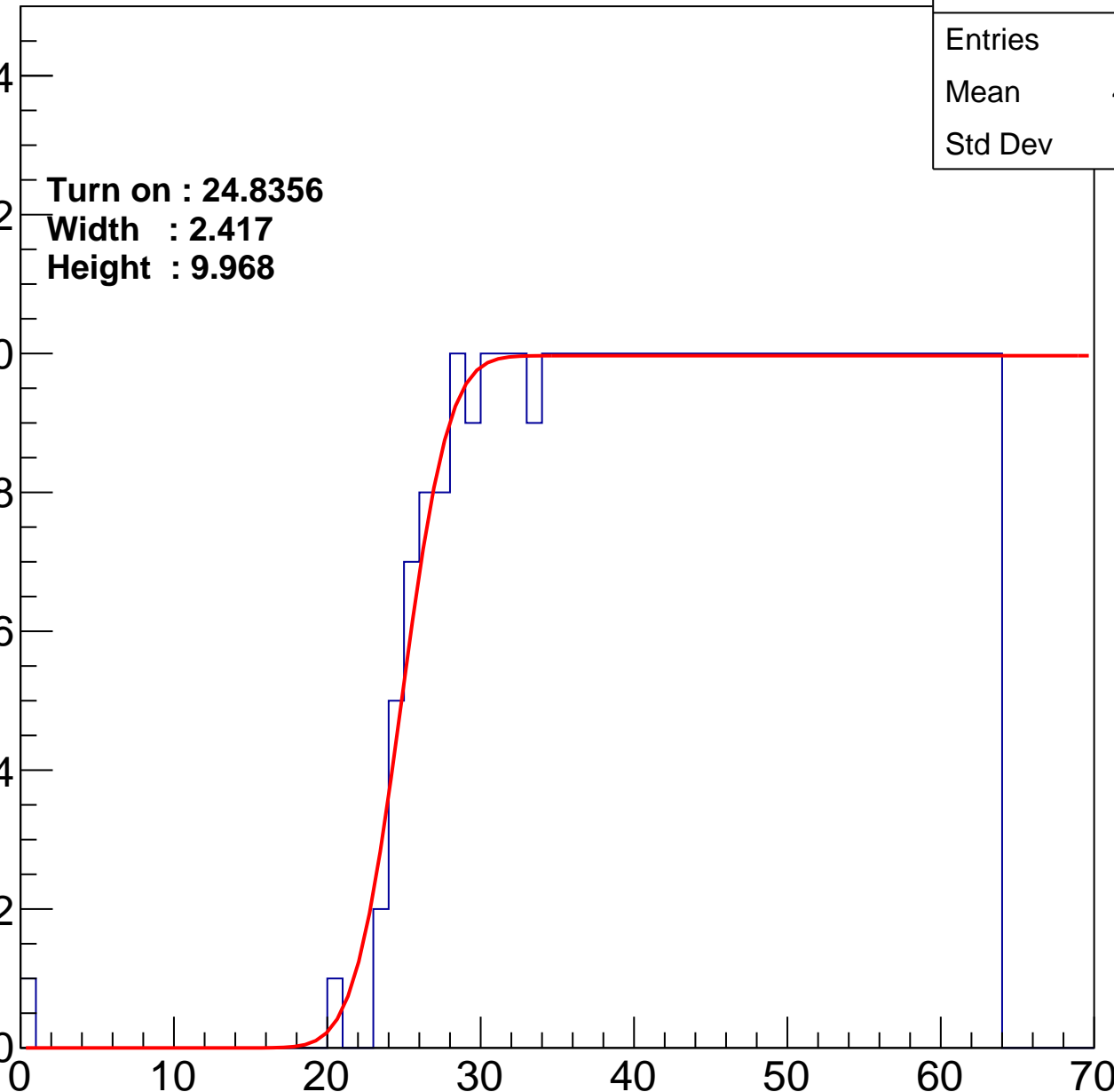
Width : 2.417

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.38
Std Dev	11.25

Turn on : 26.2462

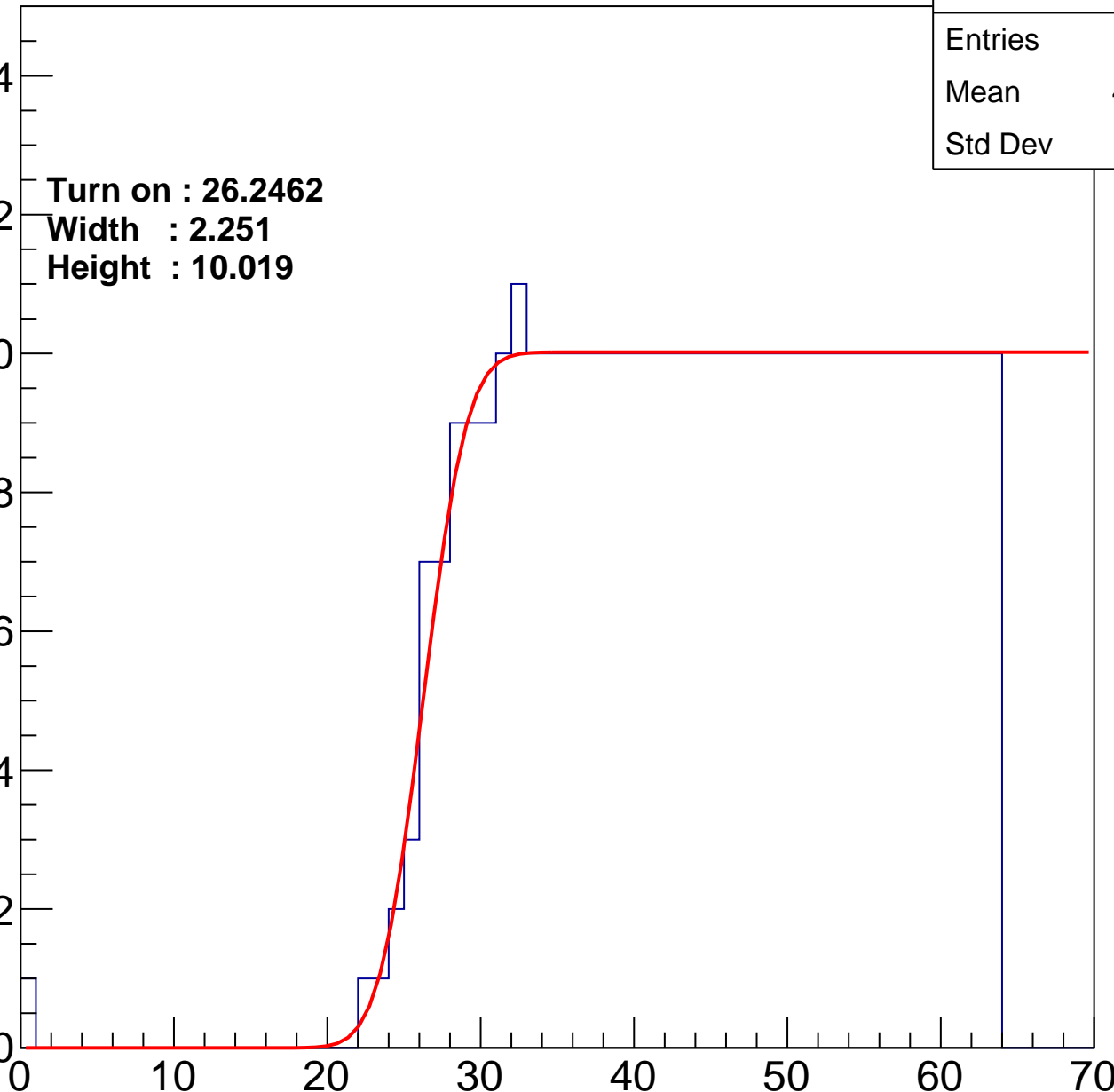
Width : 2.251

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.64
Std Dev	11.65

Turn on : 25.2123

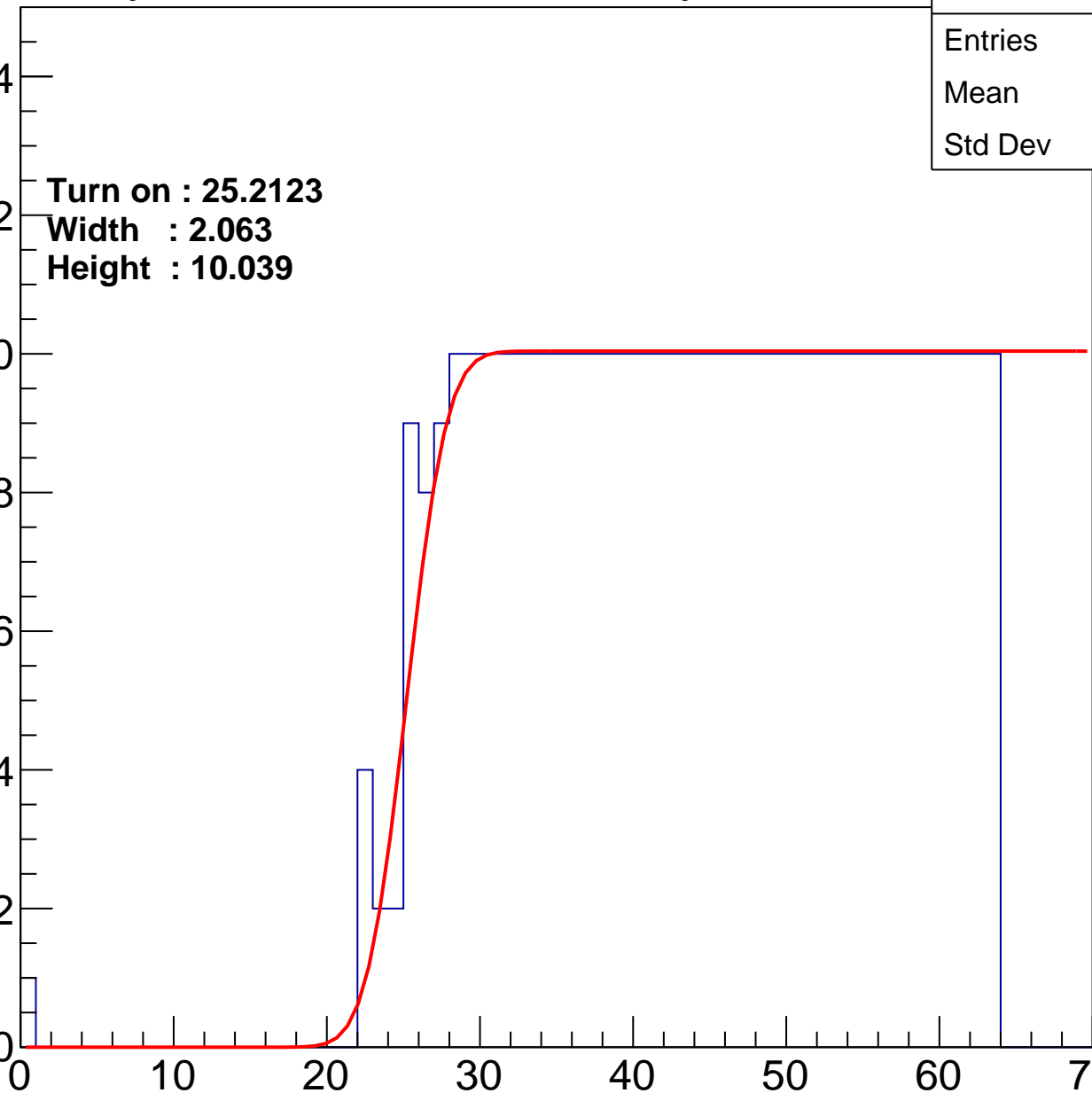
Width : 2.063

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 25.0398

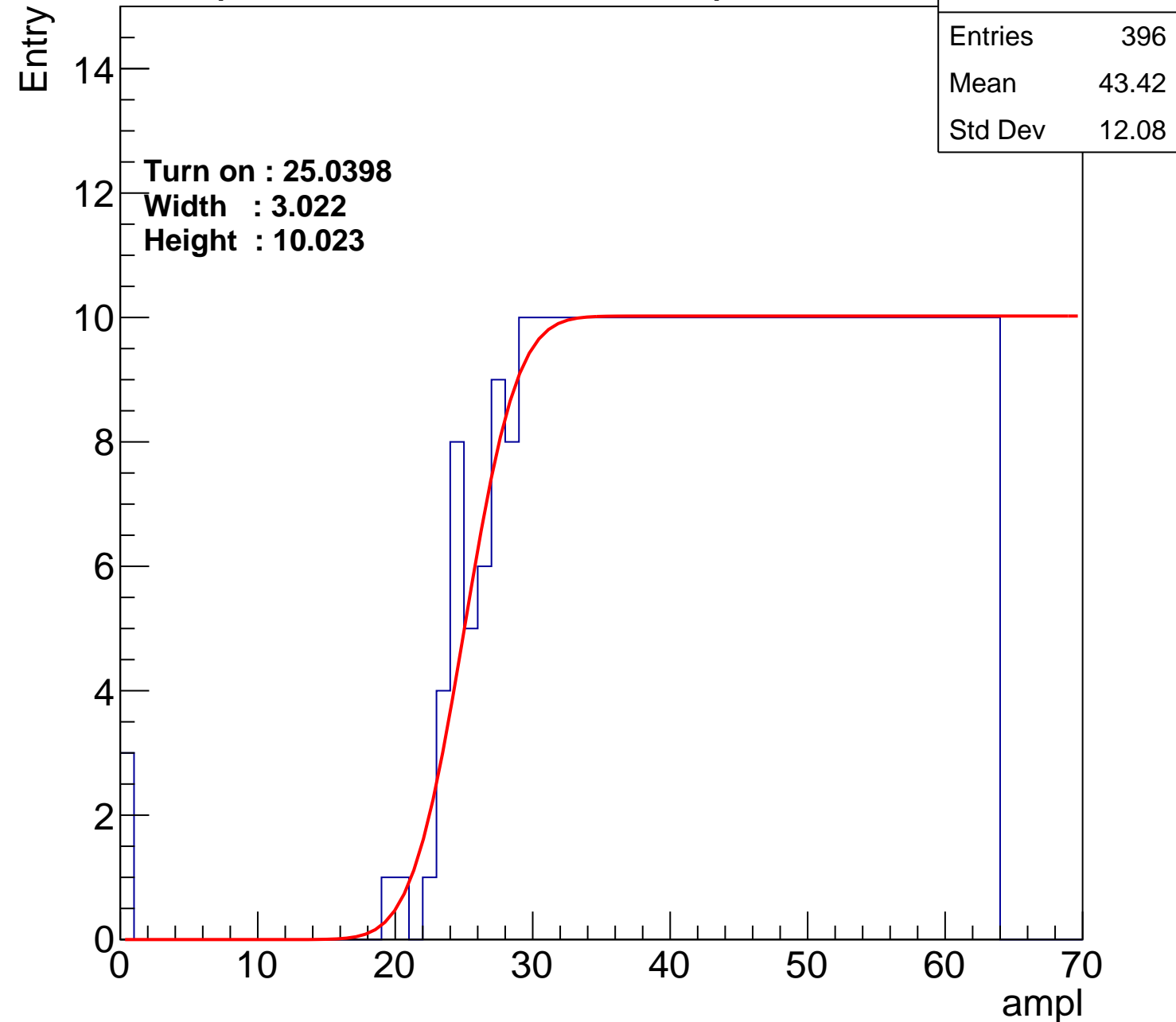
Width : 3.022

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.78

Turn on : 25.0225

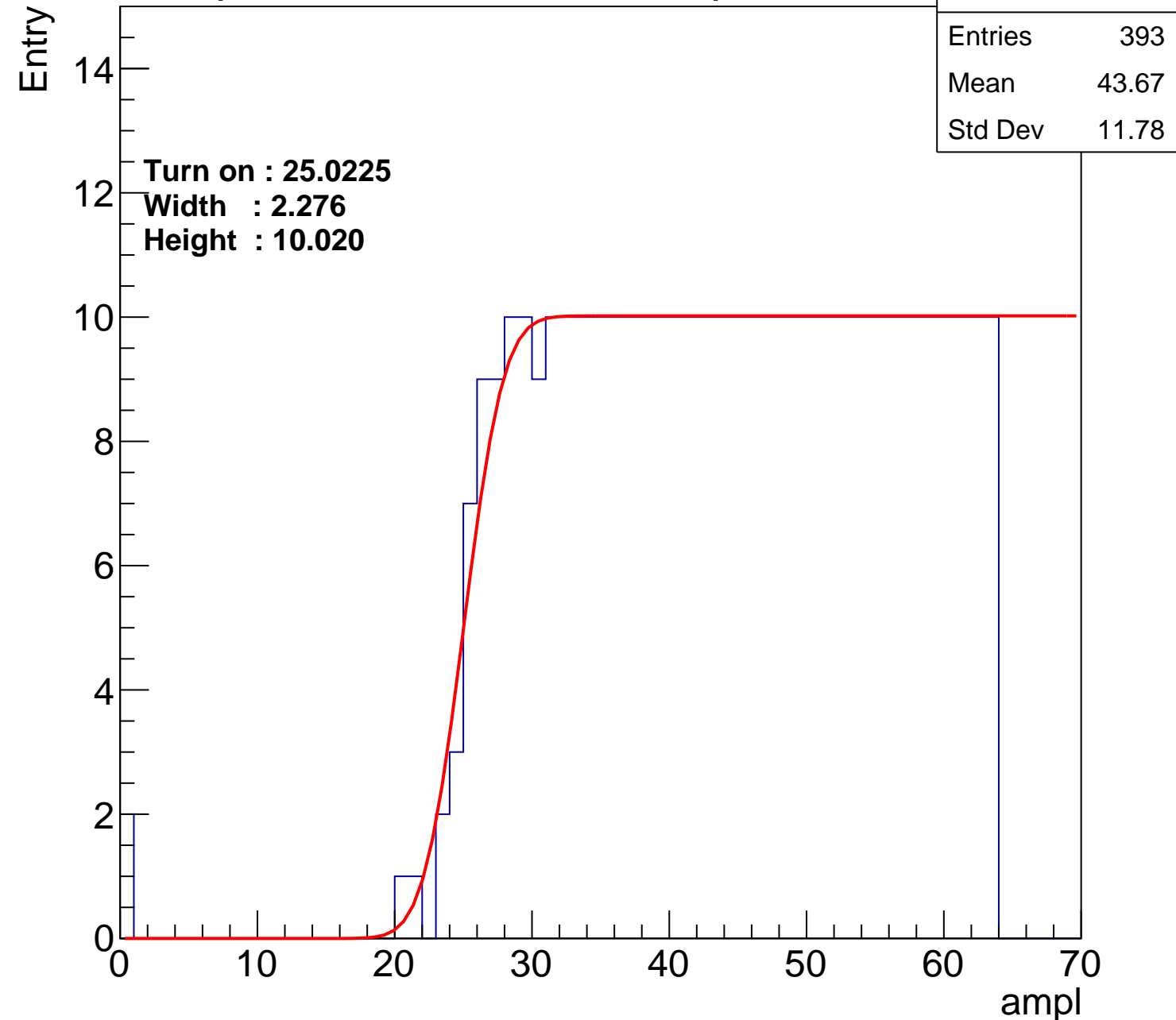
Width : 2.276

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.49
Std Dev	11.24

Turn on : 27.0727

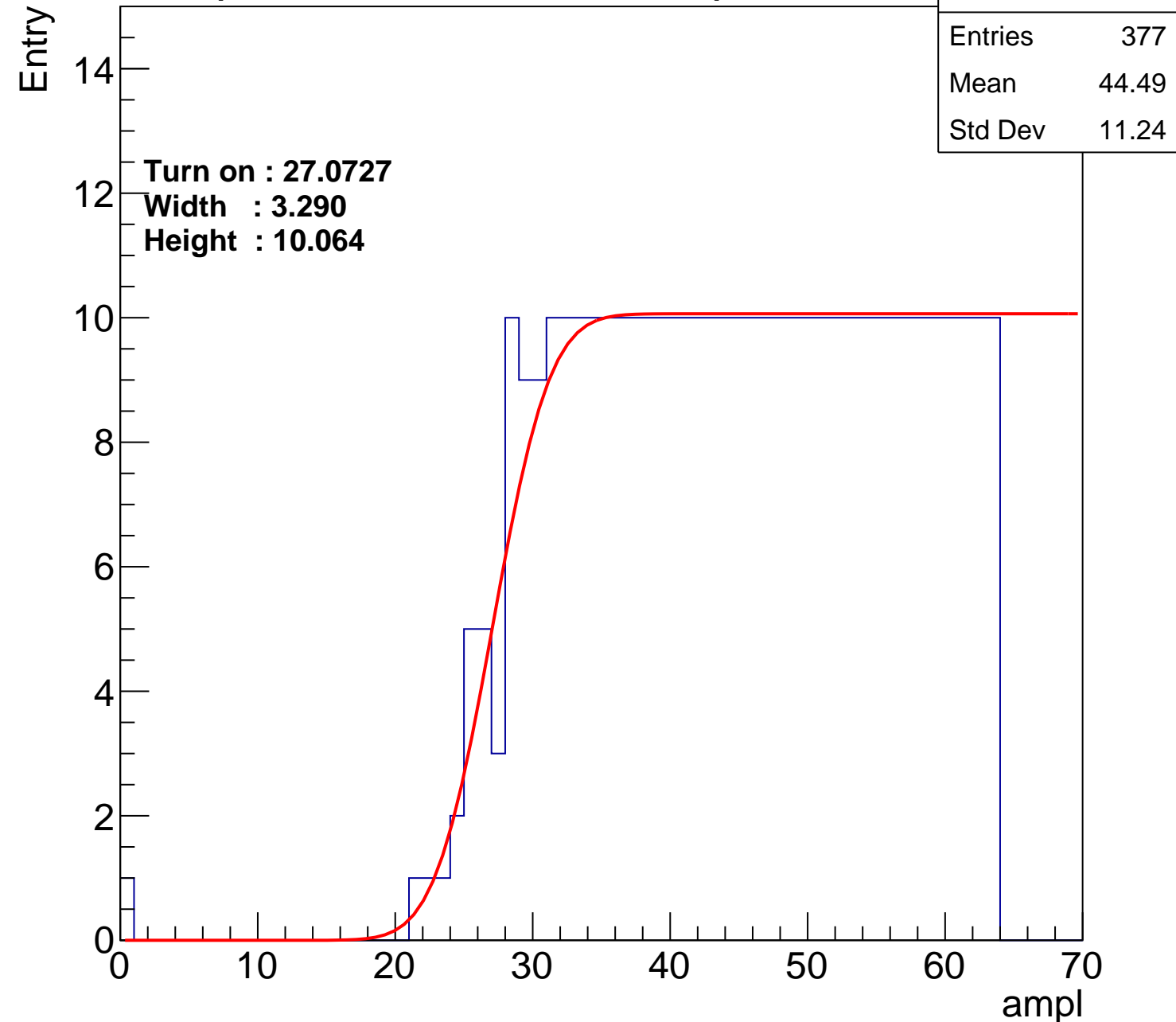
Width : 3.290

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 24.9993

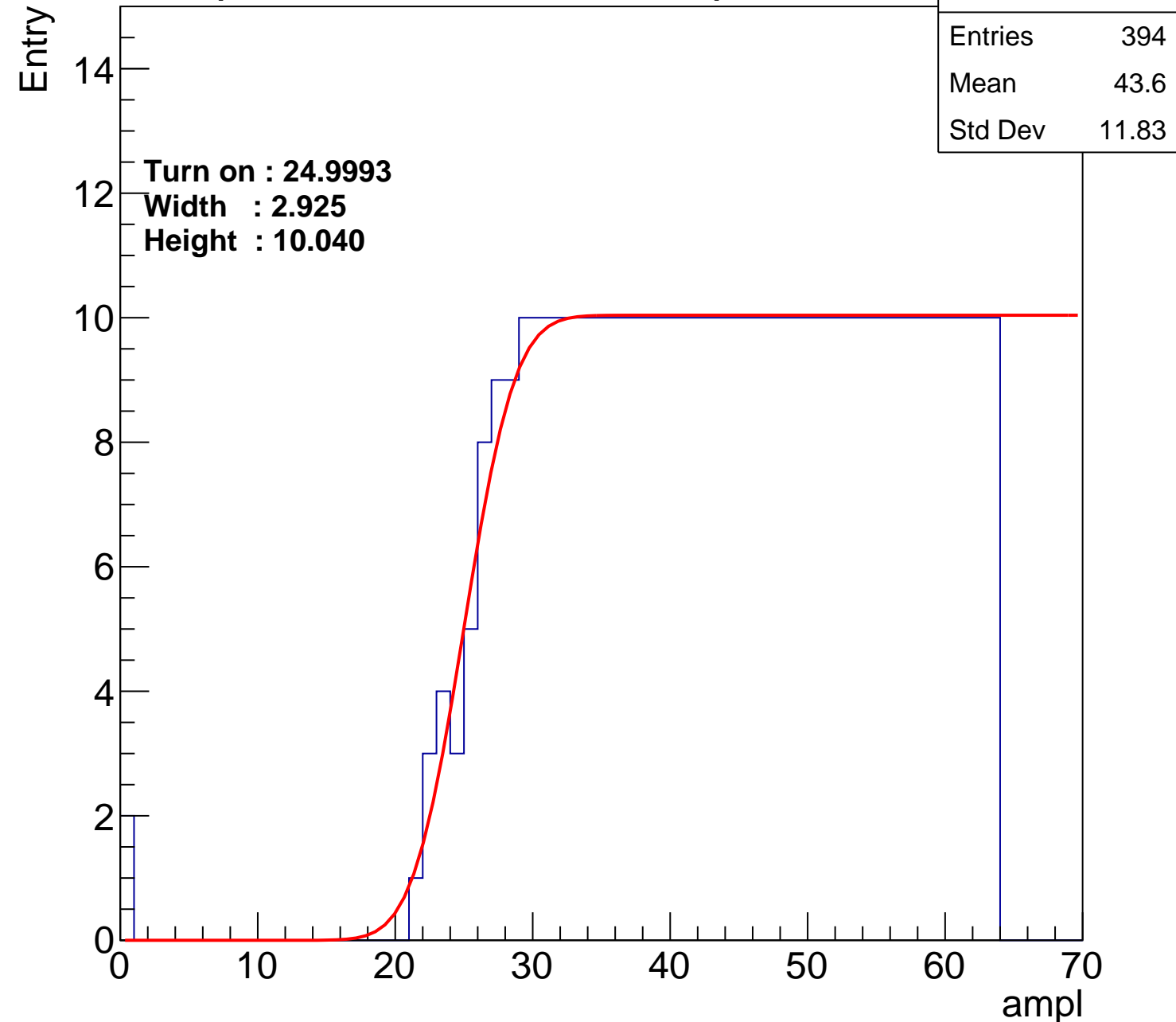
Width : 2.925

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.87
Std Dev	11.6

Turn on : 25.8509

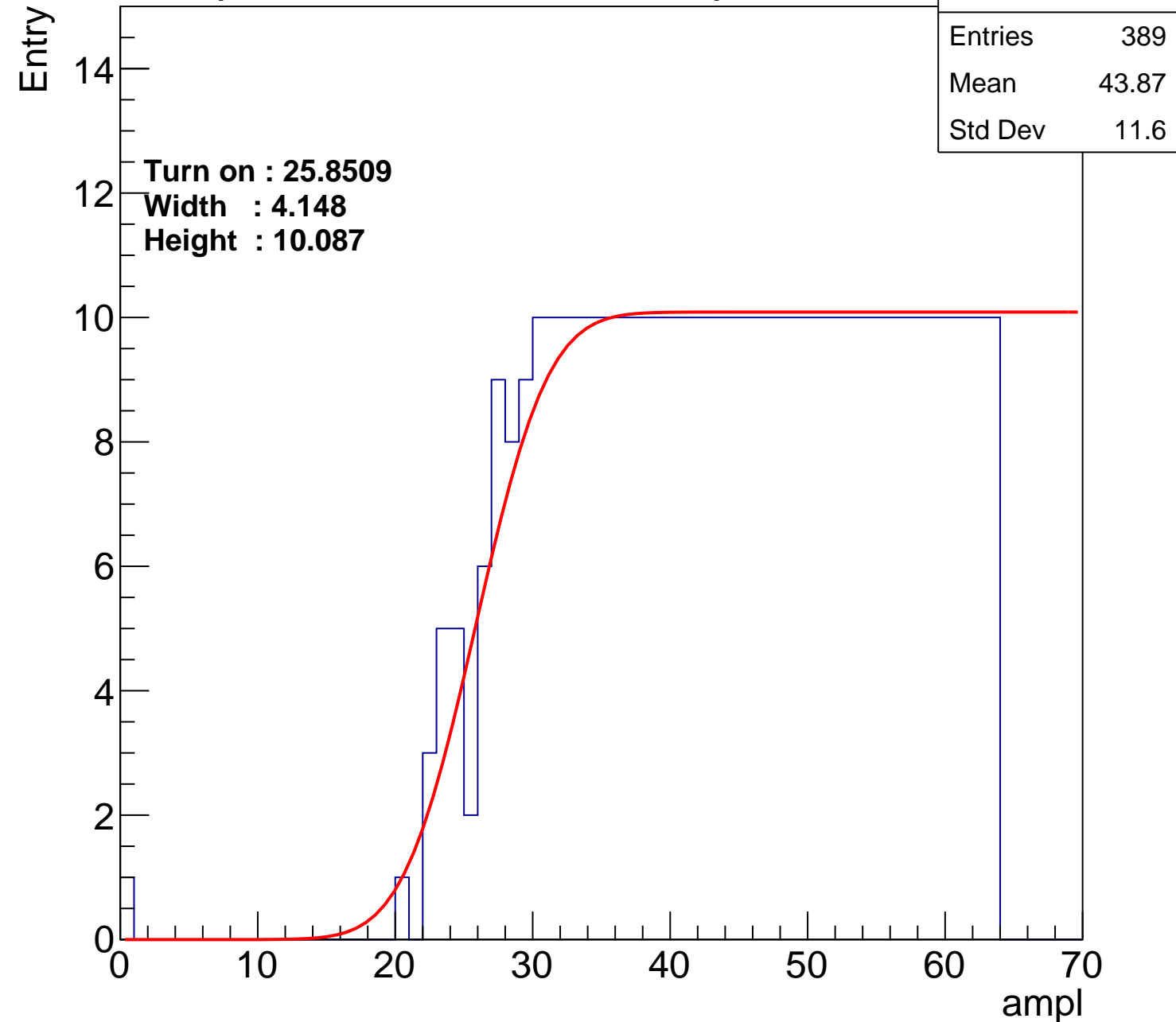
Width : 4.148

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.01
Std Dev	12.25

Turn on : 23.5279

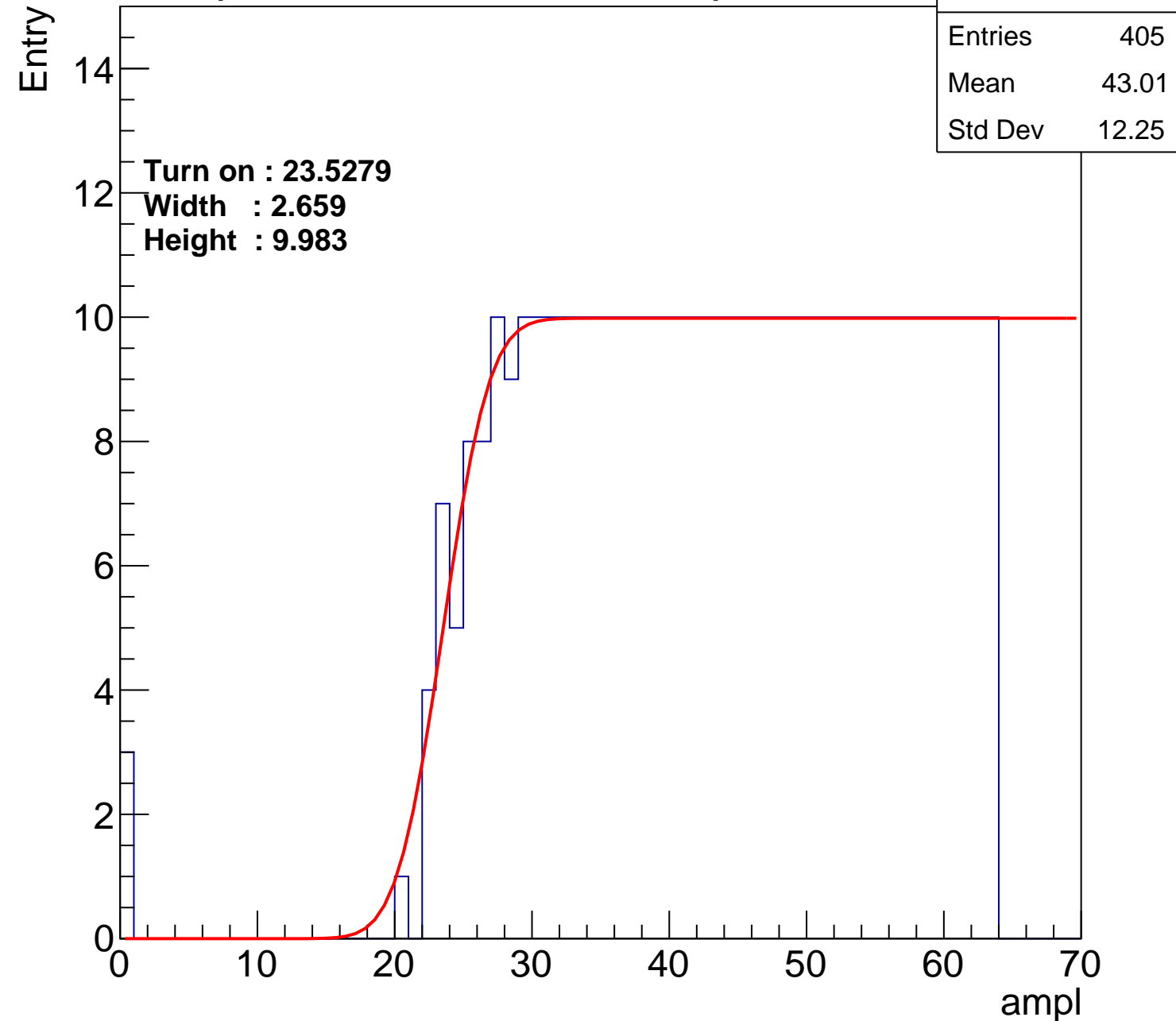
Width : 2.659

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 25.9252

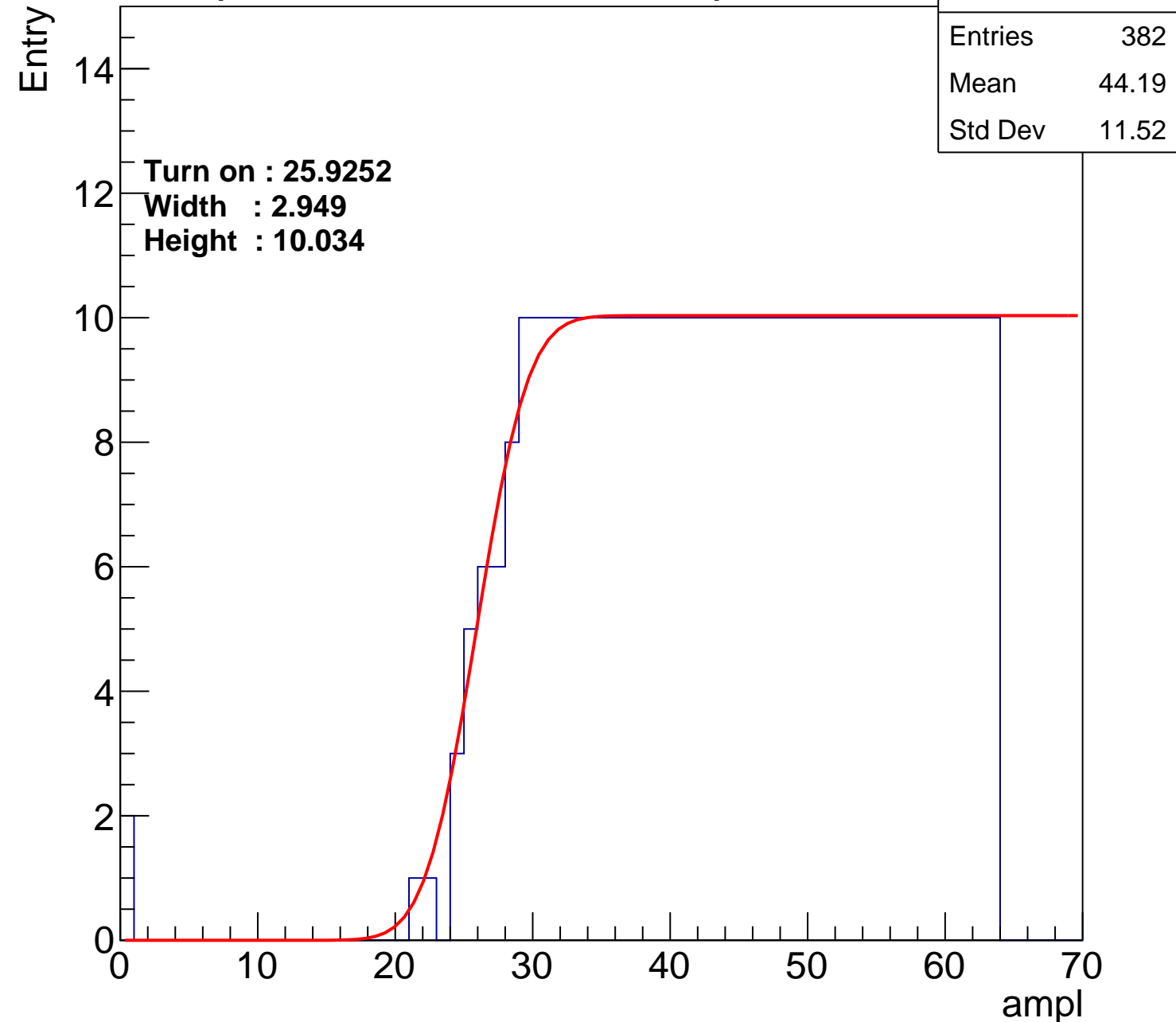
Width : 2.949

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.72
Std Dev	12.04

Turn on : 25.8252

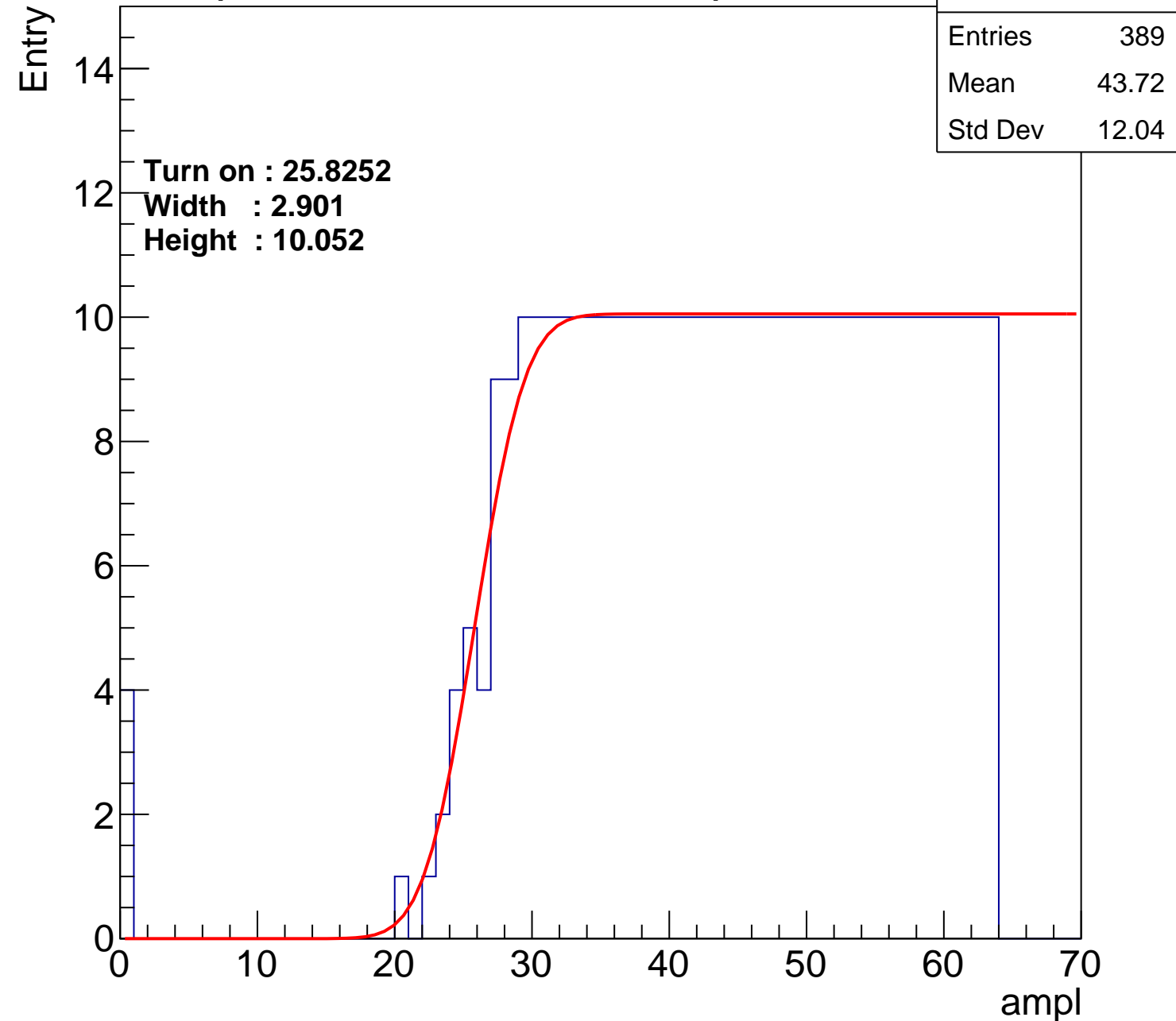
Width : 2.901

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.25
Std Dev	12.01

Turn on : 24.2032

Width : 2.896

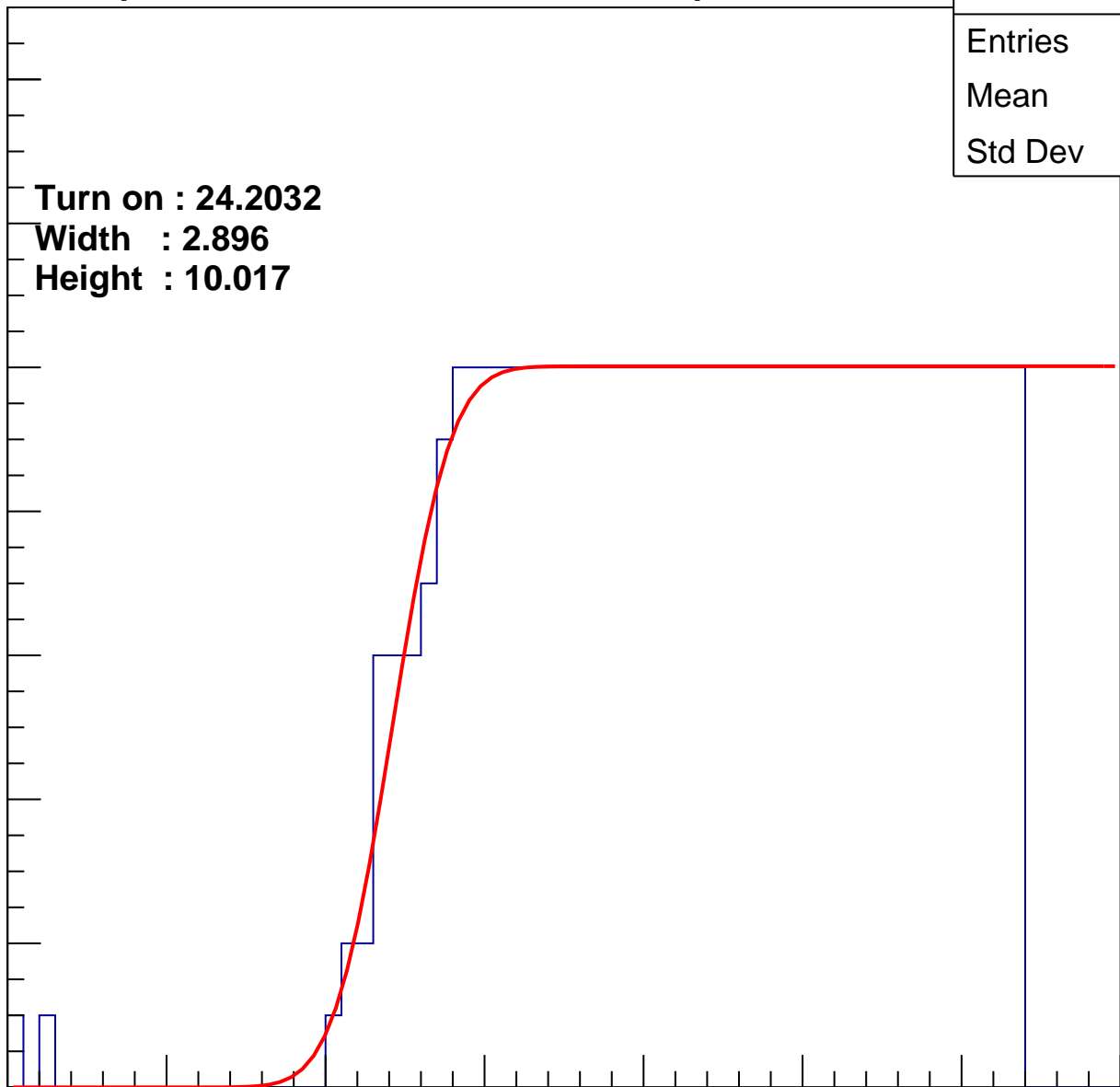
Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U3-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.32
Std Dev	12.35

Turn on : 25.1983

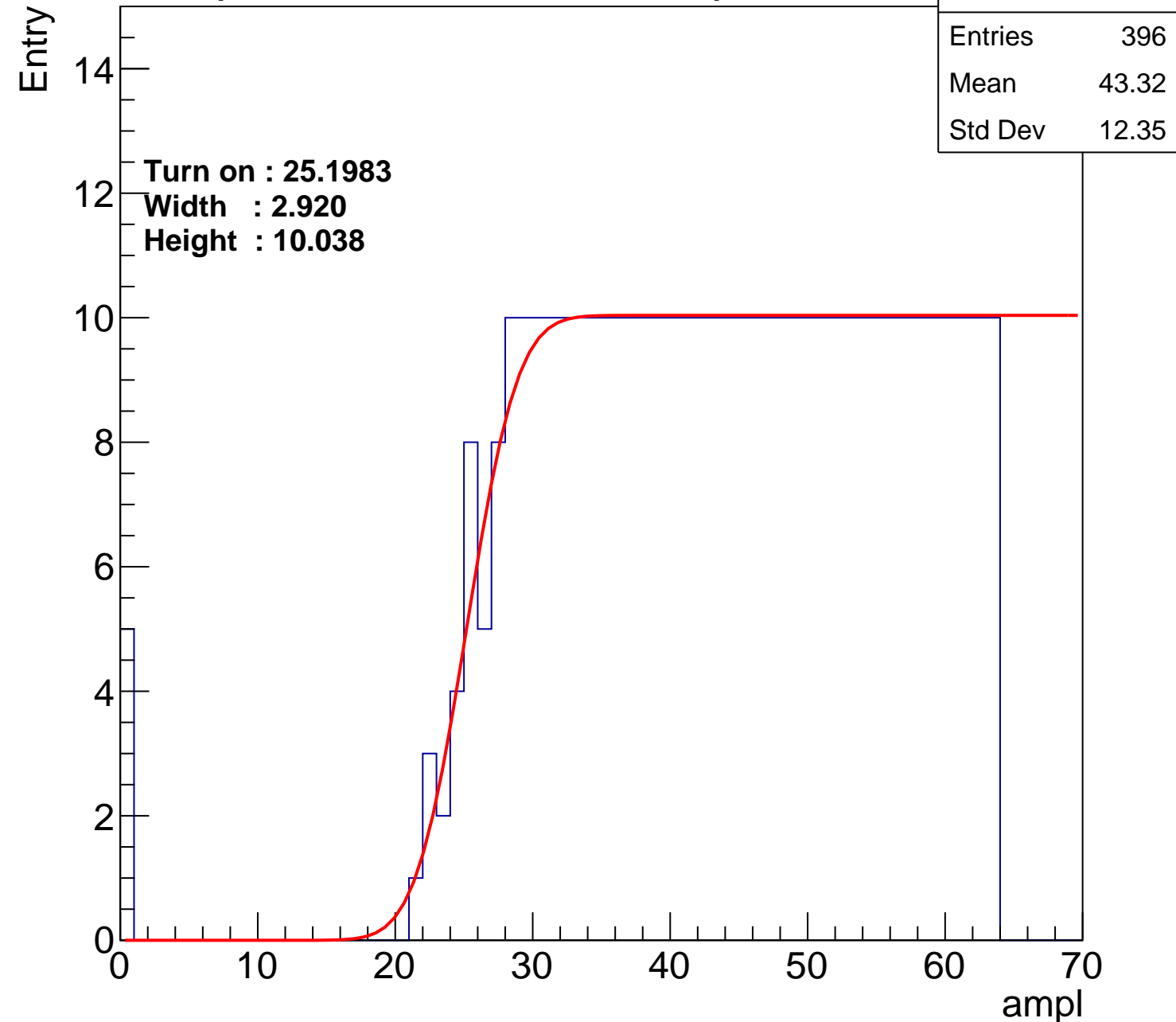
Width : 2.920

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 27.0760

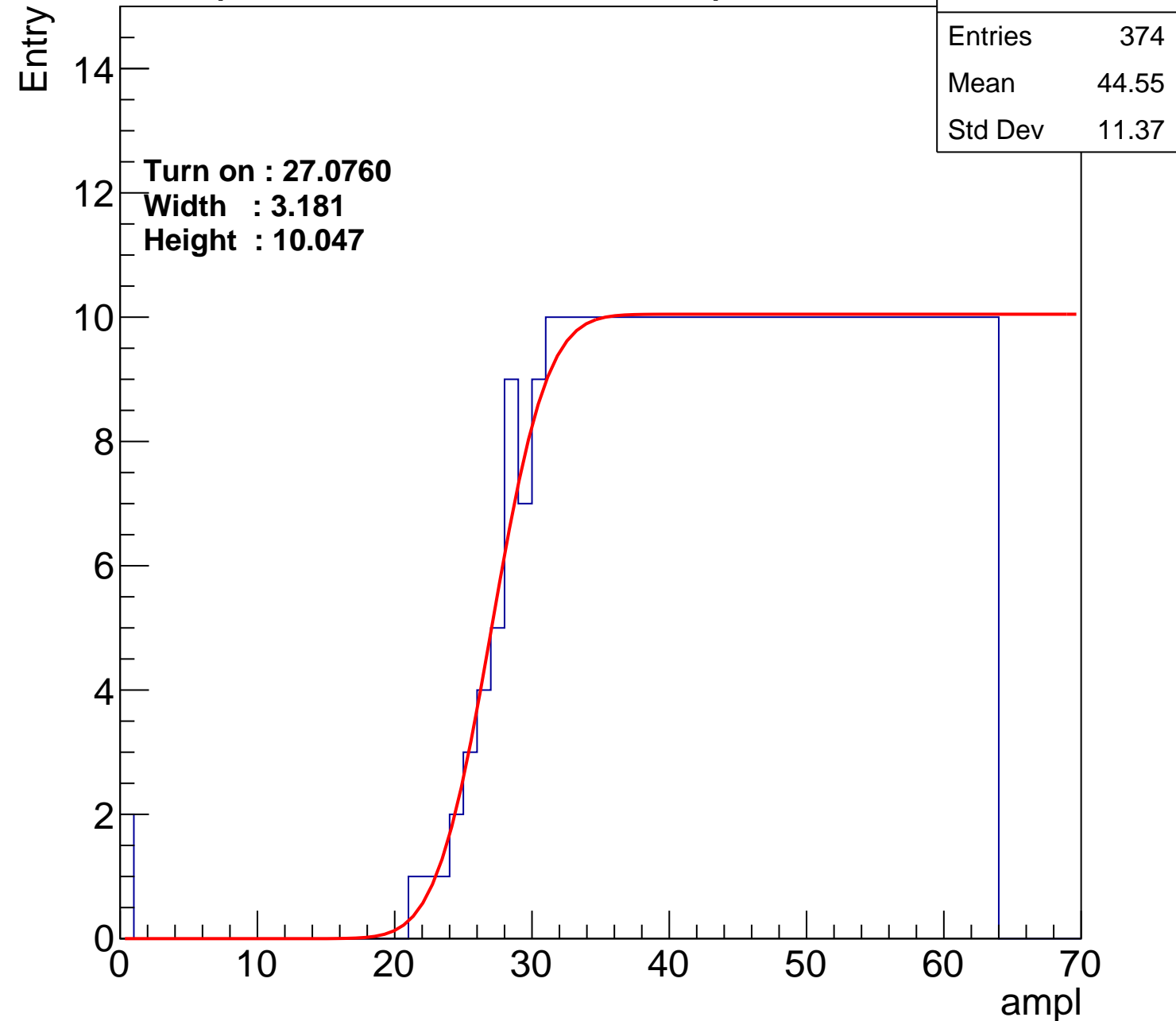
Width : 3.181

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.71
Std Dev	11.13

Turn on : 26.9251

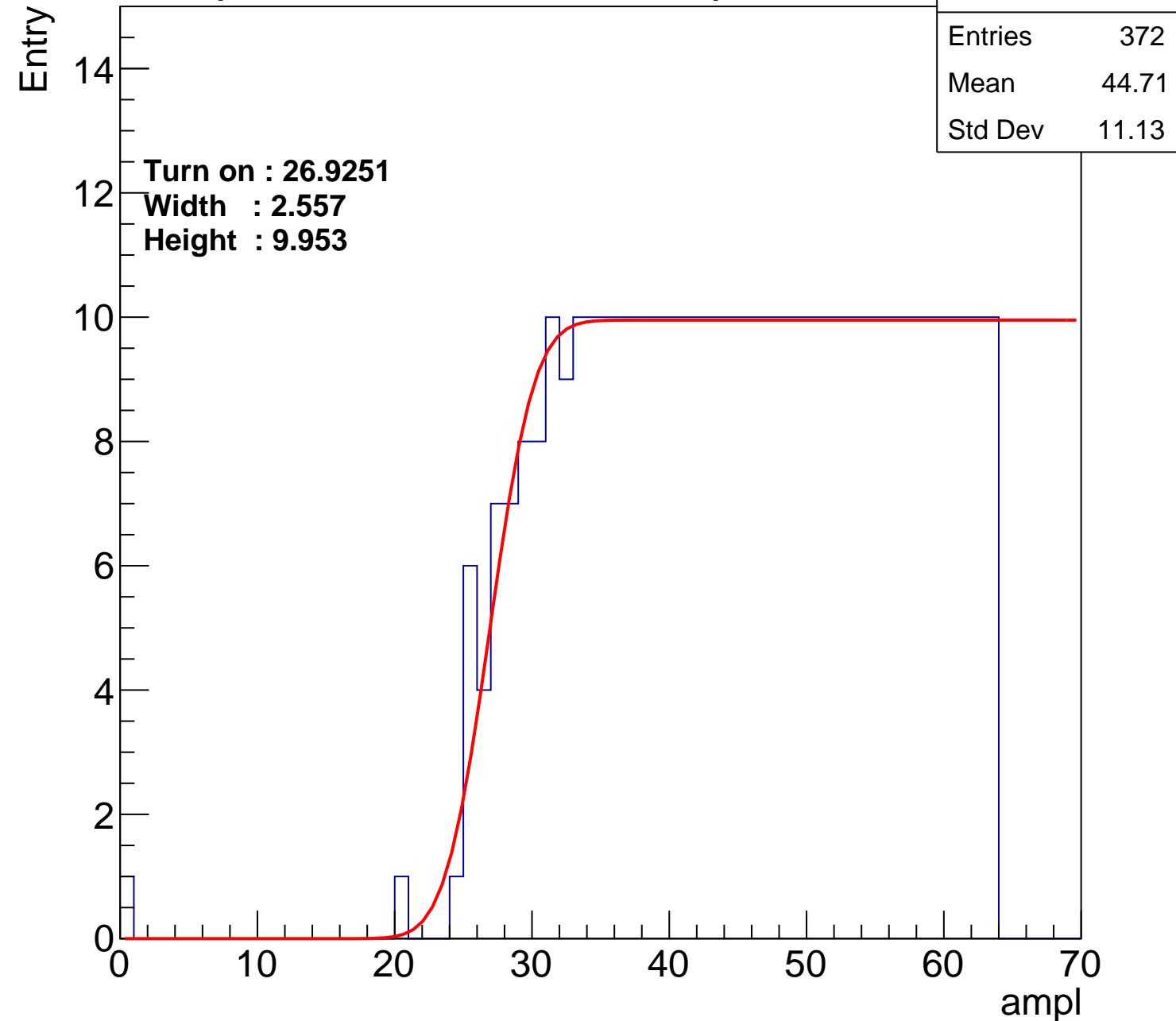
Width : 2.557

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.3019

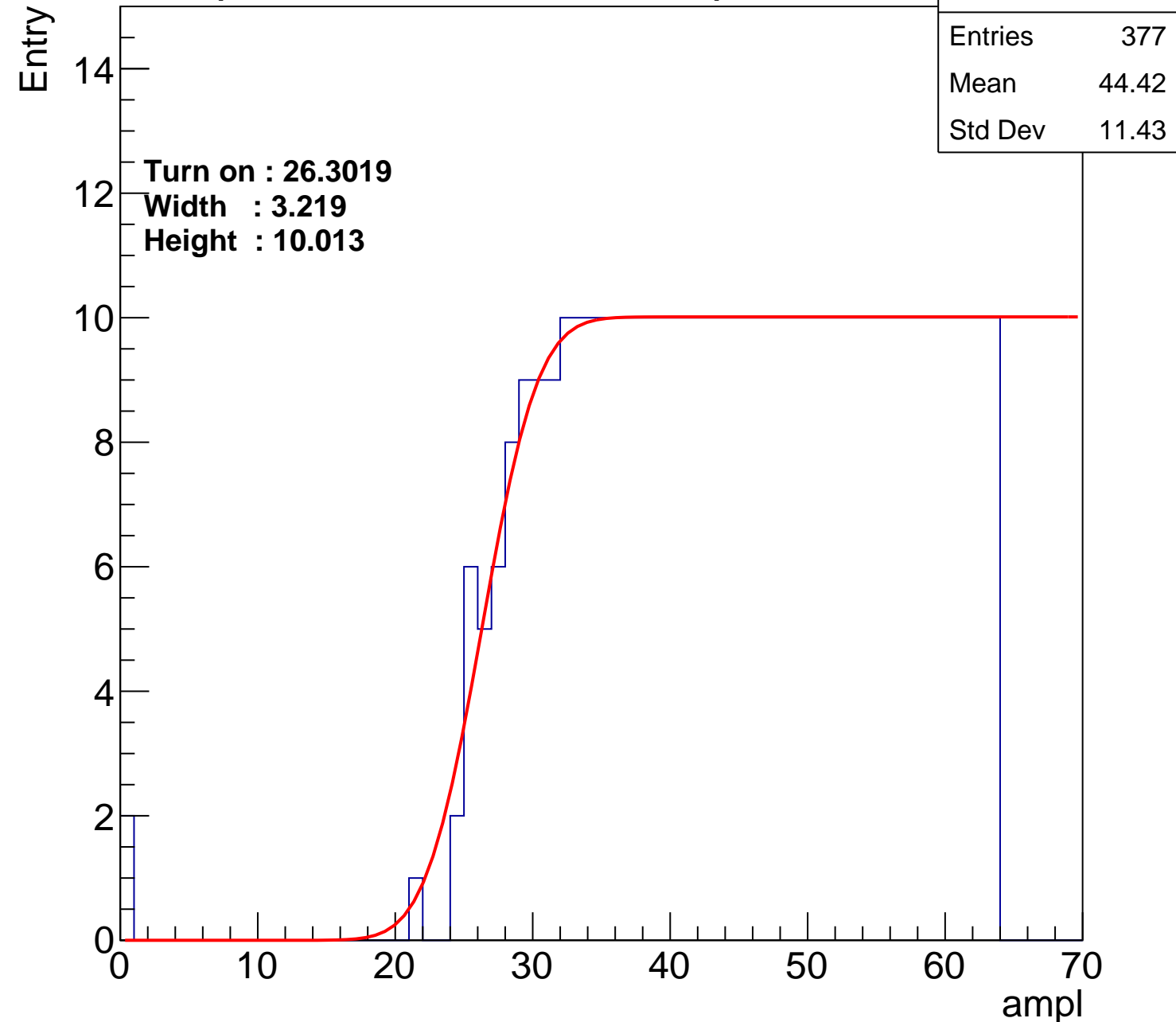
Width : 3.219

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 24.8675

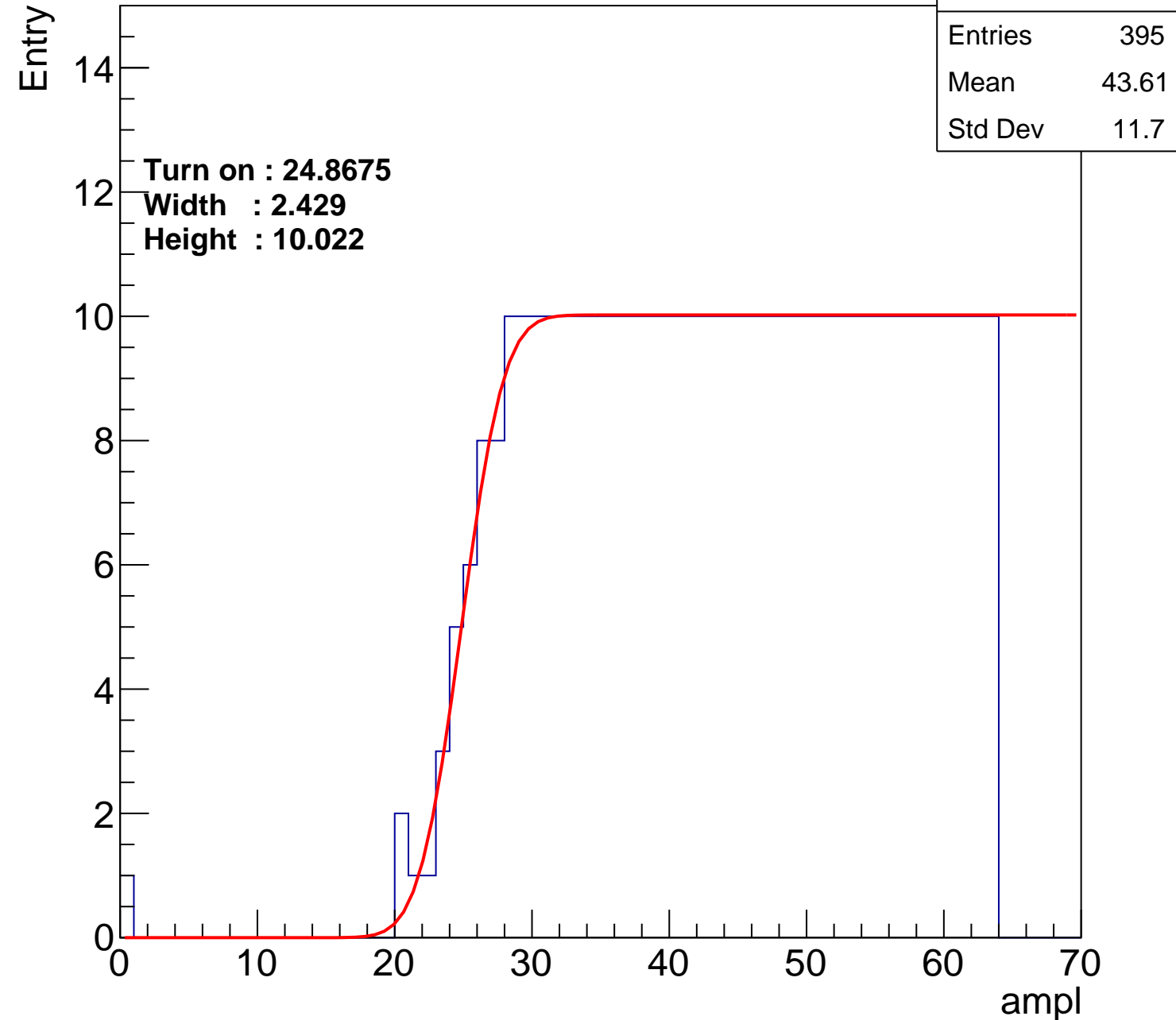
Width : 2.429

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.72
Std Dev	11.93

Turn on : 25.5055

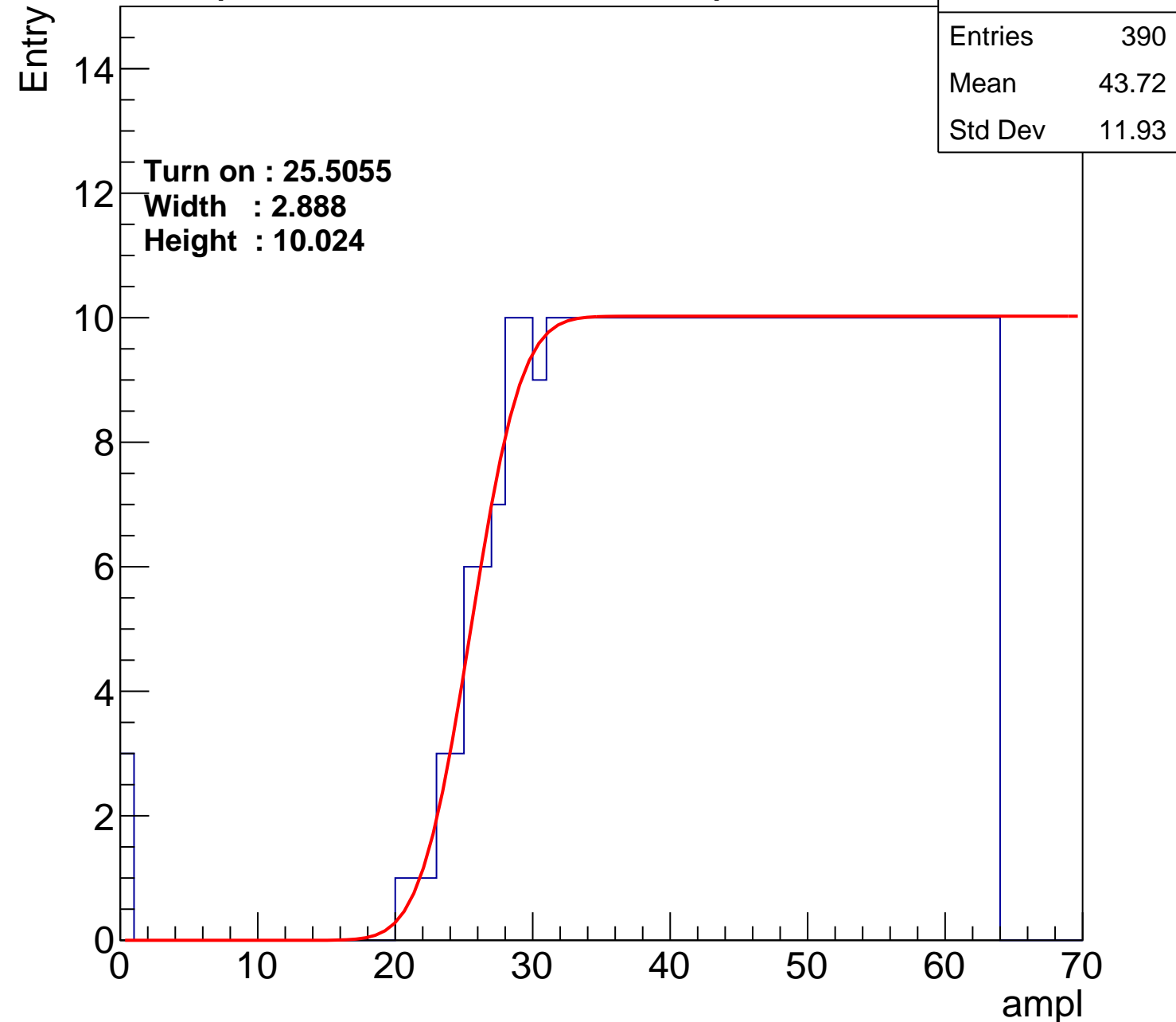
Width : 2.888

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.22
Std Dev	11.65

Turn on : 26.2520

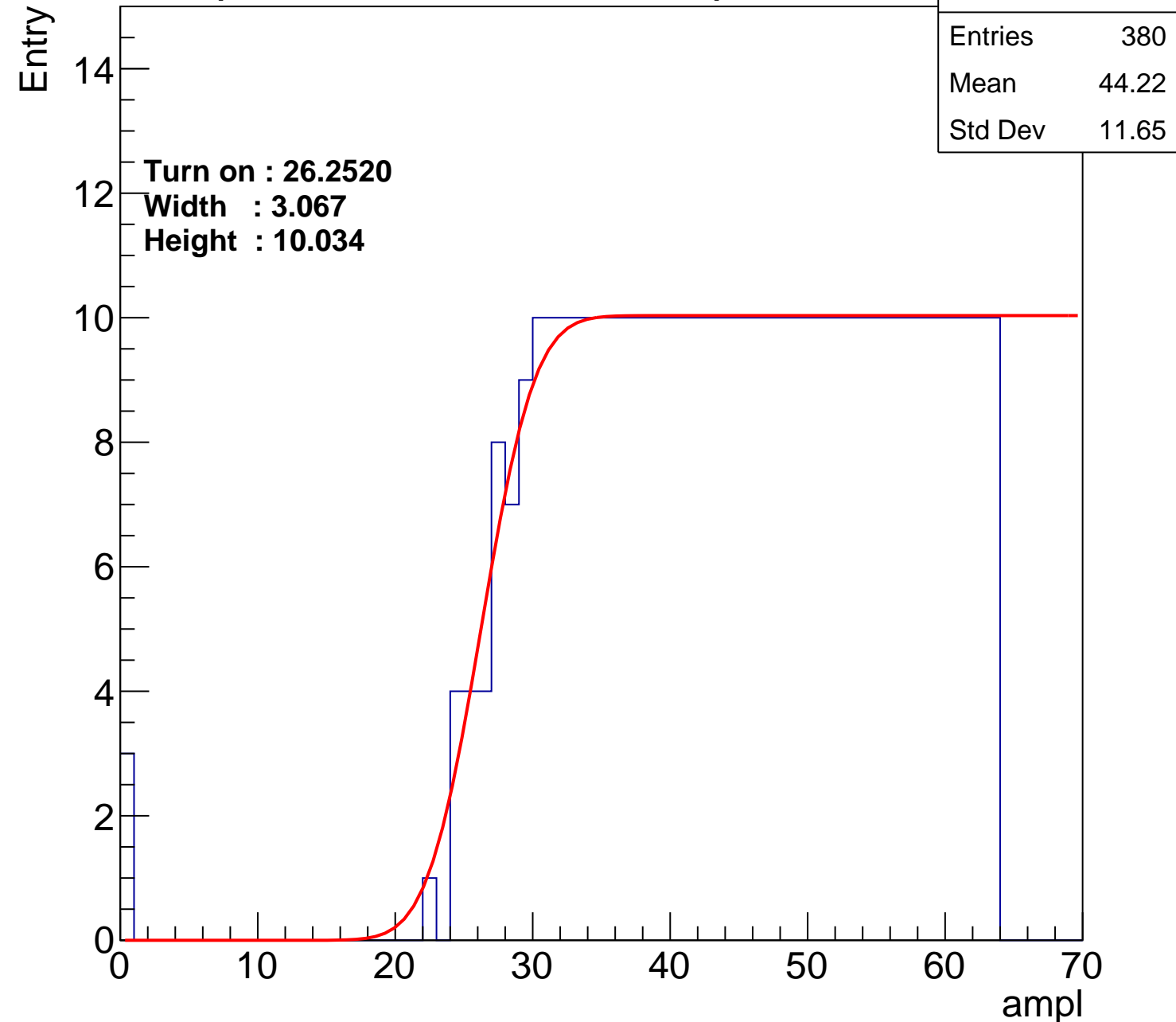
Width : 3.067

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.5194

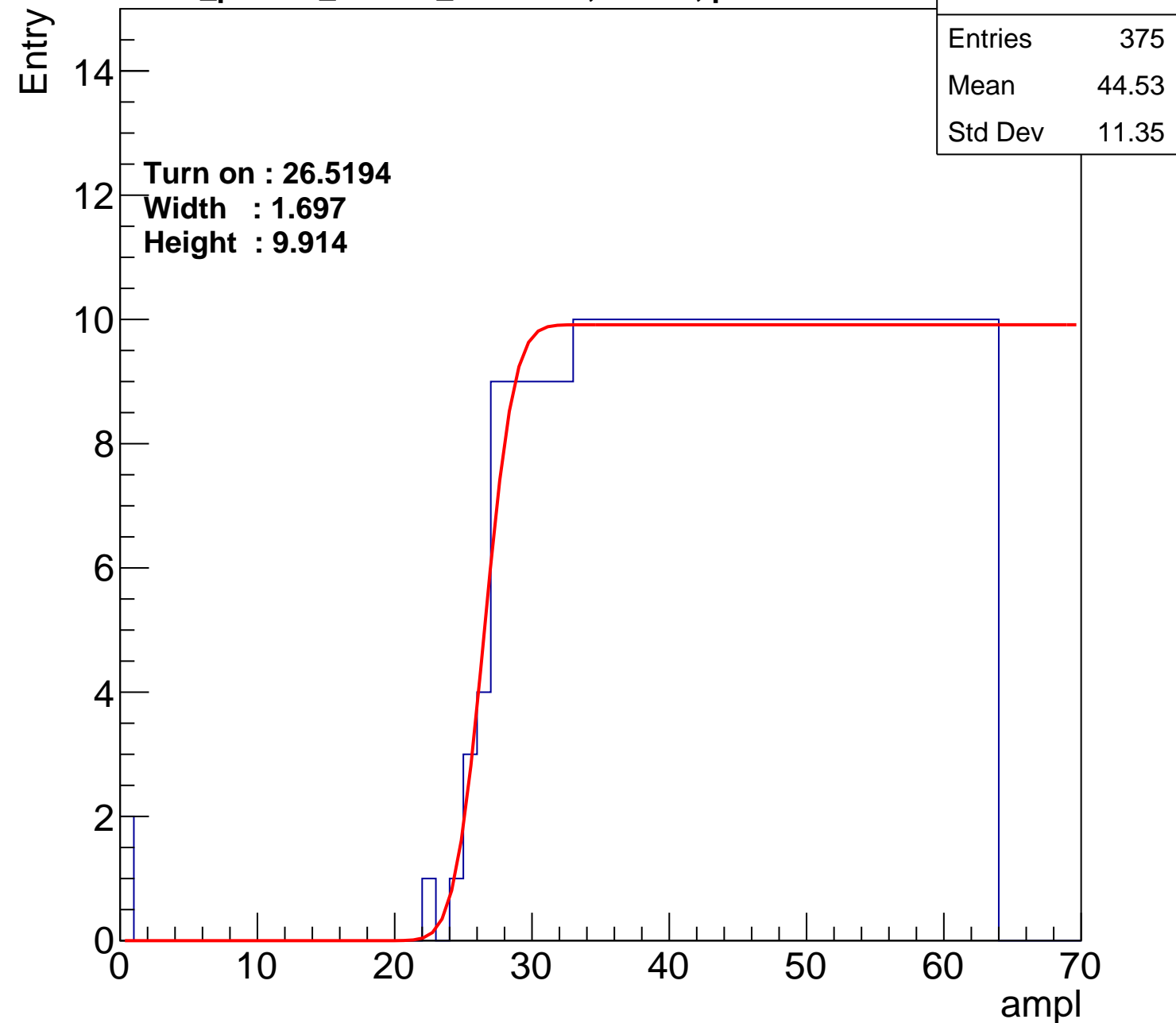
Width : 1.697

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.93
Std Dev	12.05

Turn on : 23.2709

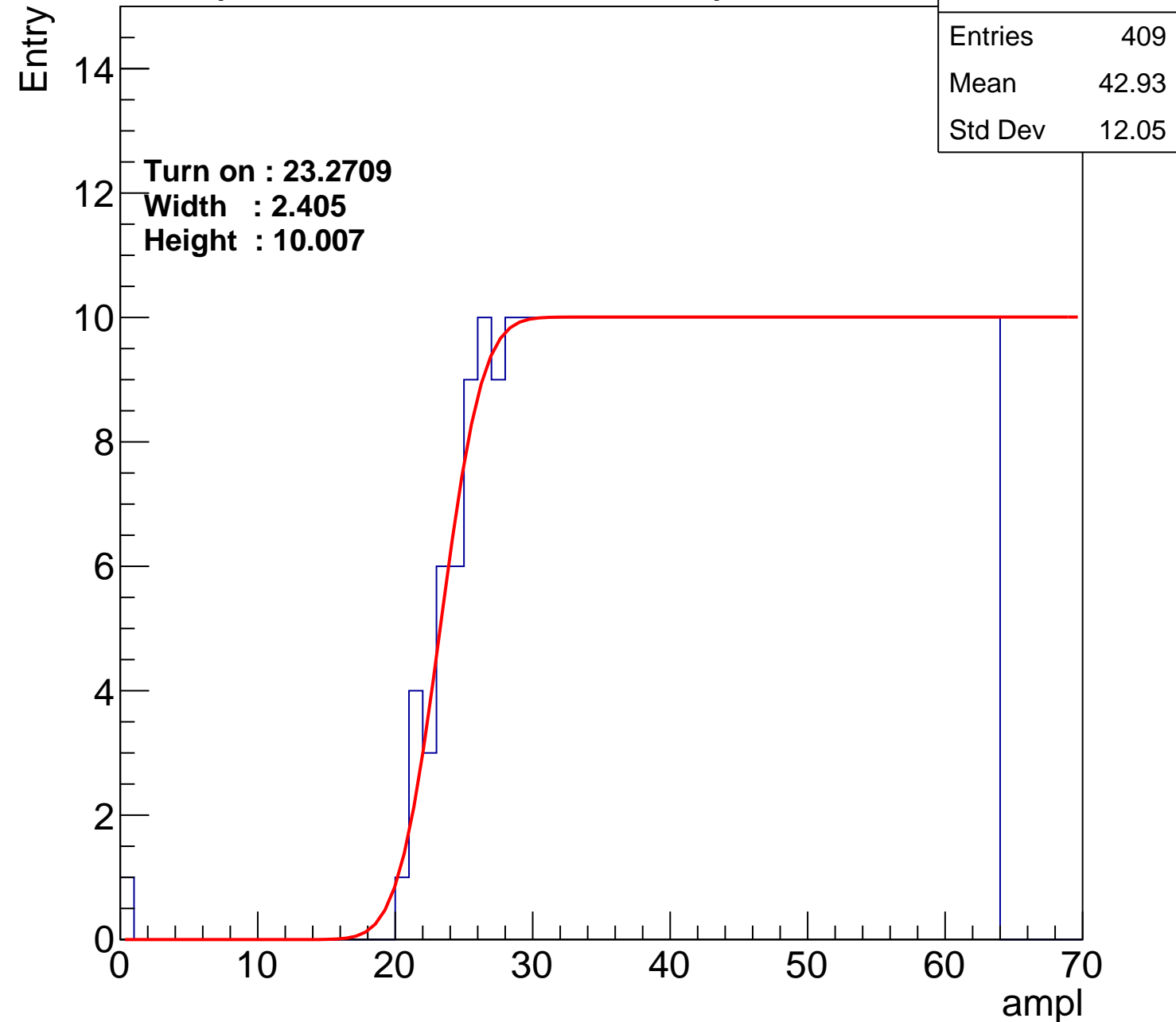
Width : 2.405

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.4
Std Dev	12.23

Turn on : 25.6743

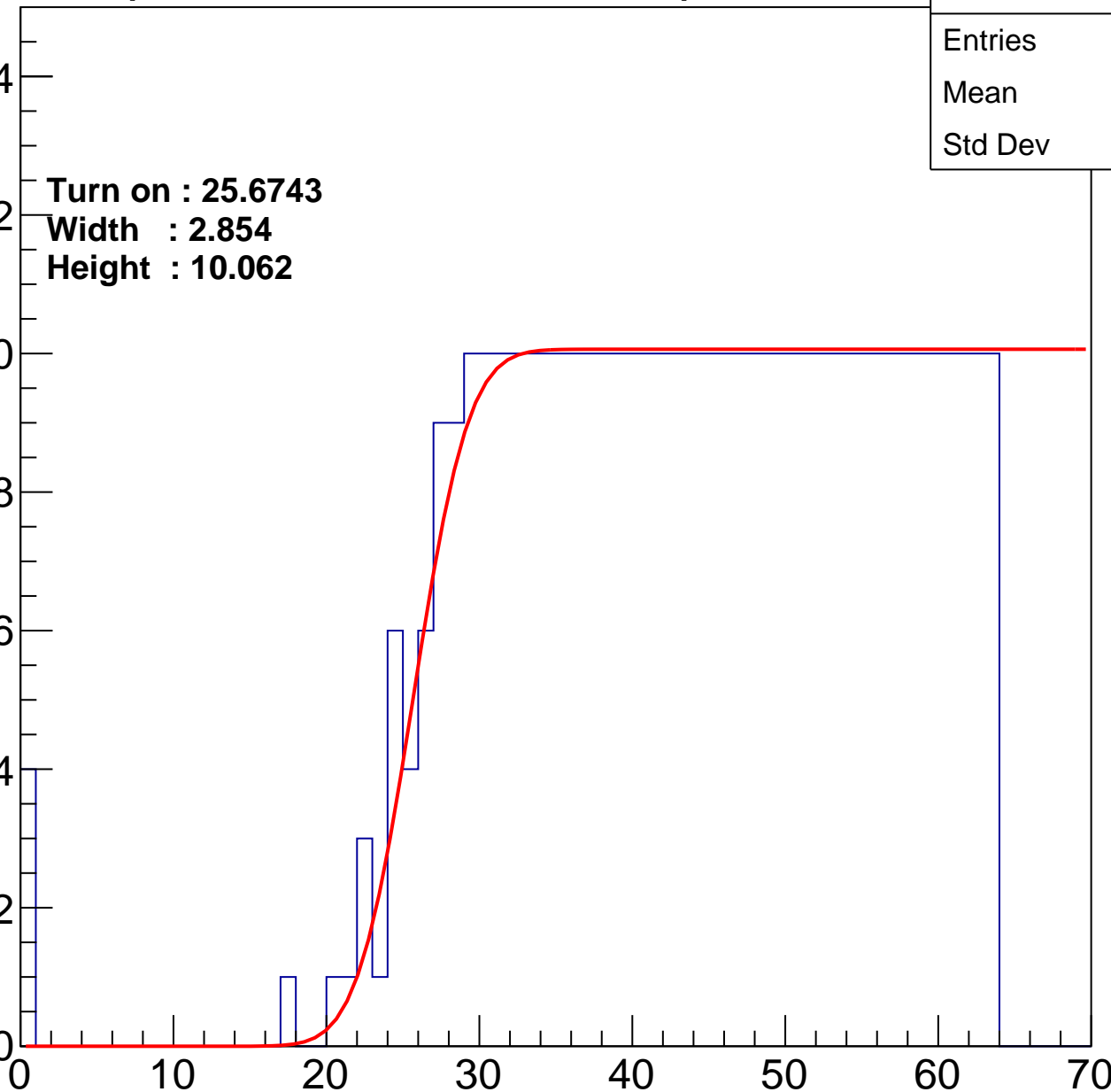
Width : 2.854

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch68

calib_packv5_042523_0143.root, FC#10, port B3

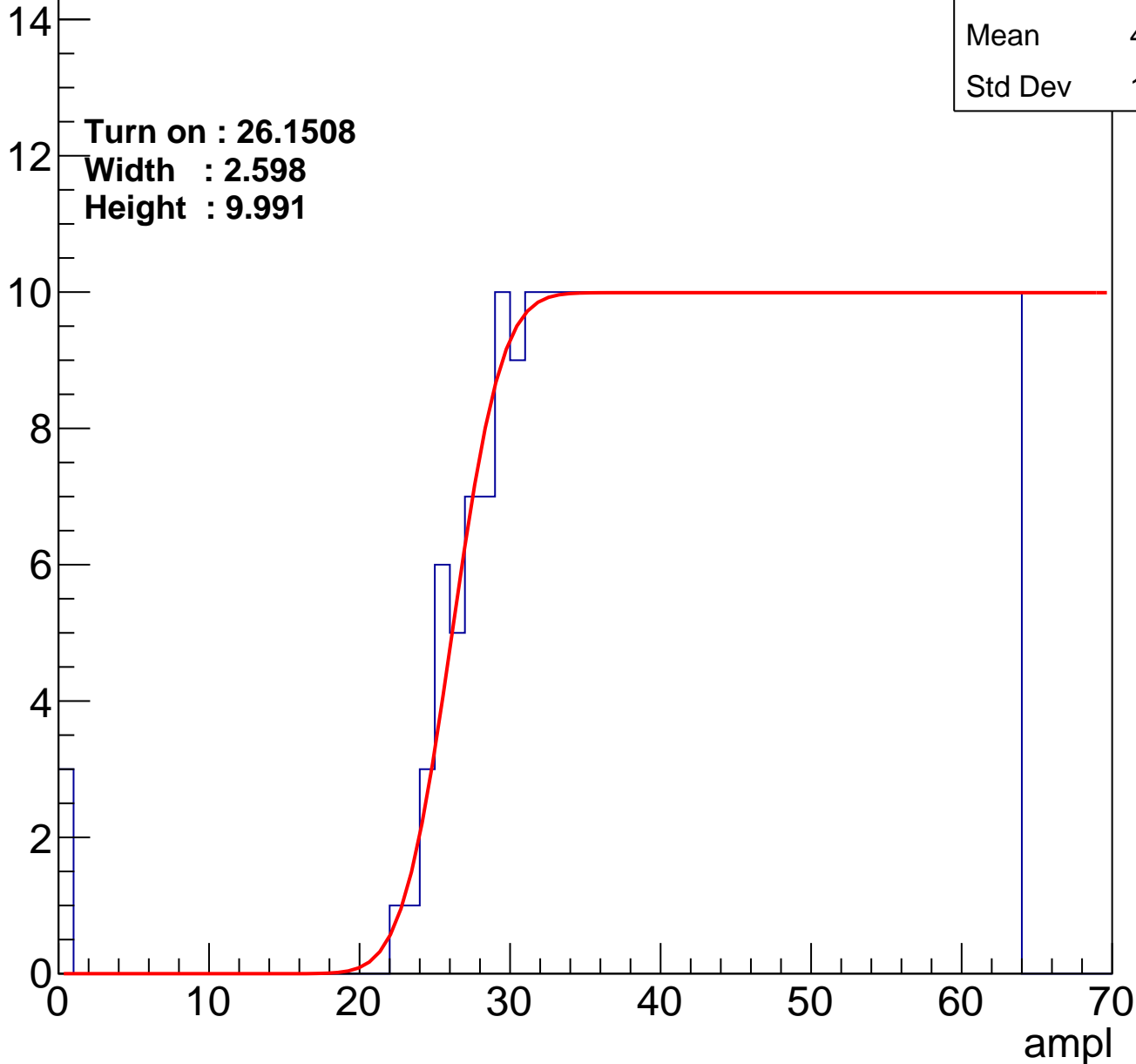
Entry

Entries	382
Mean	44.12
Std Dev	11.72

Turn on : 26.1508

Width : 2.598

Height : 9.991



B1L002S, U3-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.0319

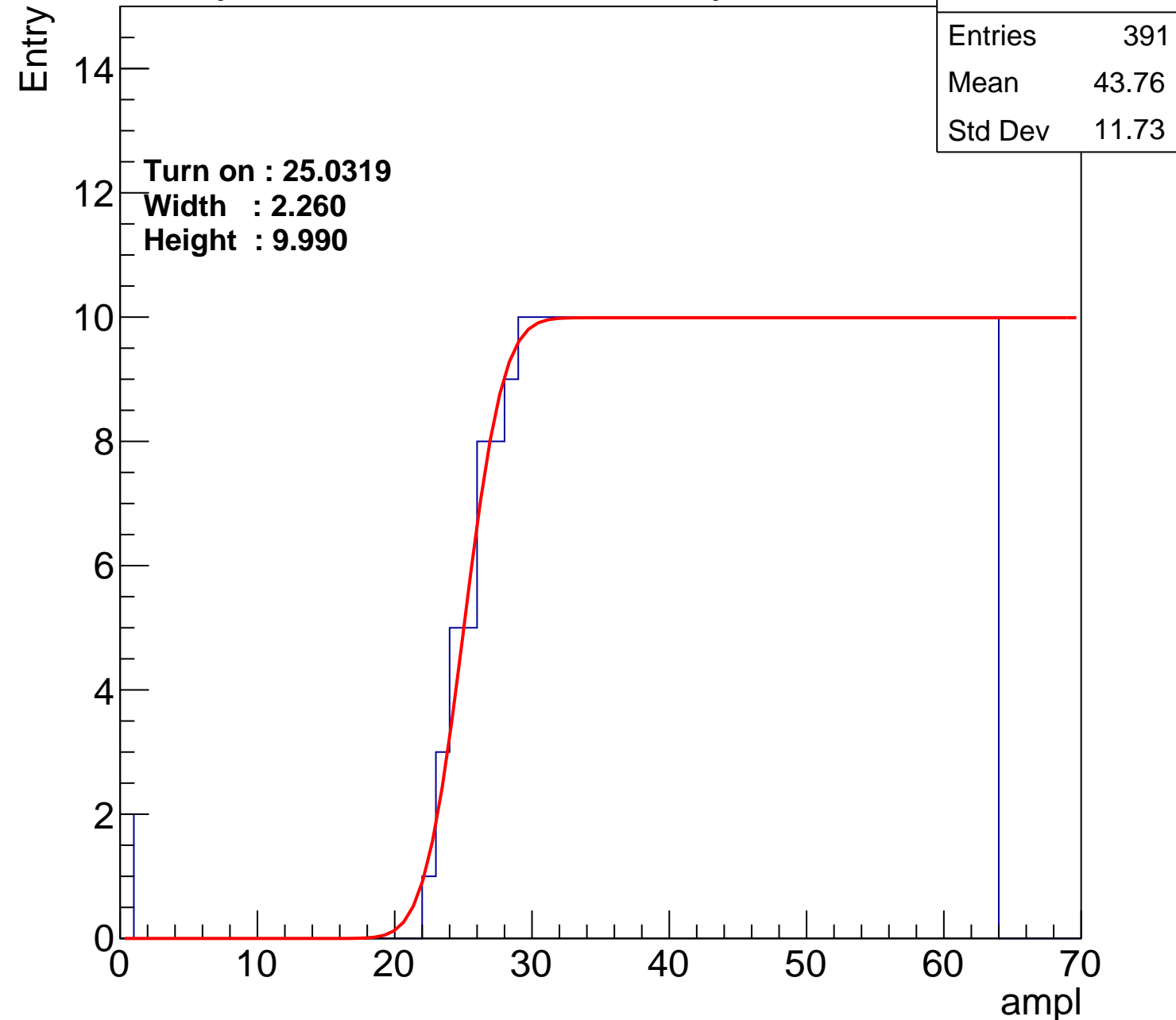
Width : 2.260

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.2788

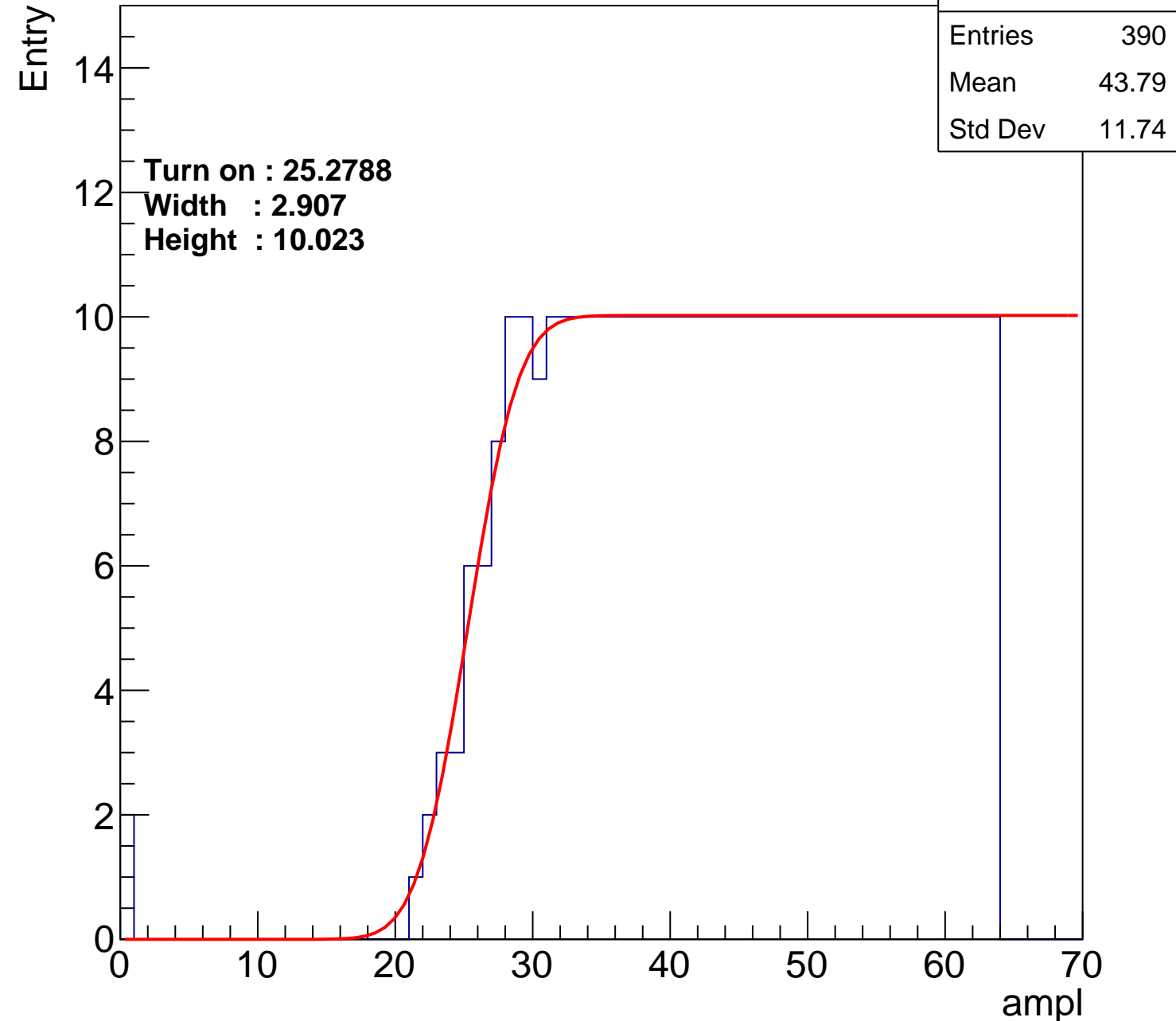
Width : 2.907

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.77
Std Dev	11.66

Turn on : 25.3353

Width : 3.317

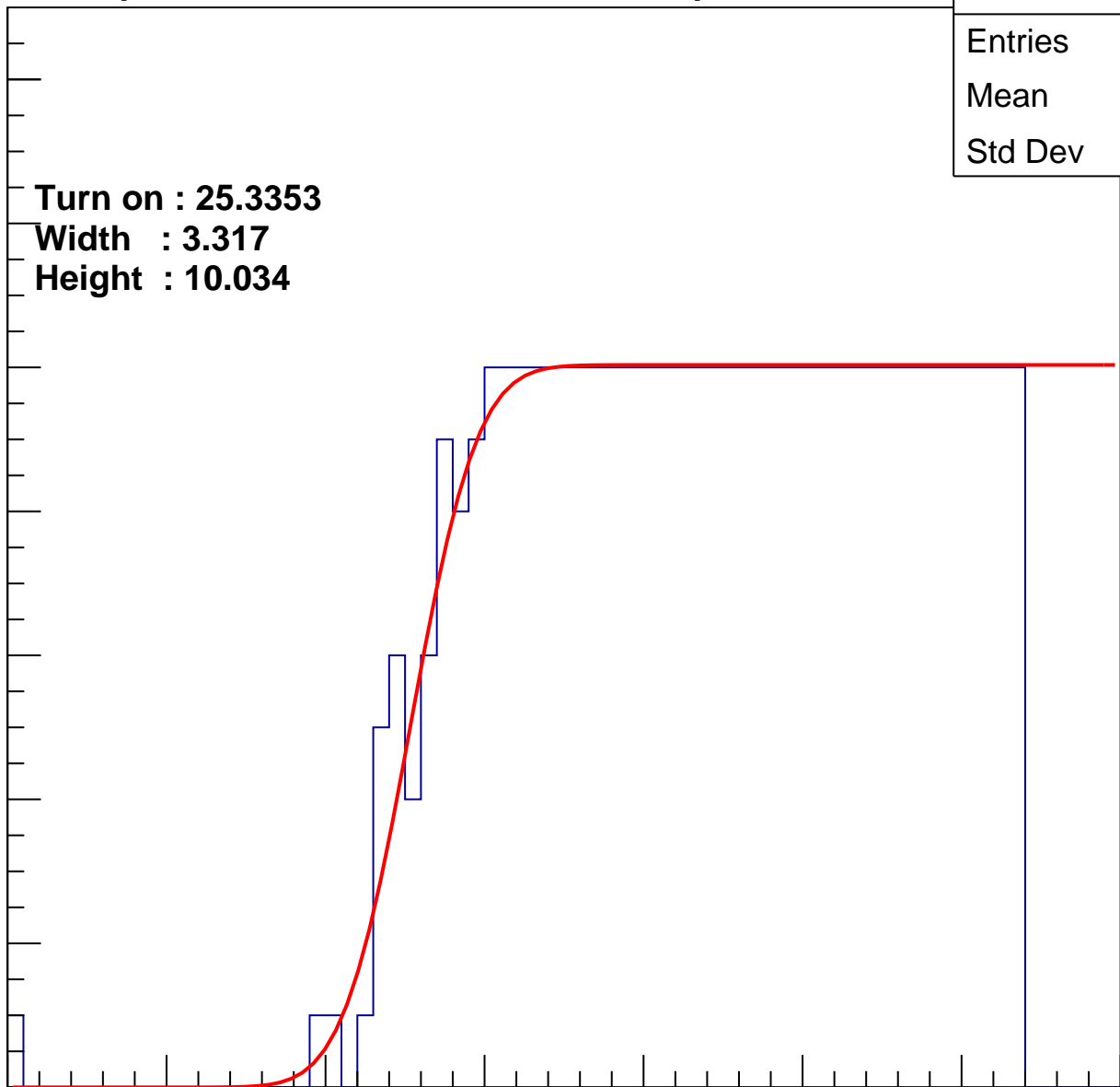
Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U3-ch72

calib_packv5_042523_0143.root, FC#10, port B3

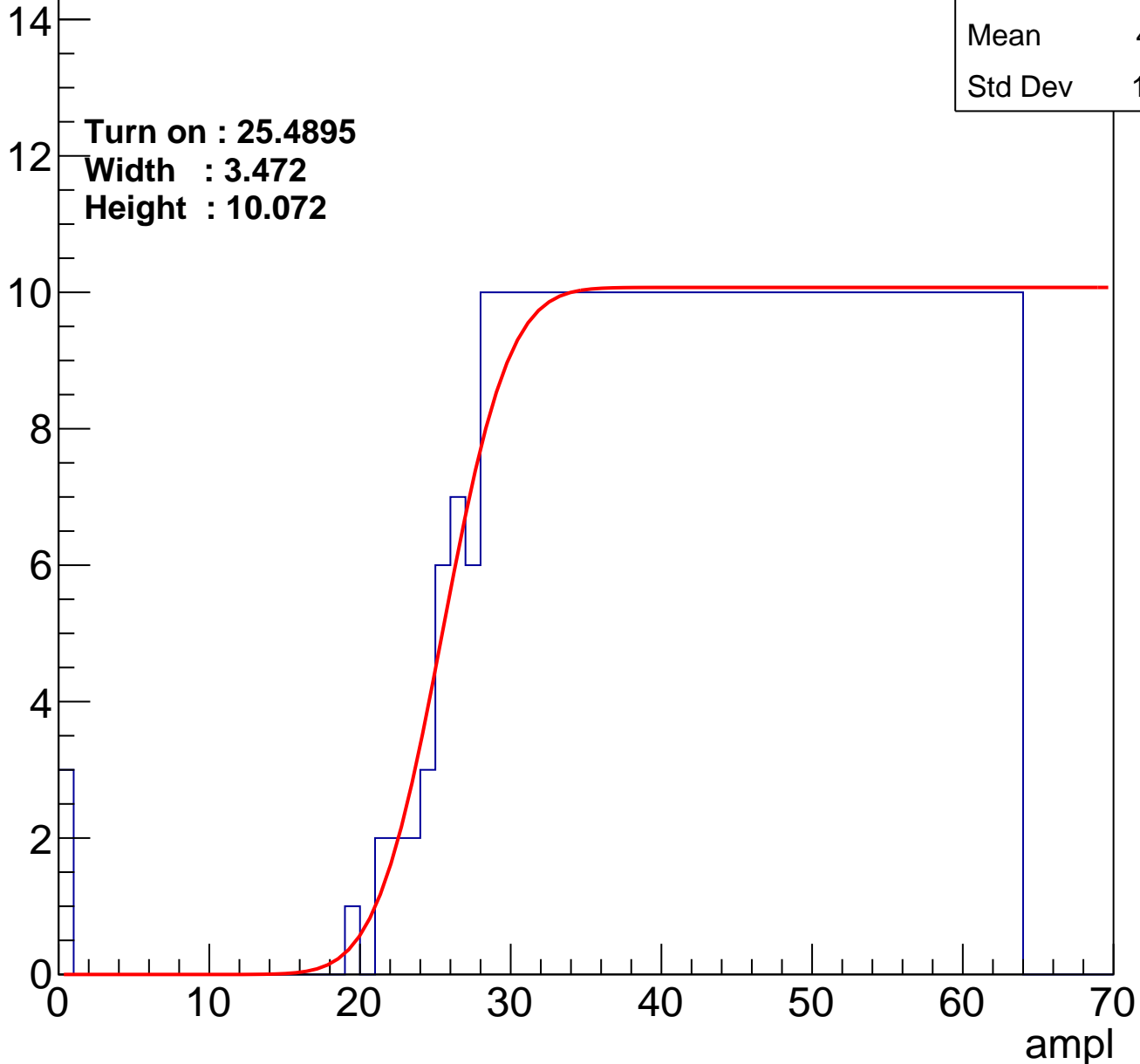
Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.4895

Width : 3.472

Height : 10.072

Entry



B1L002S, U3-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.13

Turn on : 27.1286

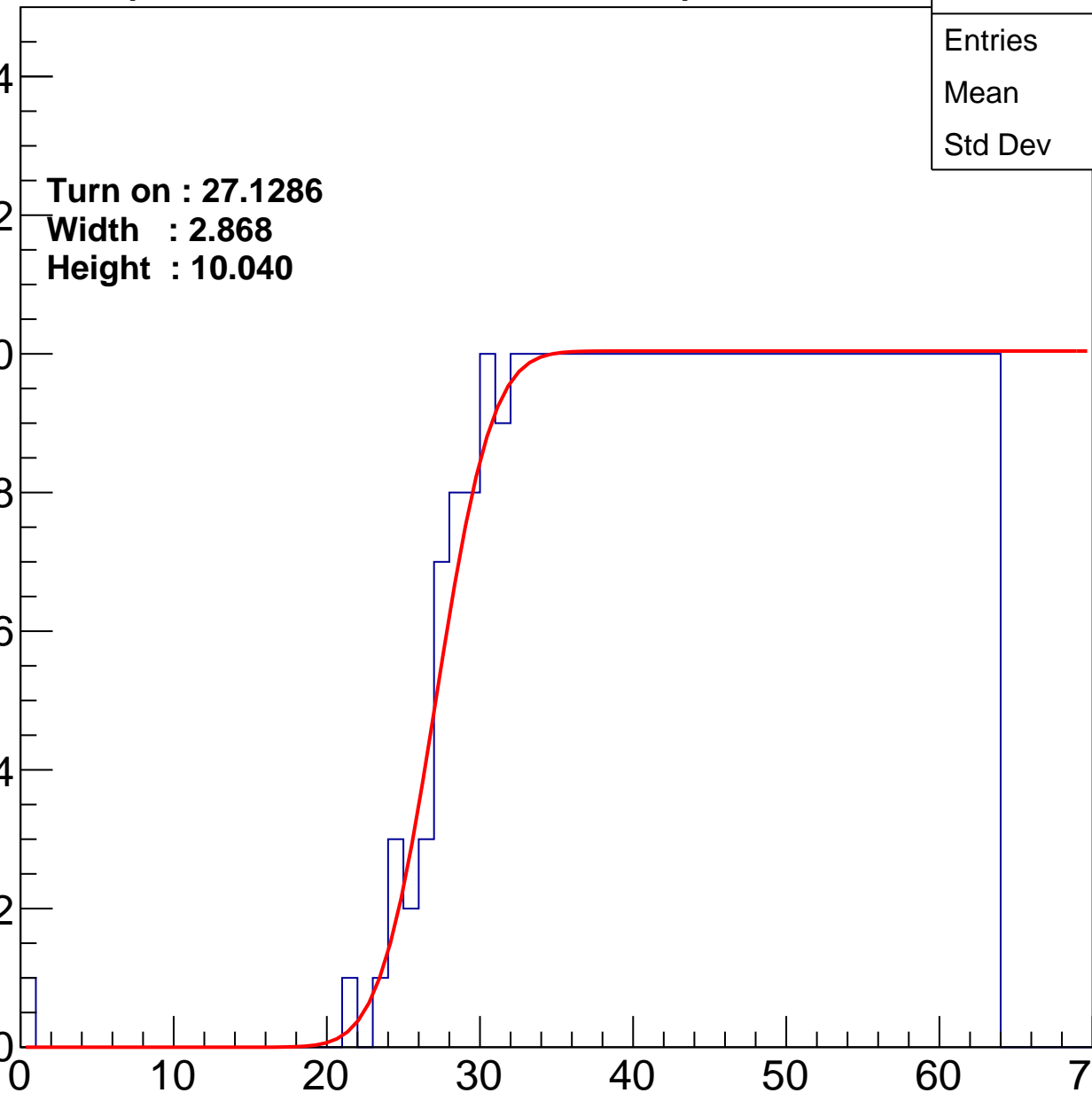
Width : 2.868

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.64
Std Dev	12.06

Turn on : 25.8548

Width : 2.734

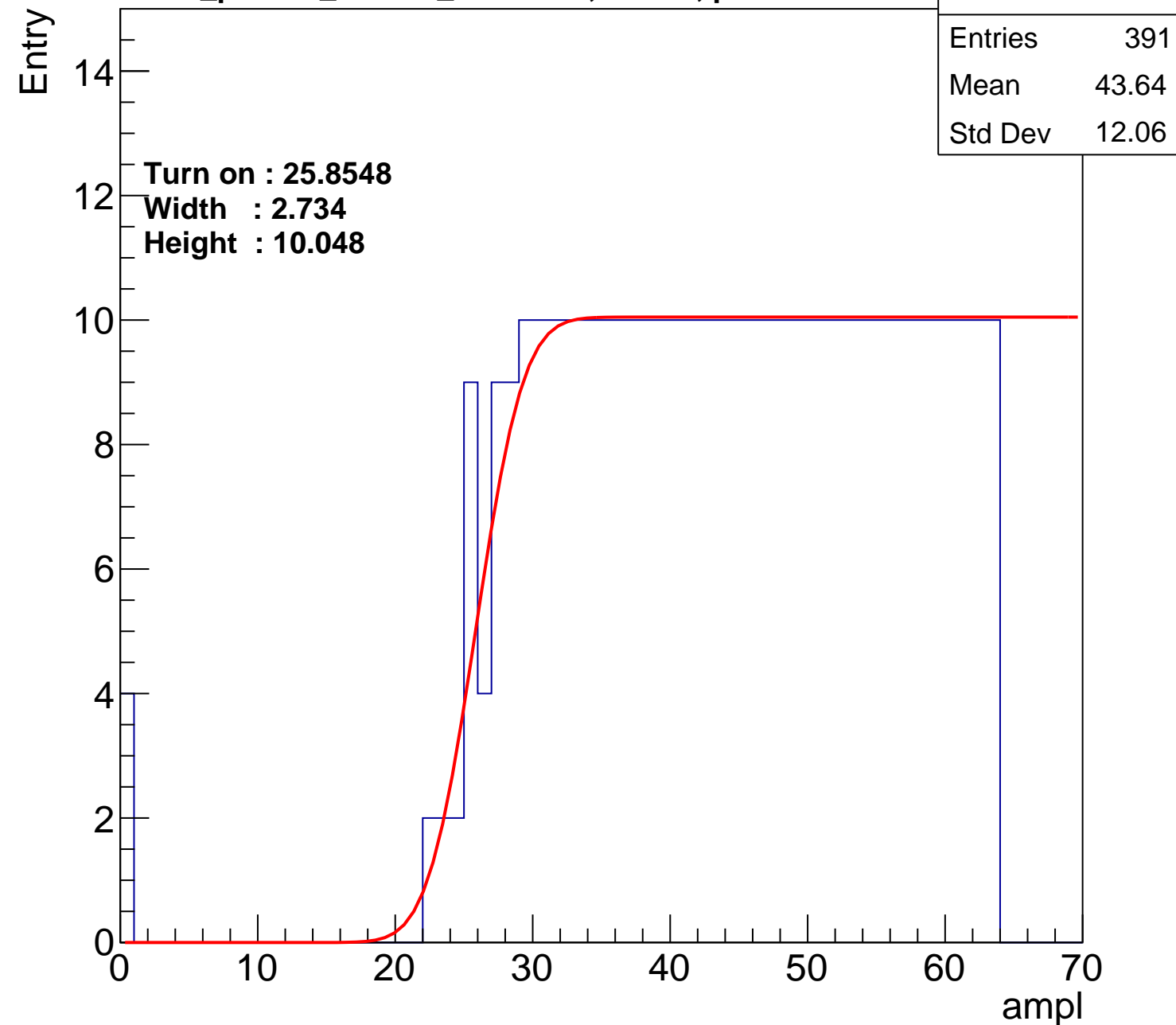
Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U3-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.98
Std Dev	11.47

Turn on : 25.3673

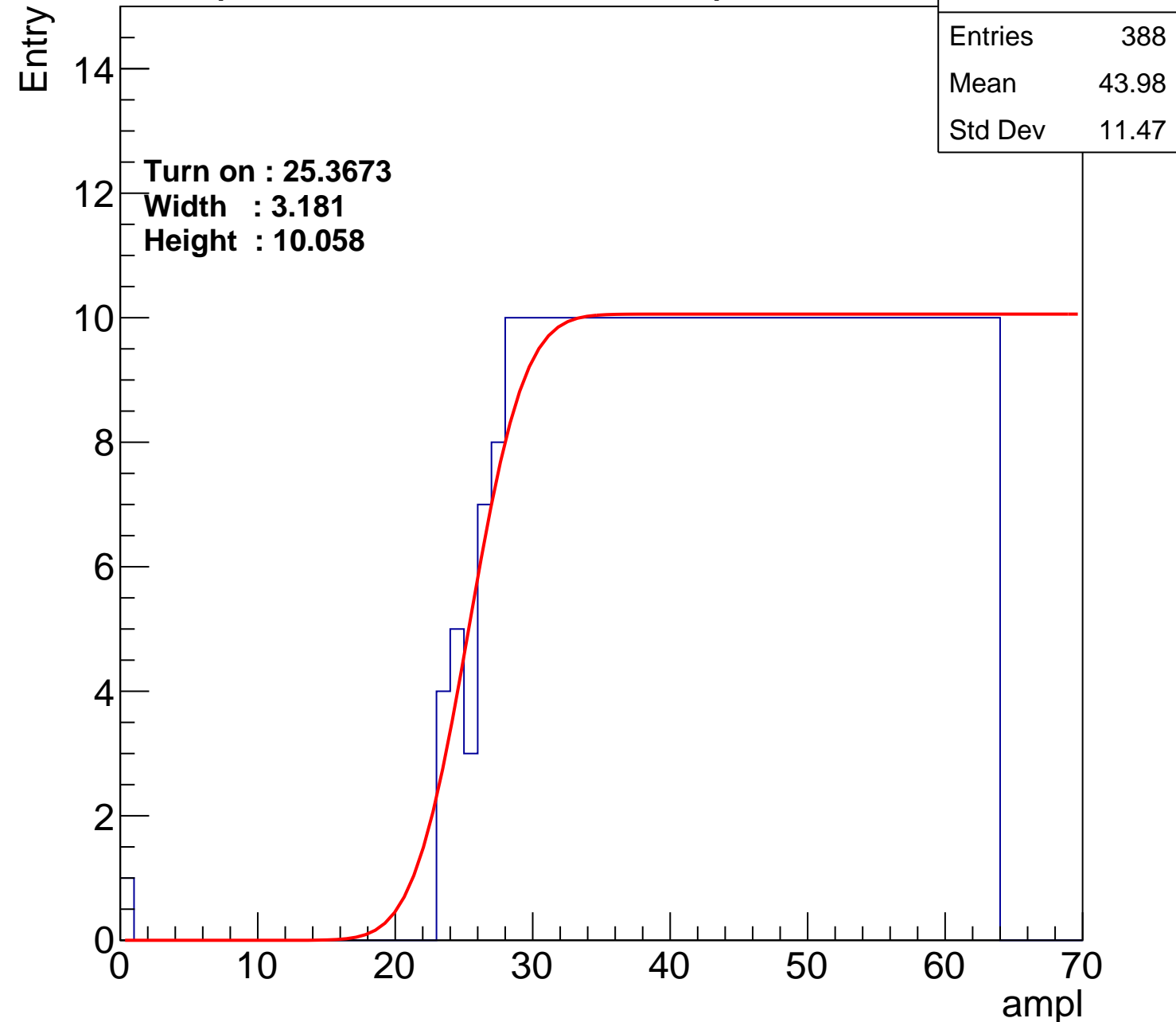
Width : 3.181

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 25.8617

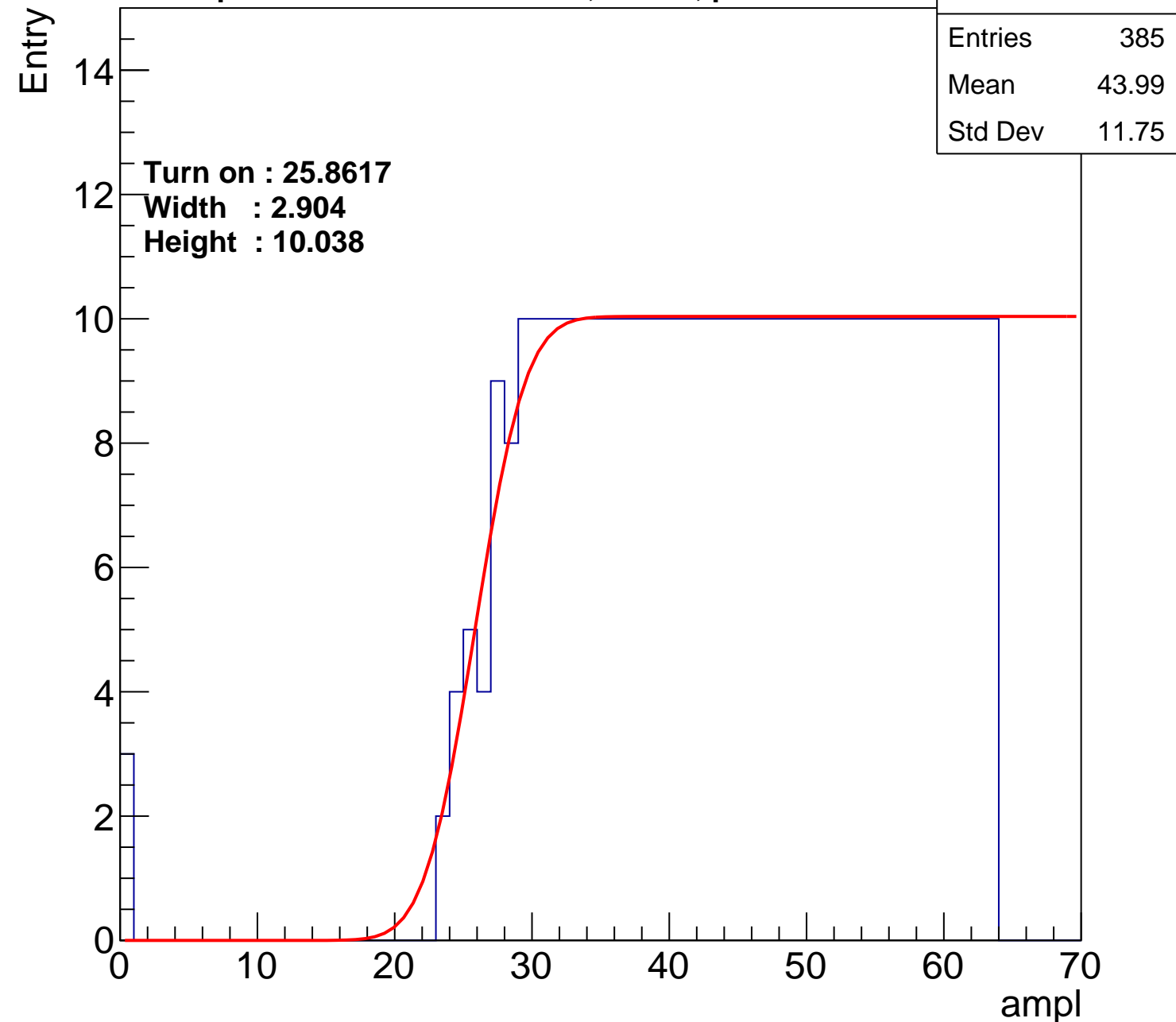
Width : 2.904

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.89
Std Dev	11.56

Turn on : 24.9632

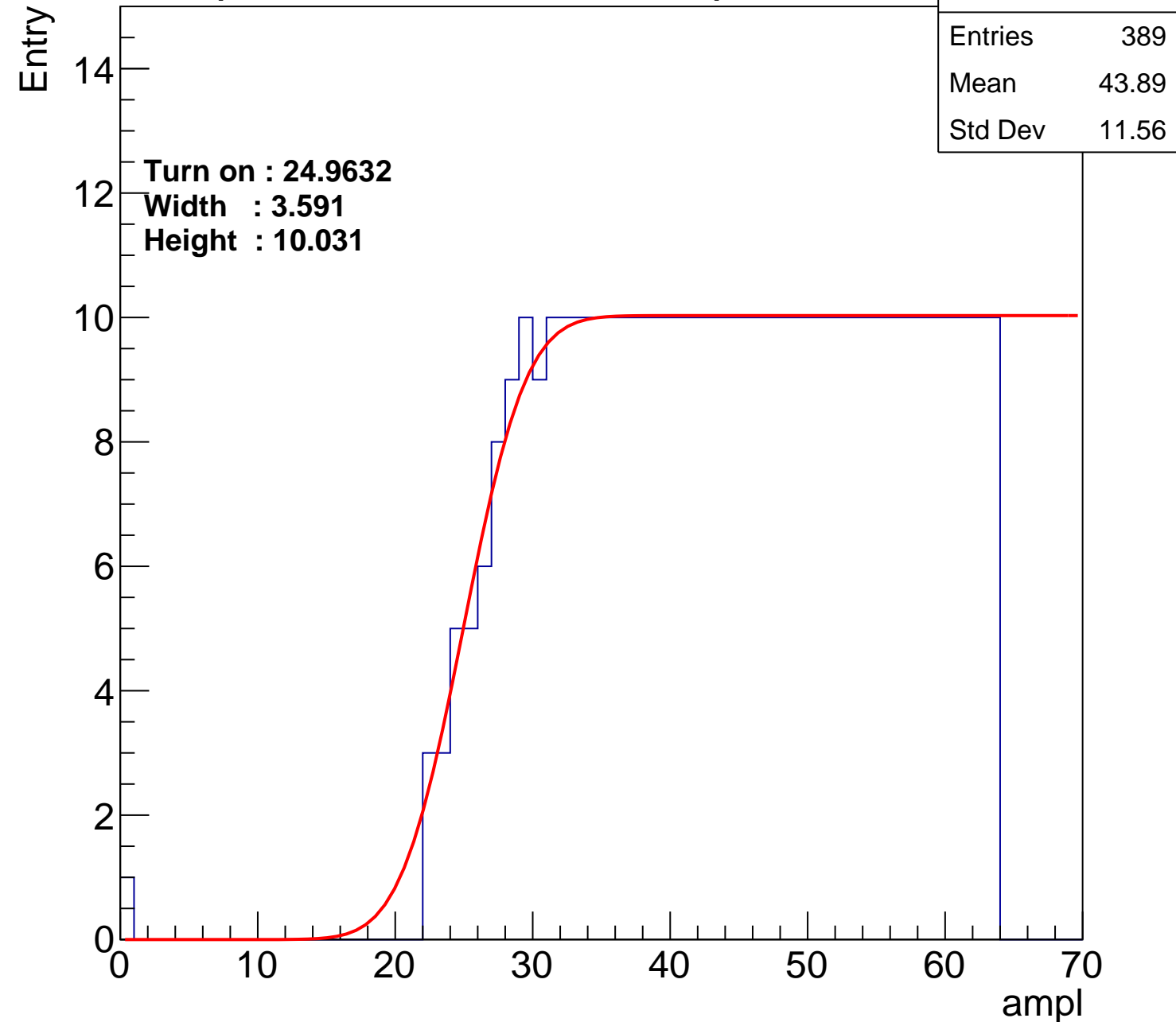
Width : 3.591

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.51
Std Dev	11.71

Turn on : 27.4751

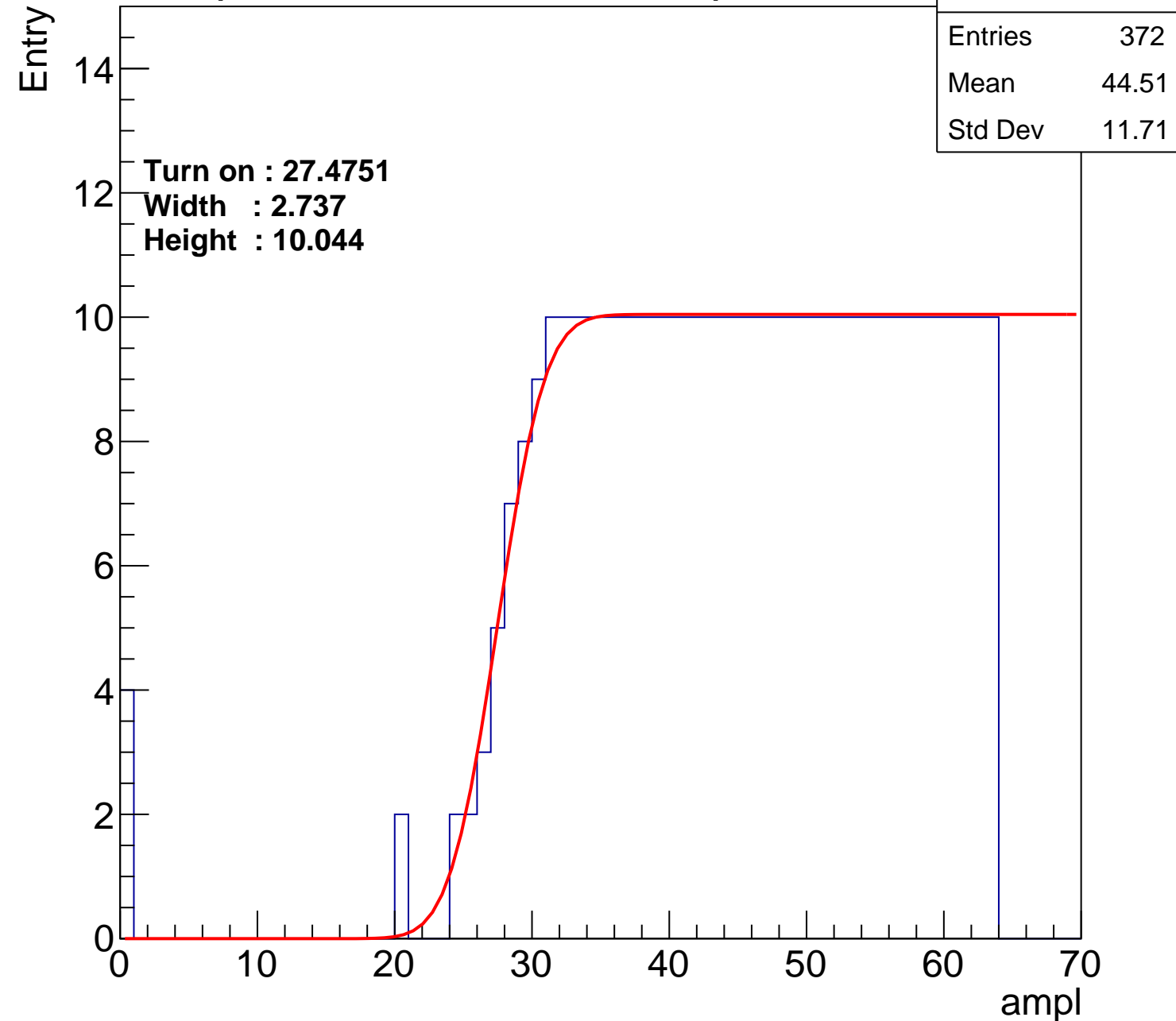
Width : 2.737

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.26
Std Dev	11.69

Turn on : 26.3844

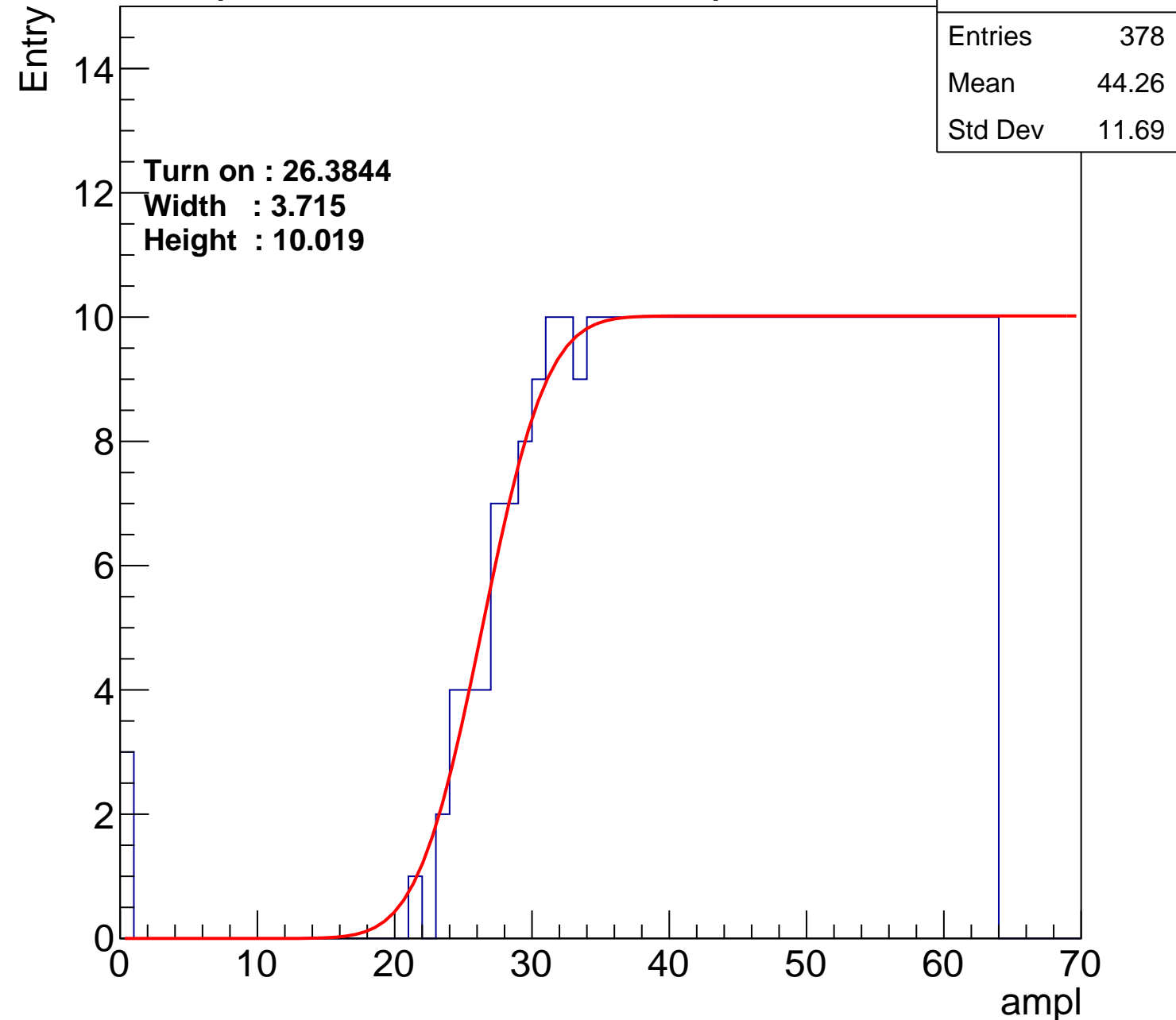
Width : 3.715

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.49
Std Dev	12.19

Turn on : 25.2927

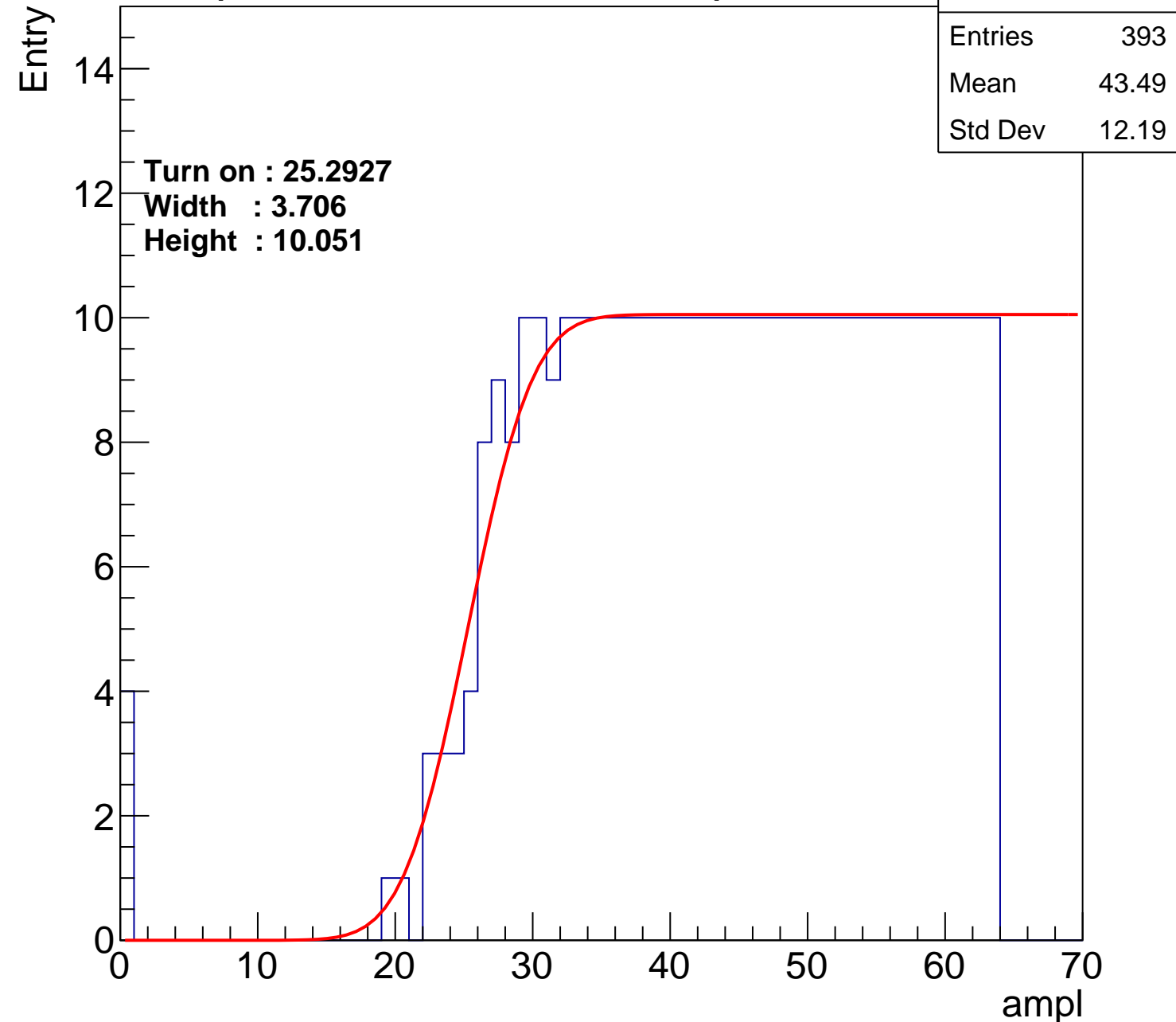
Width : 3.706

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.33
Std Dev	11.31

Turn on : 26.0095

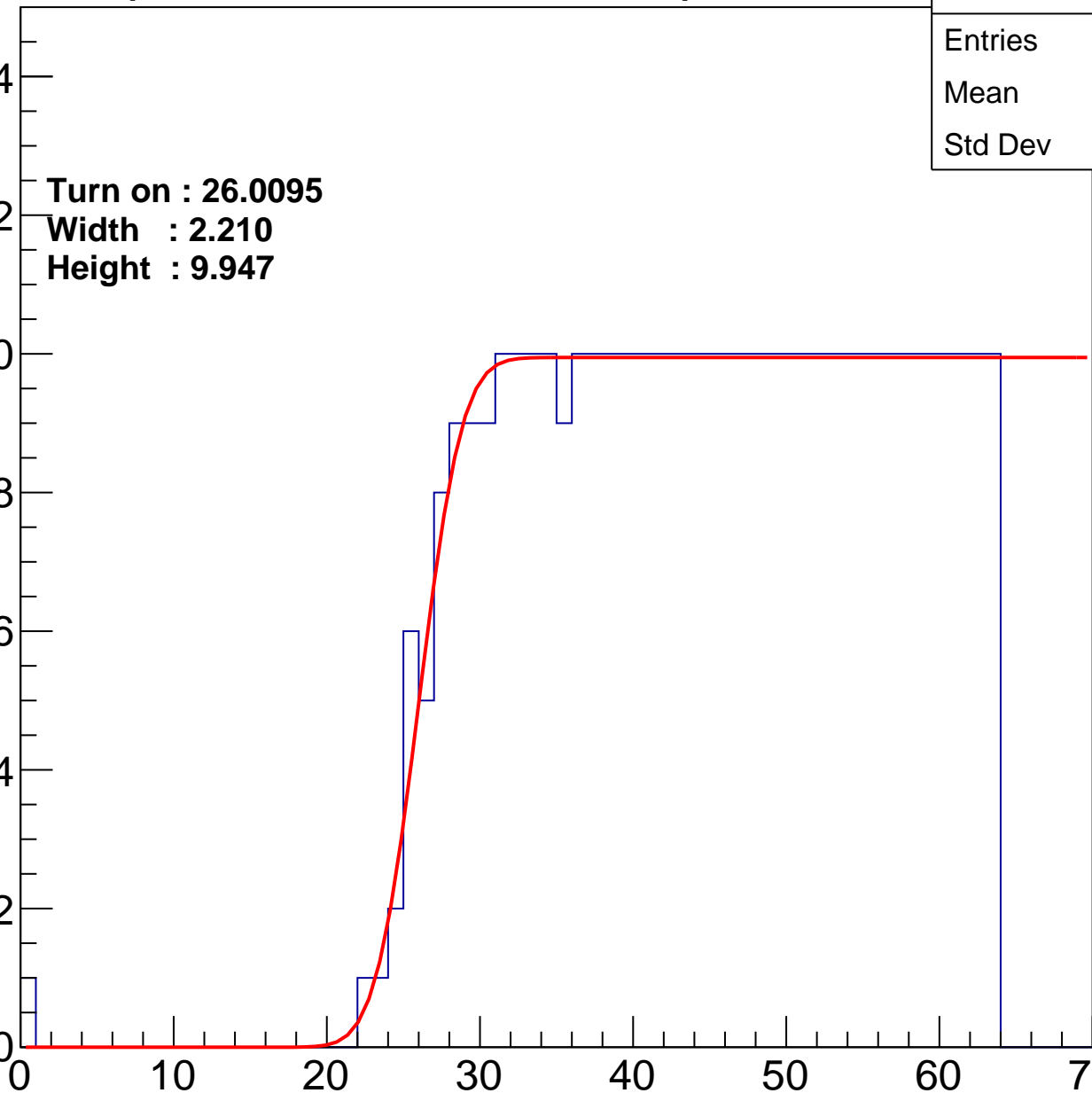
Width : 2.210

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.26
Std Dev	12.28

Turn on : 24.7412

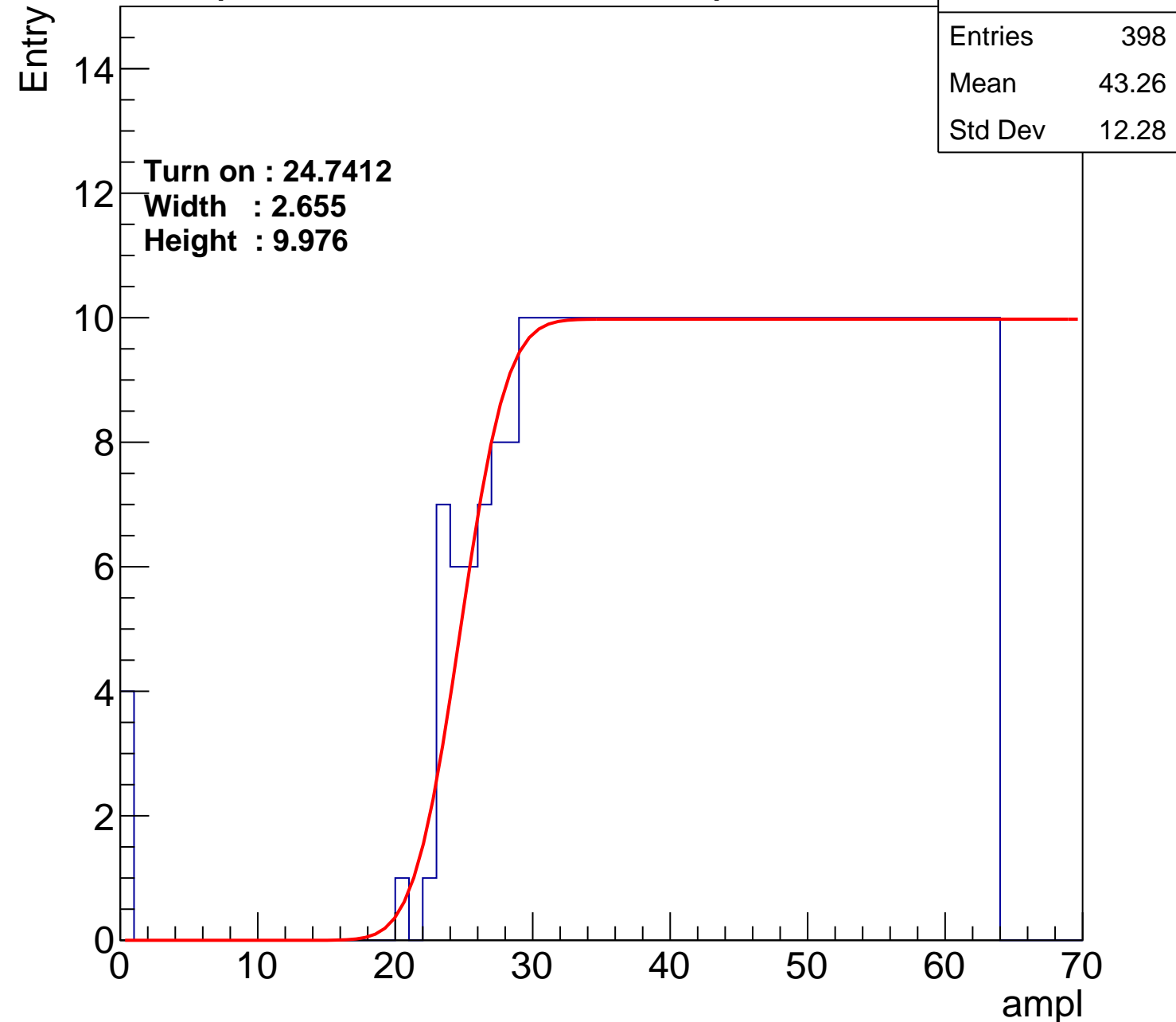
Width : 2.655

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.65
Std Dev	11.68

Turn on : 24.9339

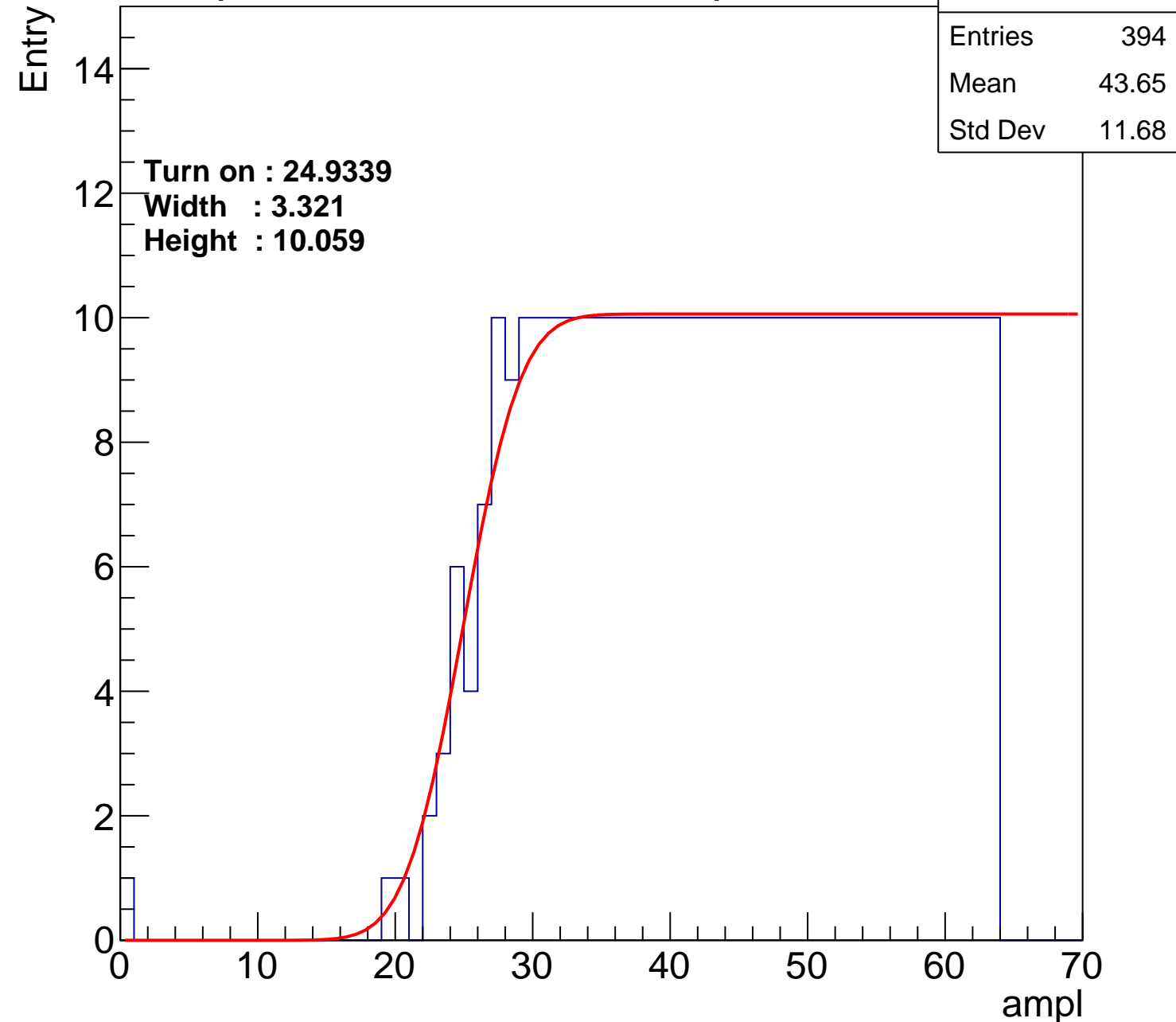
Width : 3.321

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.61
Std Dev	11.15

Turn on : 26.6862

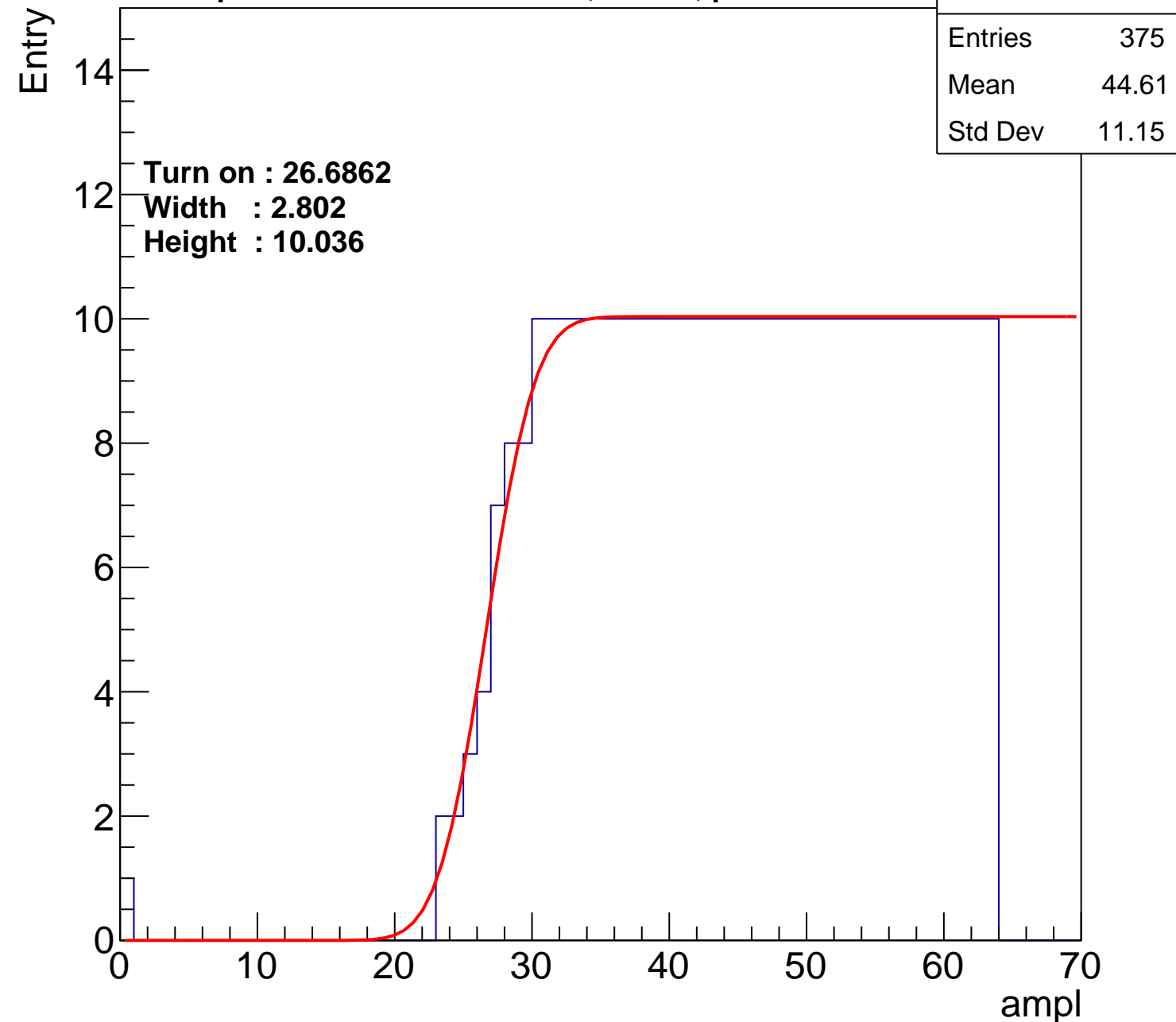
Width : 2.802

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.77
Std Dev	11.6

Turn on : 25.1043

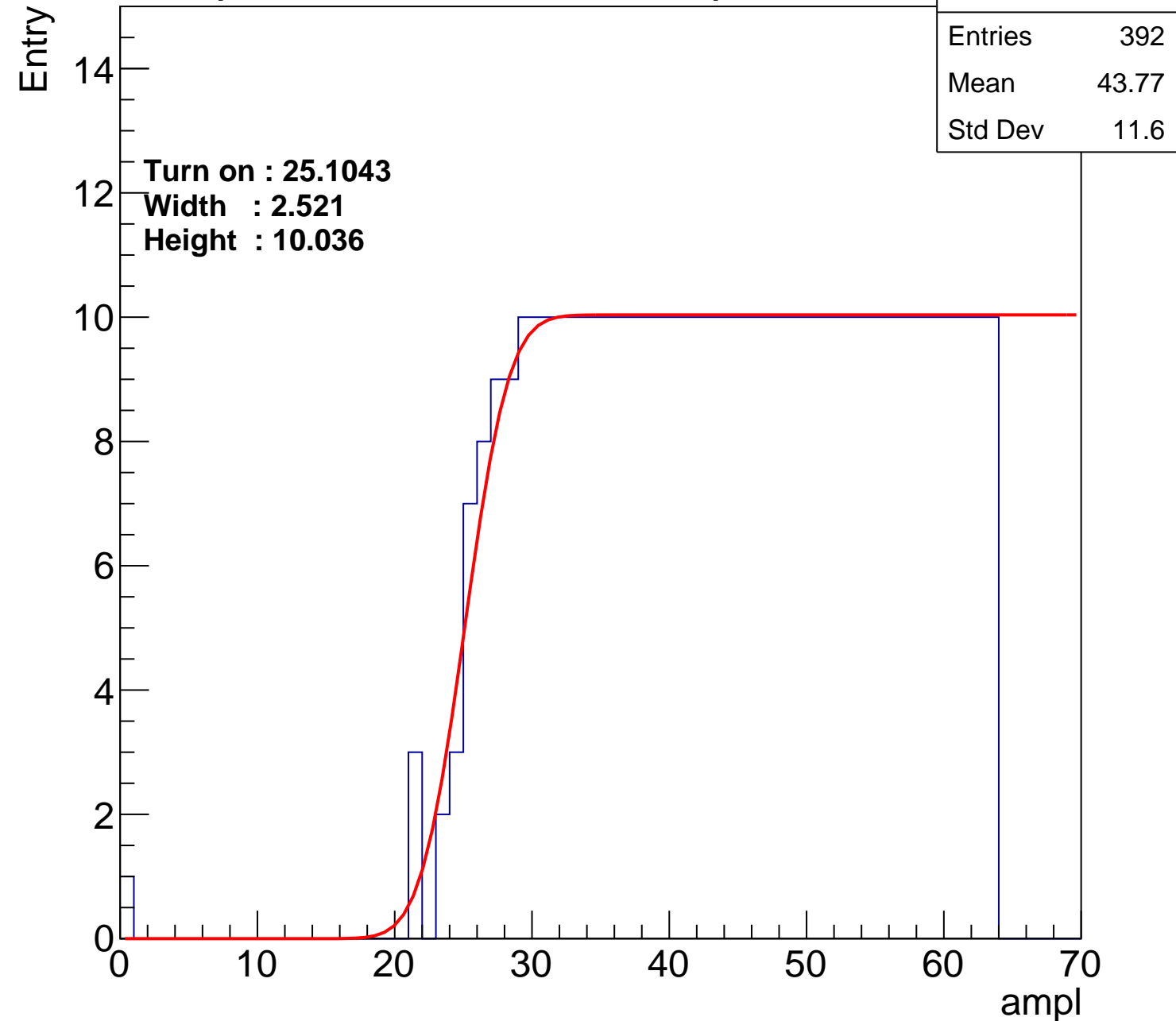
Width : 2.521

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 25.2535

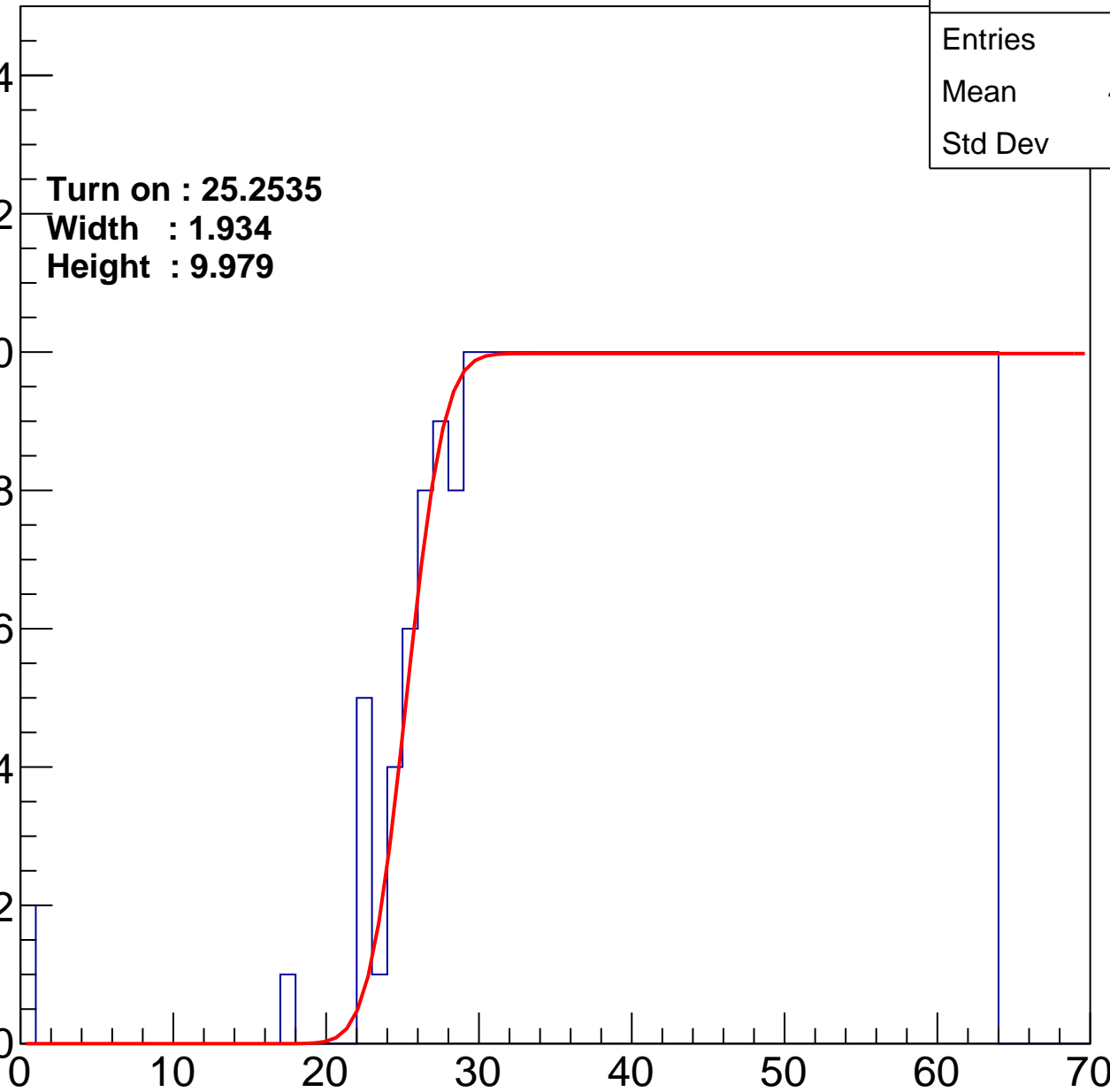
Width : 1.934

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.9504

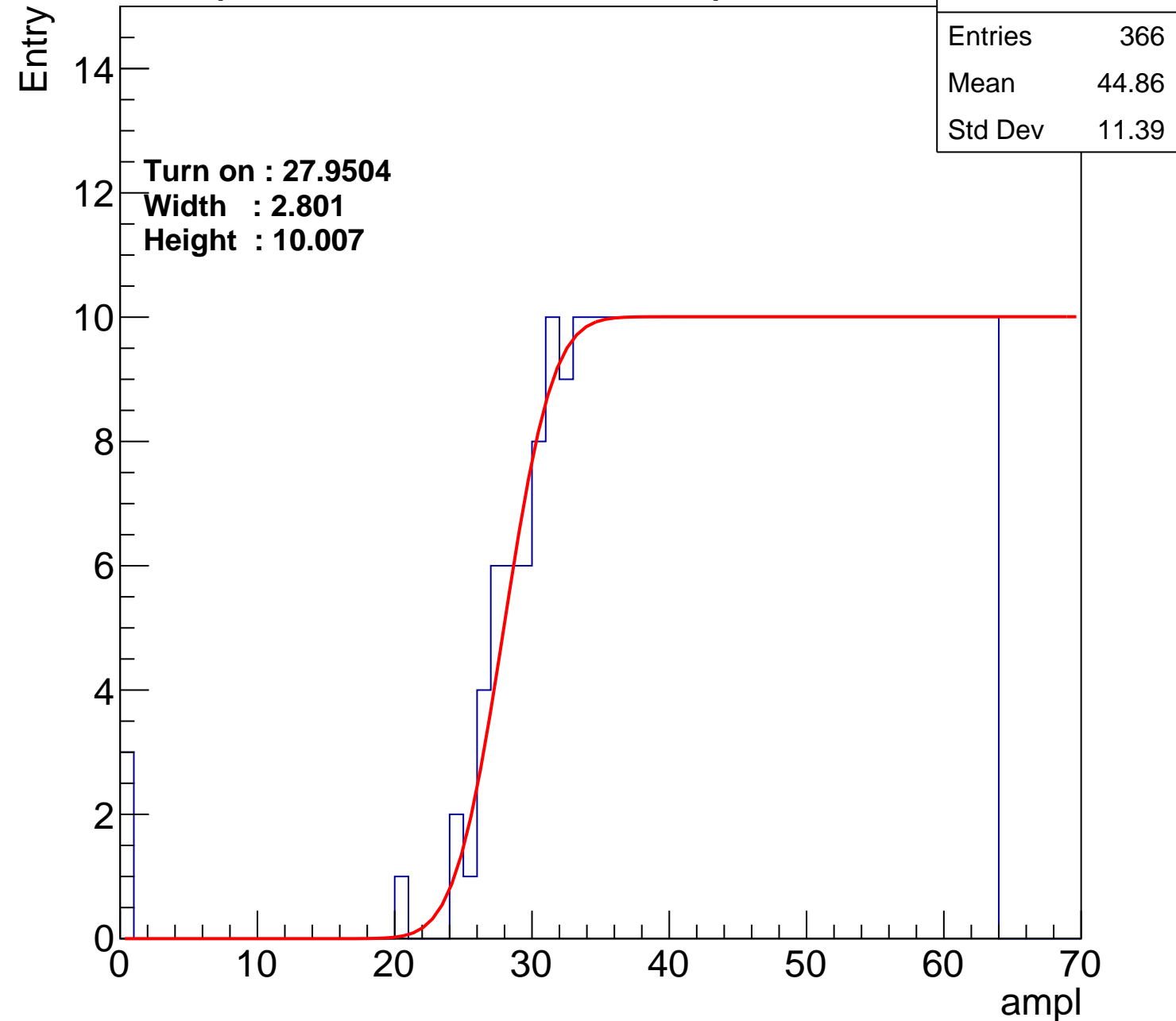
Width : 2.801

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	363
Mean	45.13
Std Dev	11.04

Turn on : 27.9780

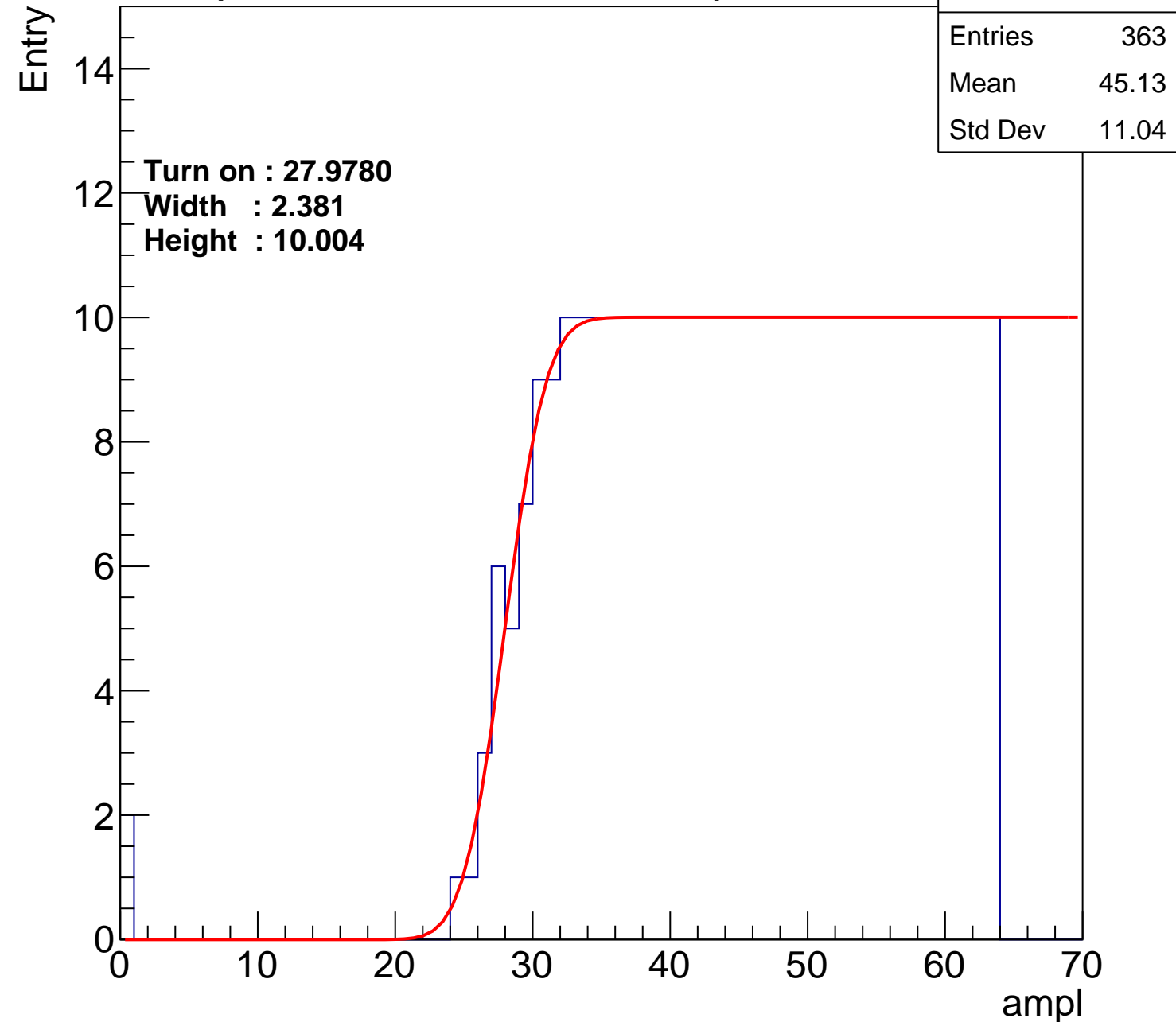
Width : 2.381

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.92
Std Dev	12.45

Turn on : 23.8183

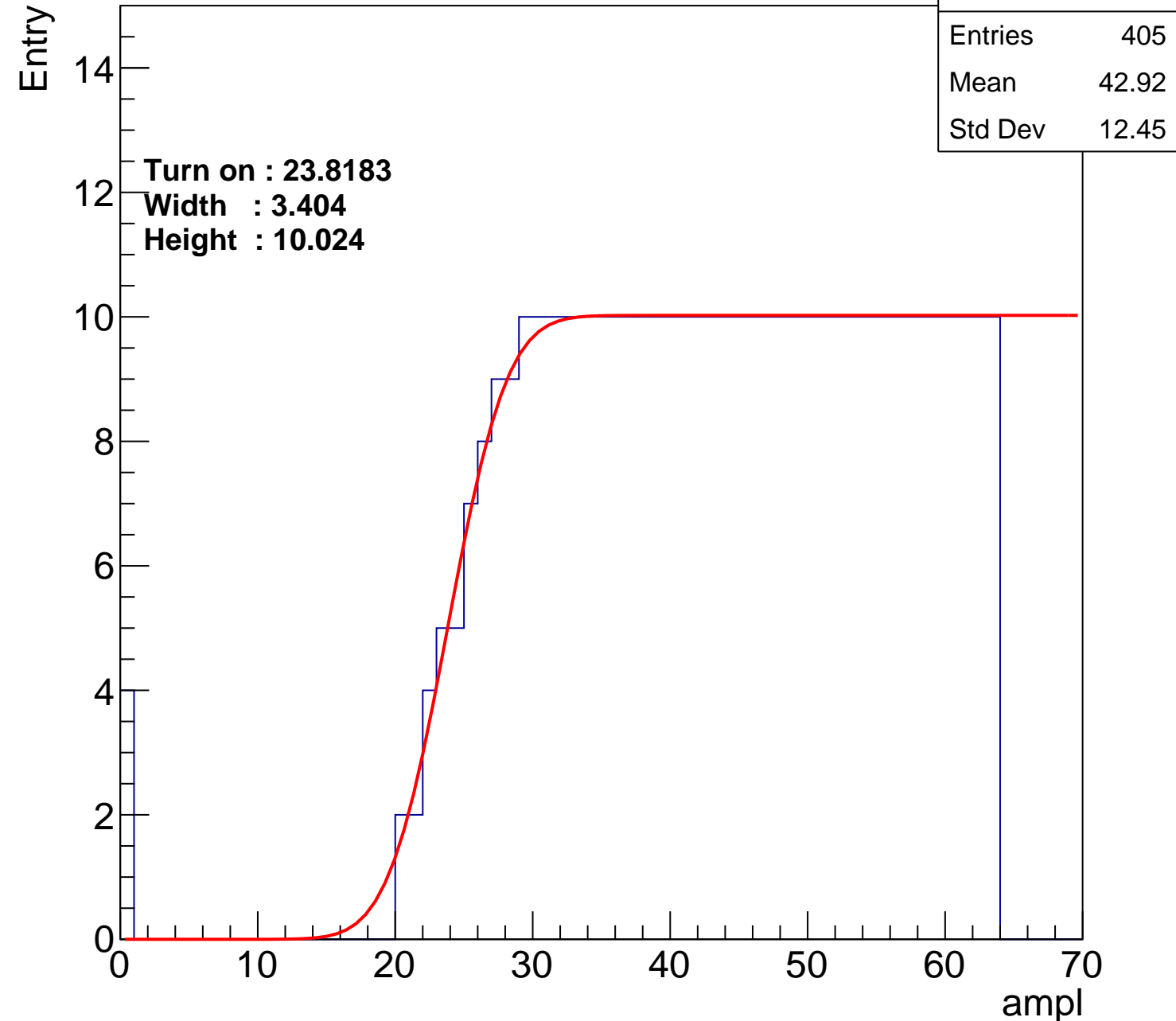
Width : 3.404

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.44
Std Dev	11.94

Turn on : 24.9700

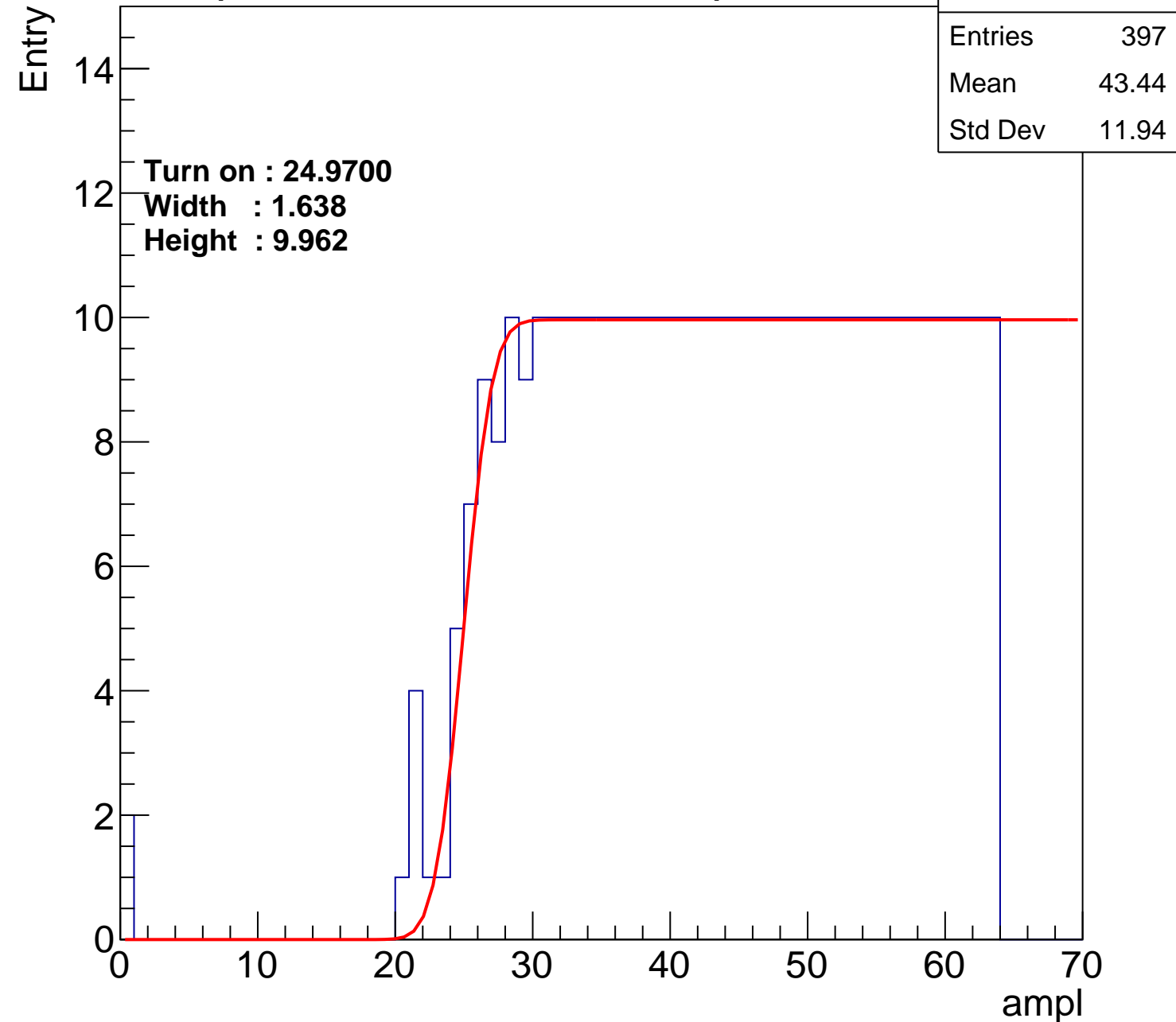
Width : 1.638

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
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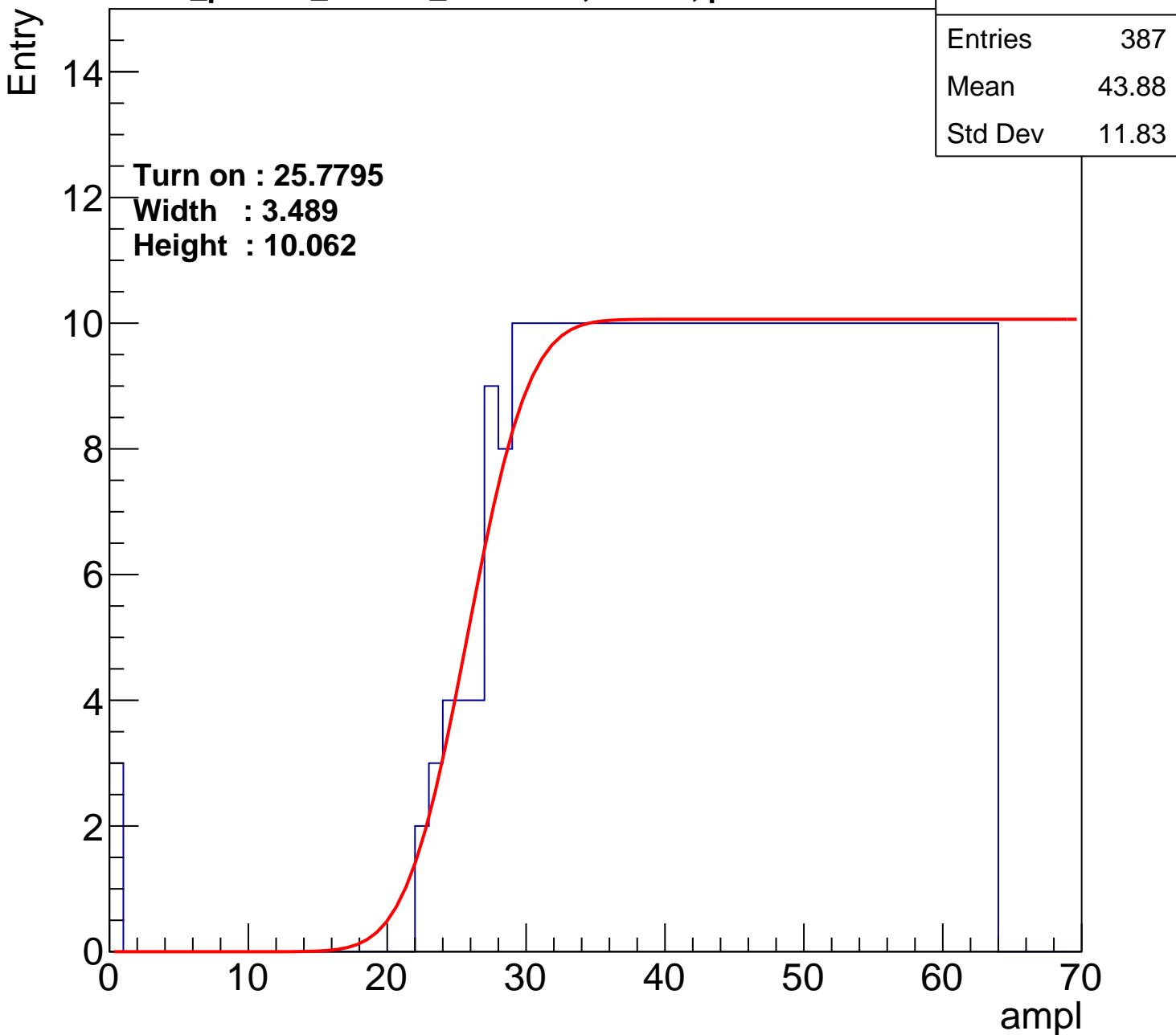
Mean	43.88
------	-------

Std Dev	11.83
---------	-------

Turn on : 25.7795

Width : 3.489

Height : 10.062



B1L002S, U3-ch92

calib_packv5_042523_0143.root, FC#10, port B3

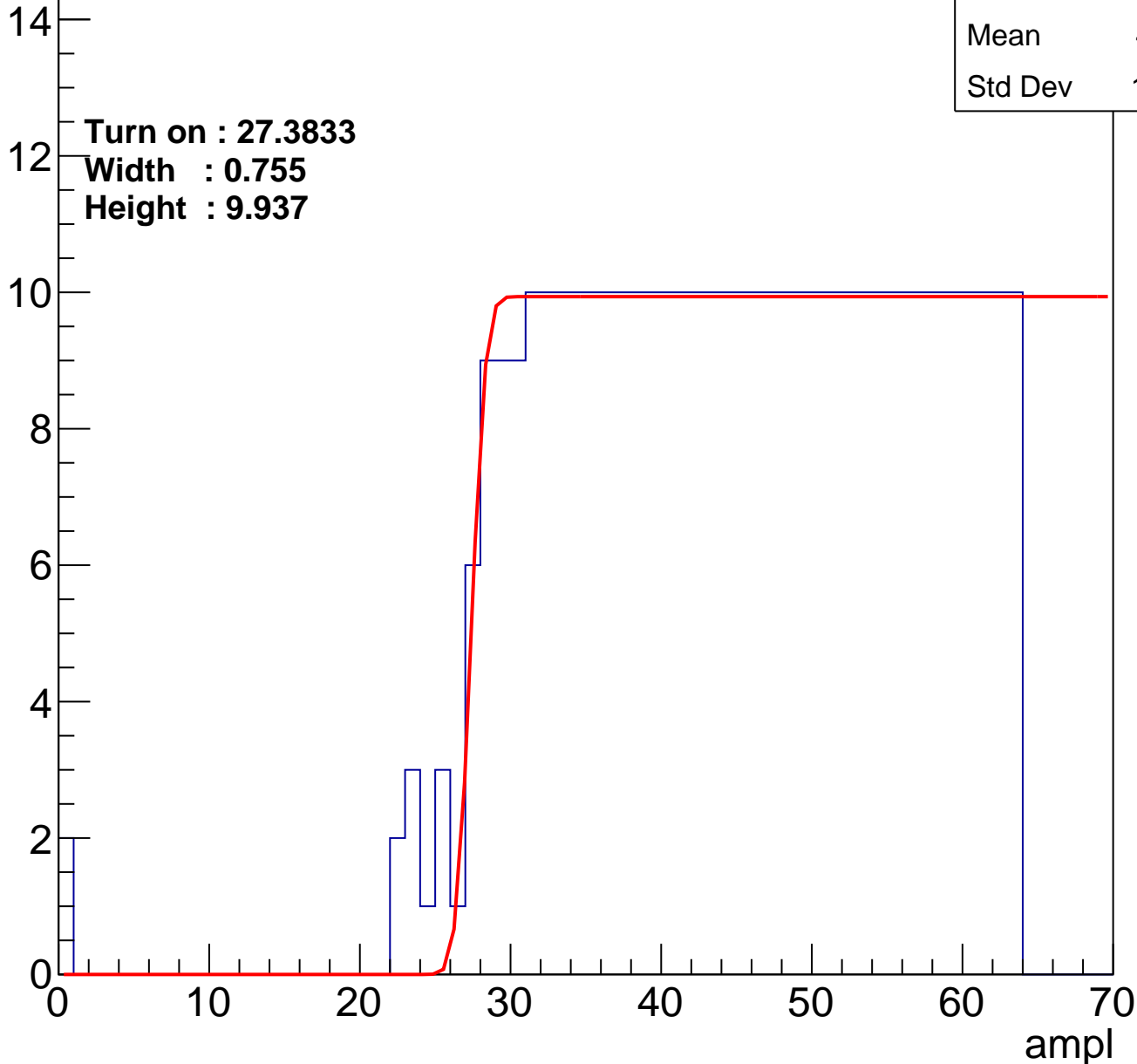
Entry

Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 27.3833

Width : 0.755

Height : 9.937



B1L002S, U3-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.95
Std Dev	12.2

Turn on : 23.8067

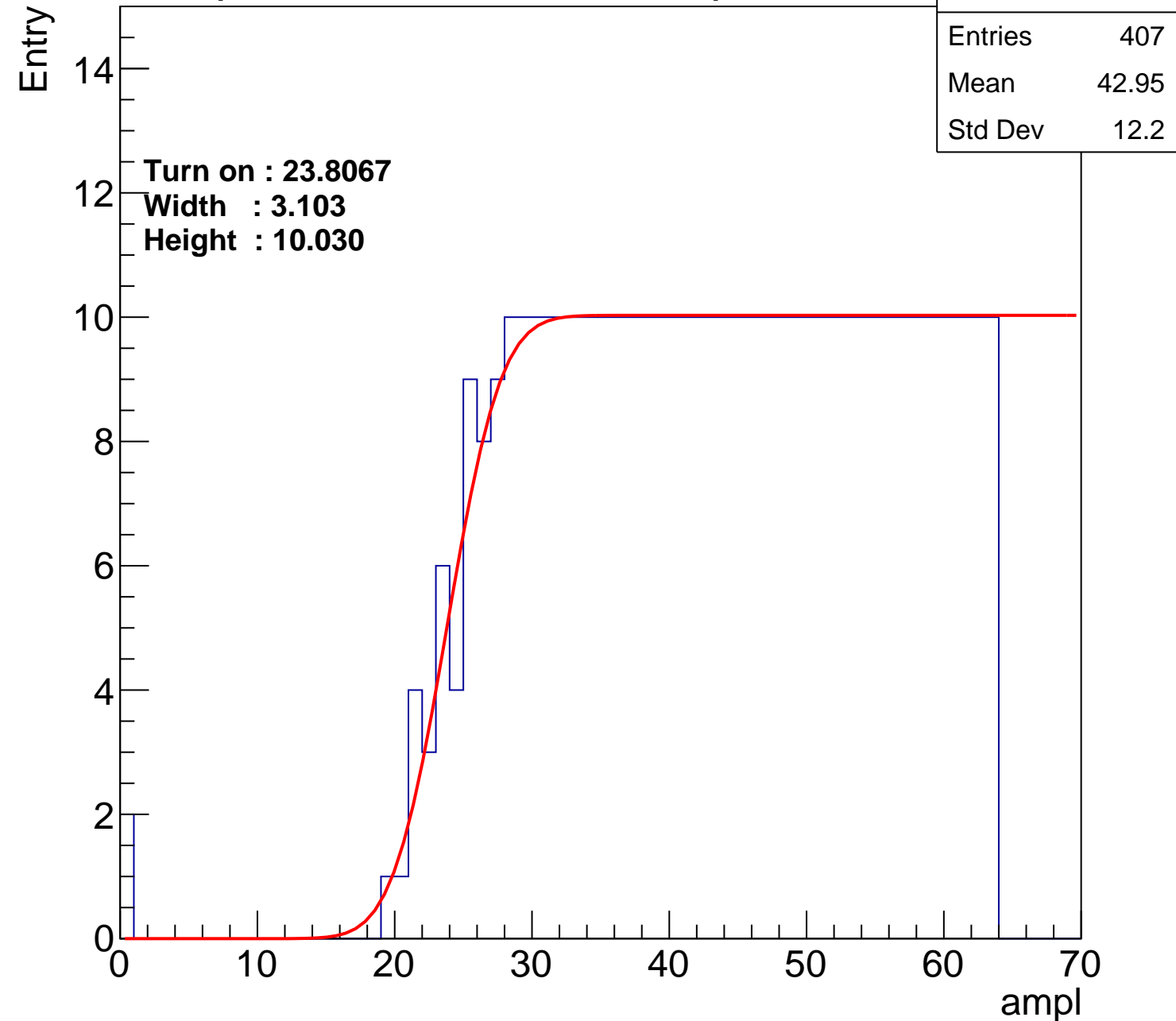
Width : 3.103

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.96
Std Dev	11.67

Turn on : 25.5413

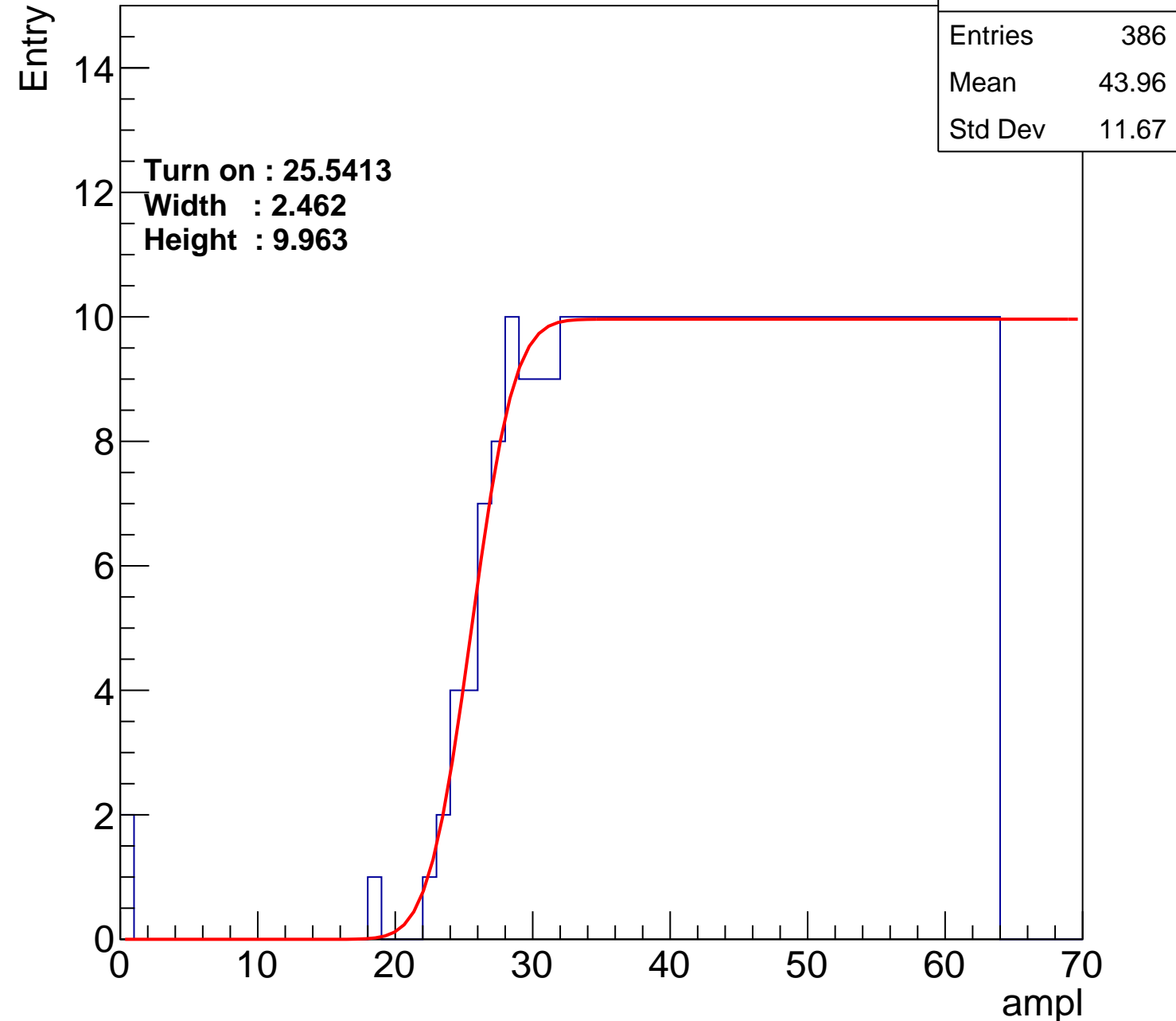
Width : 2.462

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 25.6314

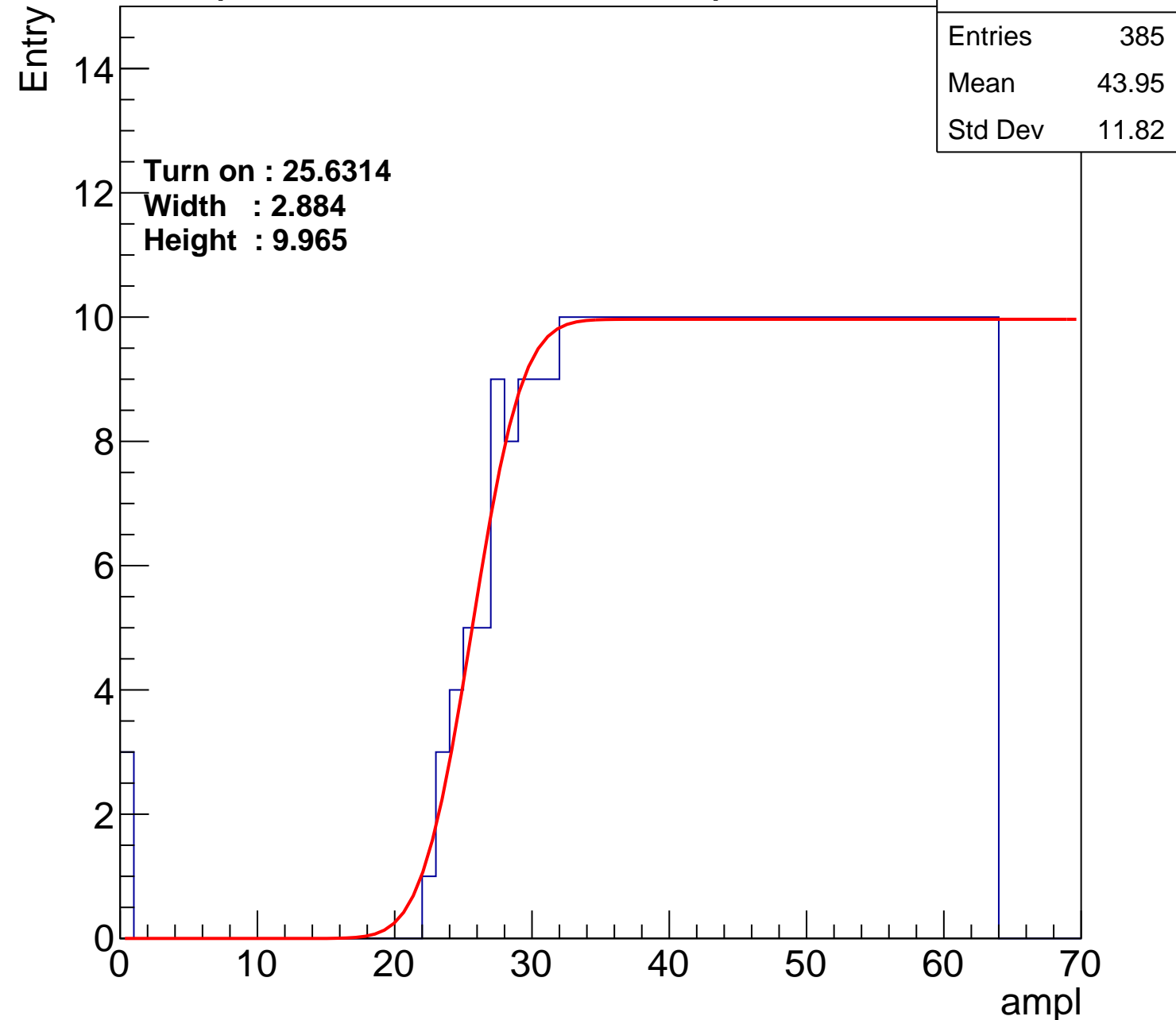
Width : 2.884

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.5569

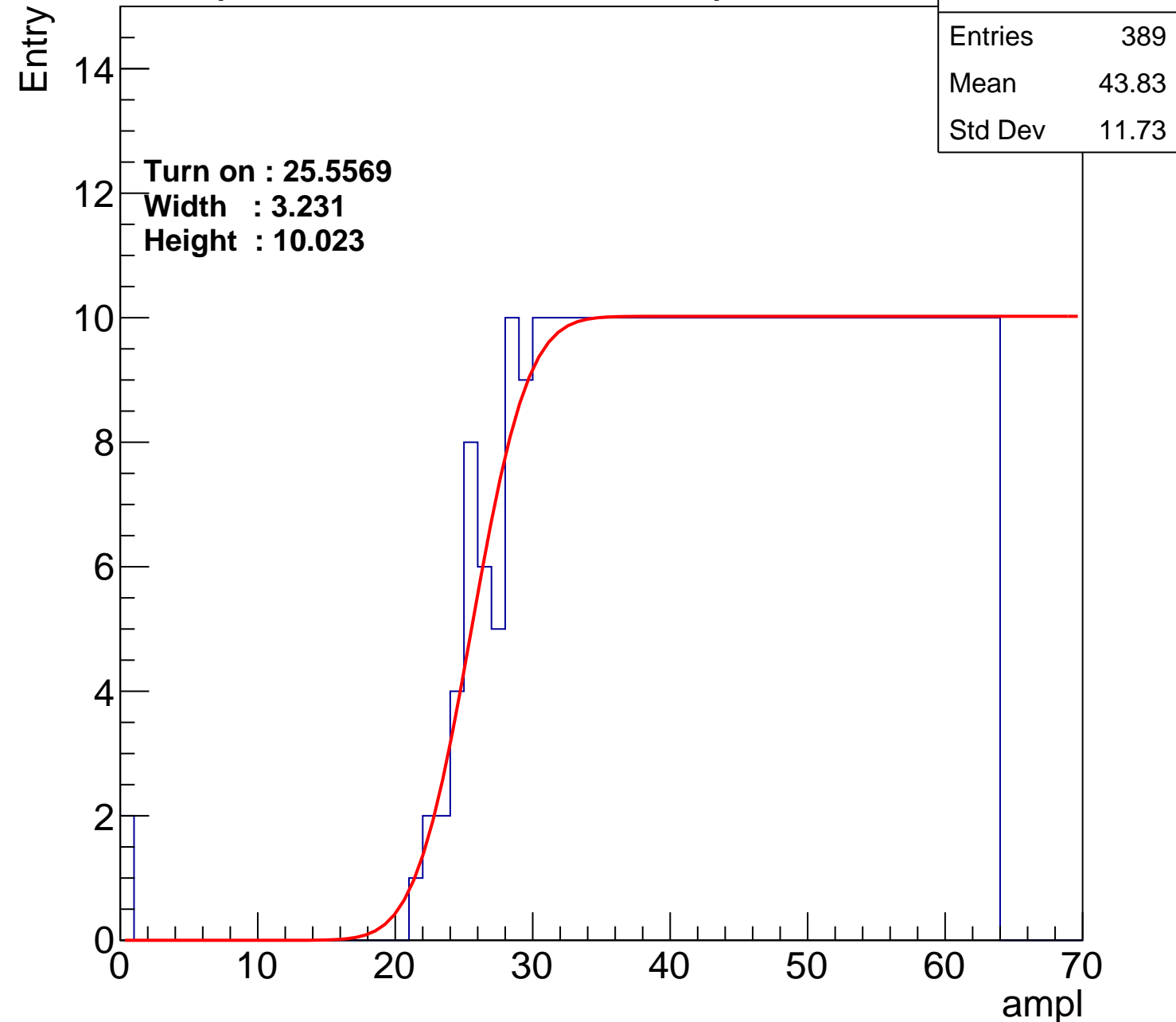
Width : 3.231

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.4
Std Dev	12.17

Turn on : 24.8070

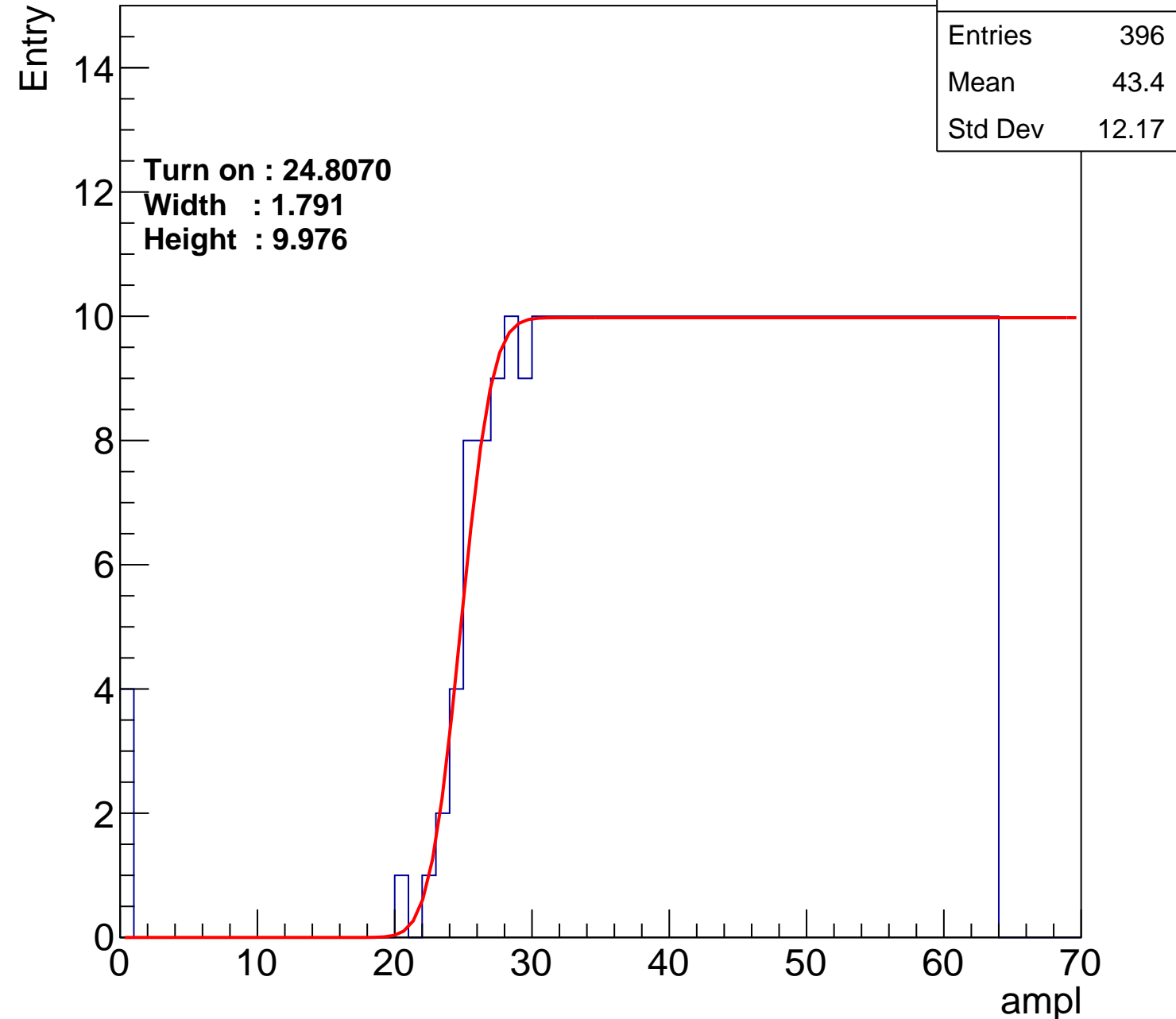
Width : 1.791

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.46
Std Dev	12.17

Turn on : 24.8347

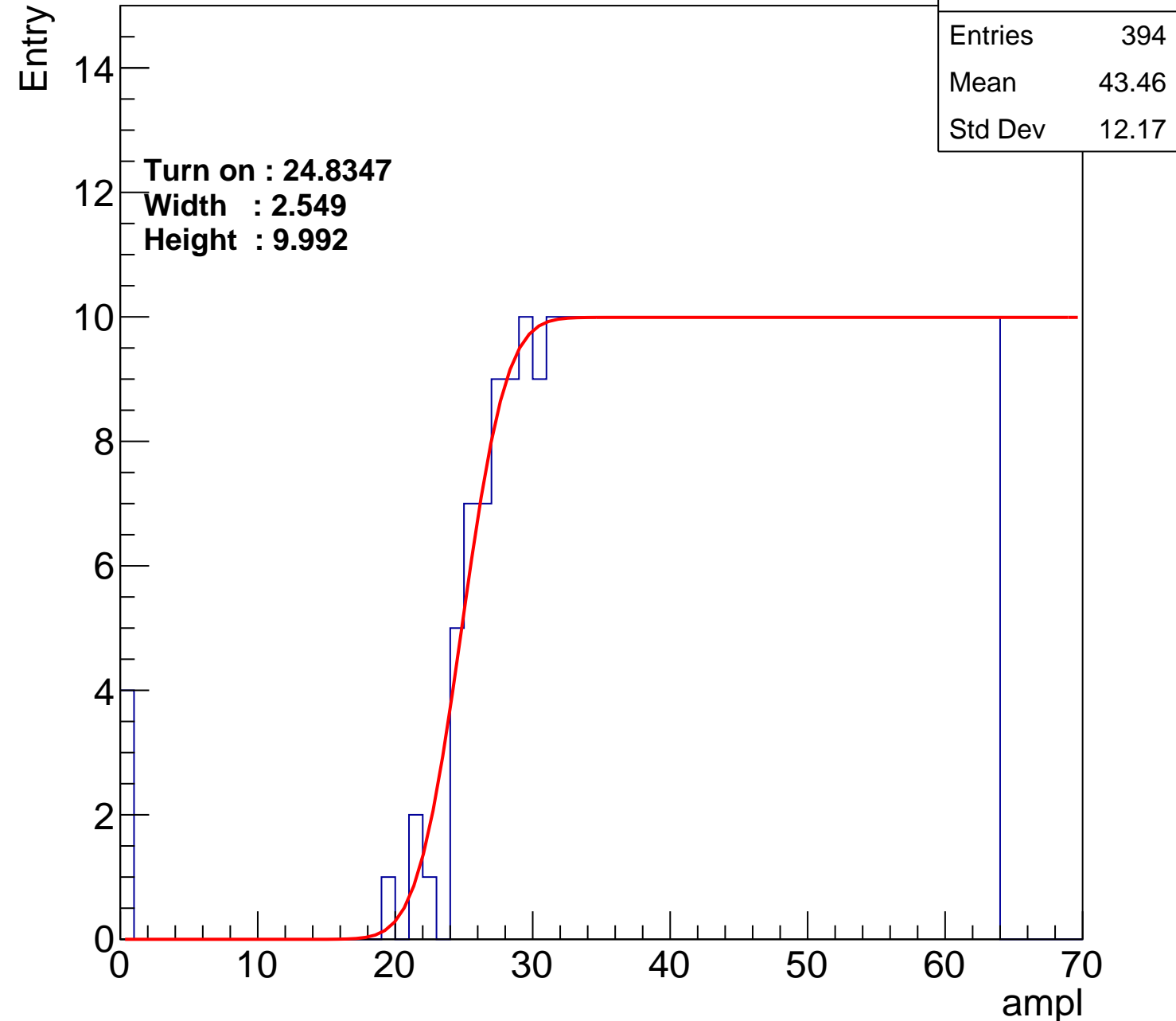
Width : 2.549

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.4
Std Dev	11.83

Turn on : 24.2842

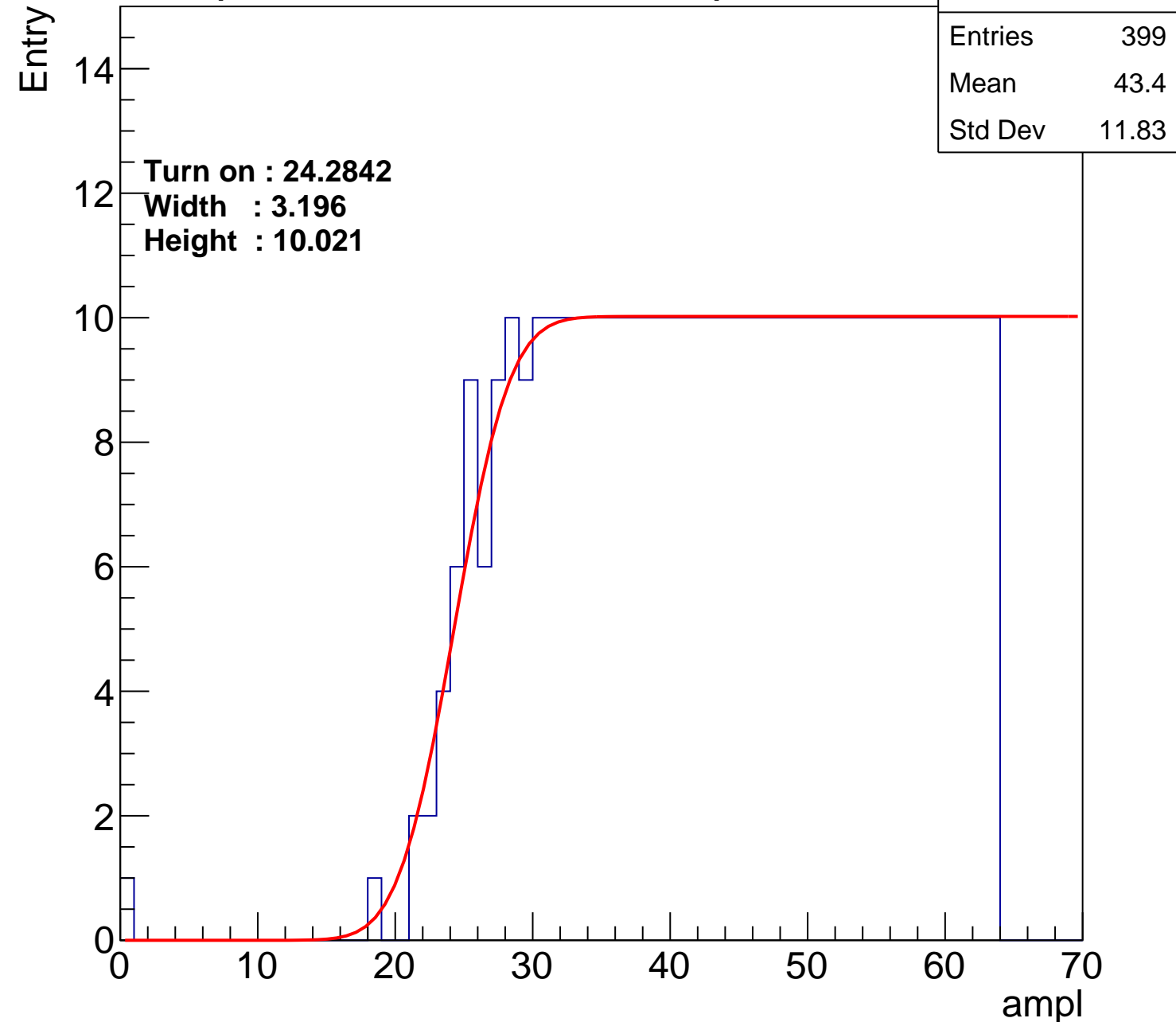
Width : 3.196

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.89
Std Dev	12.22

Turn on : 23.4558

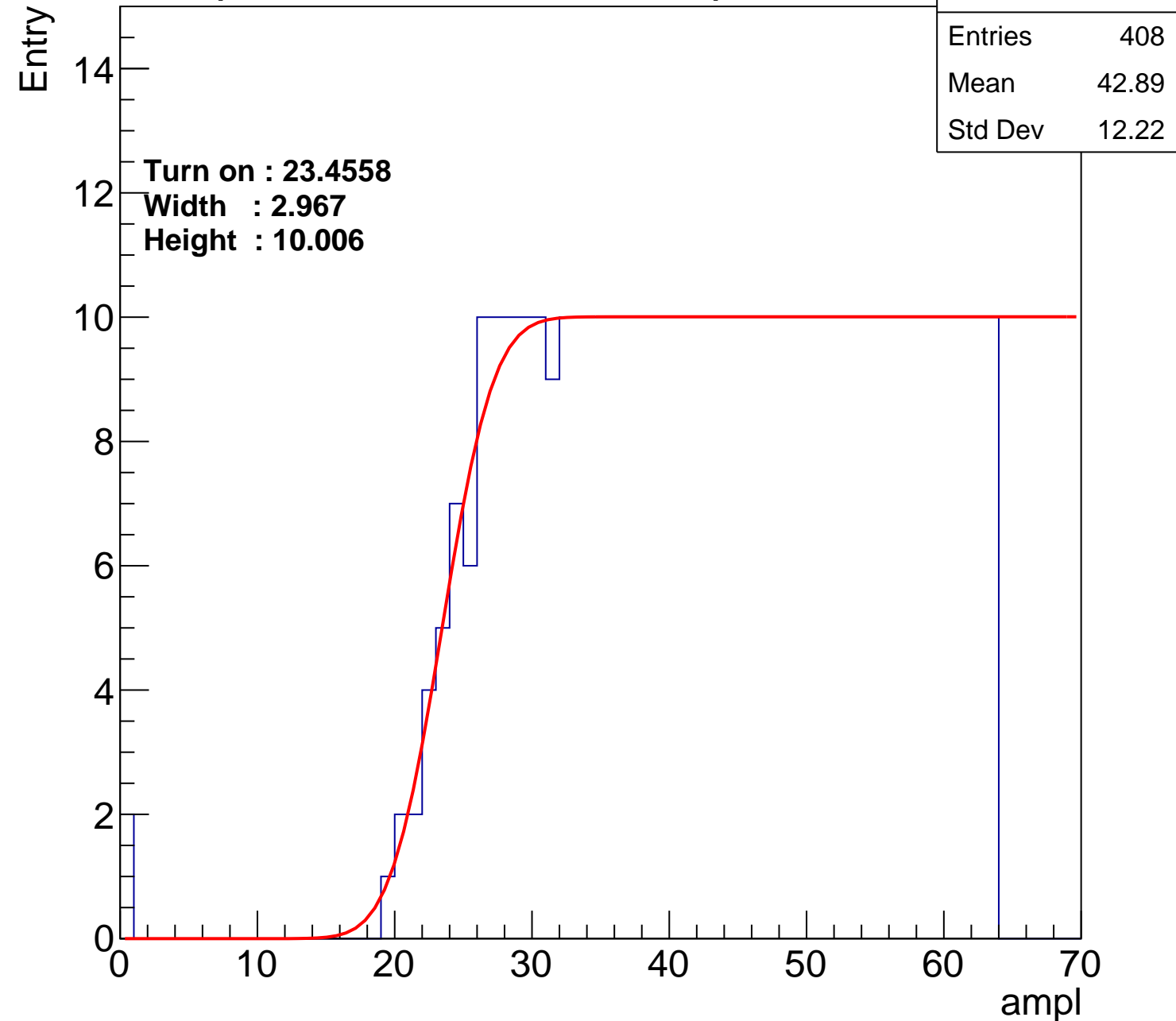
Width : 2.967

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.24
Std Dev	11.68

Turn on : 26.2153

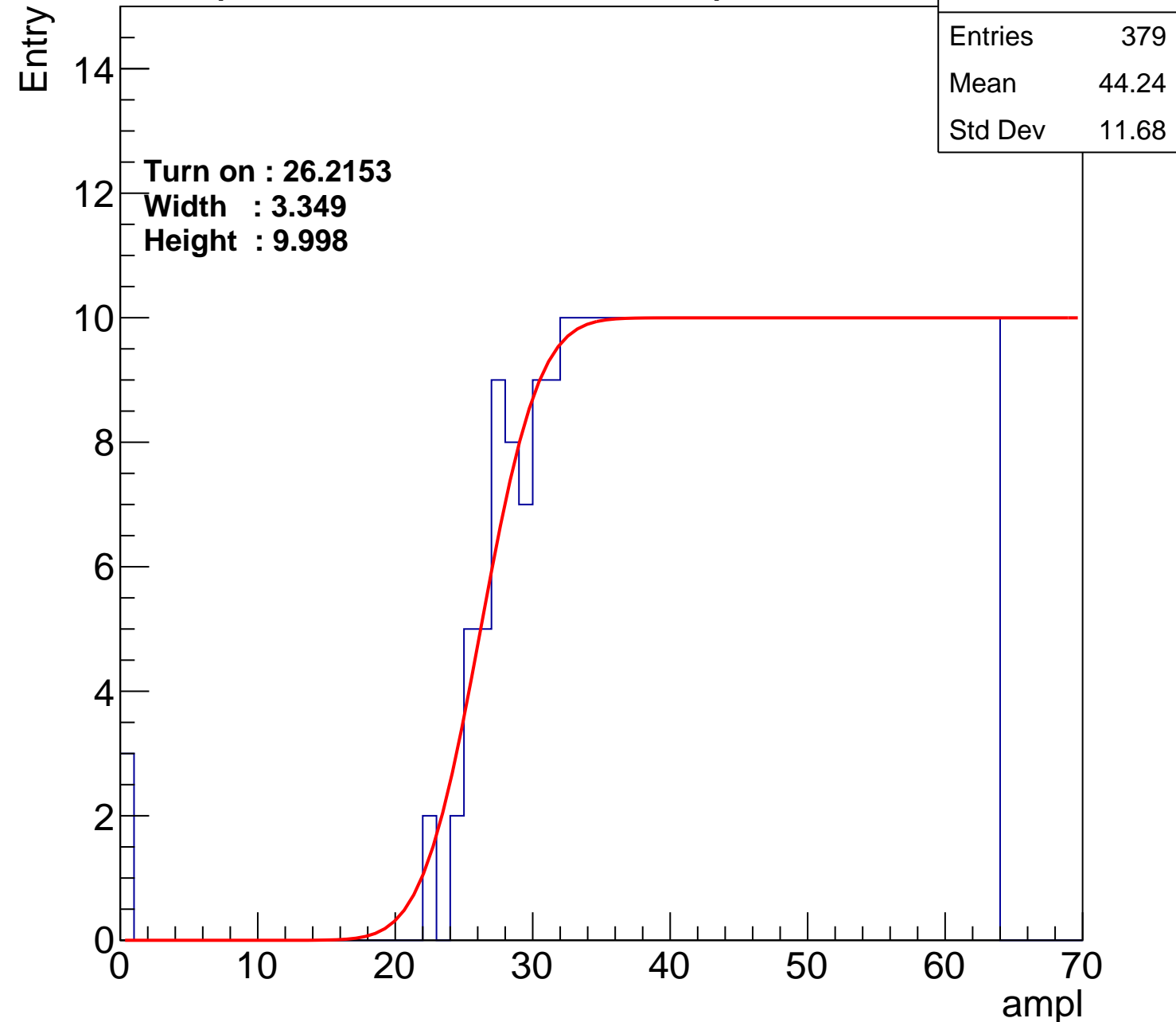
Width : 3.349

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.0485

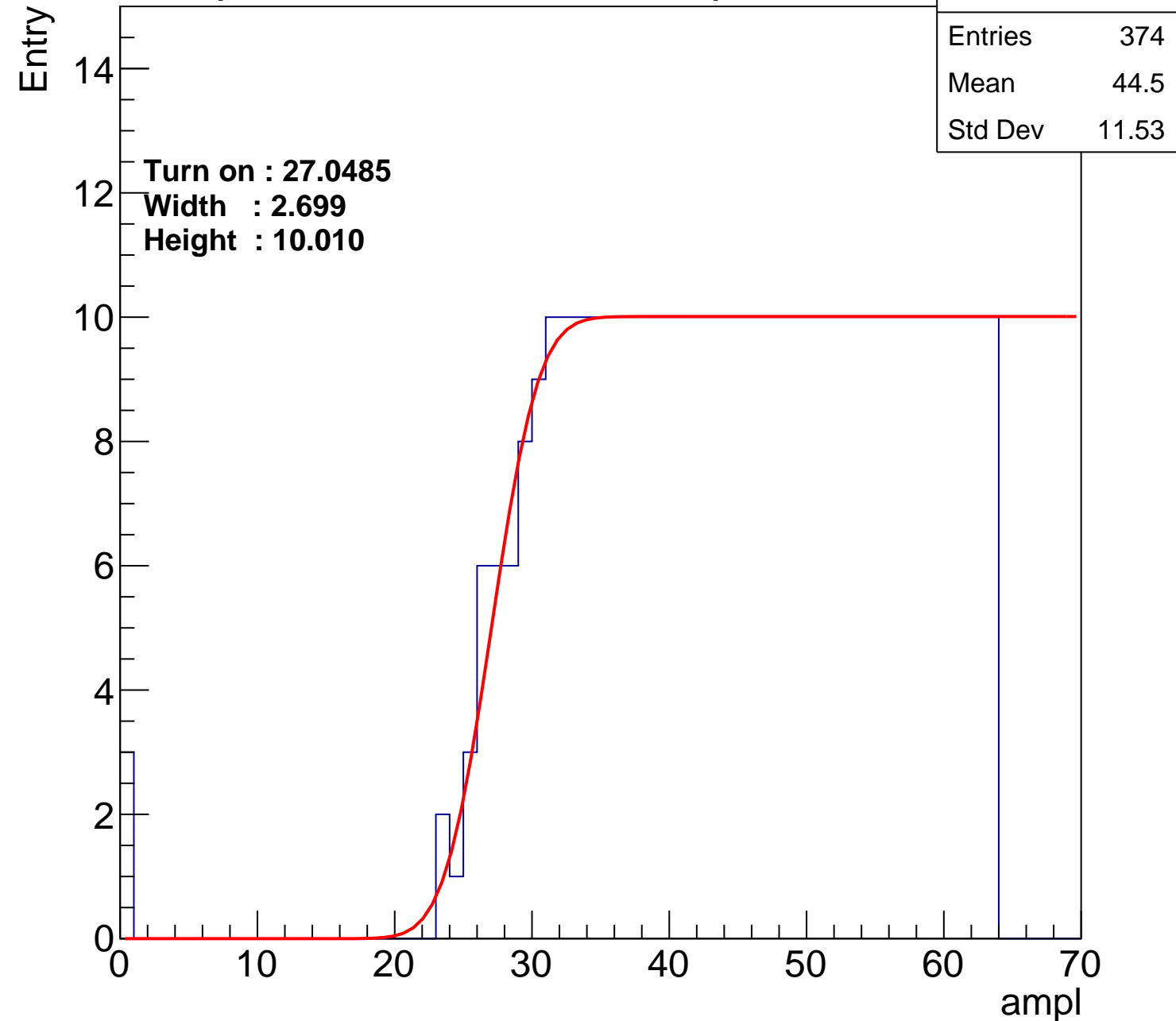
Width : 2.699

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch103

calib_packv5_042523_0143.root, FC#10, port B3

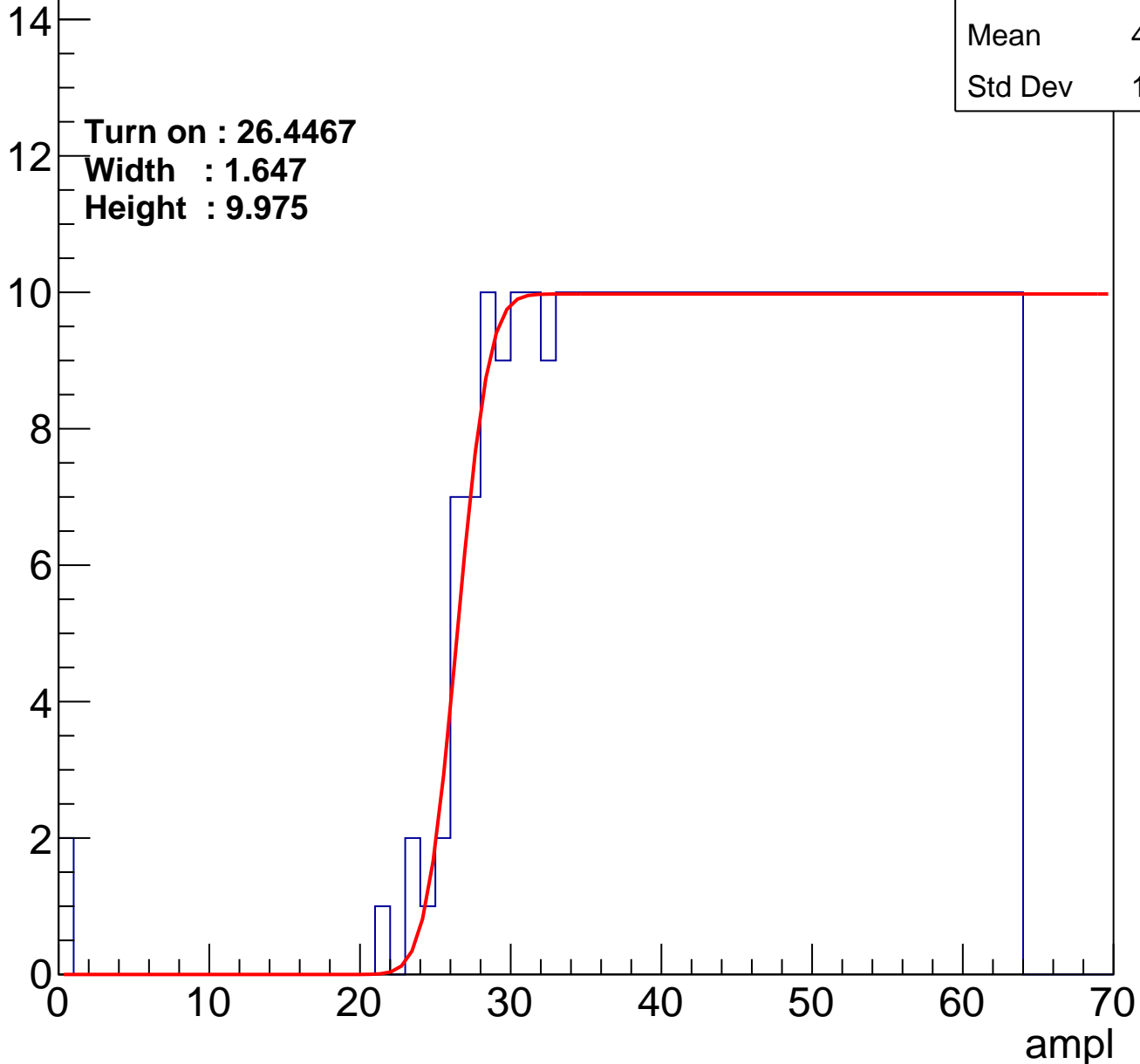
Entry

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.4467

Width : 1.647

Height : 9.975



B1L002S, U3-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.98
Std Dev	12.32

Turn on : 24.6540

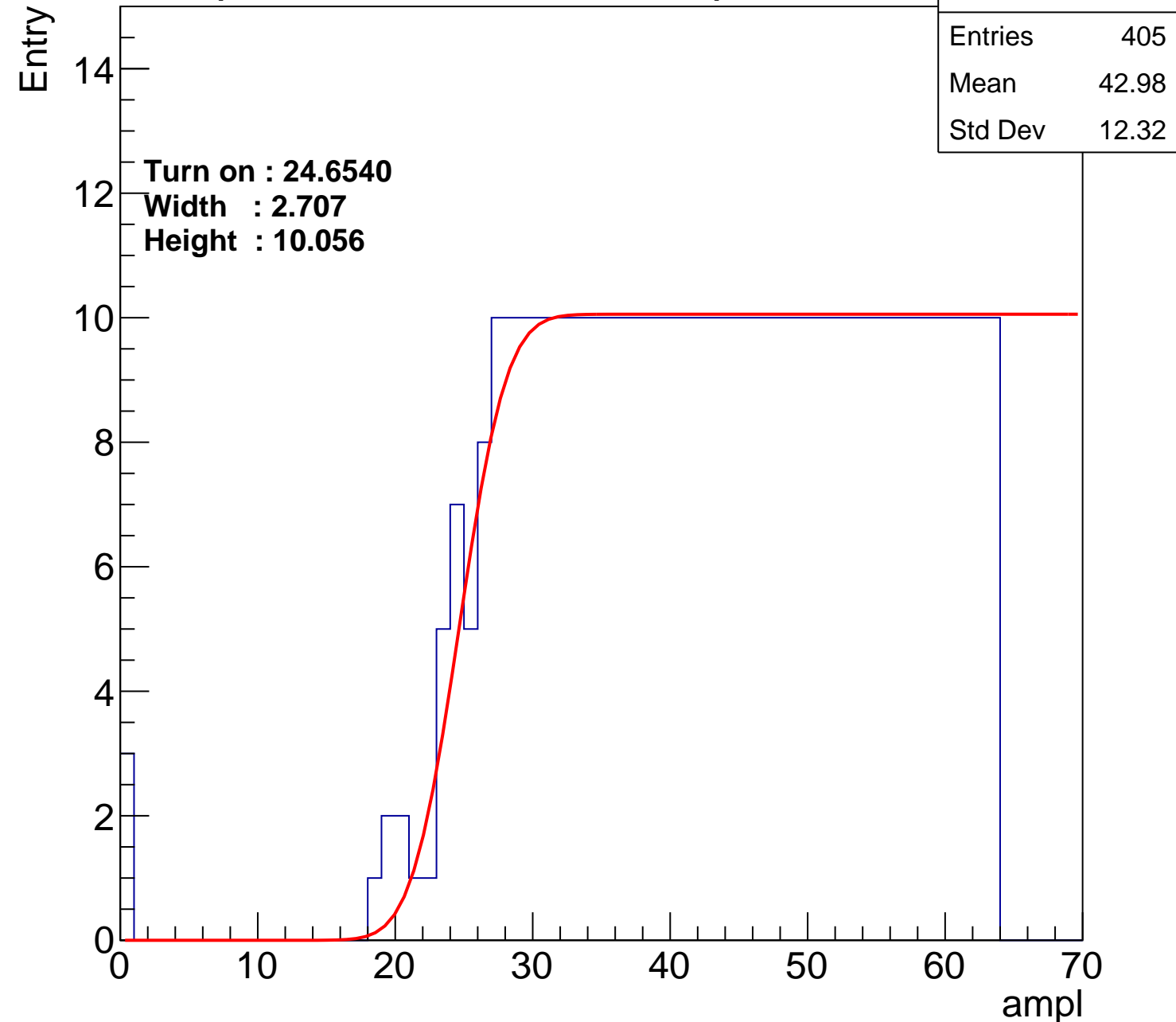
Width : 2.707

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.56
Std Dev	11.84

Turn on : 24.8325

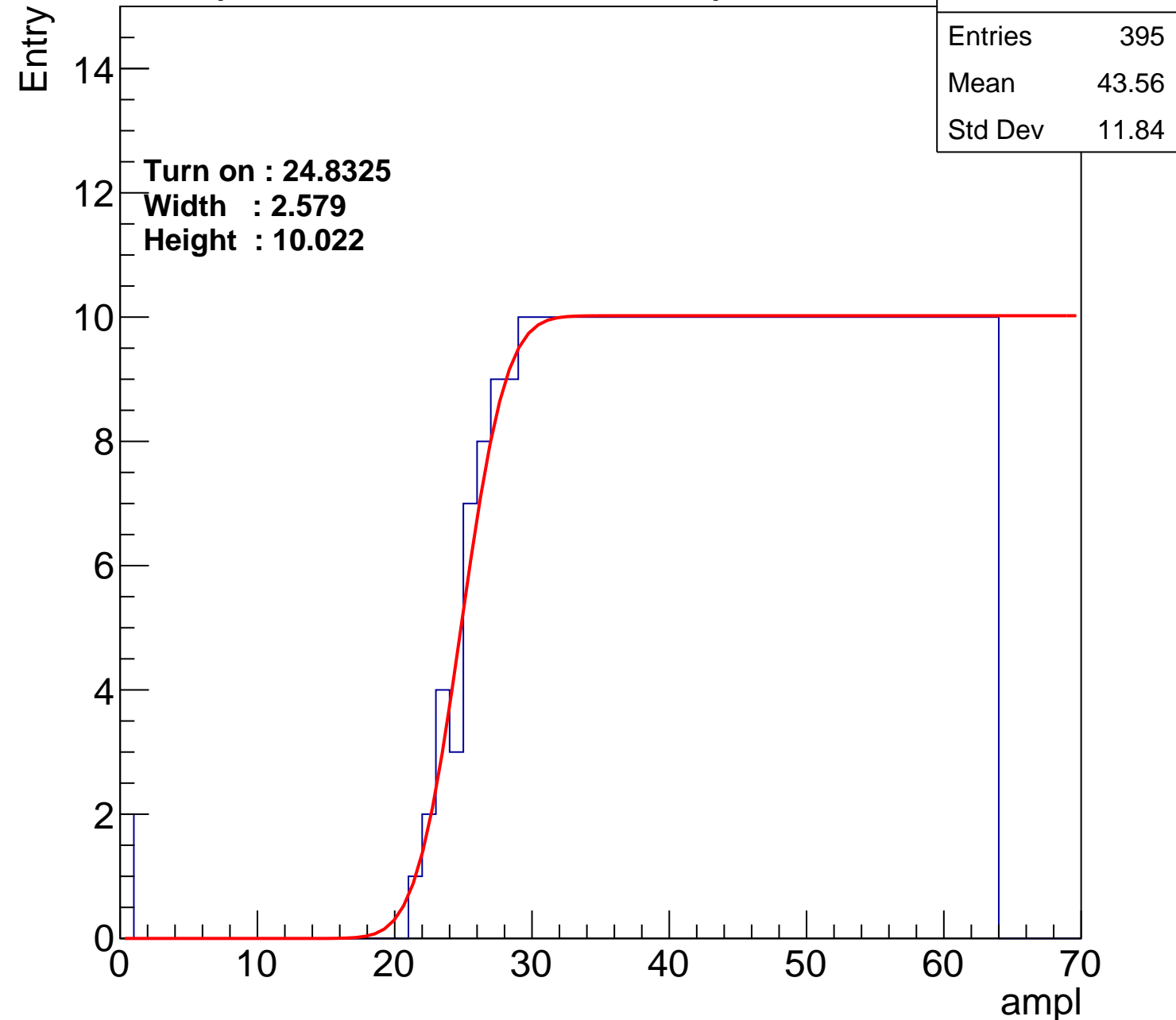
Width : 2.579

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.3
Std Dev	11.99

Turn on : 24.0044

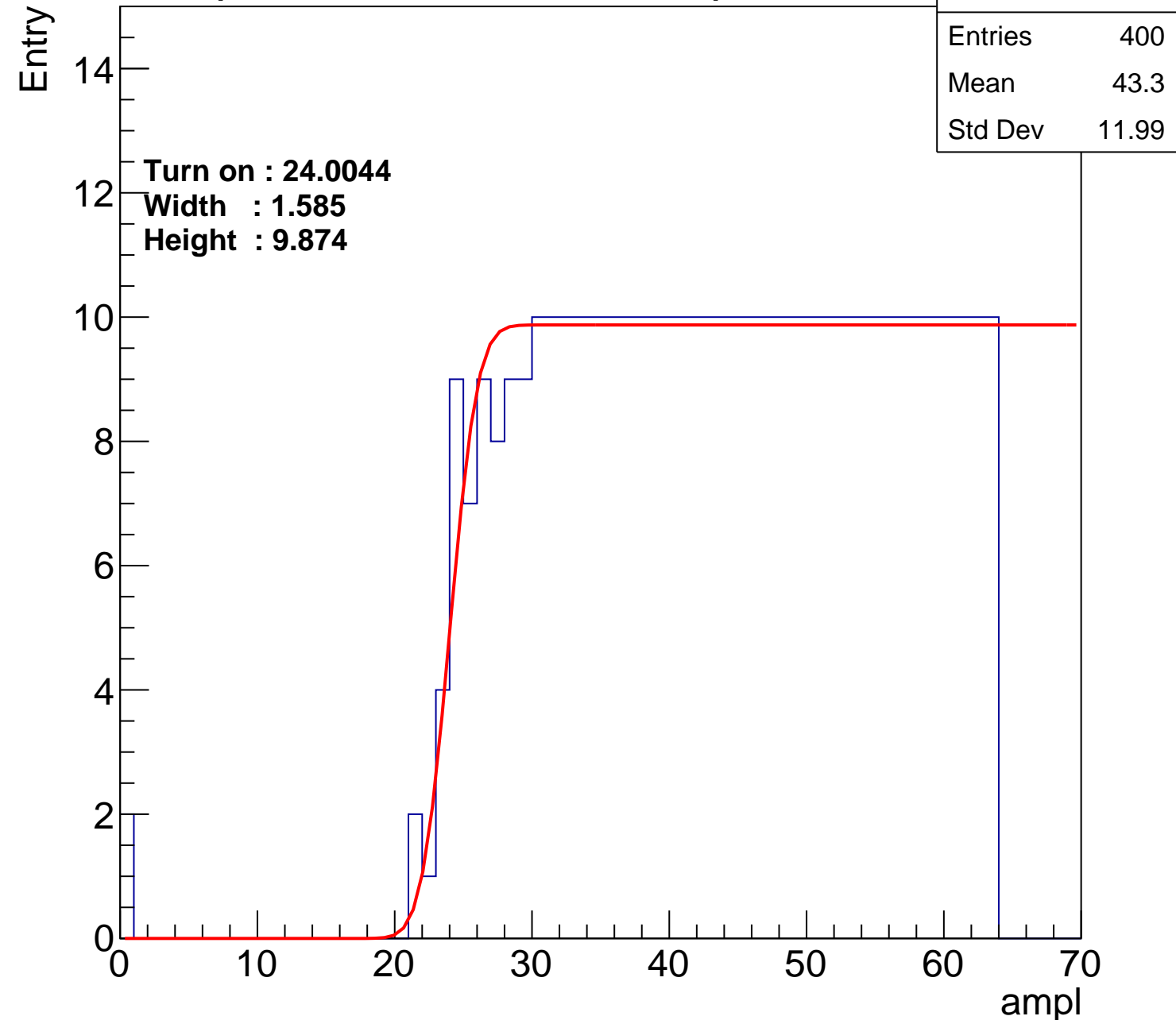
Width : 1.585

Height : 9.874

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.59
Std Dev	12.08

Turn on : 25.4087

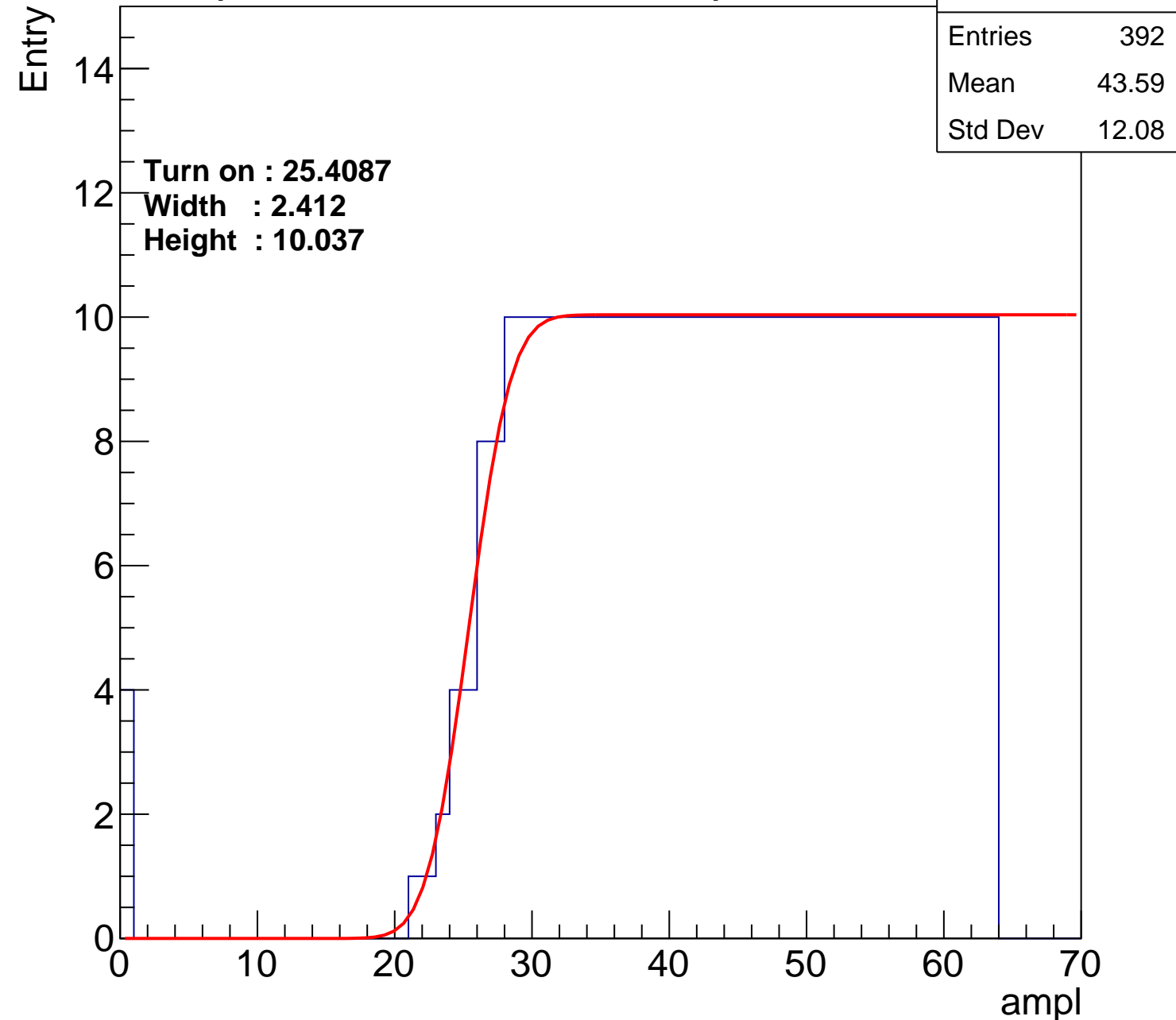
Width : 2.412

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch108

calib_packv5_042523_0143.root, FC#10, port B3

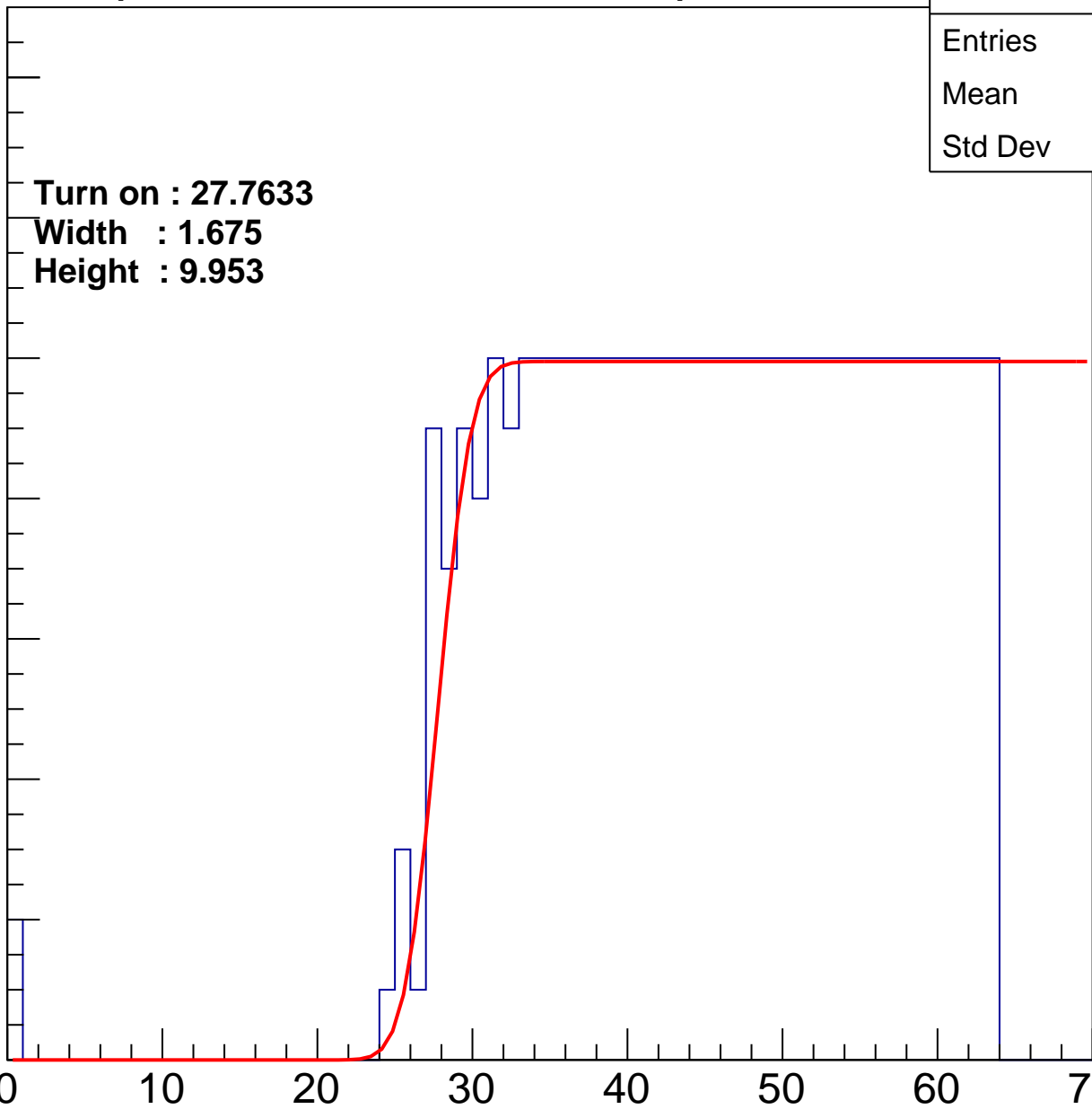
Entry

14
12
10
8
6
4
2
0

Turn on : 27.7633
Width : 1.675
Height : 9.953

Entries	369
Mean	44.83
Std Dev	11.18

ampl



B1L002S, U3-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.3165

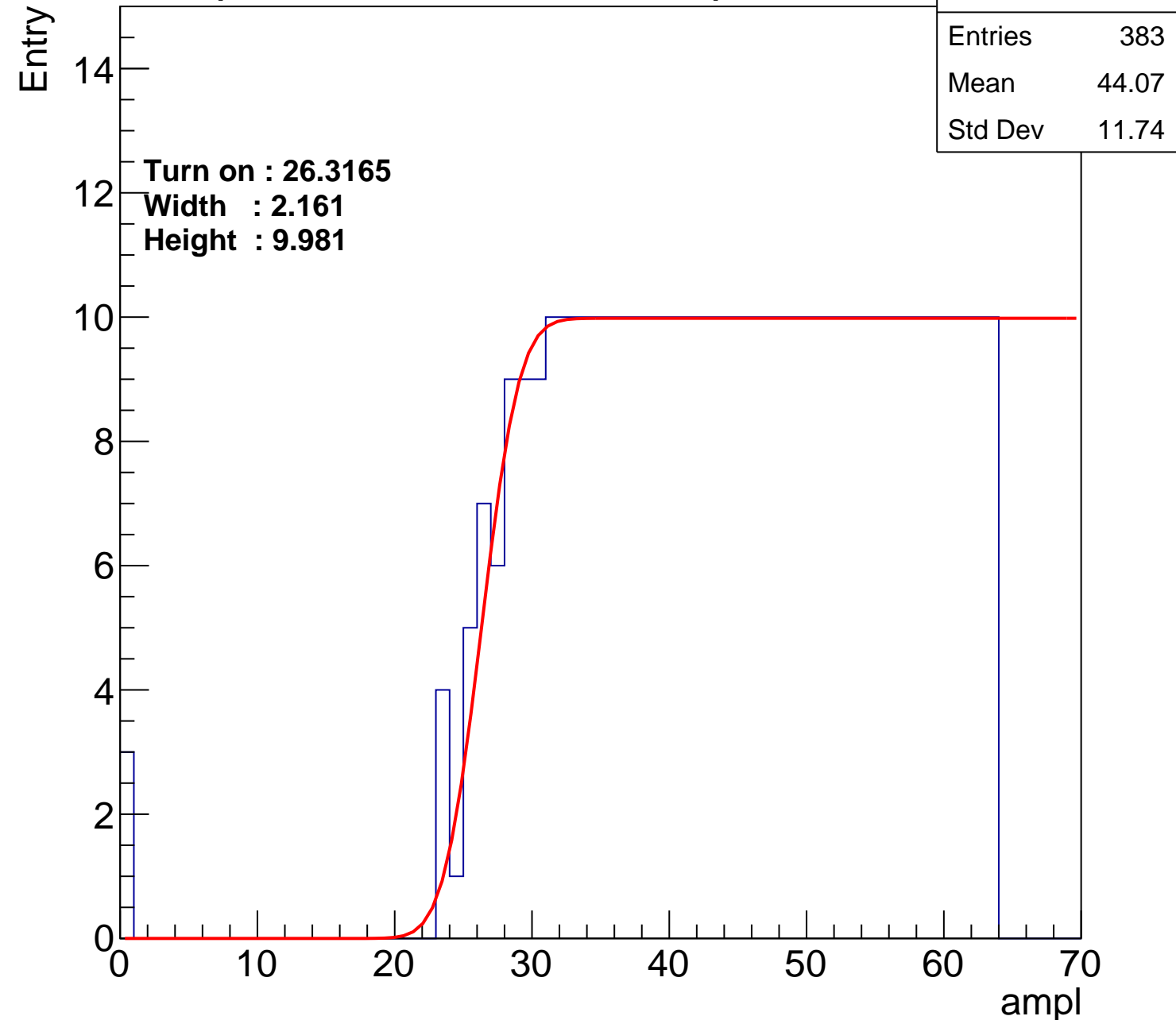
Width : 2.161

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.1
Std Dev	11.45

Turn on : 26.3255

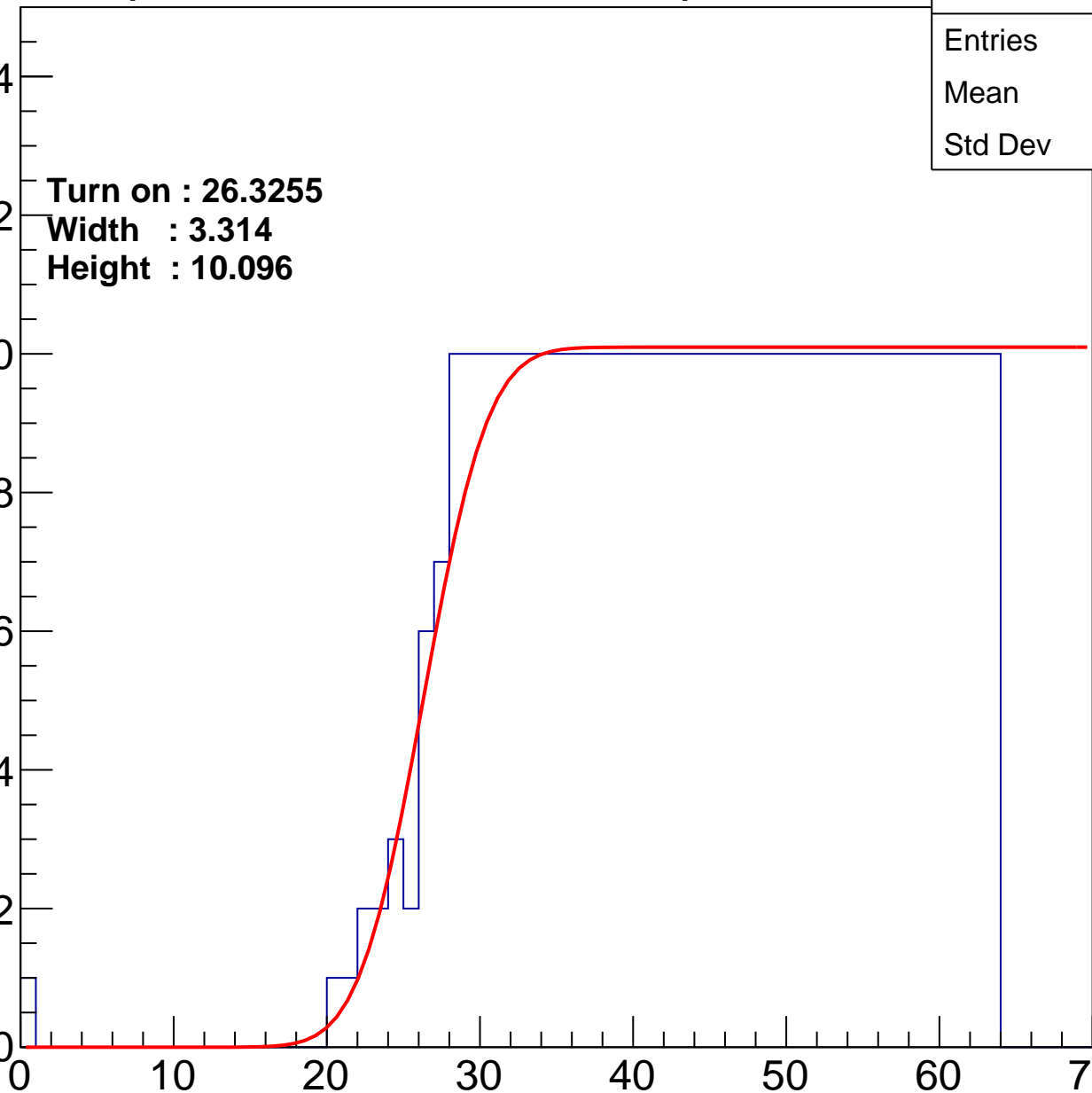
Width : 3.314

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.33
Std Dev	12.01

Turn on : 24.8780

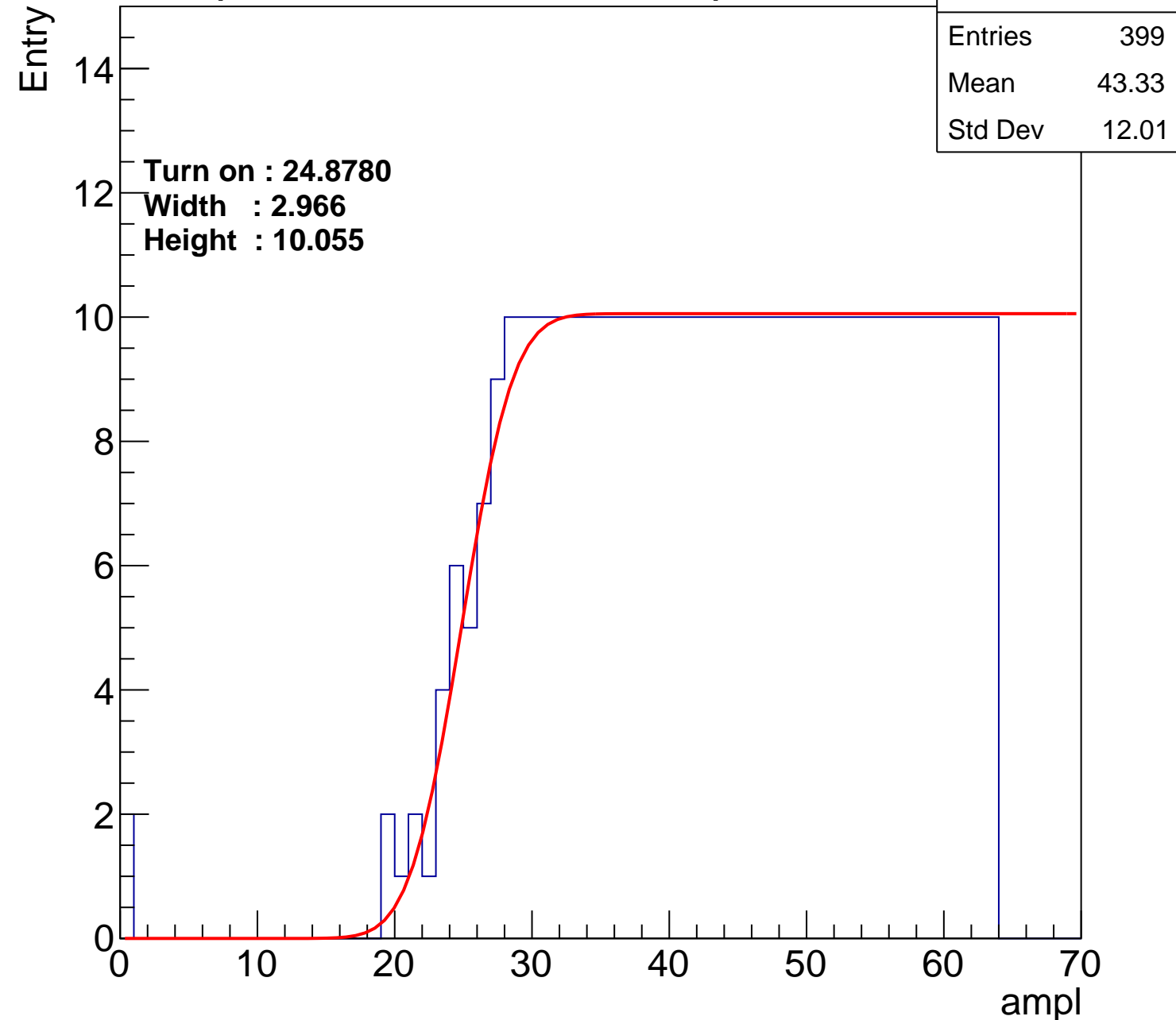
Width : 2.966

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.58
Std Dev	11.82

Turn on : 24.8072

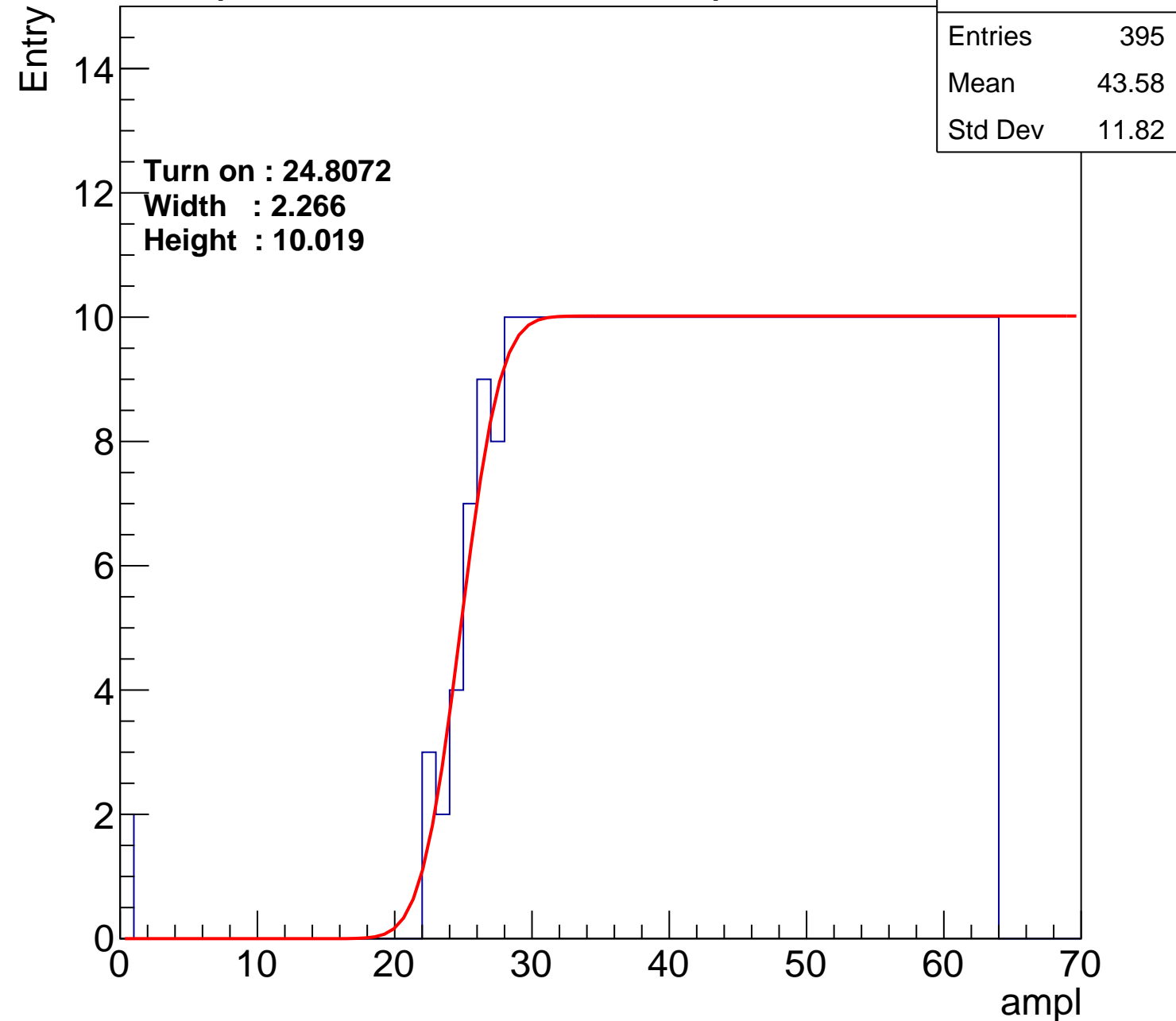
Width : 2.266

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.36
Std Dev	12.12

Turn on : 24.8960

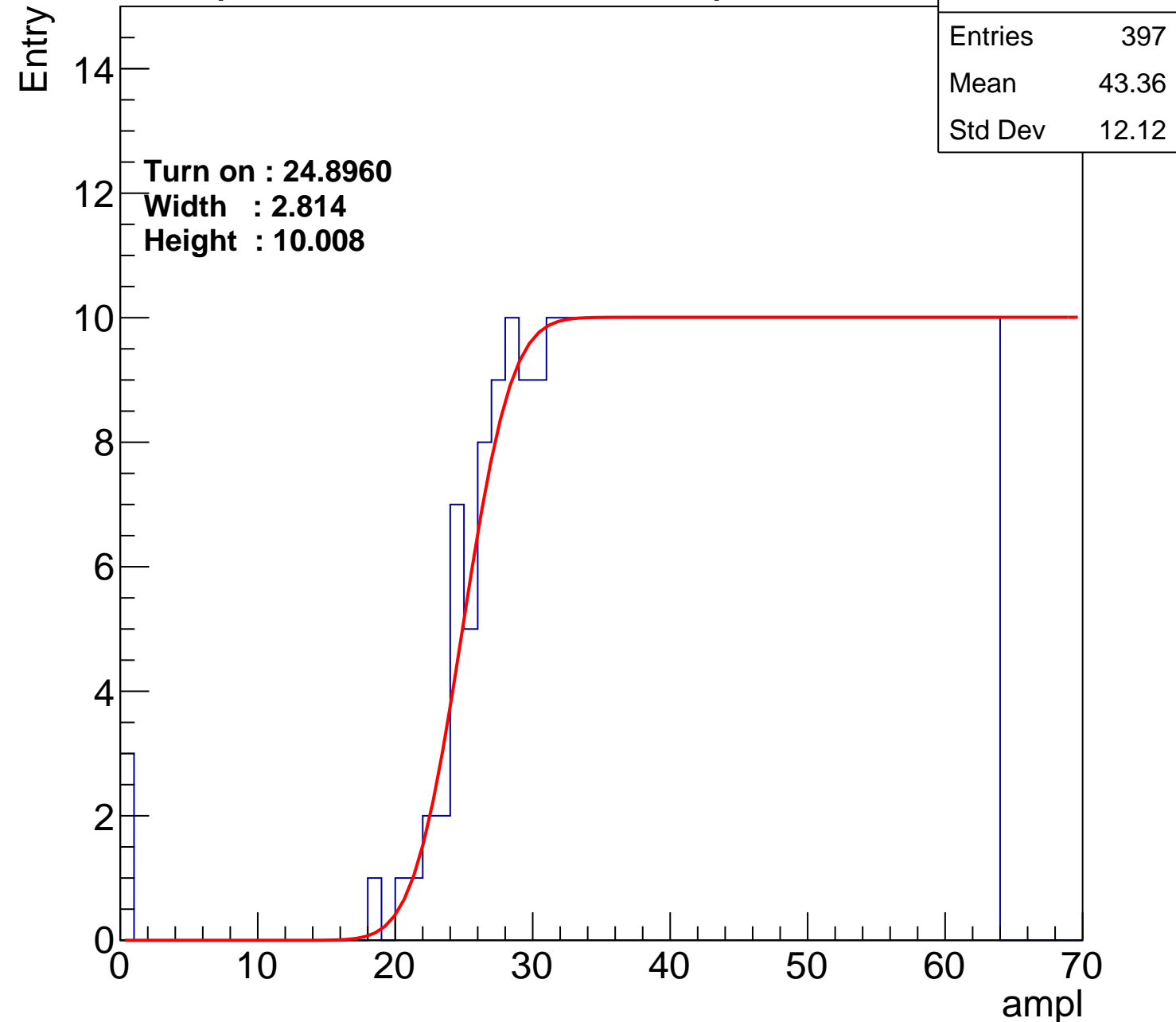
Width : 2.814

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	11.99

Turn on : 25.0405

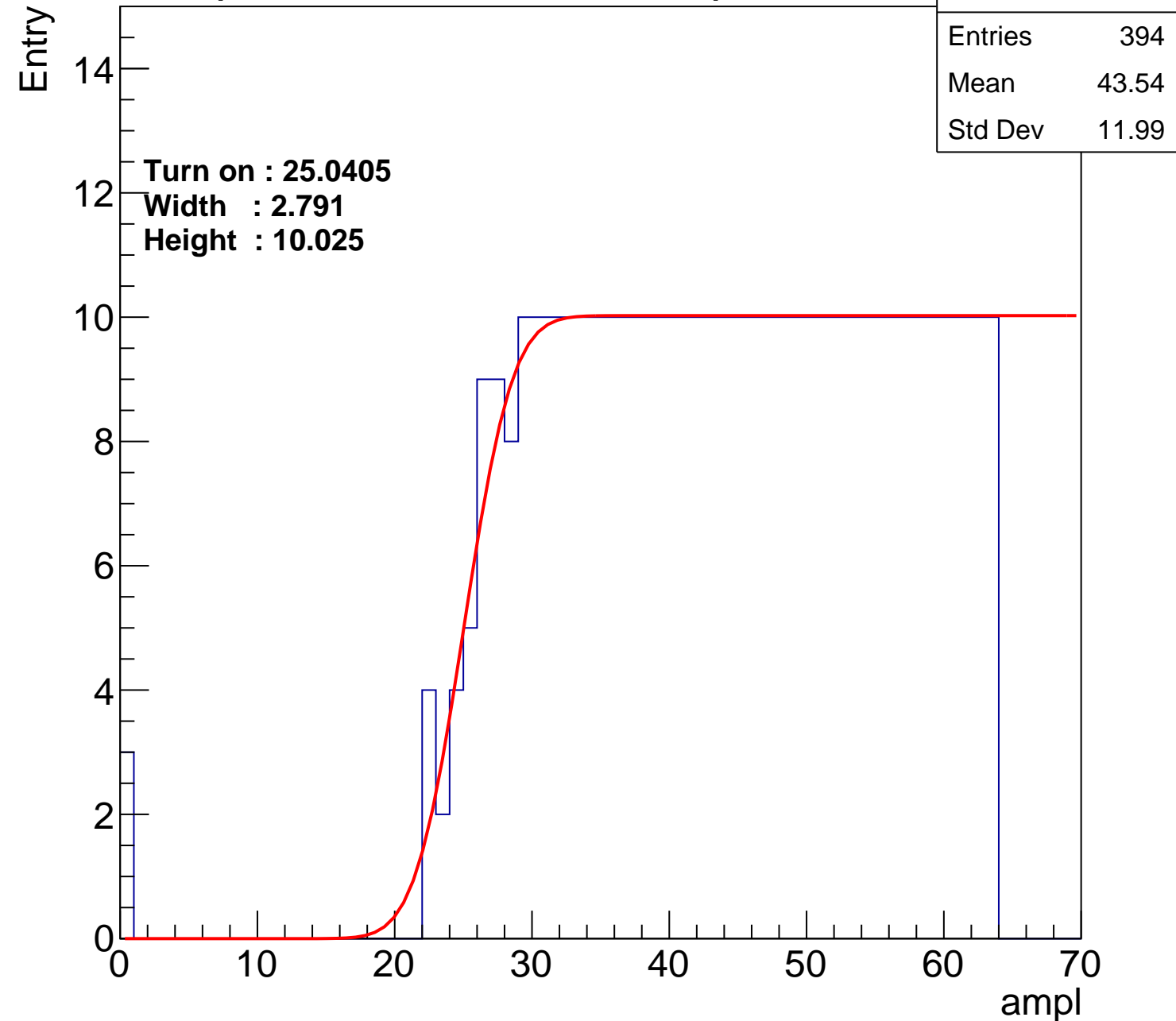
Width : 2.791

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch115

calib_packv5_042523_0143.root, FC#10, port B3

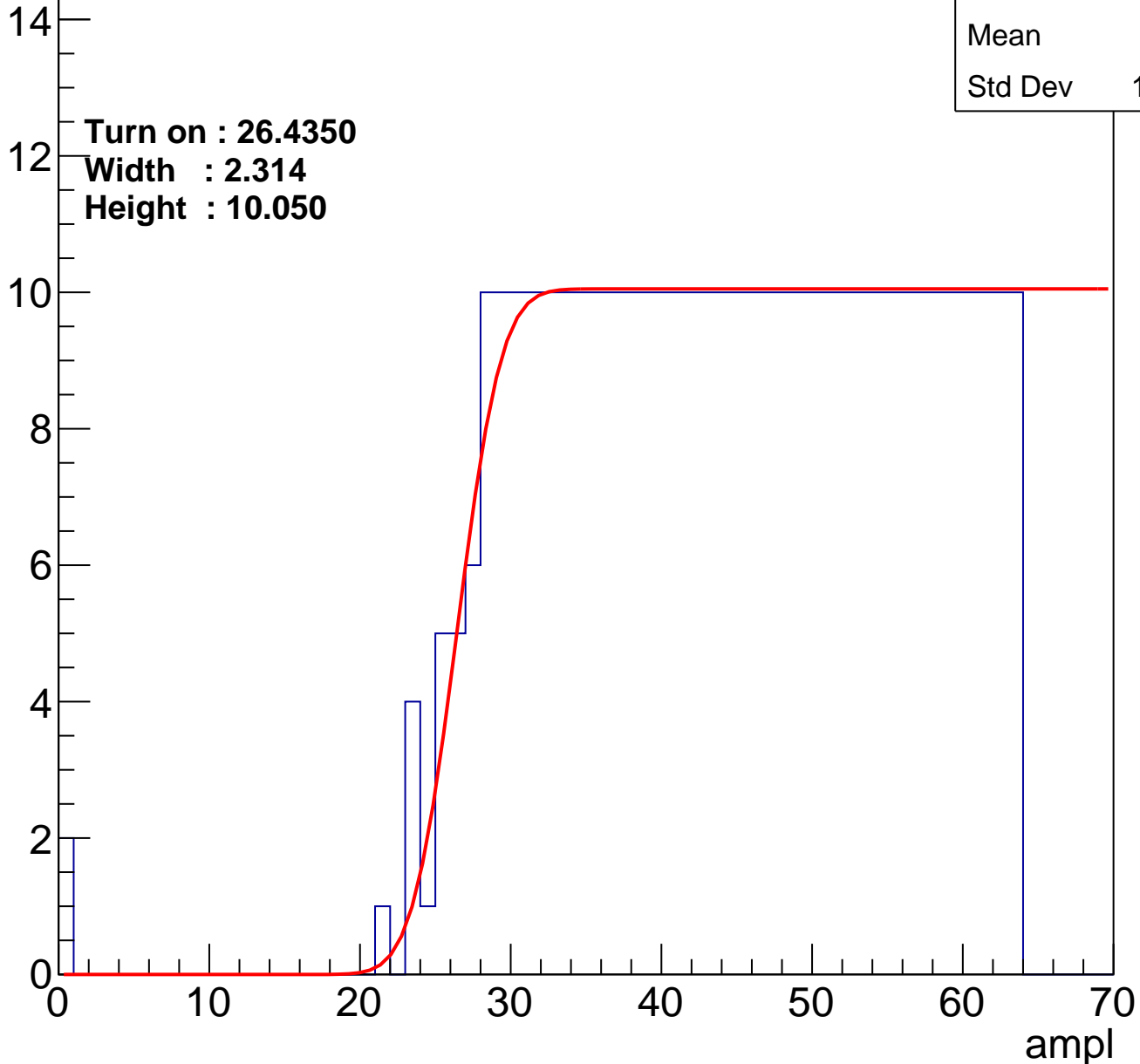
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 26.4350

Width : 2.314

Height : 10.050

Entry



B1L002S, U3-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 25.5997

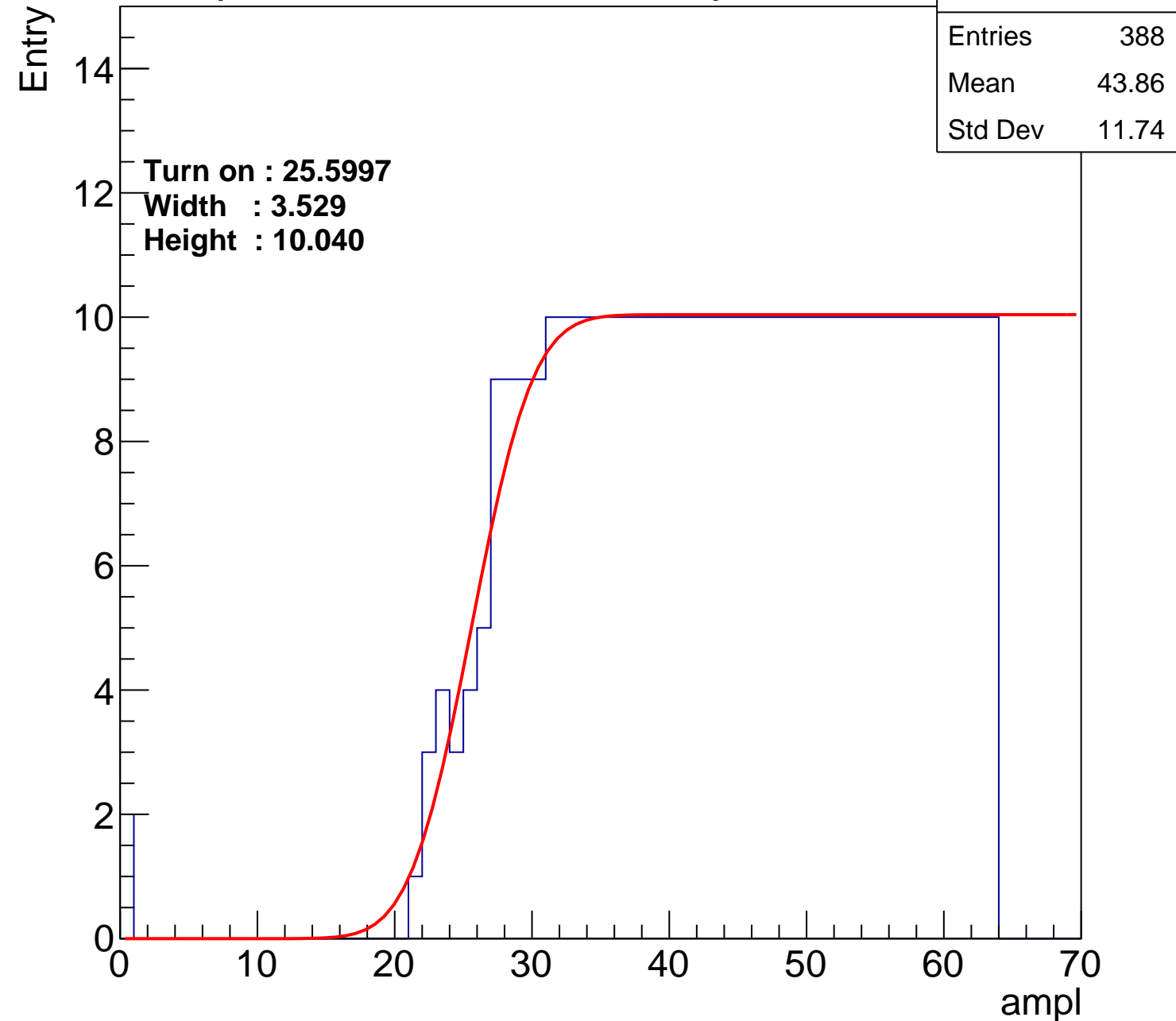
Width : 3.529

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.1830

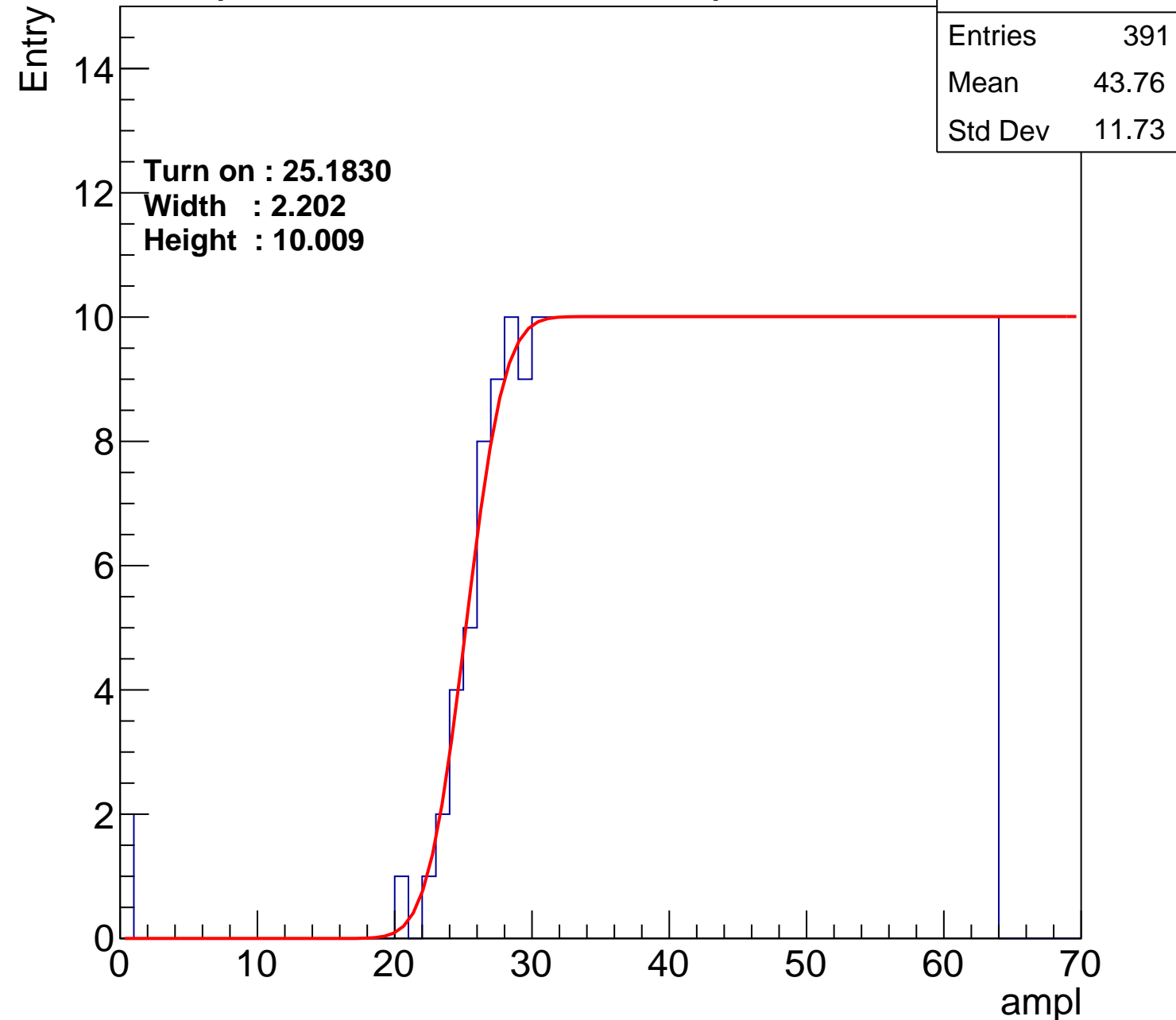
Width : 2.202

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.6387

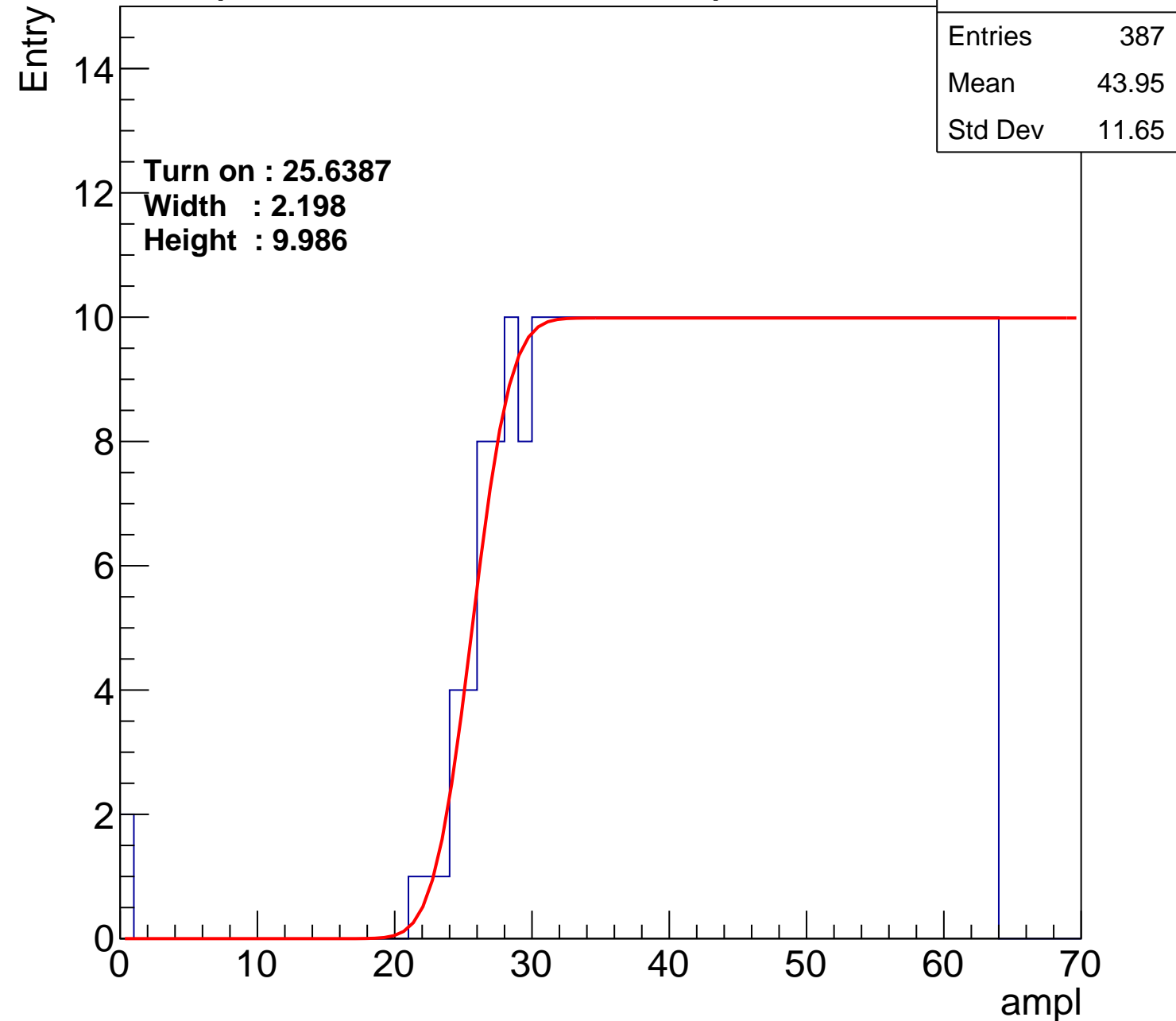
Width : 2.198

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.2
Std Dev	12.16

Turn on : 24.4959

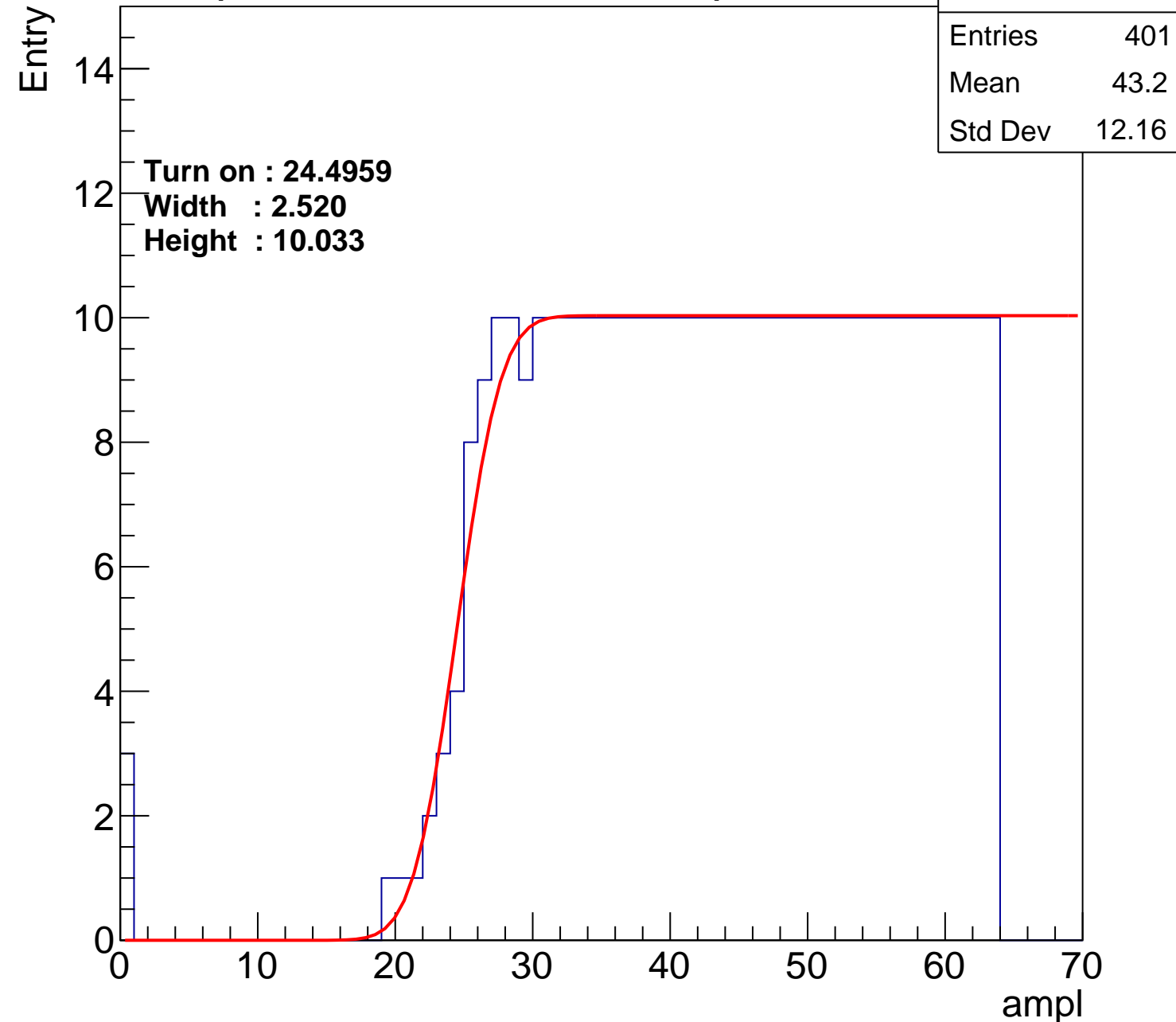
Width : 2.520

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.61
Std Dev	12.93

Turn on : 24.1690

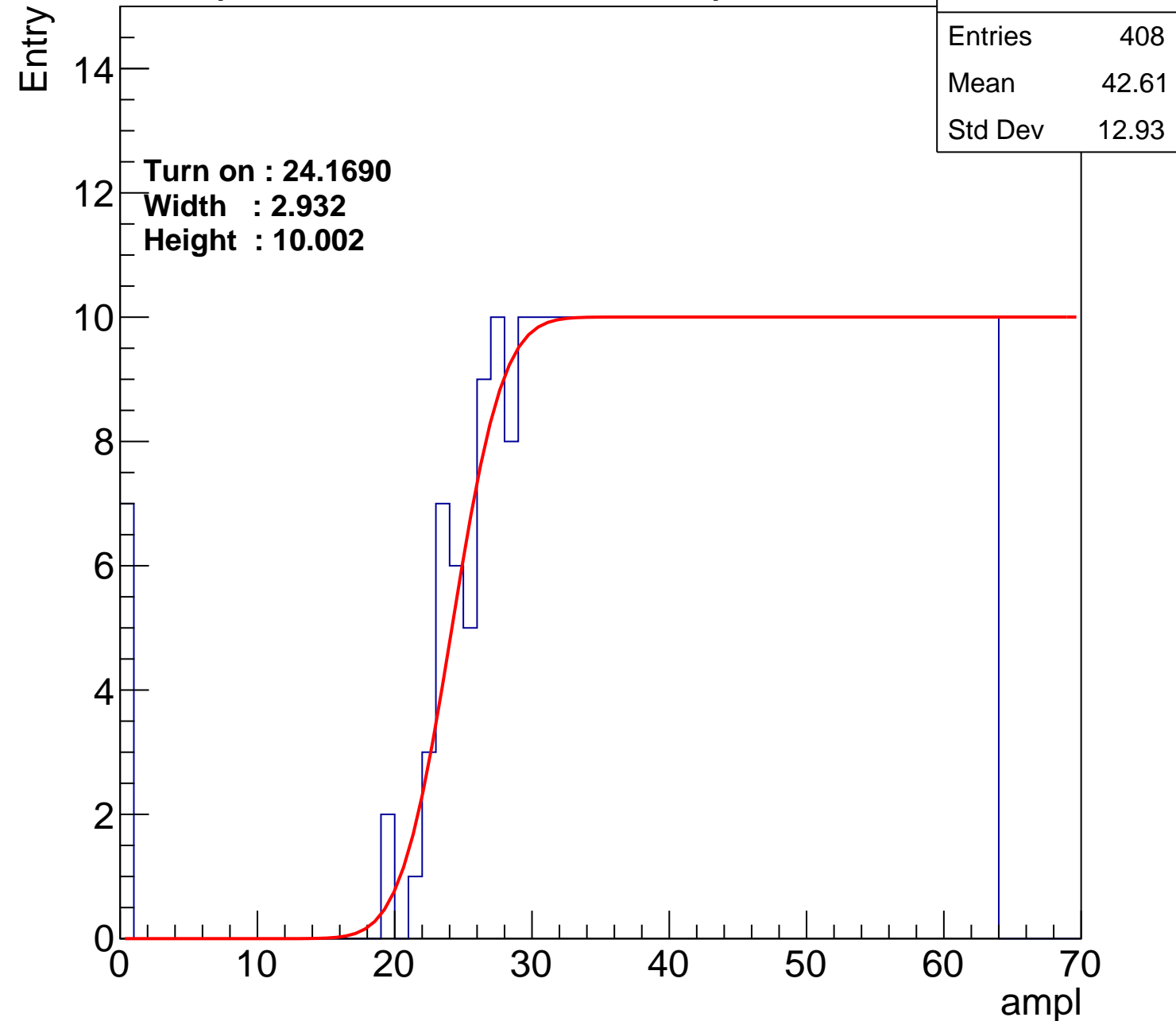
Width : 2.932

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch121

calib_packv5_042523_0143.root, FC#10, port B3

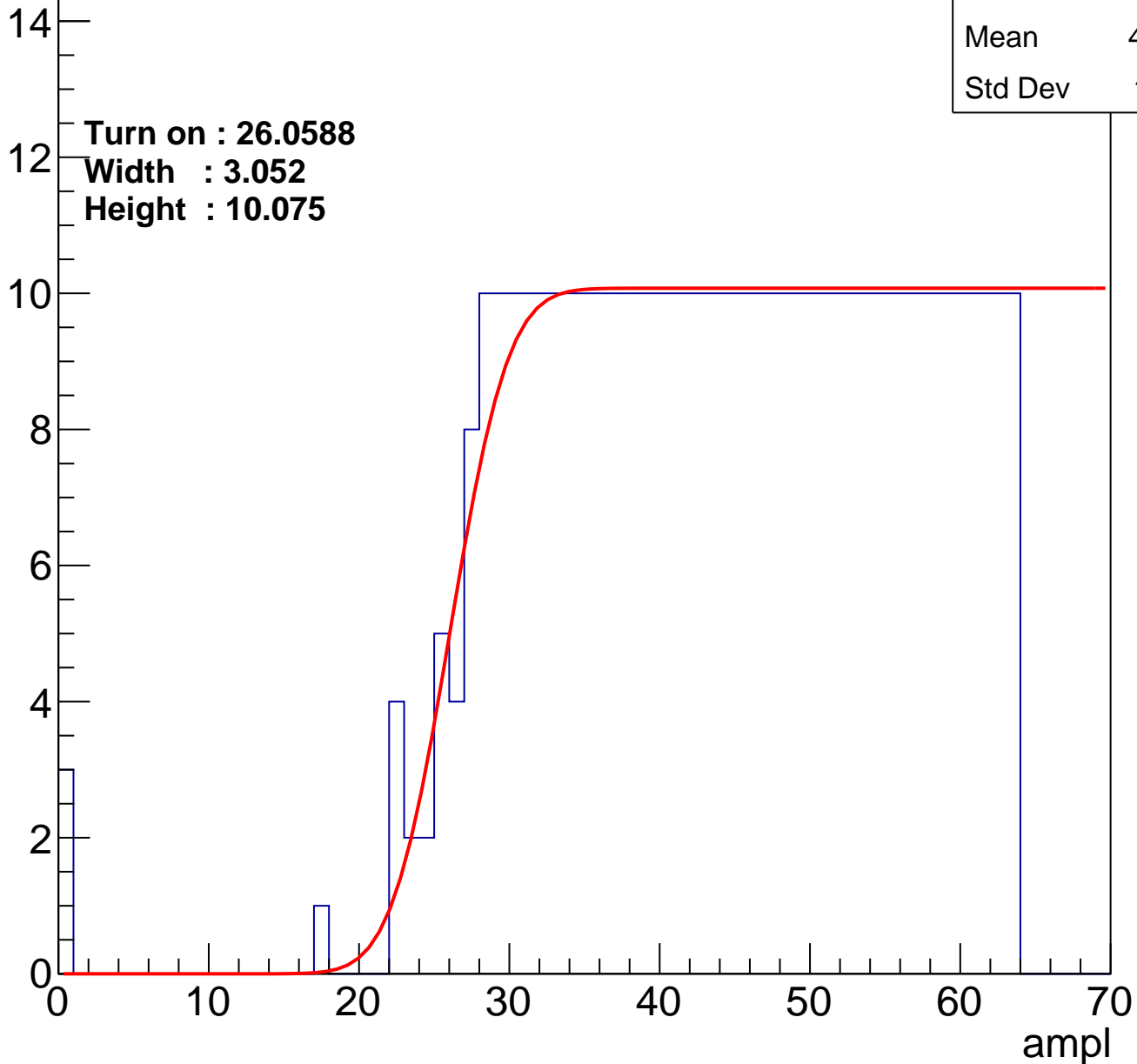
Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 26.0588

Width : 3.052

Height : 10.075

Entry



B1L002S, U3-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.67
Std Dev	12.52

Turn on : 23.3709

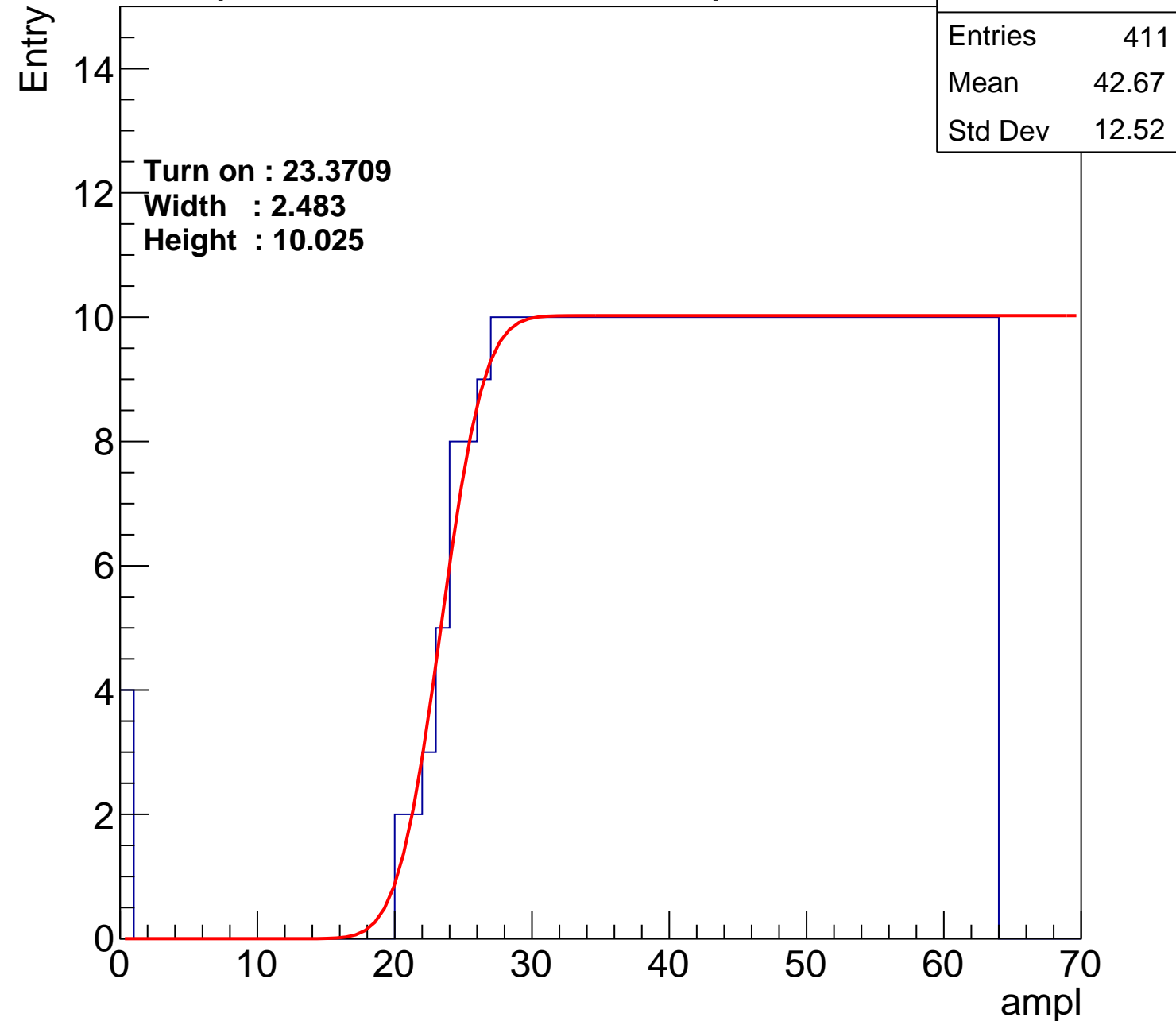
Width : 2.483

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.01
Std Dev	11.72

Turn on : 25.8710

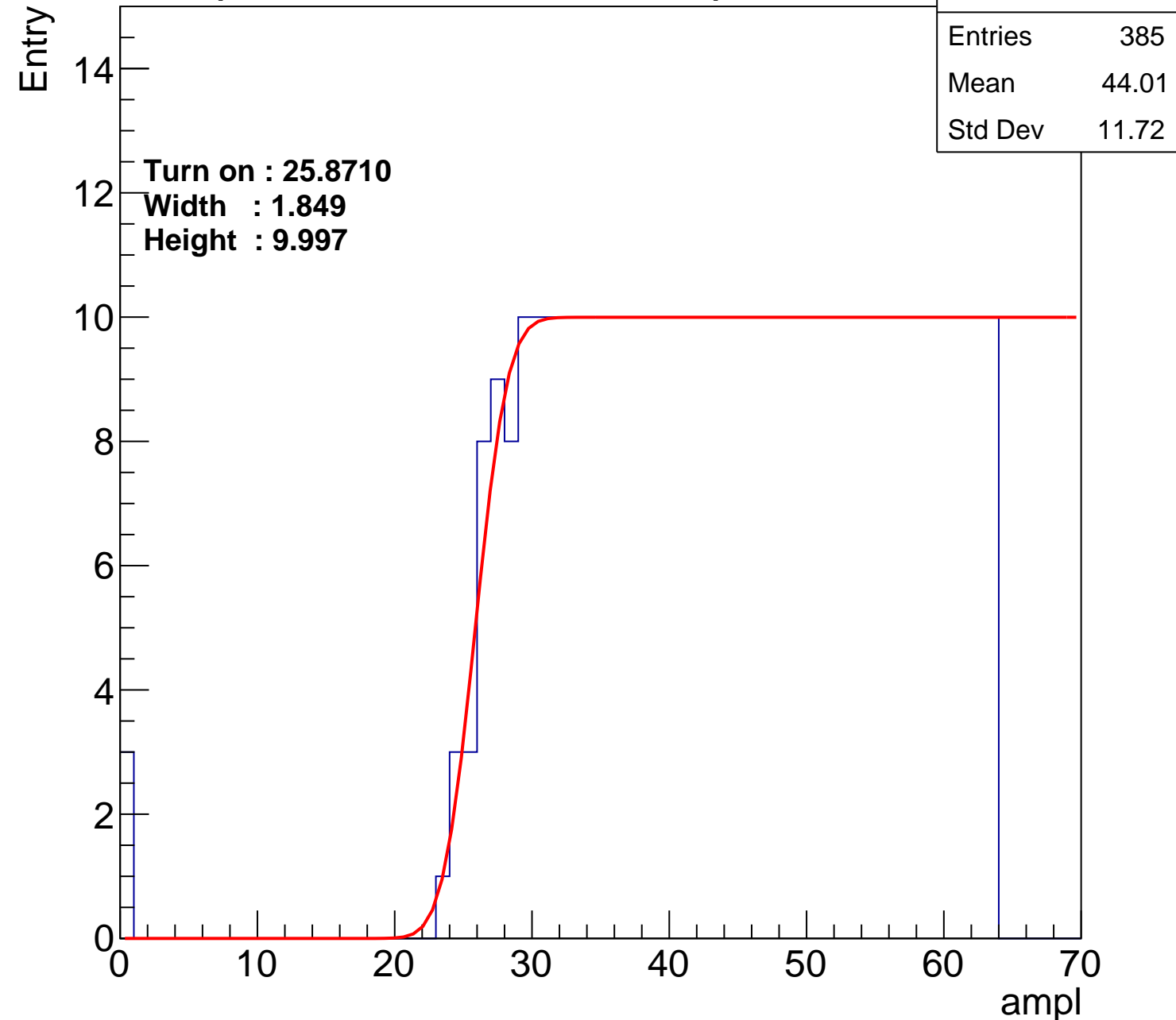
Width : 1.849

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.4241

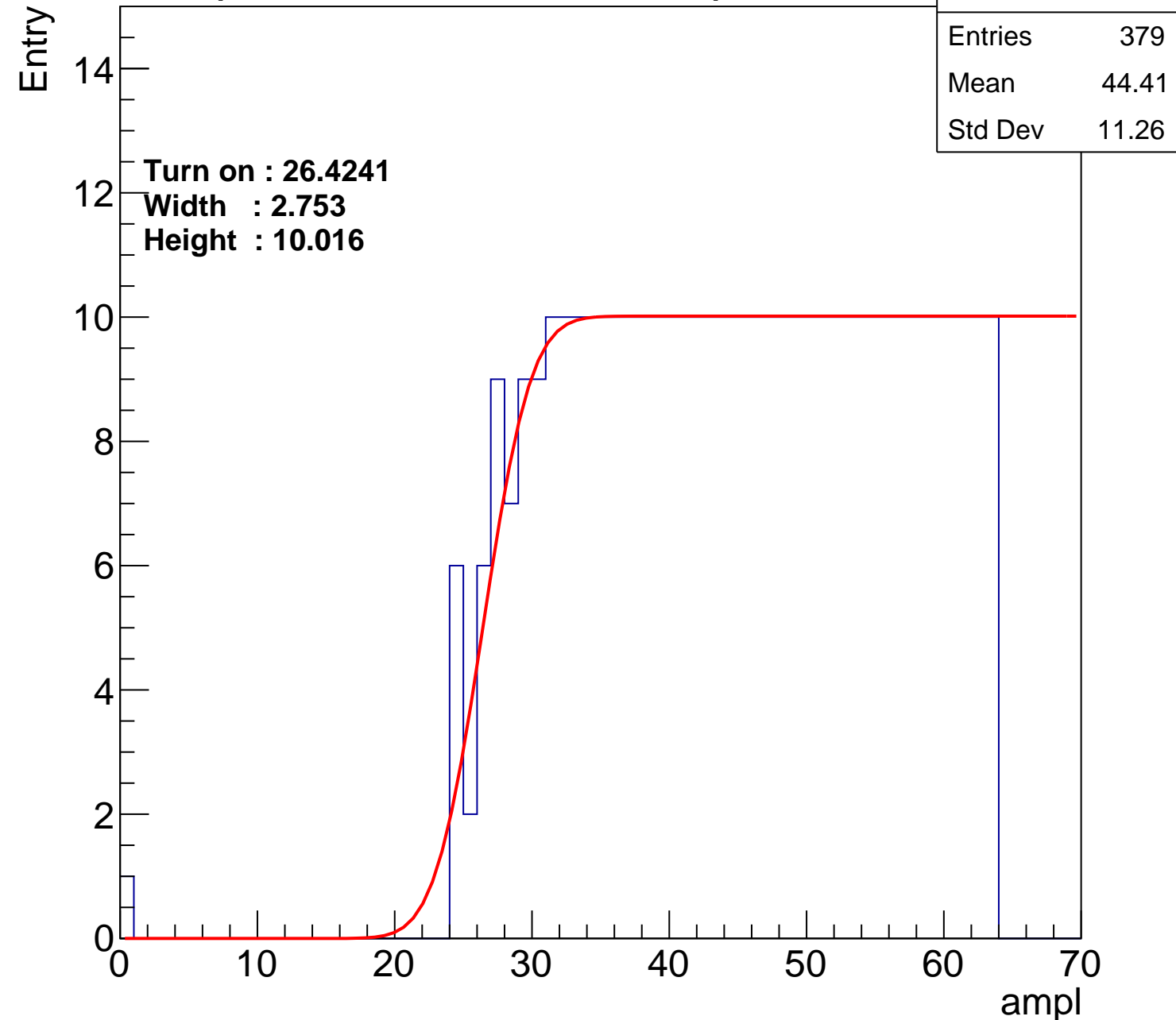
Width : 2.753

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.53
Std Dev	11.78

Turn on : 25.1807

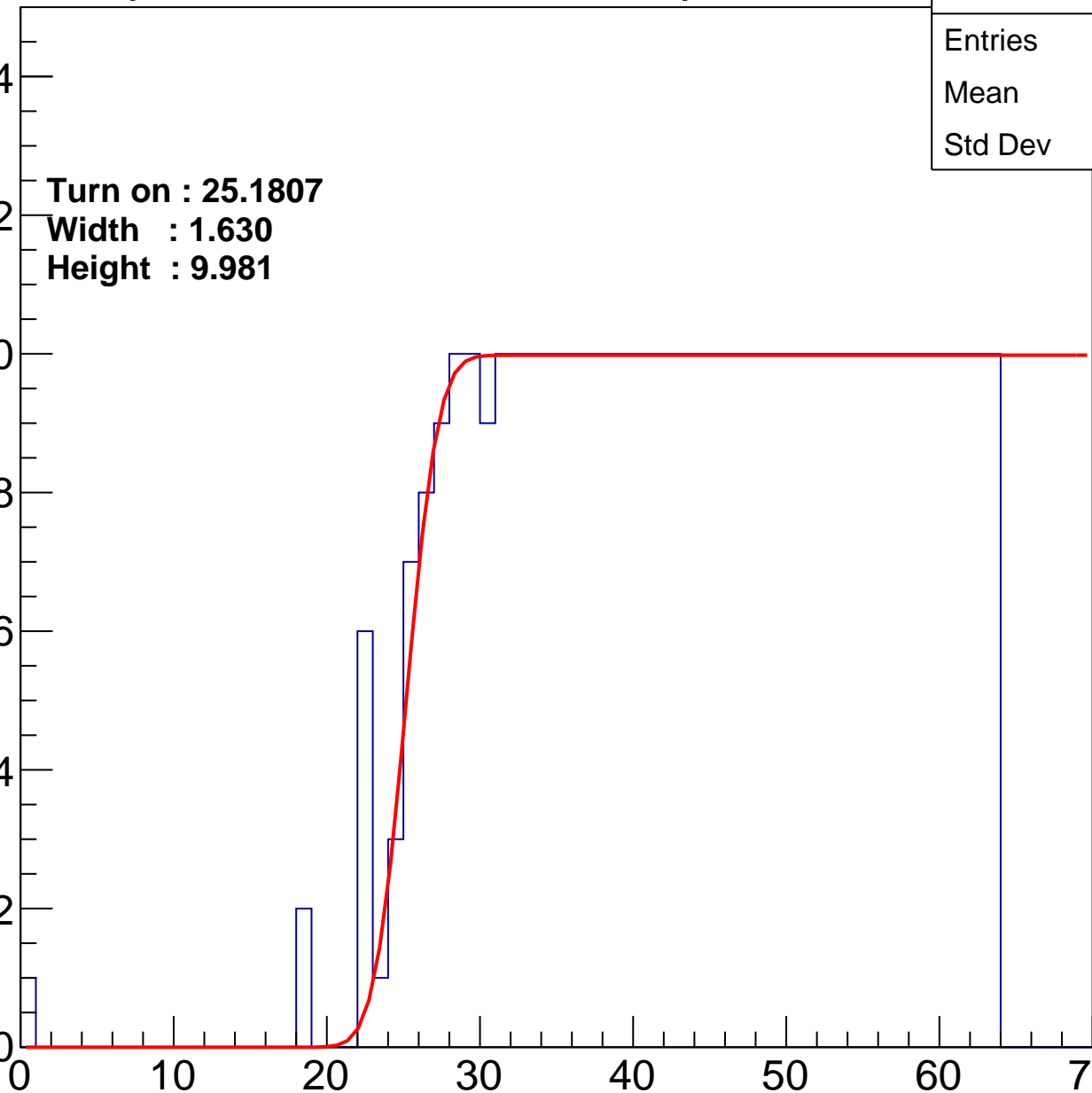
Width : 1.630

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.74
Std Dev	11.93

Turn on : 25.6686

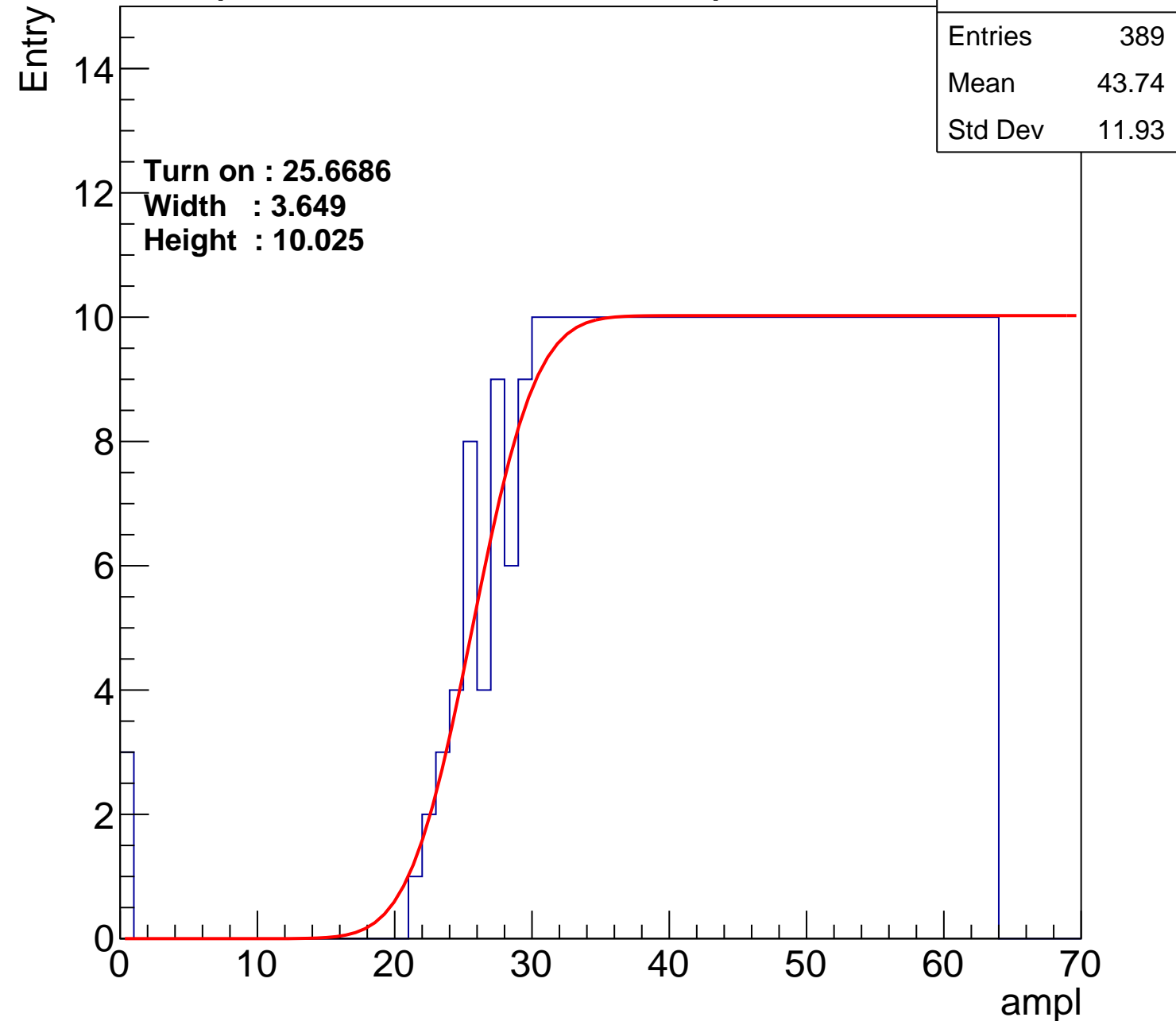
Width : 3.649

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U3-ch127

calib_packv5_042523_0143.root, FC#10, port B3

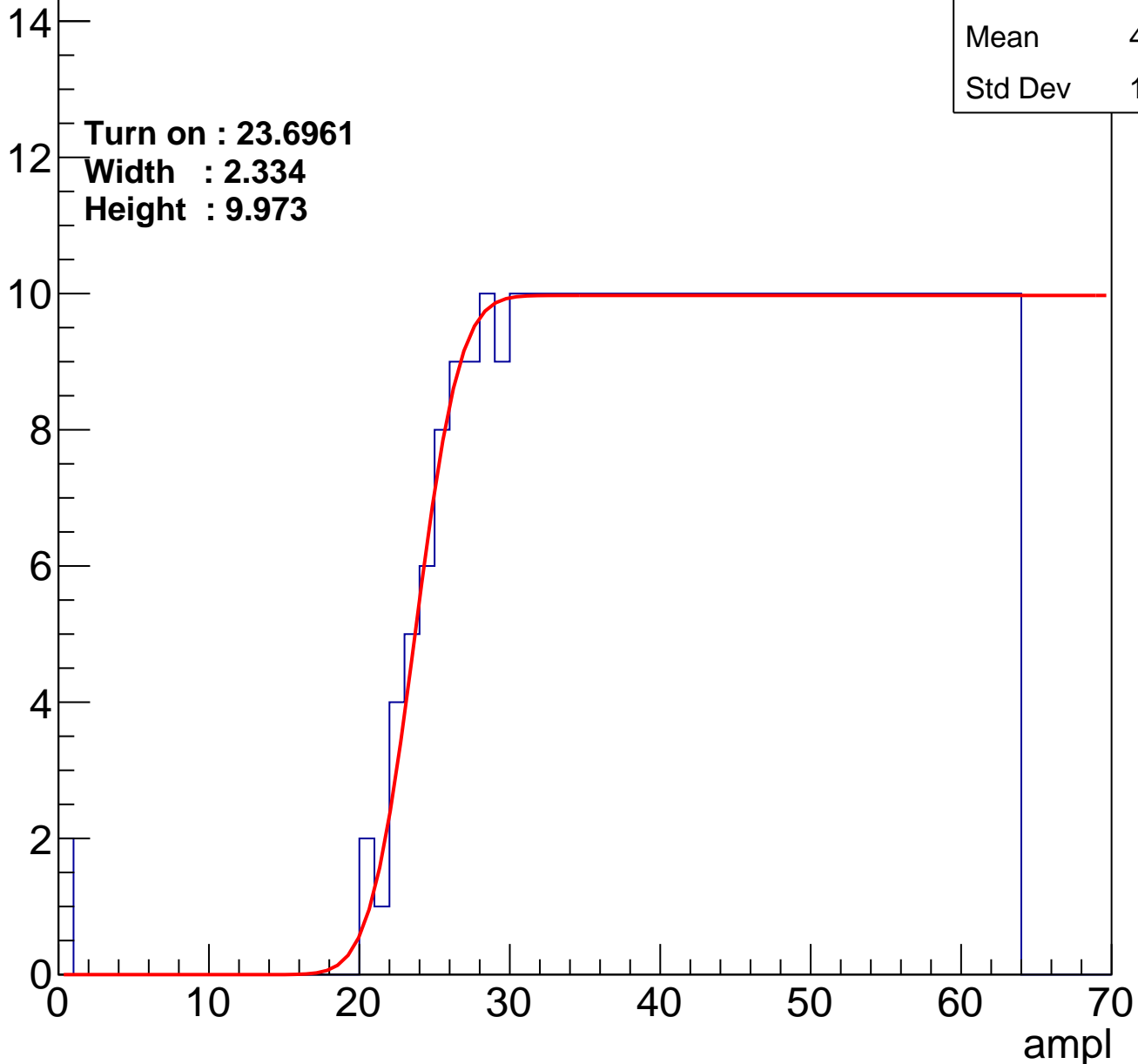
Entries	405
Mean	43.05
Std Dev	12.13

Turn on : 23.6961

Width : 2.334

Height : 9.973

Entry



B1L002S, U3-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.05
Std Dev	12.13

Turn on : 23.6961

Width : 2.334

Height : 9.973

Entry

