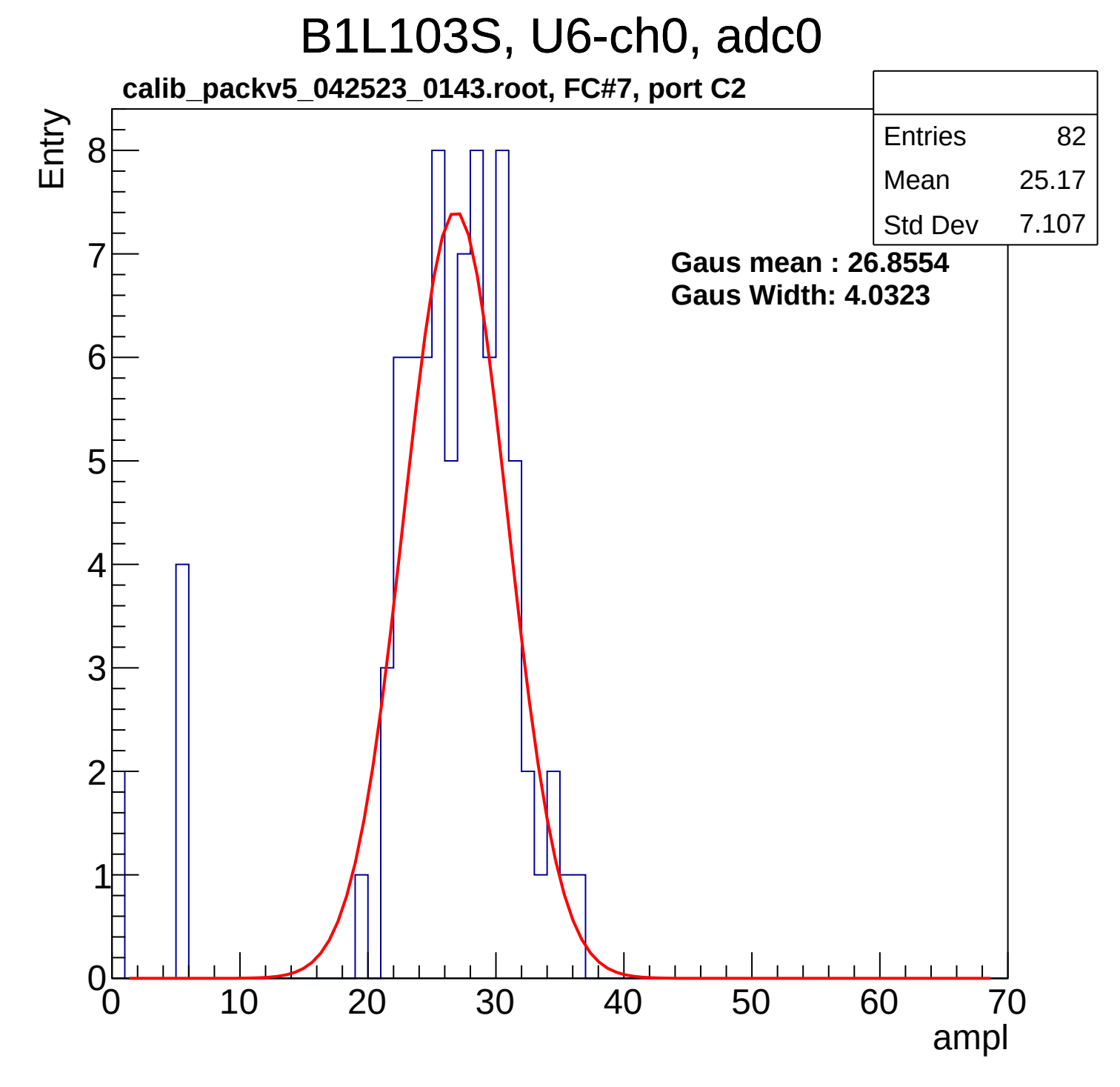
B1L103S, U6-ch0, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 25.17
Std Dev 7.107
Gaus mean : 26.8554
Gaus Width: 4.0323
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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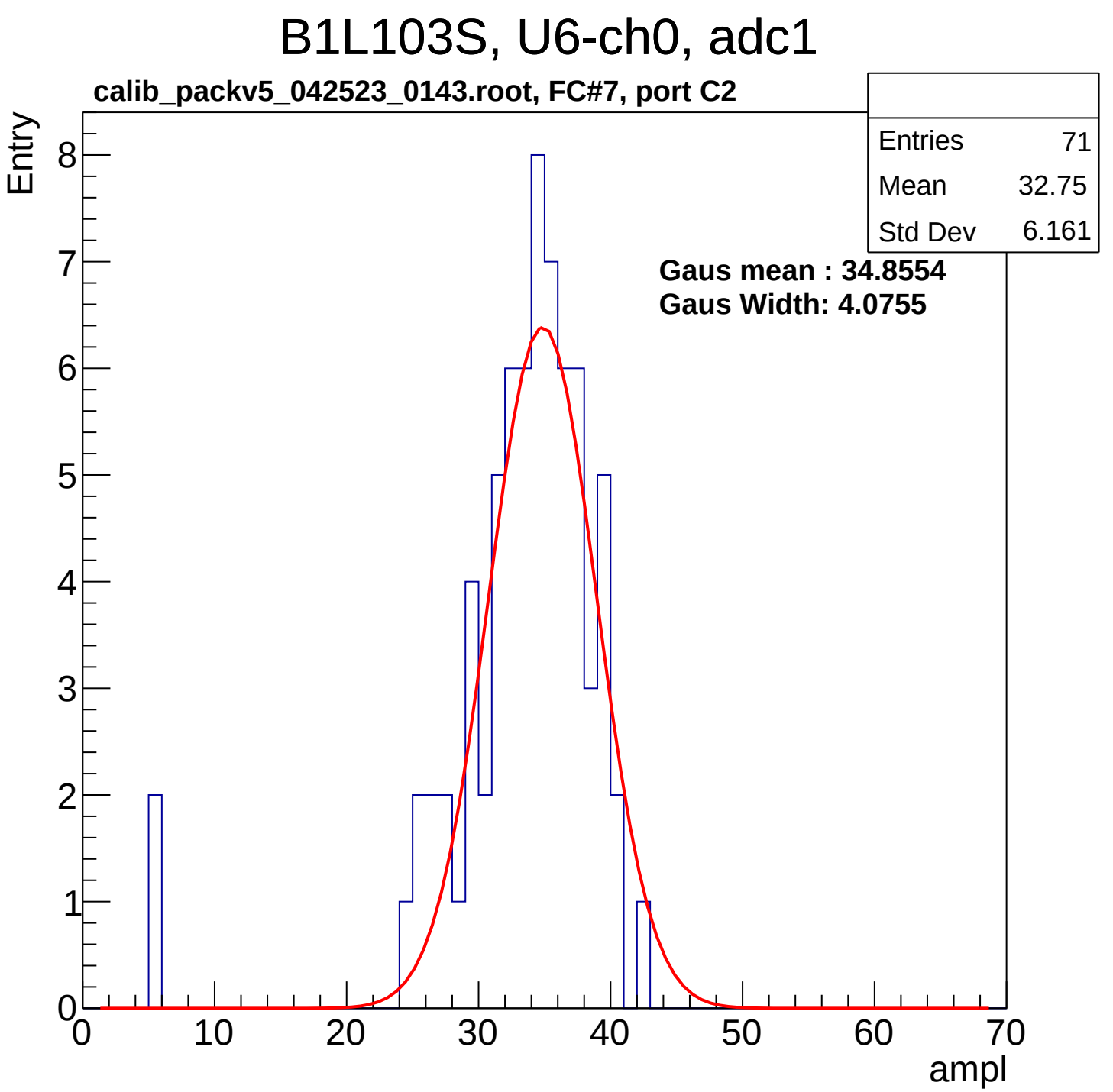
B1L103S, U6-ch0, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 32.75
Std Dev 6.161
Gaus mean : 34.8554
Gaus Width: 4.0755
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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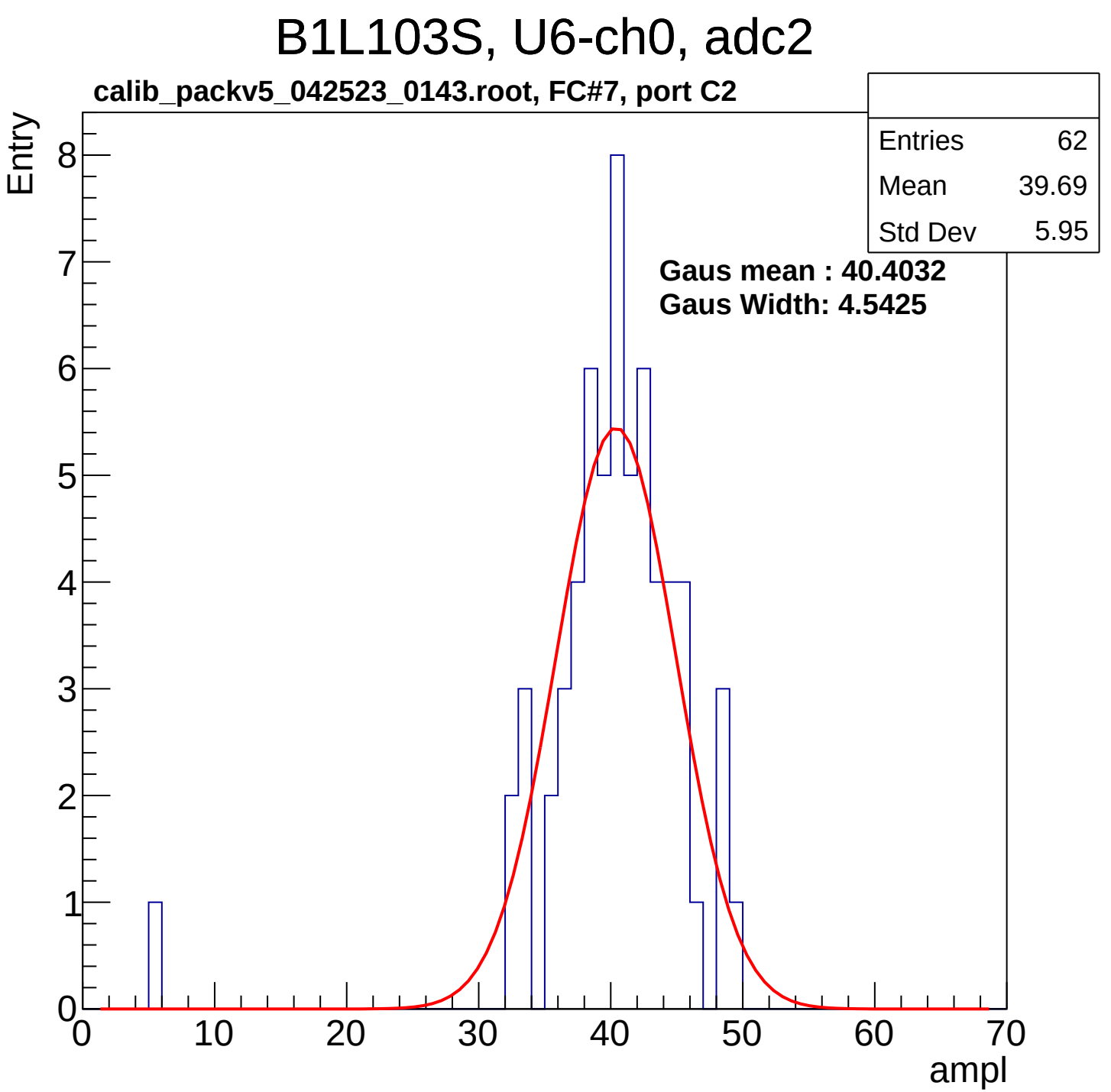
B1L103S, U6-ch0, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 39.69
Std Dev 5.95
Gaus mean : 40.4032
Gaus Width: 4.5425
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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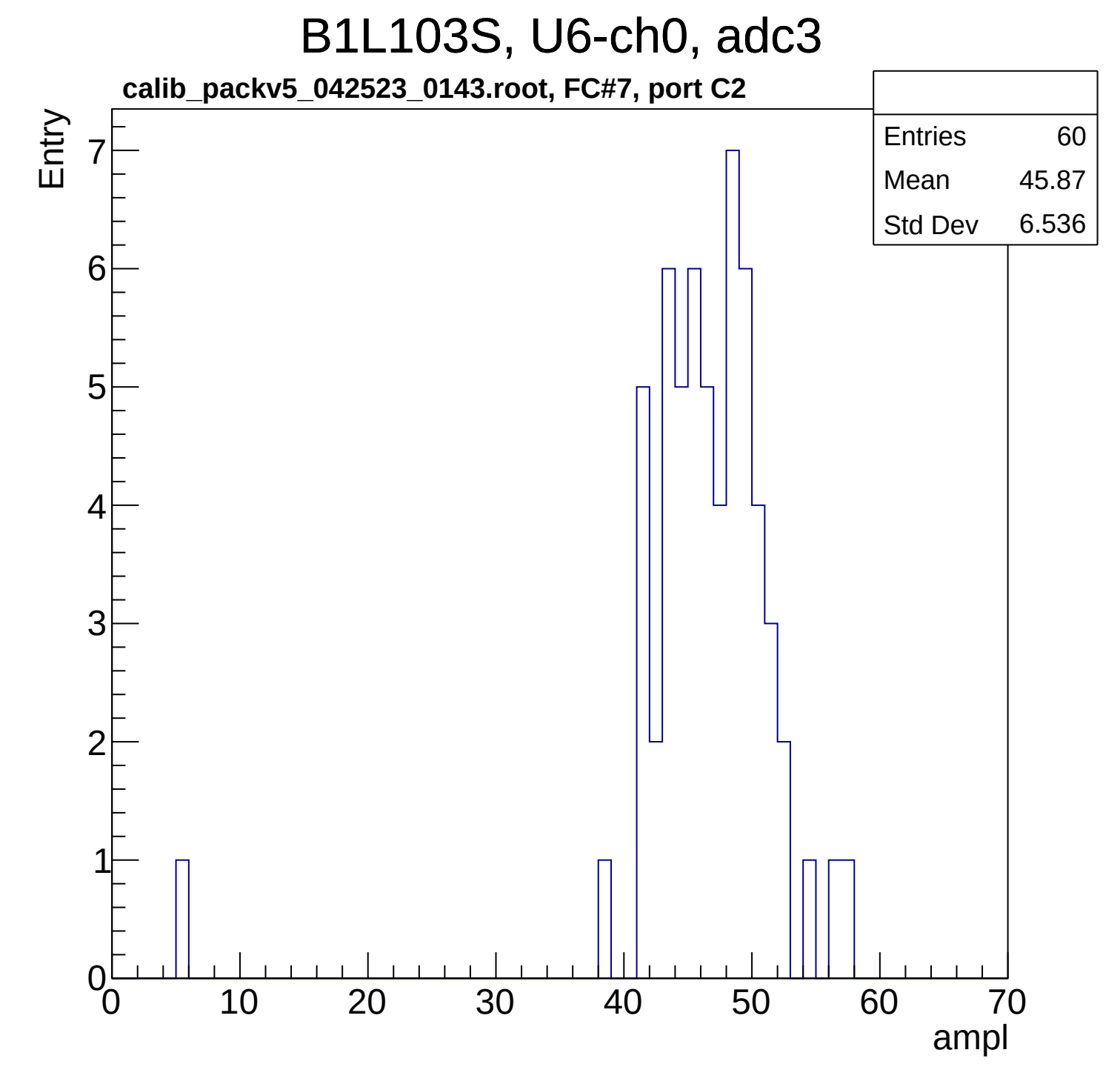
B1L103S, U6-ch0, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 45.87
Std Dev 6.536
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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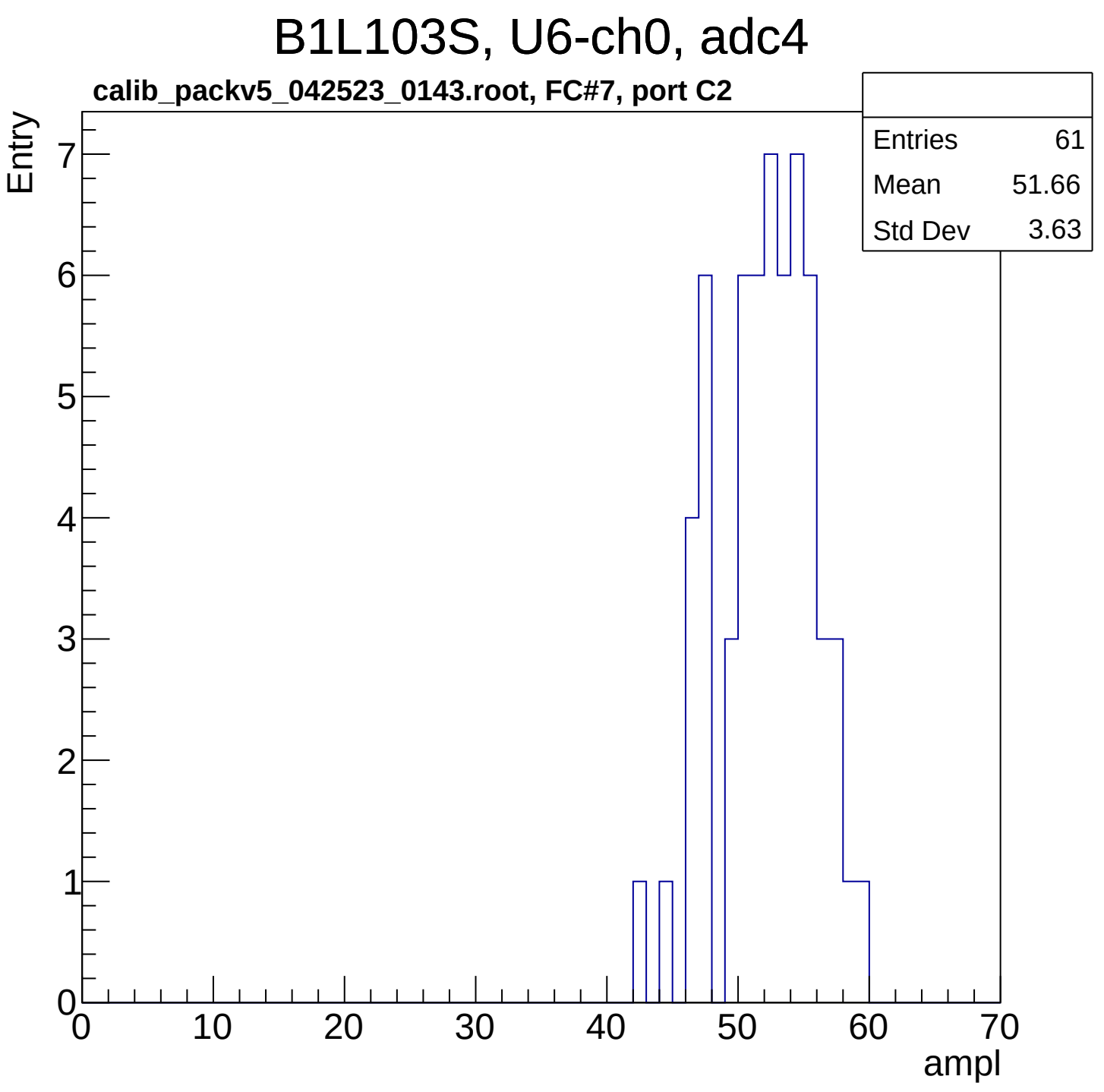
B1L103S, U6-ch0, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 51.66
Std Dev 3.63
Entry
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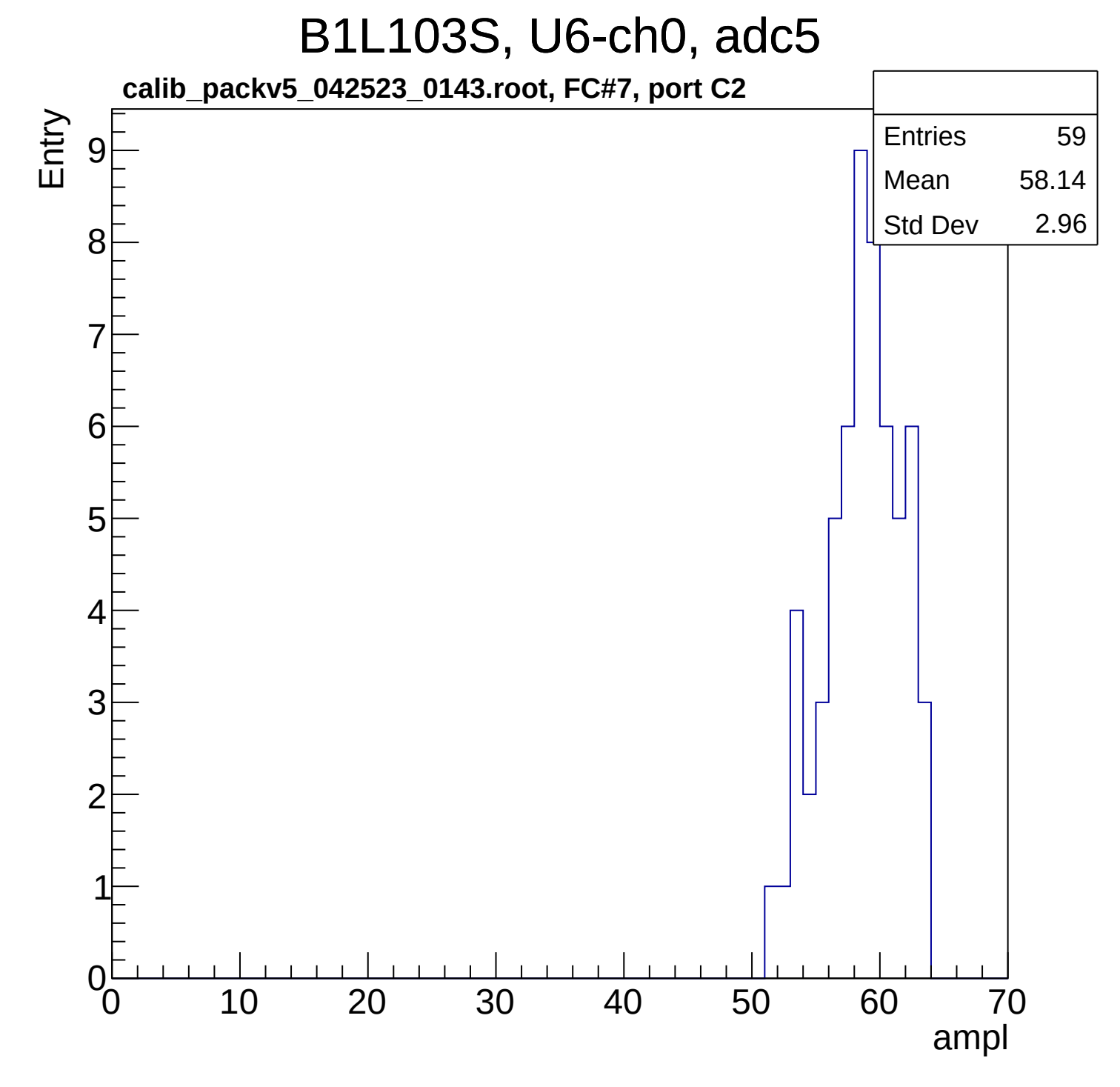
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch0, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.14
Std Dev 2.96
Entry
ampl
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3
4
5
6
7
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9
0
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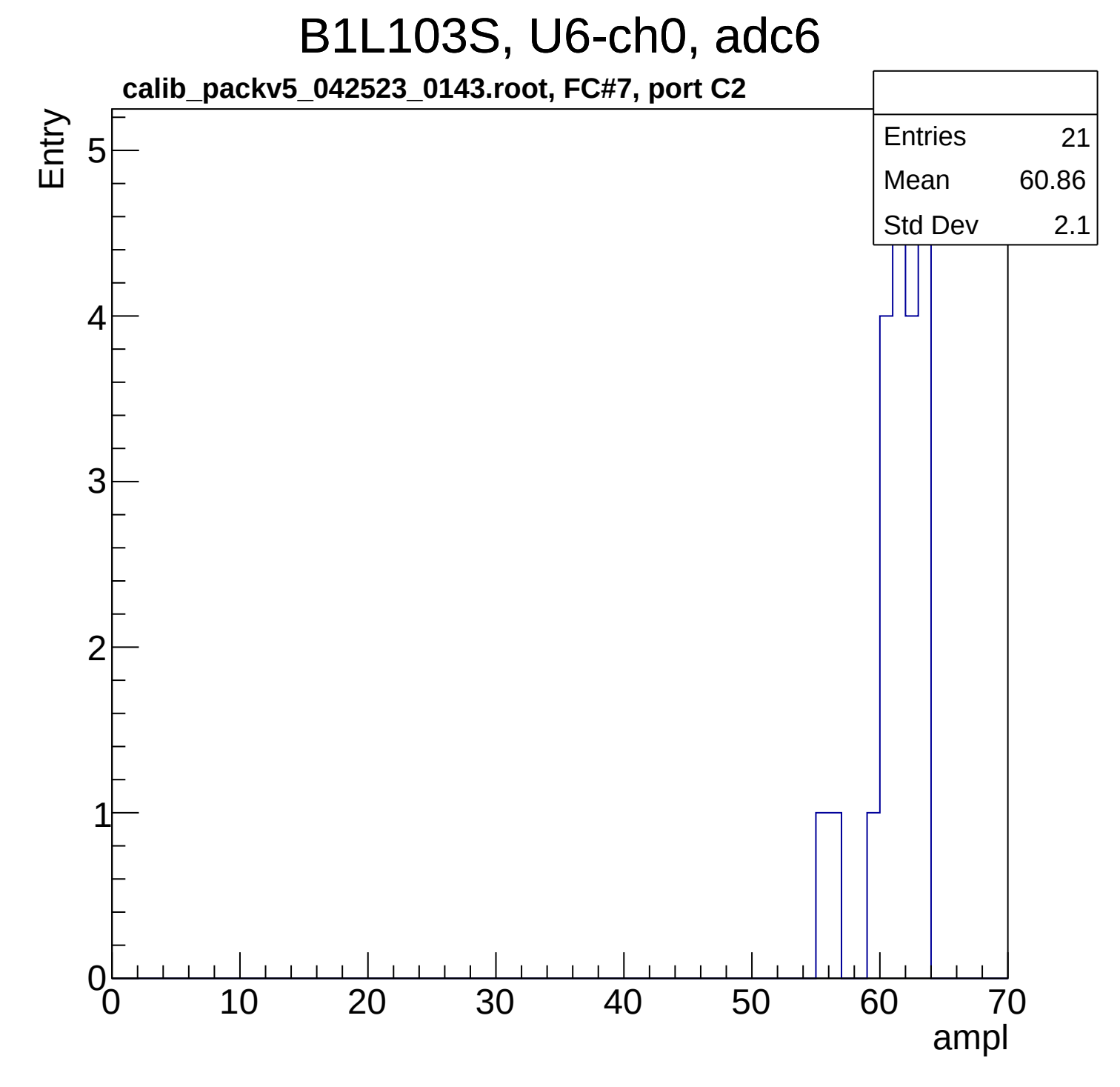
B1L103S, U6-ch0, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 60.86
Std Dev 2.1
Entry
ampl
0
1
2
3
4
5
0
10
20
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40
50
60
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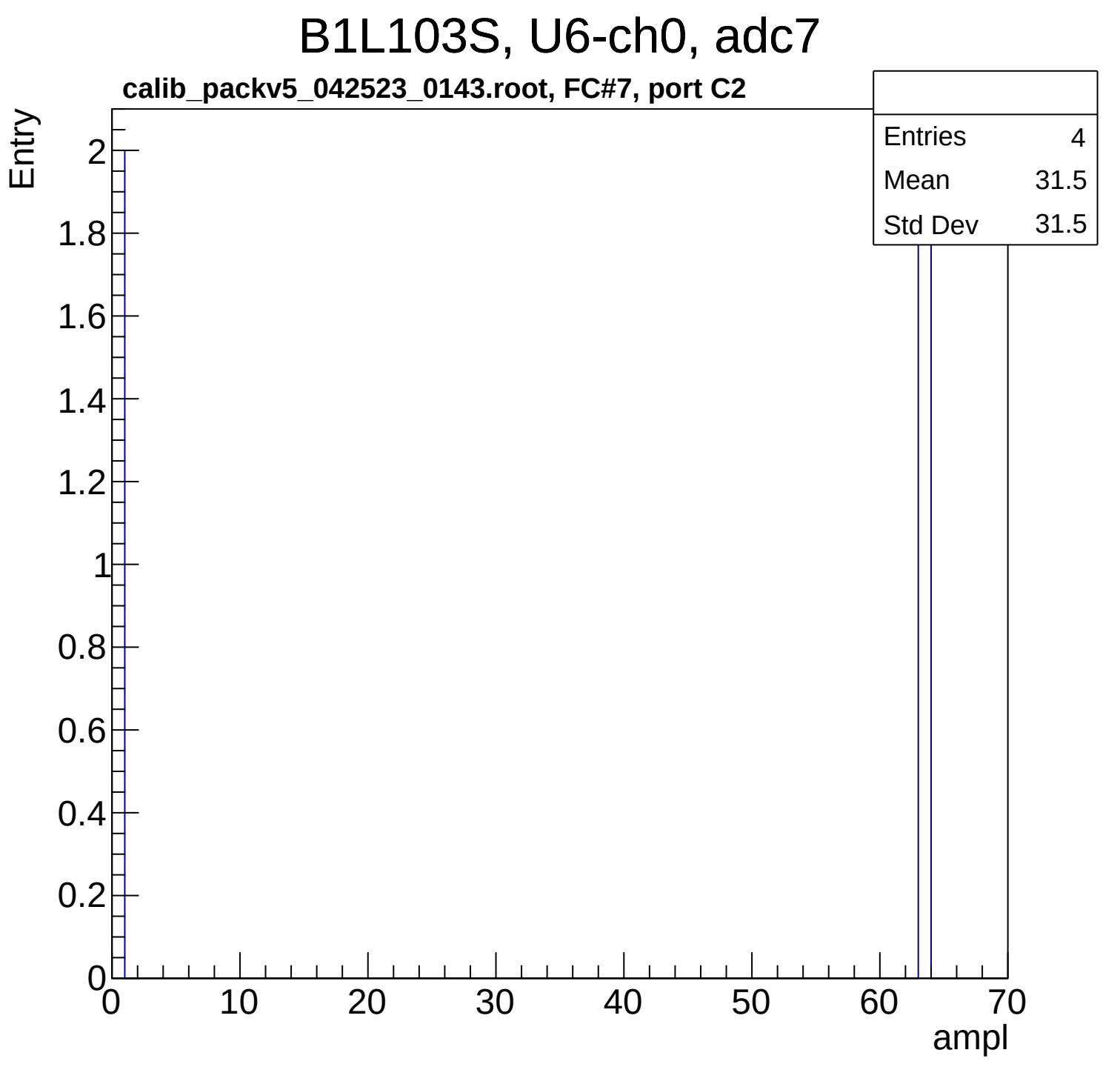
B1L103S, U6-ch0, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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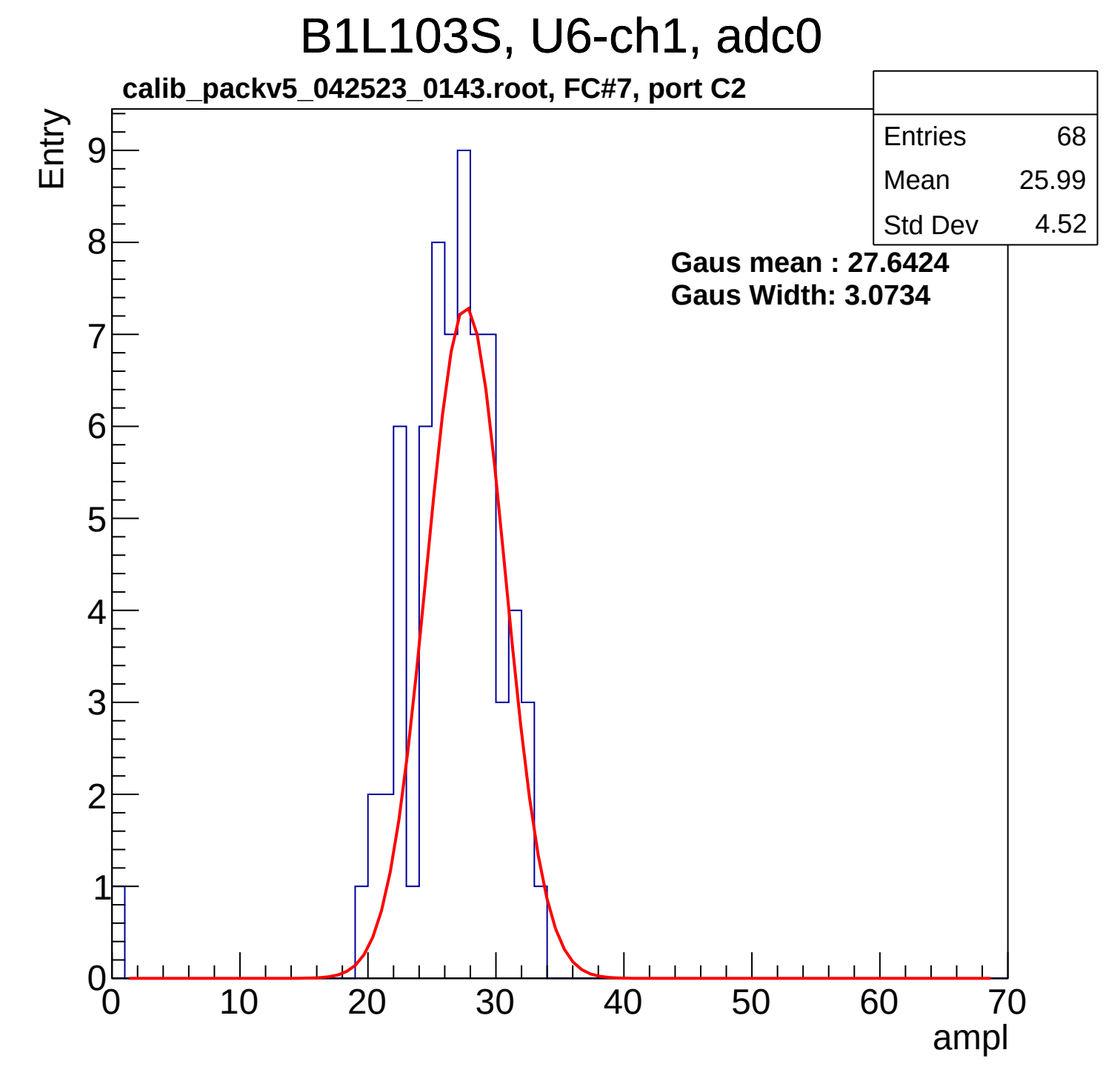
B1L103S, U6-ch1, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 25.99
Std Dev 4.52
Gaus mean : 27.6424
Gaus Width: 3.0734
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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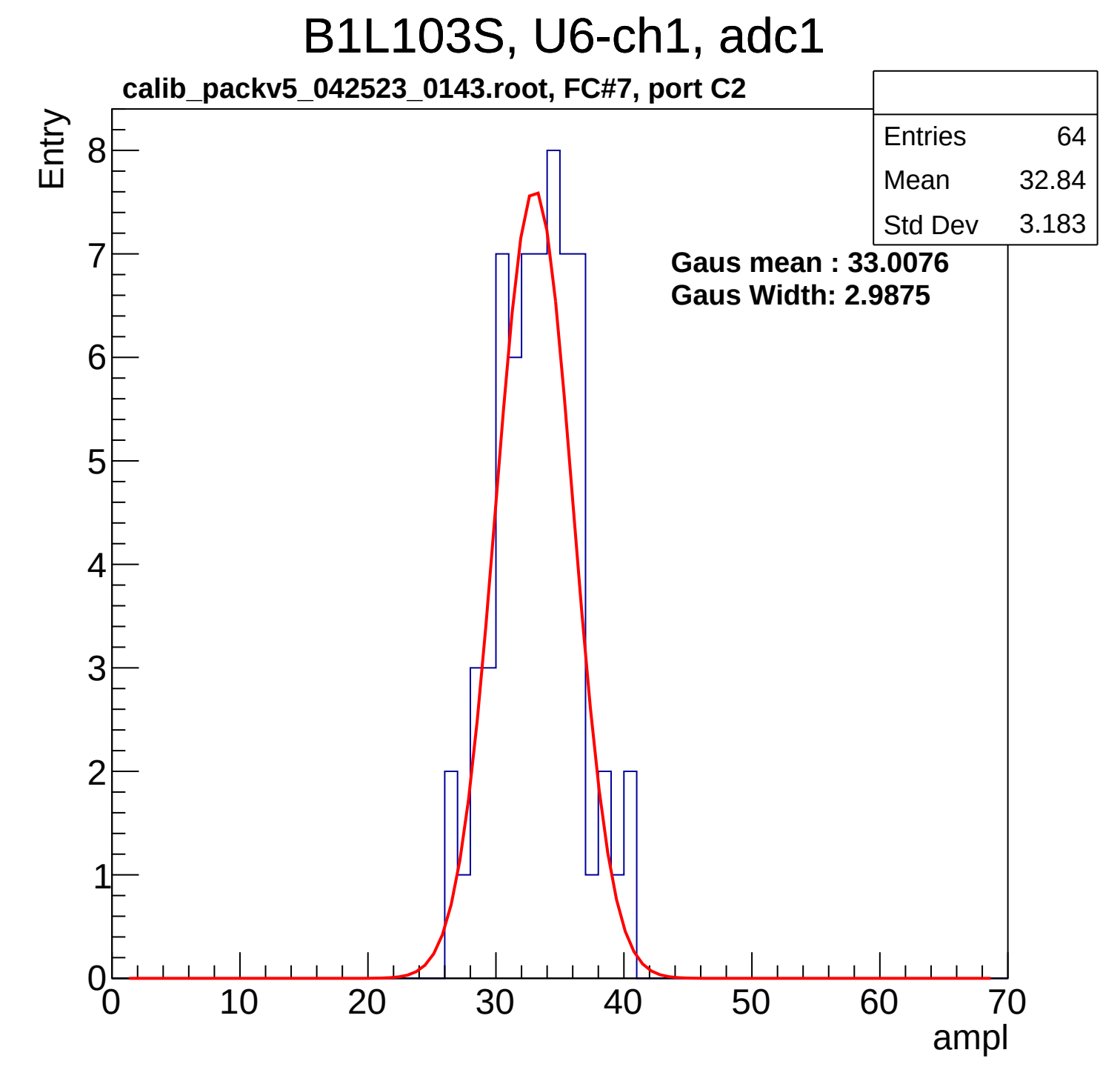
B1L103S, U6-ch1, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 32.84
Std Dev 3.183
Gaus mean : 33.0076
Gaus Width: 2.9875
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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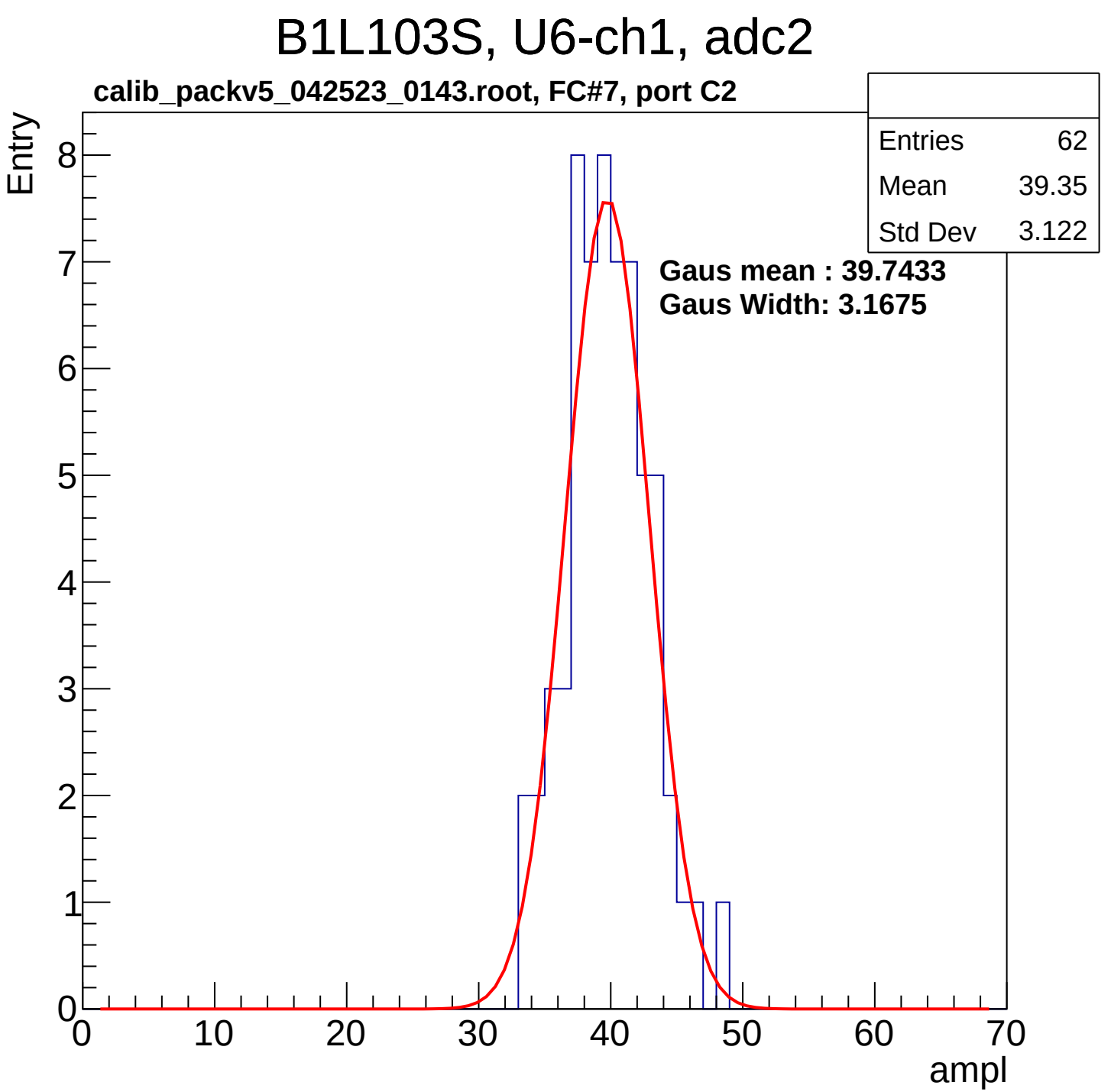
B1L103S, U6-ch1, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 39.35
Std Dev 3.122
Gaus mean : 39.7433
Gaus Width: 3.1675
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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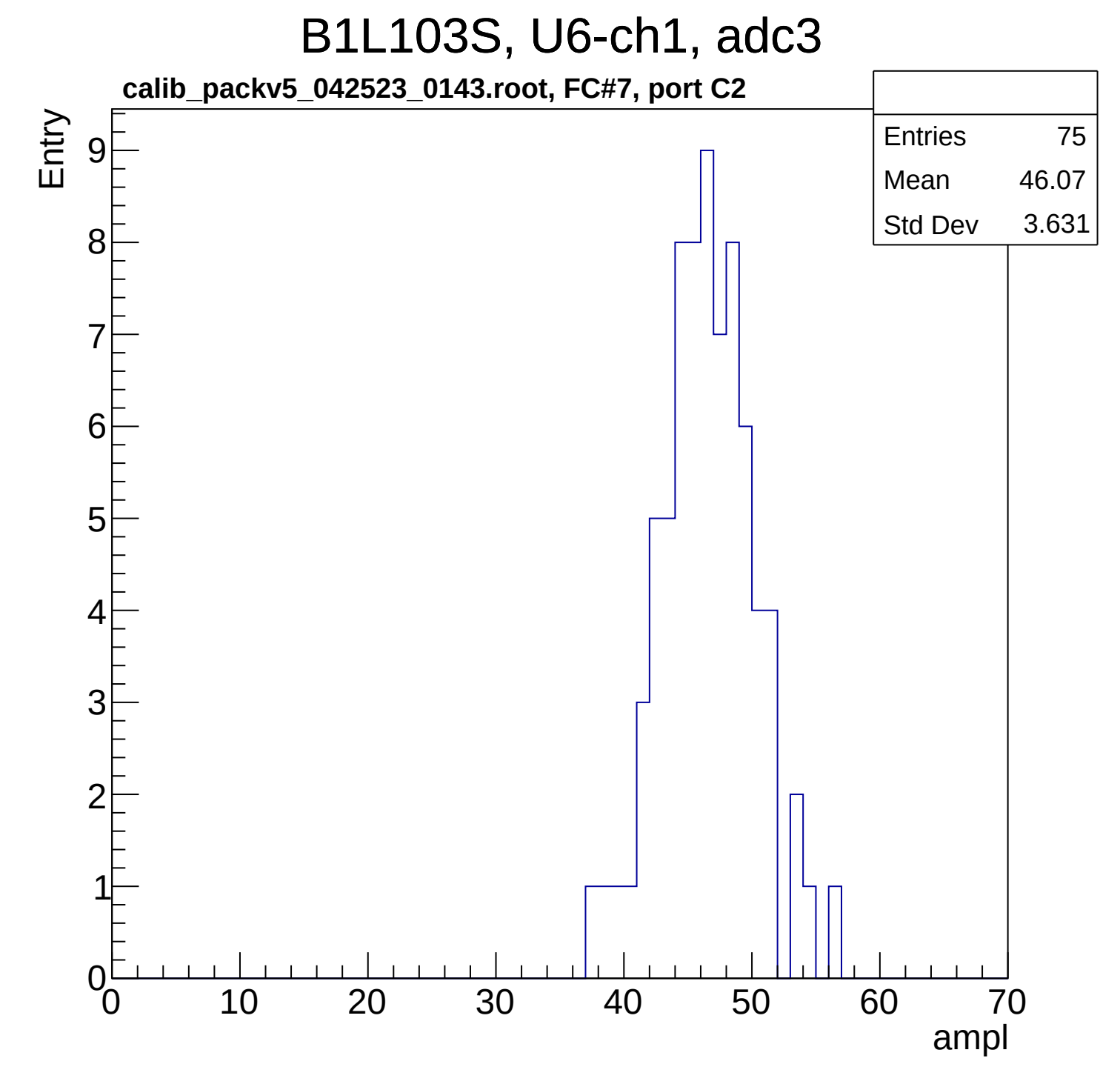
B1L103S, U6-ch1, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 46.07
Std Dev 3.631
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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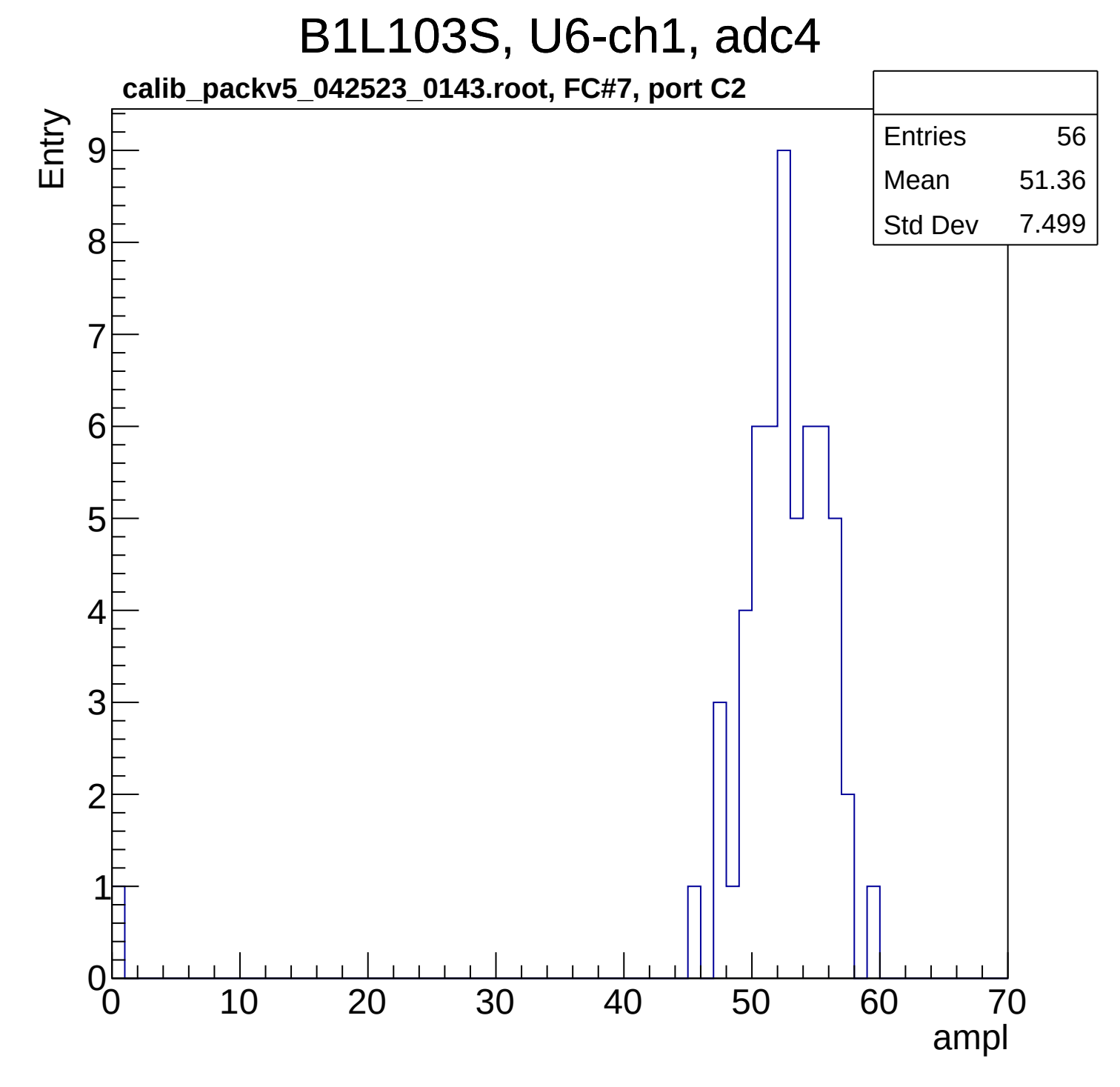
B1L103S, U6-ch1, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 51.36
Std Dev 7.499
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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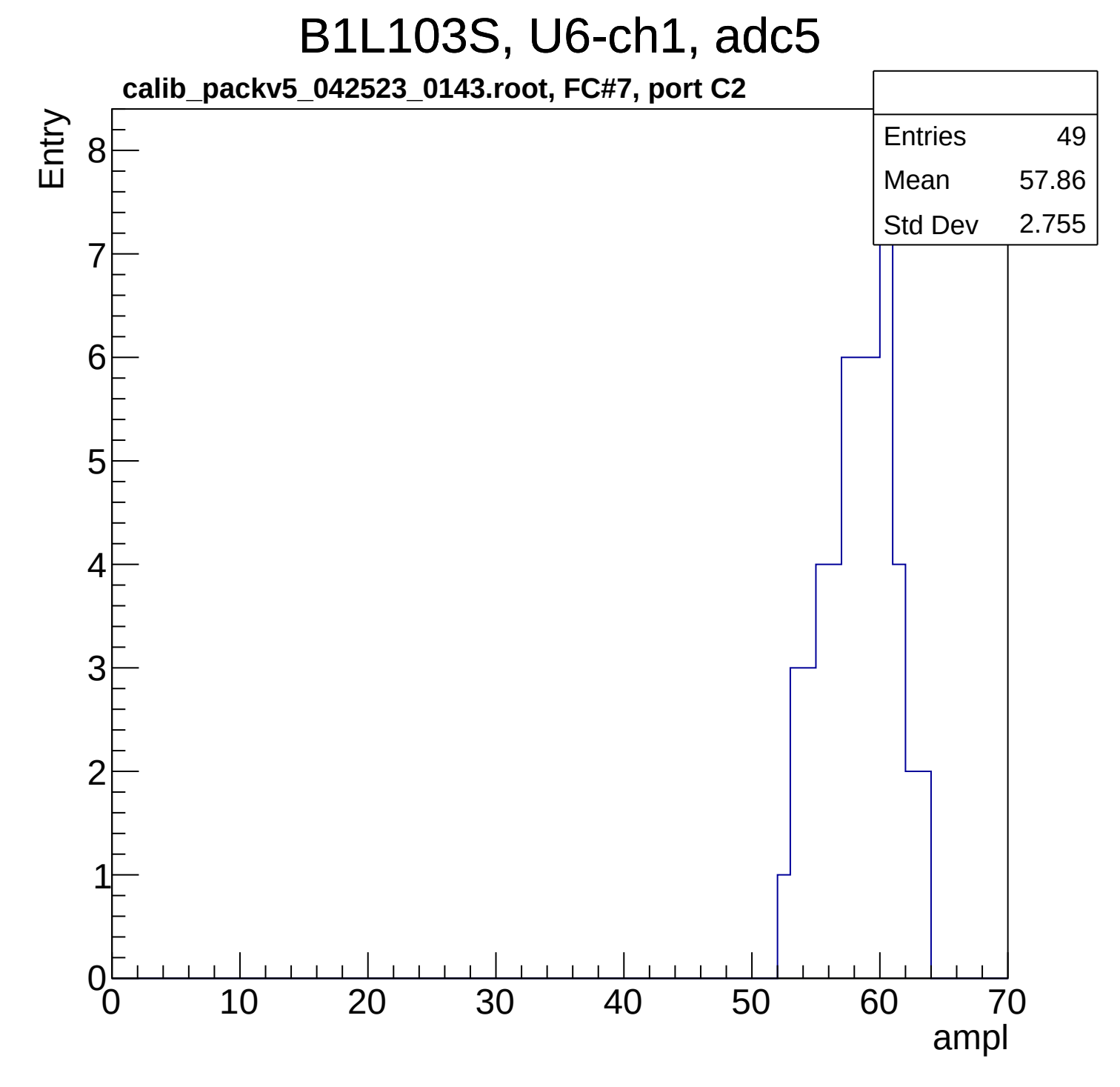
B1L103S, U6-ch1, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 57.86
Std Dev 2.755
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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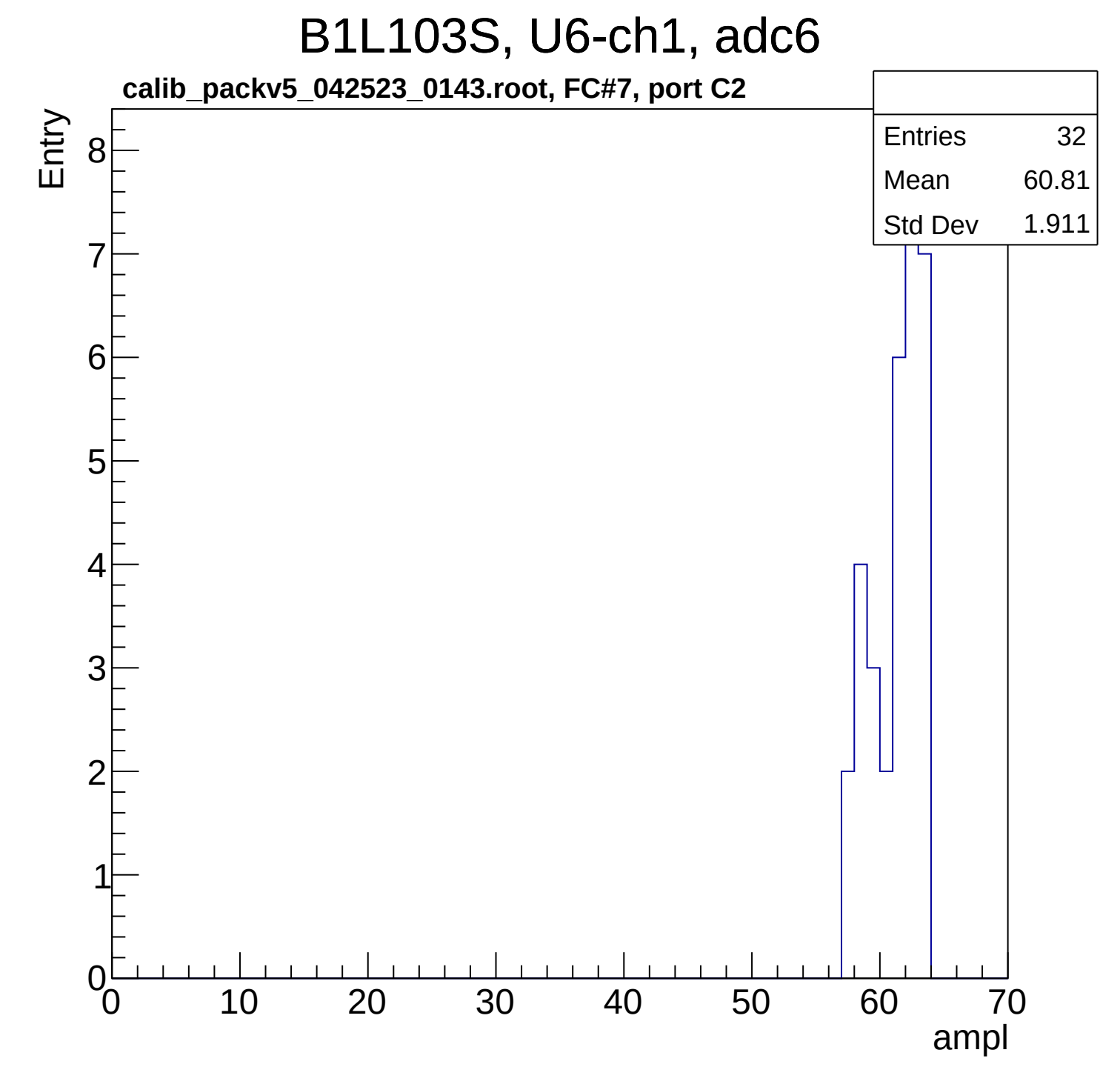
B1L103S, U6-ch1, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 60.81
Std Dev 1.911
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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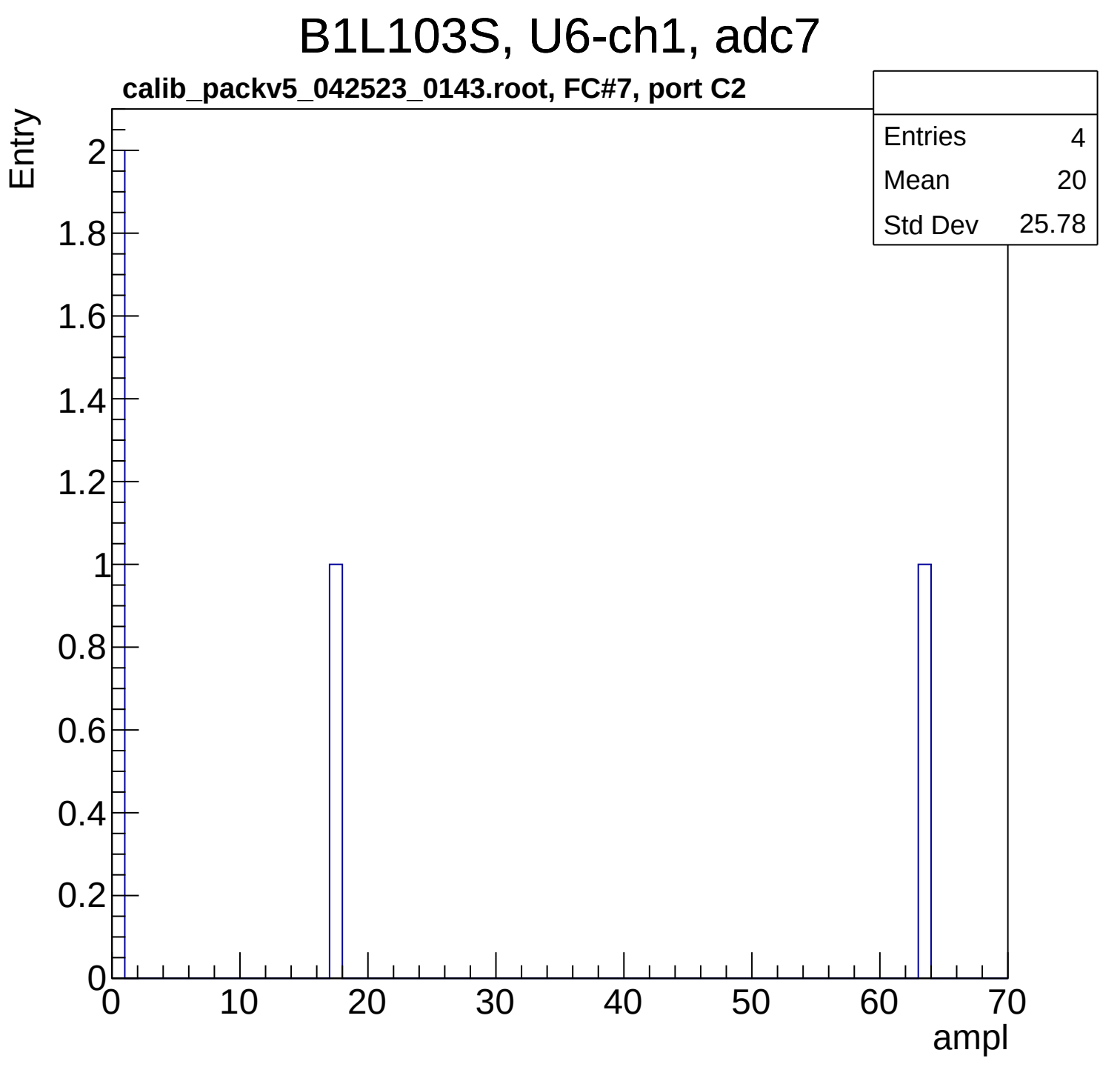
B1L103S, U6-ch1, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 20
Std Dev 25.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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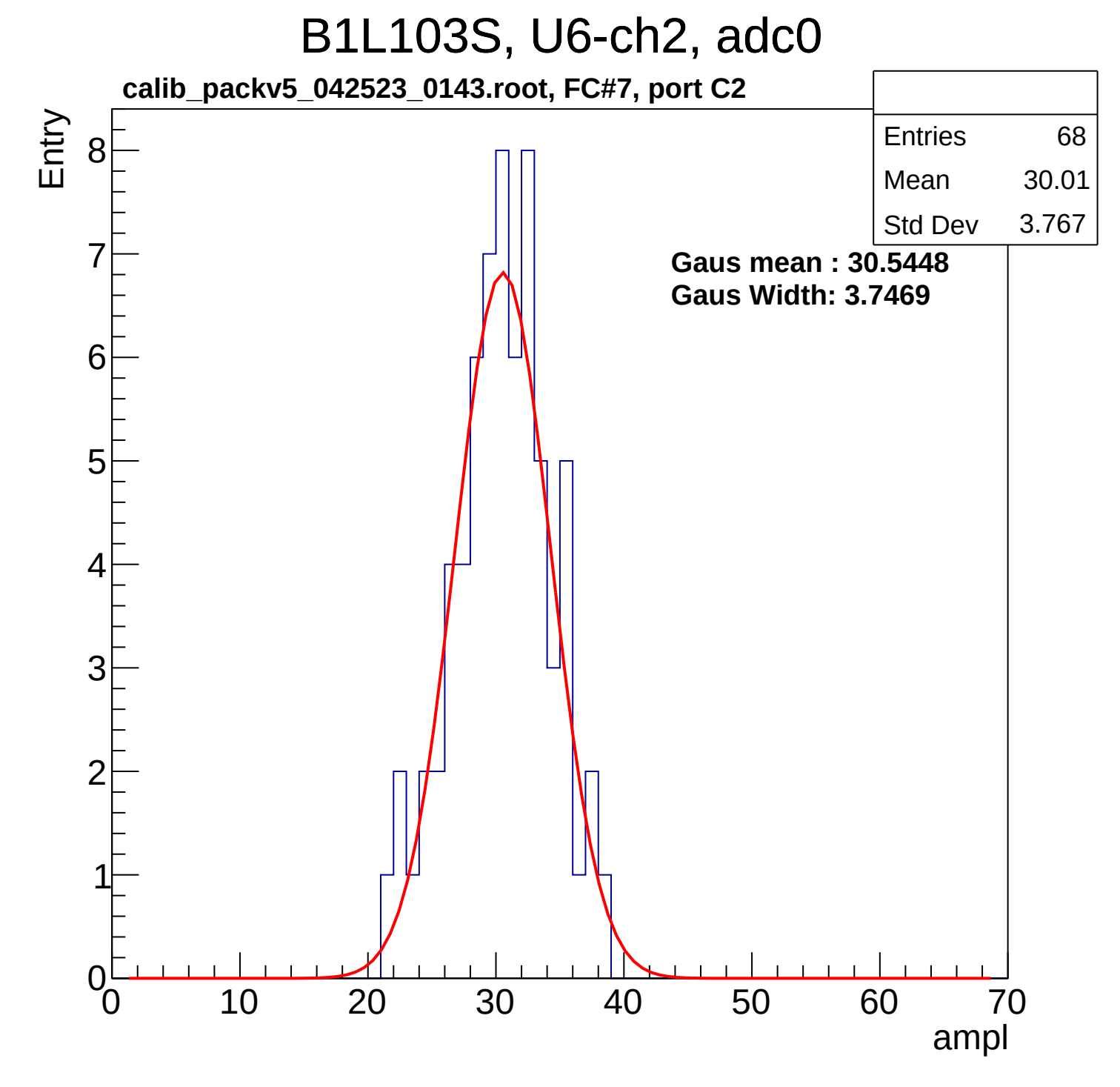
B1L103S, U6-ch2, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.01
Std Dev 3.767
Gaus mean : 30.5448
Gaus Width: 3.7469
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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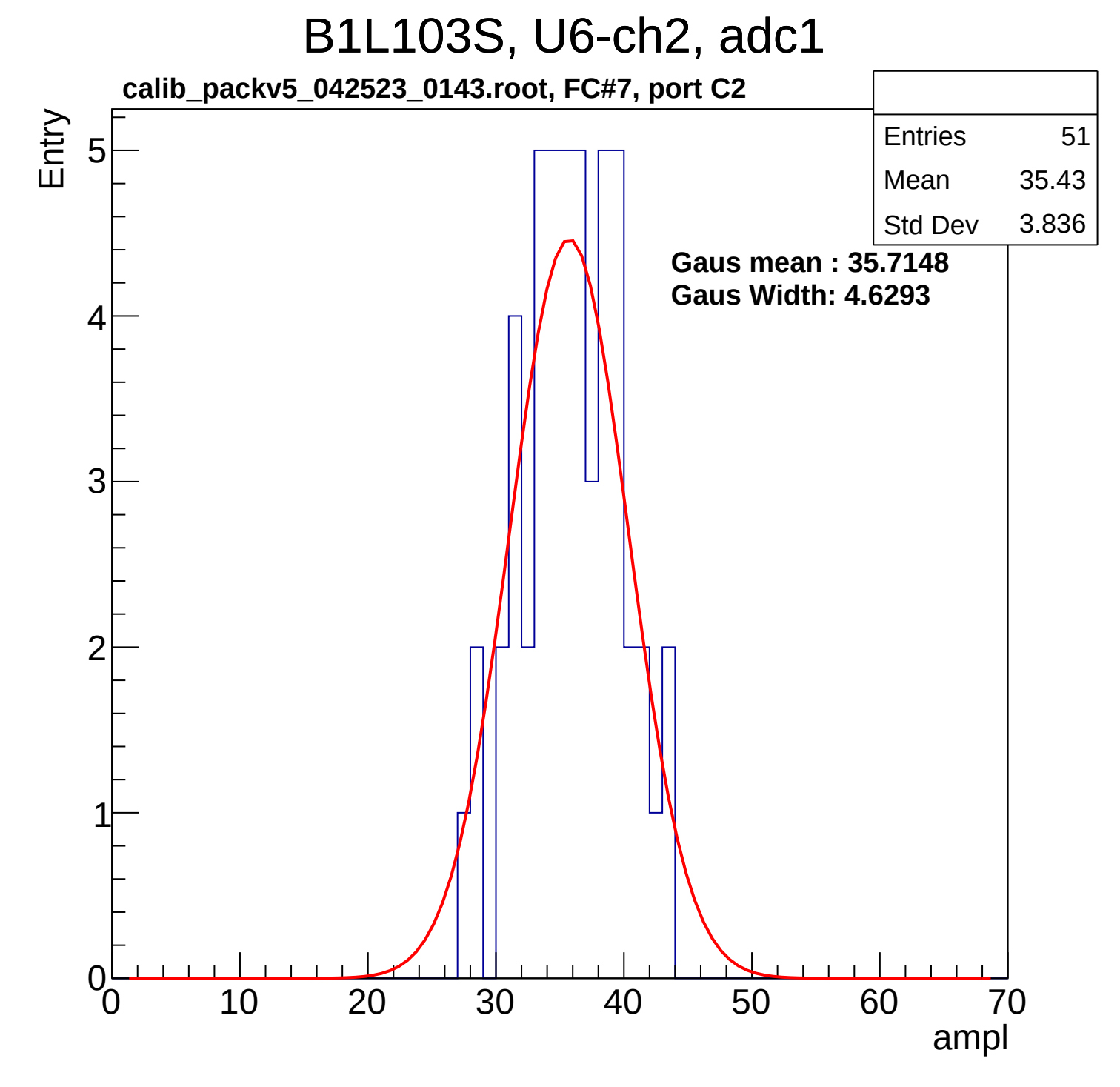
B1L103S, U6-ch2, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 35.43
Std Dev 3.836
Gaus mean : 35.7148
Gaus Width: 4.6293
Entry
ampl
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4
5
0
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60
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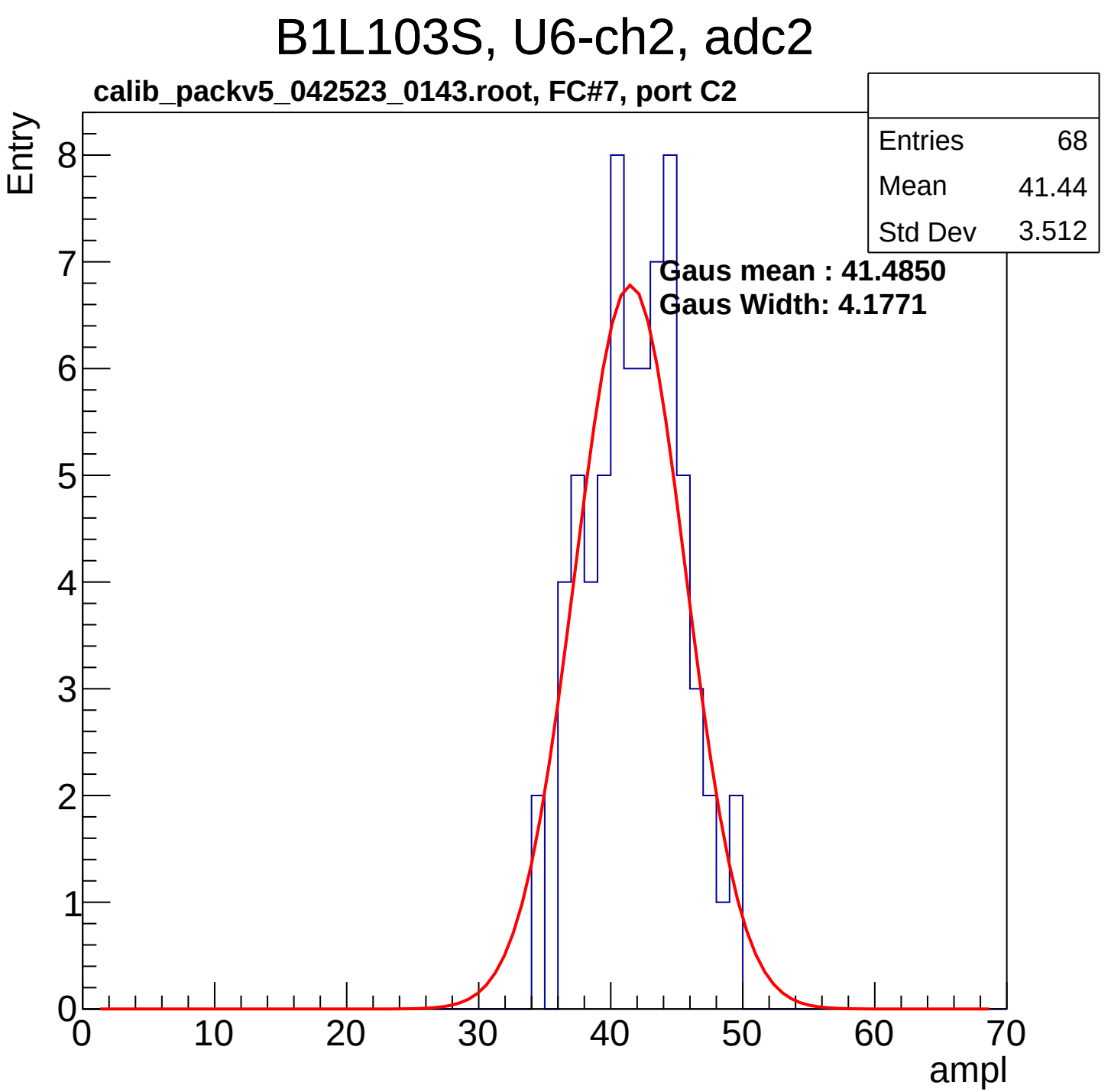
B1L103S, U6-ch2, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 41.44
Std Dev 3.512
Gaus mean : 41.4850
Gaus Width: 4.1771
Entry
ampl
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3
4
5
6
7
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0
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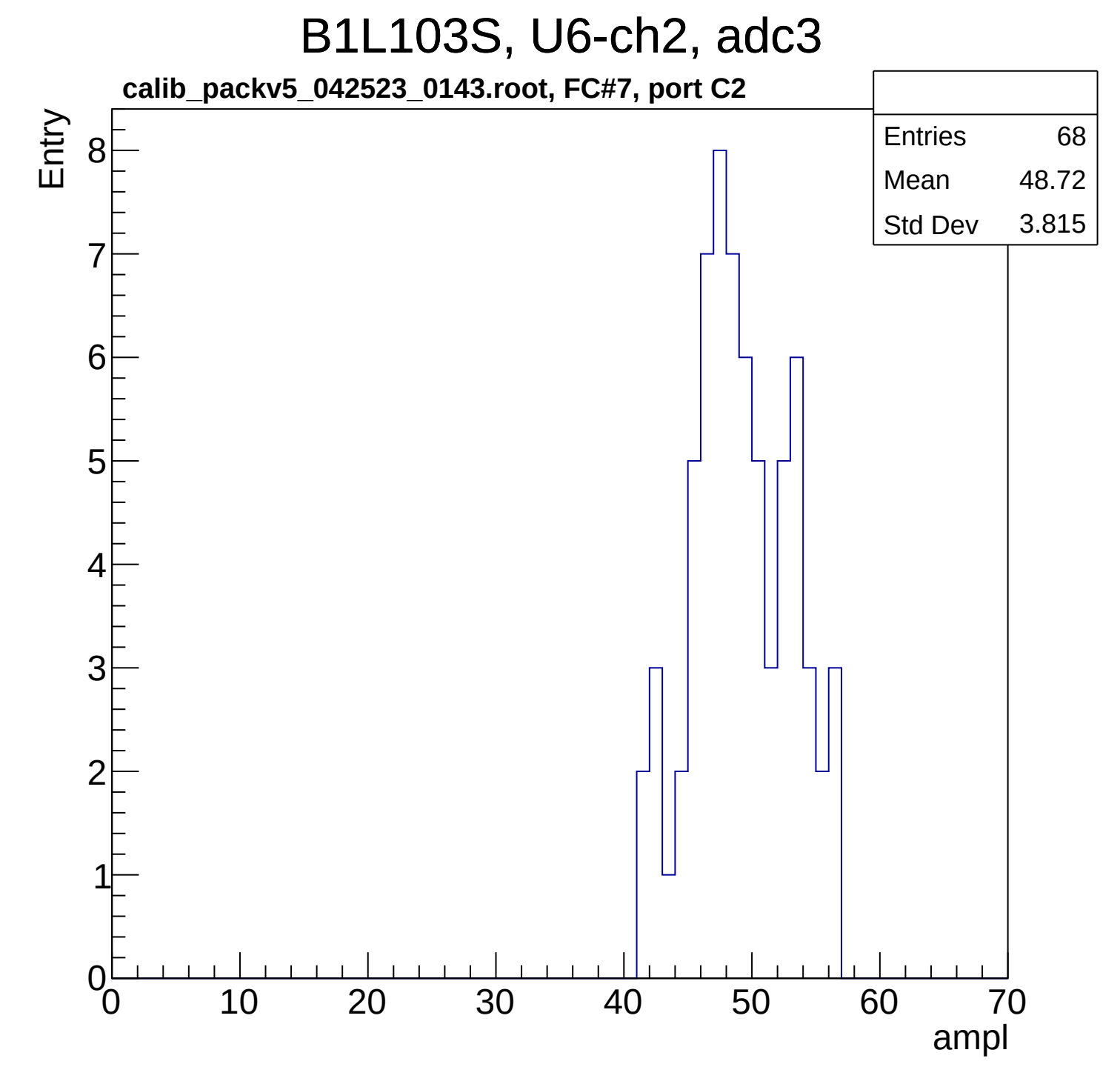
B1L103S, U6-ch2, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 48.72
Std Dev 3.815
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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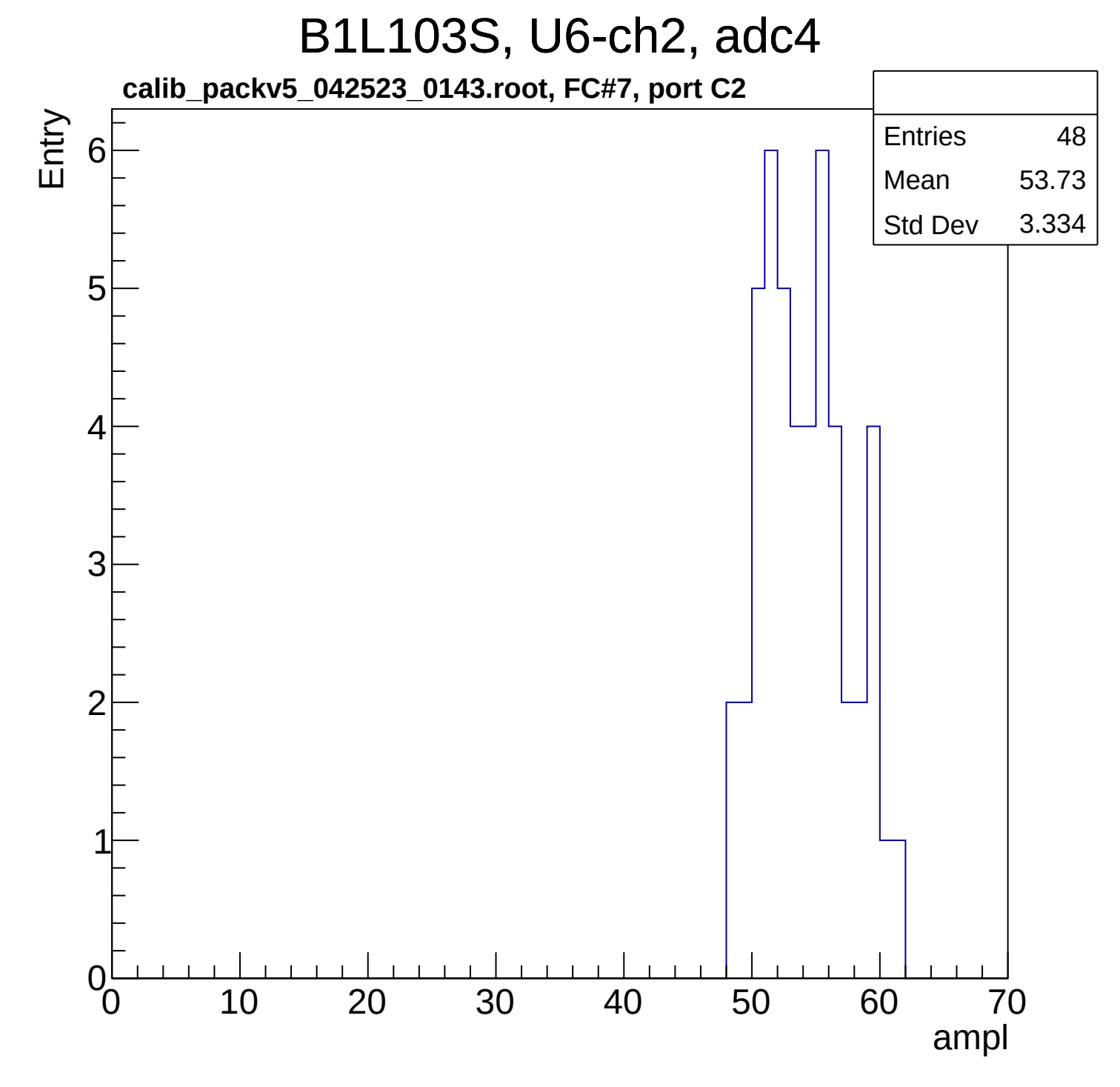
B1L103S, U6-ch2, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 53.73
Std Dev 3.334
Entry
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6
0
10
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30
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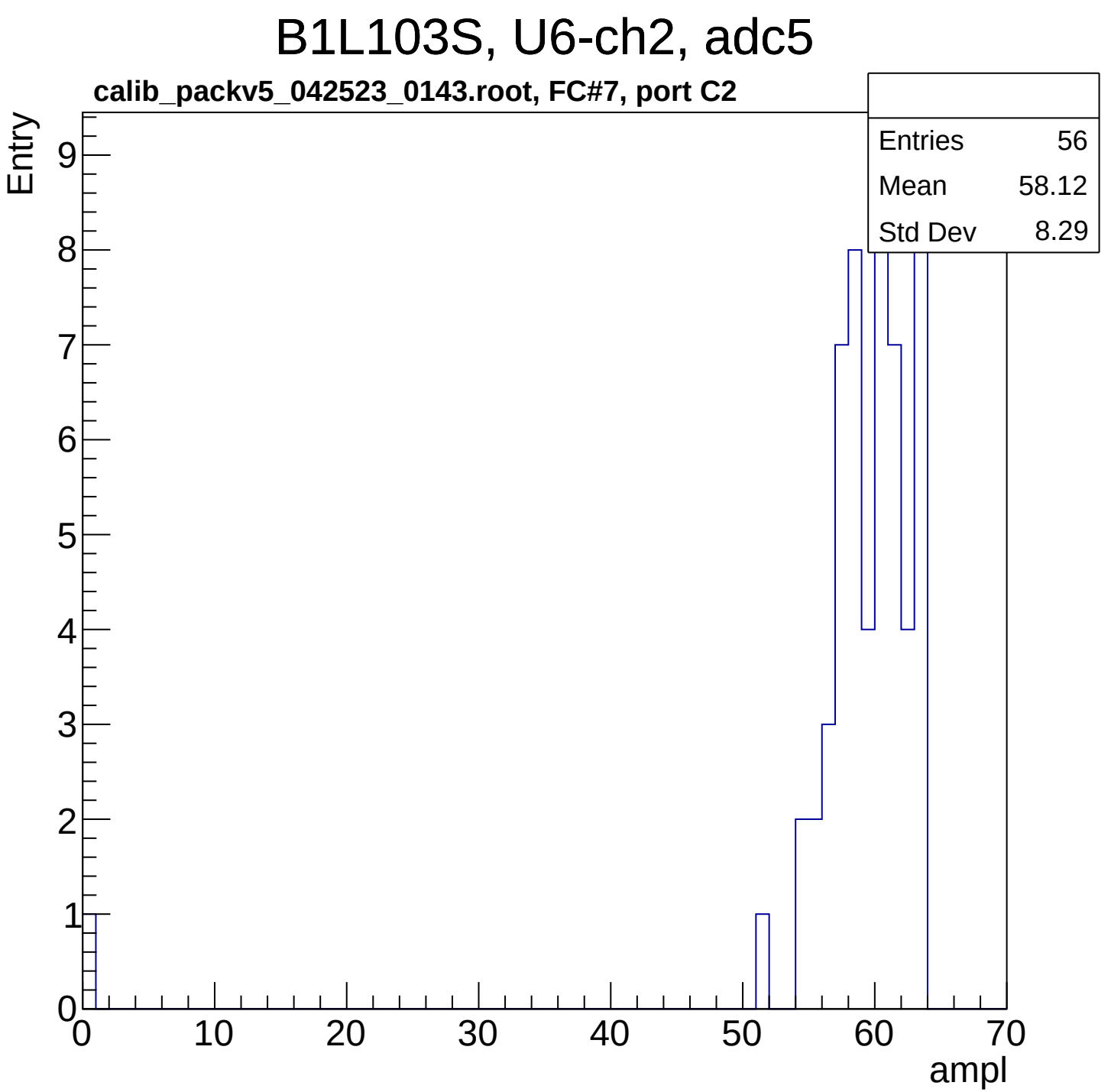
50
60
70
ampl


B1L103S, U6-ch2, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.12
Std Dev 8.29
Entry
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5
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7
8
9
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30
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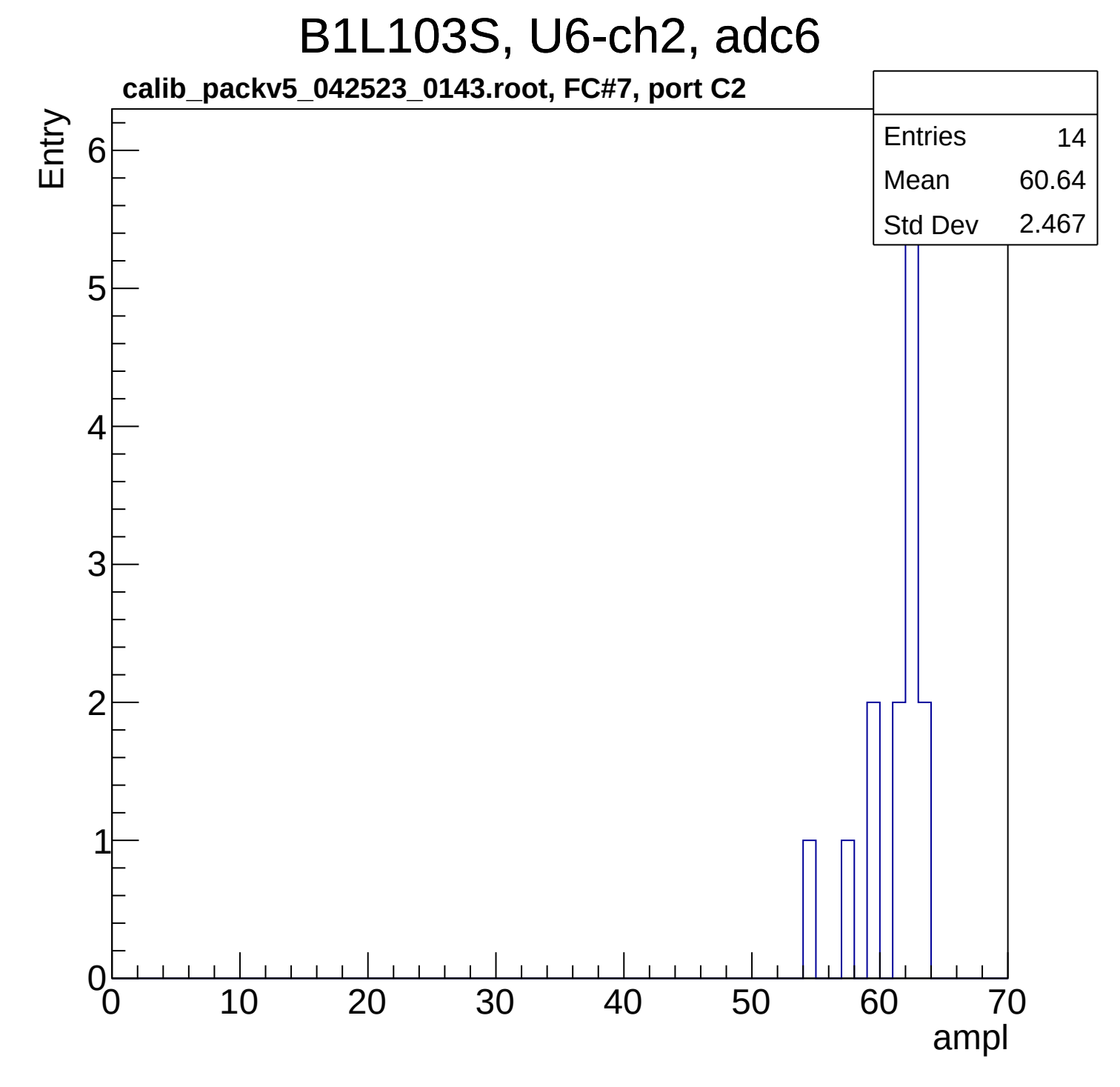
70
ampl


B1L103S, U6-ch2, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 60.64
Std Dev 2.467
Entry
ampl
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3
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5
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0
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20
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40
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60
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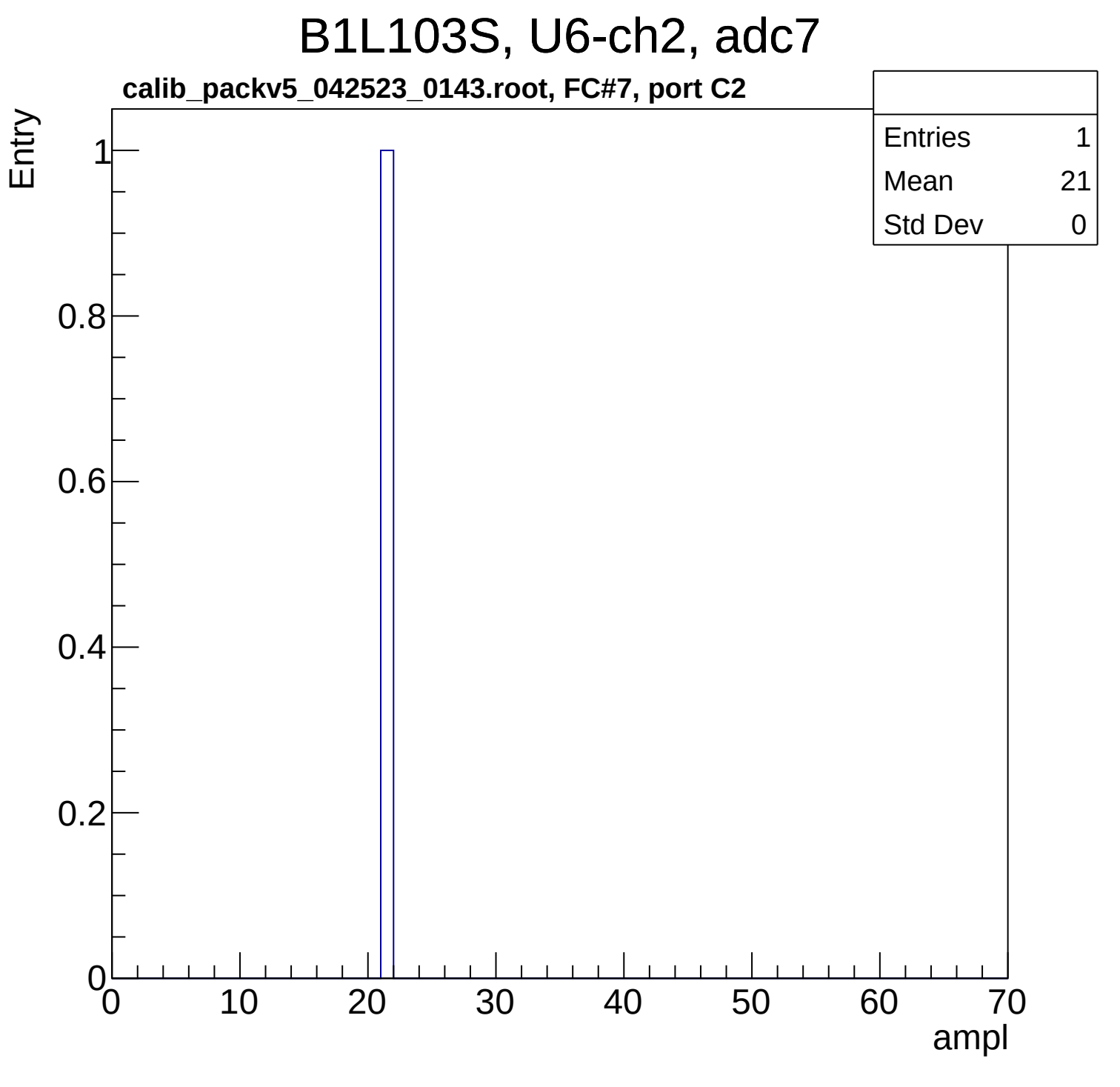
B1L103S, U6-ch2, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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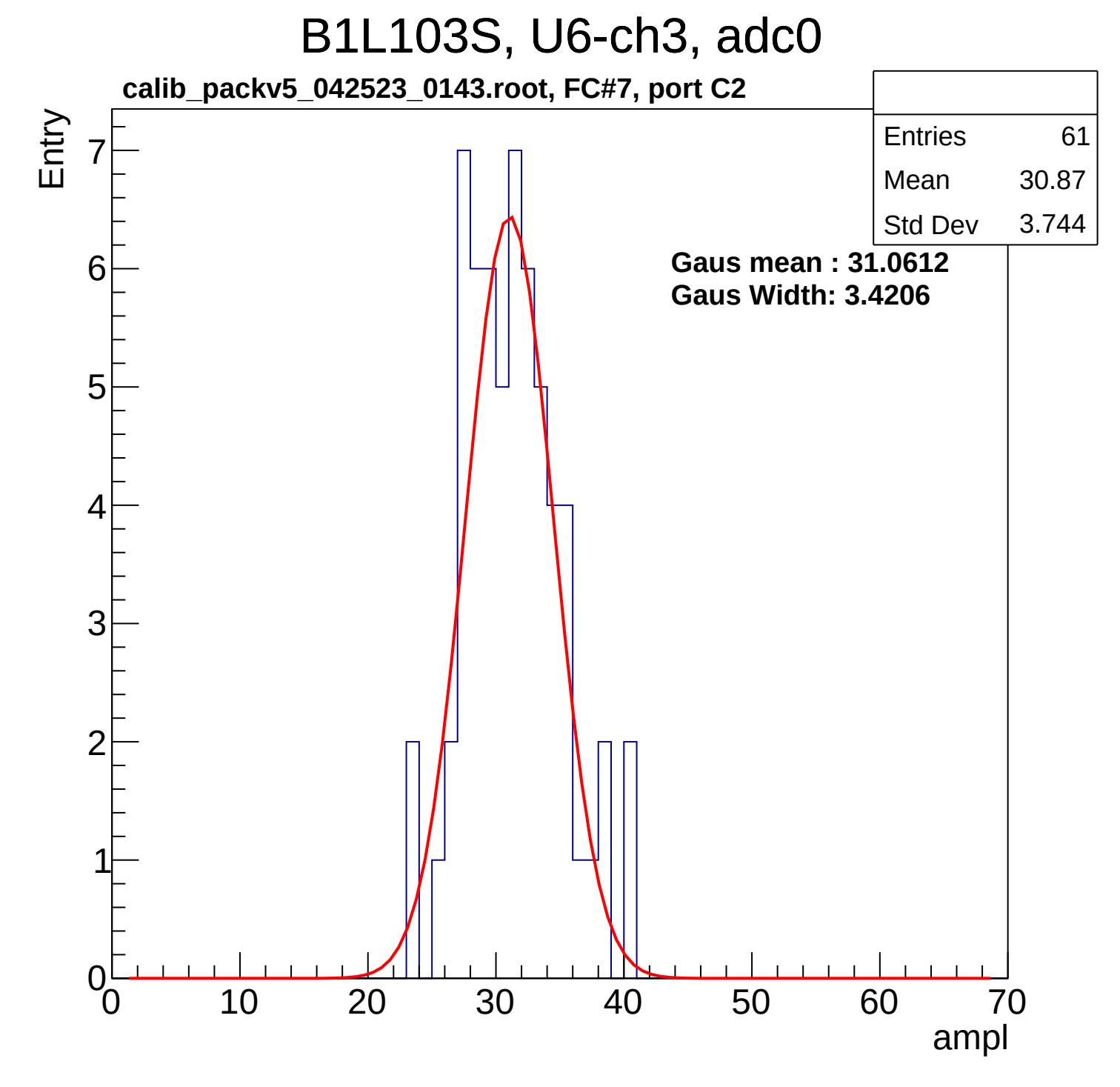
30
40
50
60
70
ampl


B1L103S, U6-ch3, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 30.87
Std Dev 3.744
Gaus mean : 31.0612
Gaus Width: 3.4206
Entry
ampl
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3
4
5
6
7
0
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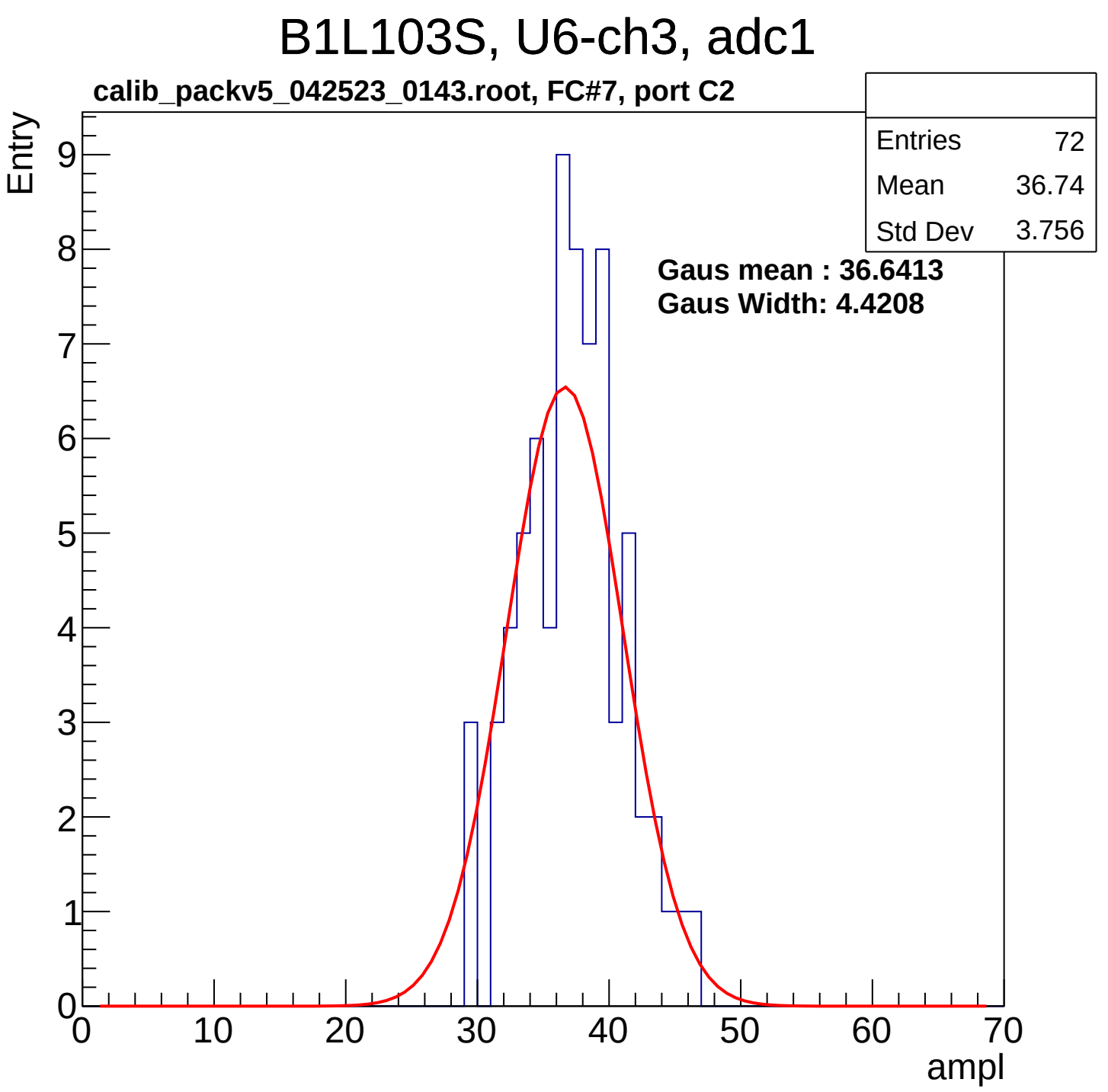
B1L103S, U6-ch3, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.74
Std Dev 3.756
Gaus mean : 36.6413
Gaus Width: 4.4208
Entry
ampl
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2
3
4
5
6
7
8
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0
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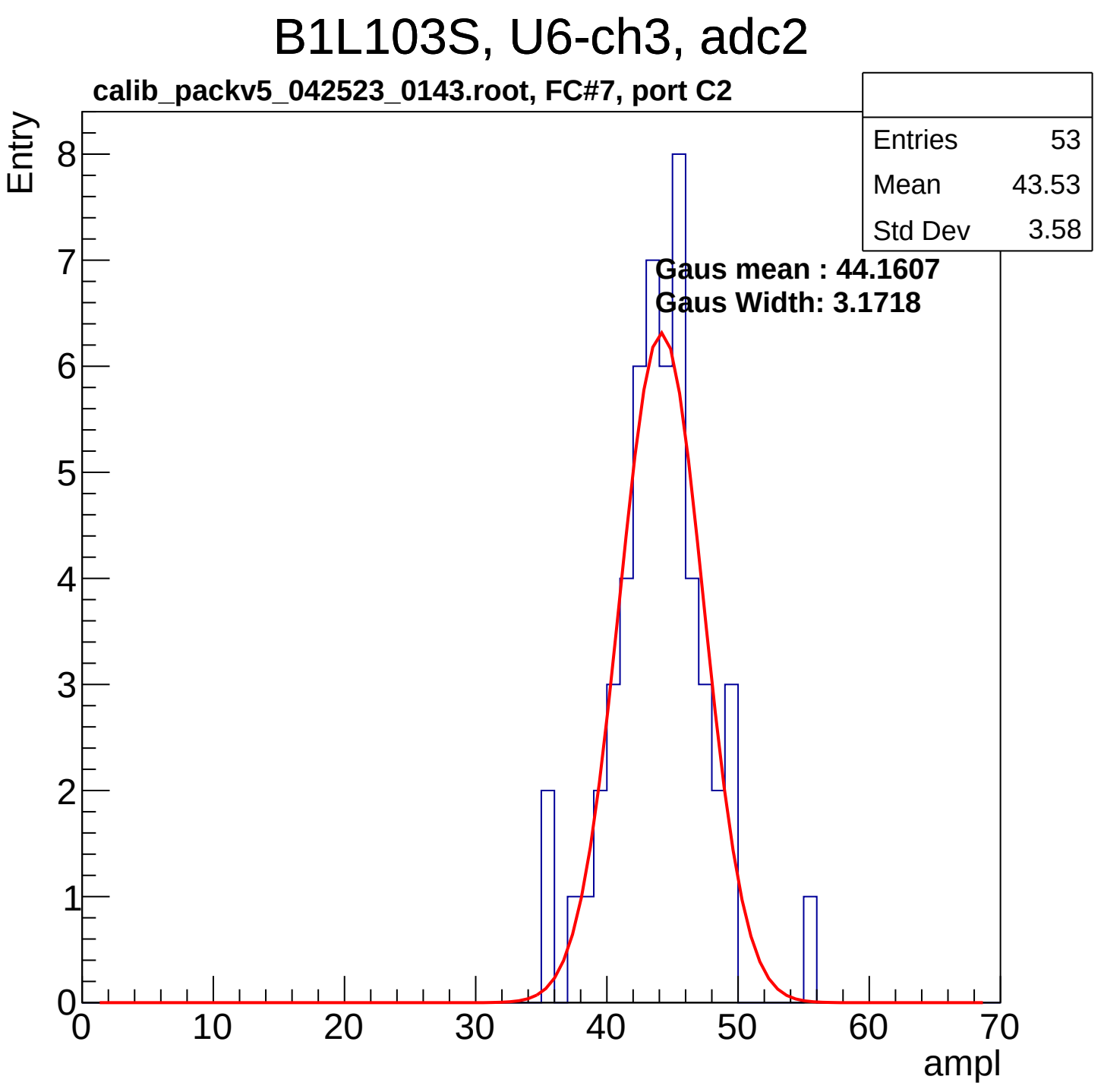
B1L103S, U6-ch3, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 43.53
Std Dev 3.58
Gaus mean : 44.1607
Gaus Width: 3.1718
Entry
ampl
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2
3
4
5
6
7
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0
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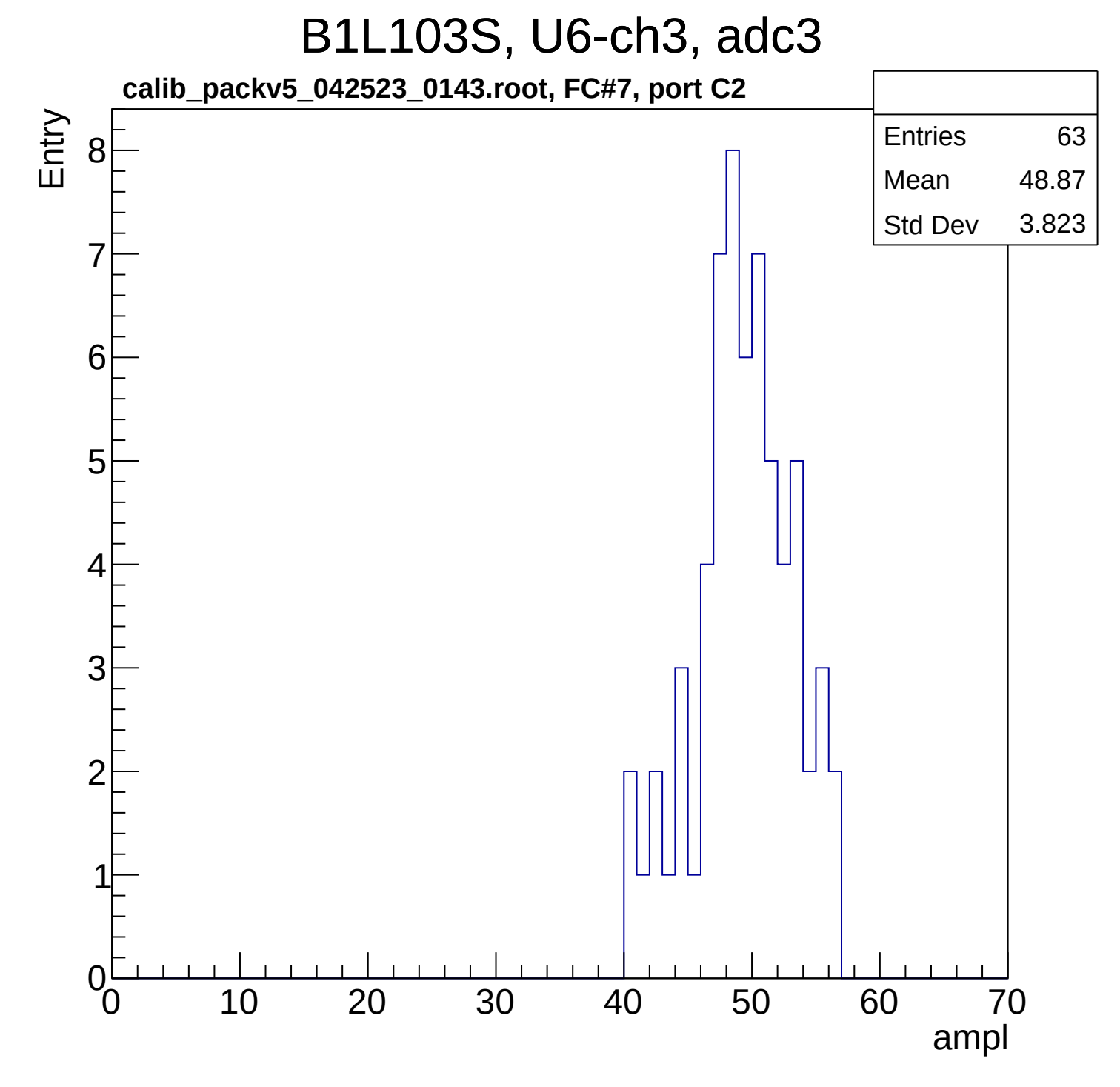
B1L103S, U6-ch3, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.87
Std Dev 3.823
Entry
ampl
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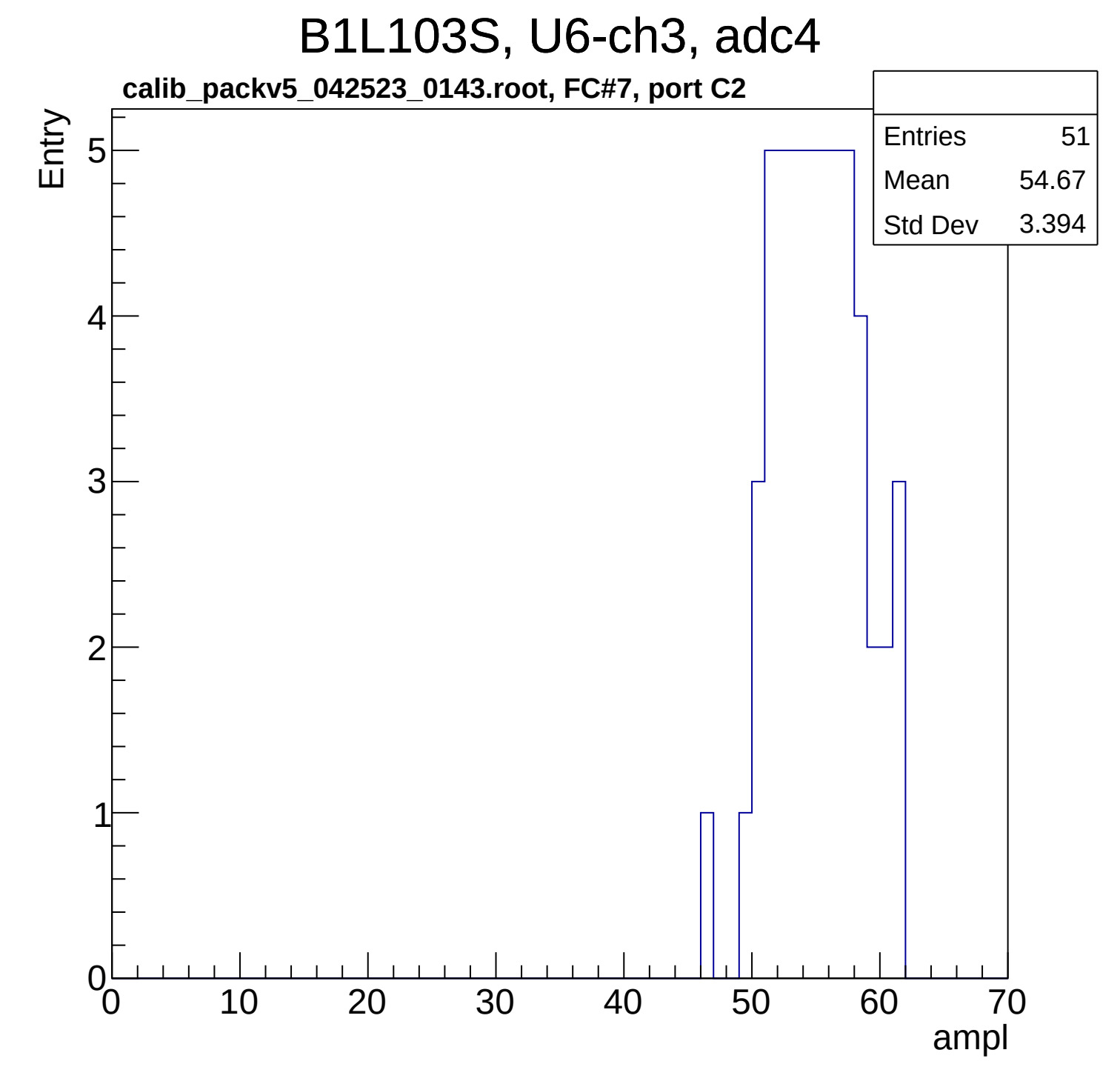
B1L103S, U6-ch3, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.67
Std Dev 3.394
Entry
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2
3
4
5
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10
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30
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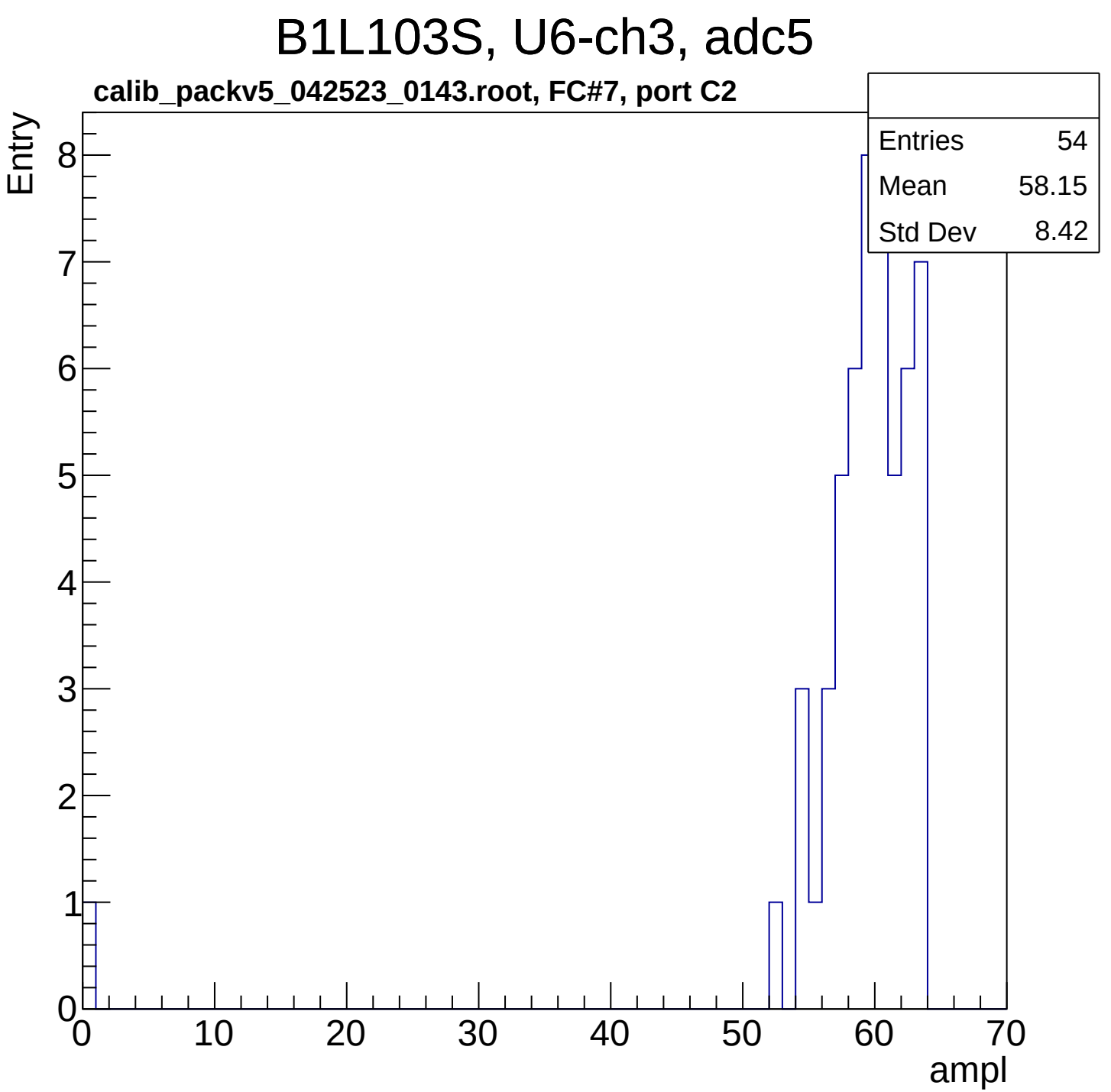
50
60
70
ampl


B1L103S, U6-ch3, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.15
Std Dev 8.42
Entry
ampl
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5
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7
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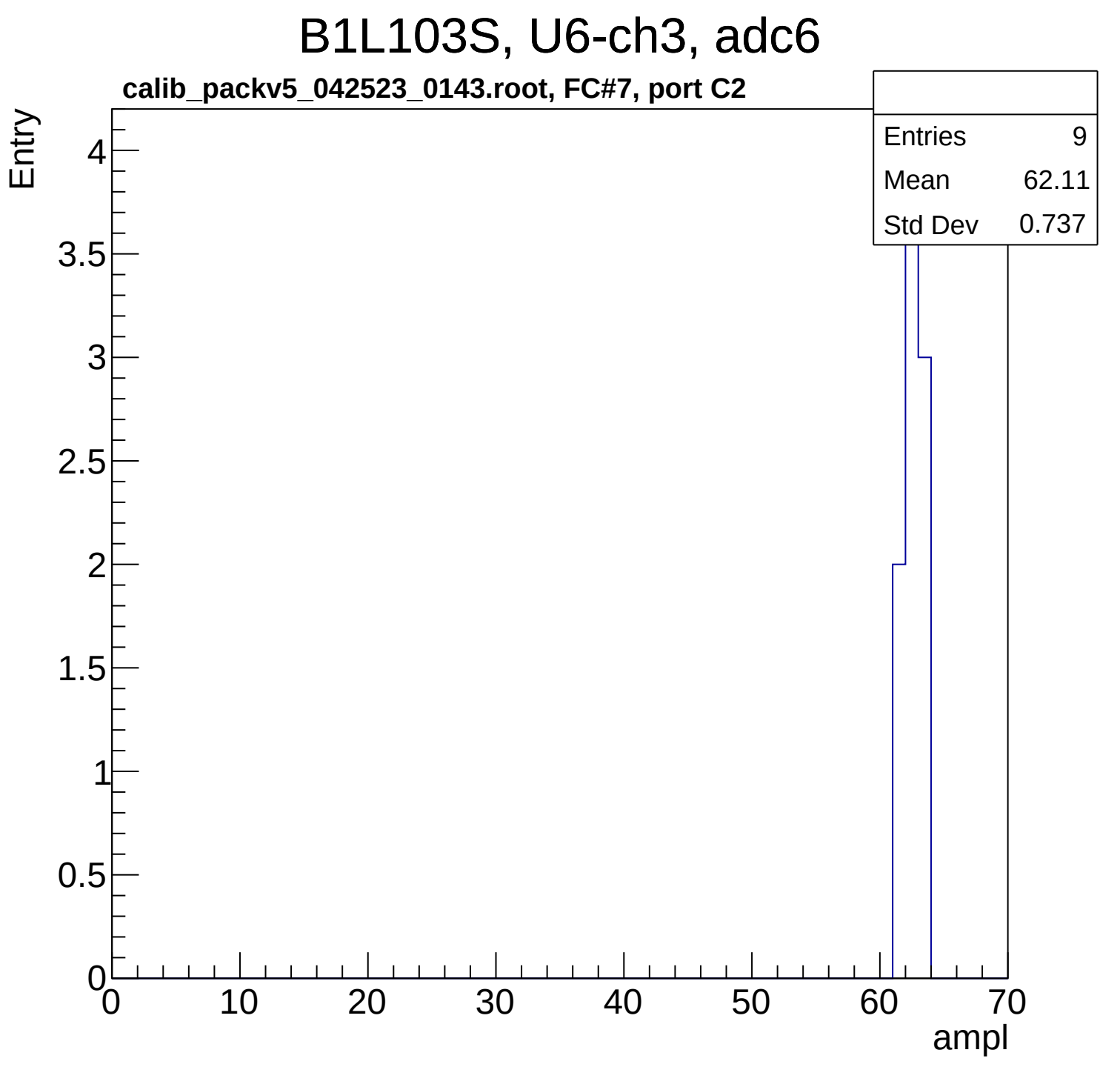
B1L103S, U6-ch3, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 62.11
Std Dev 0.737
Entry
ampl
0
0.5
1
1.5
2
2.5
3
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4
0
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60
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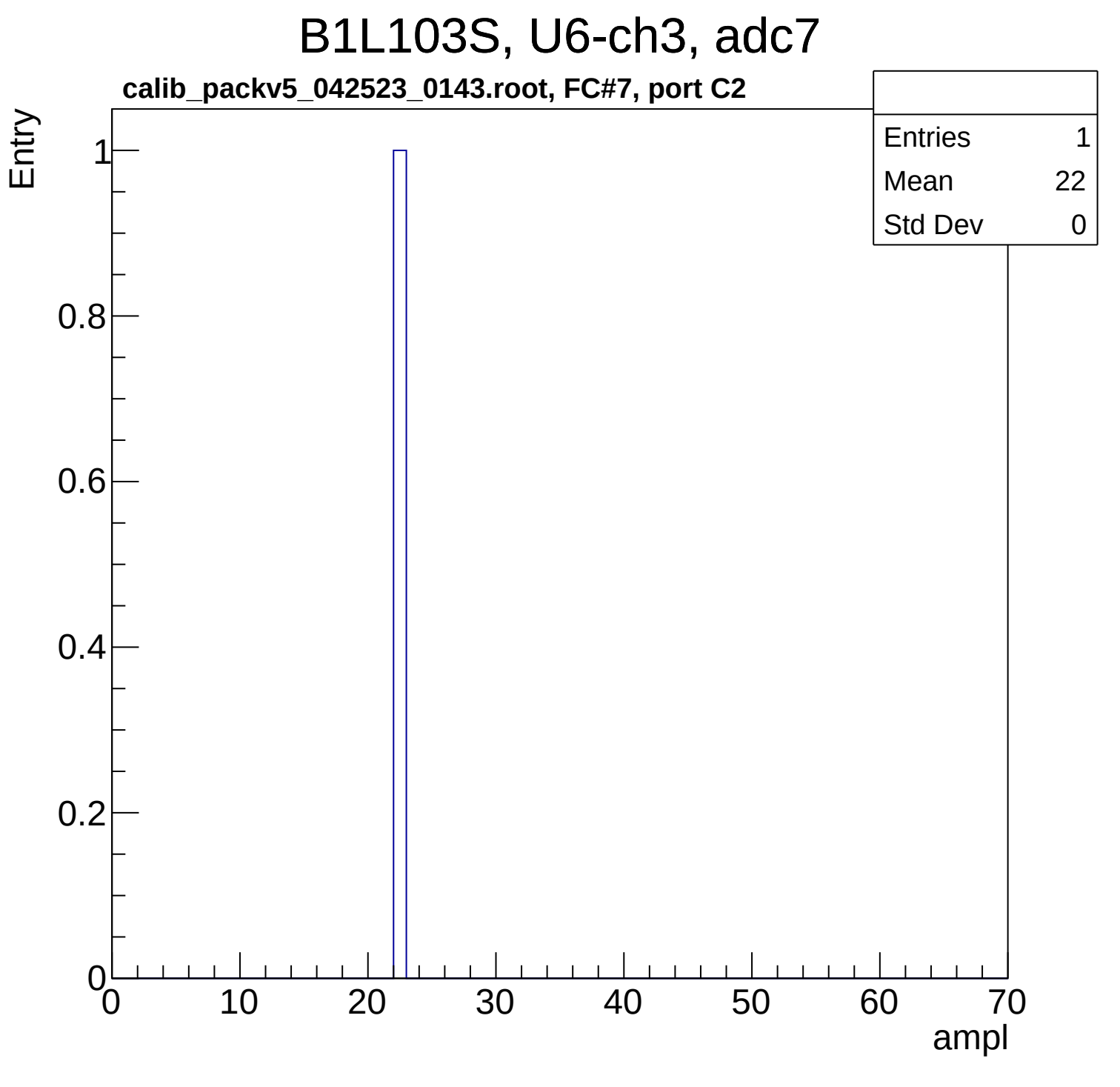
B1L103S, U6-ch3, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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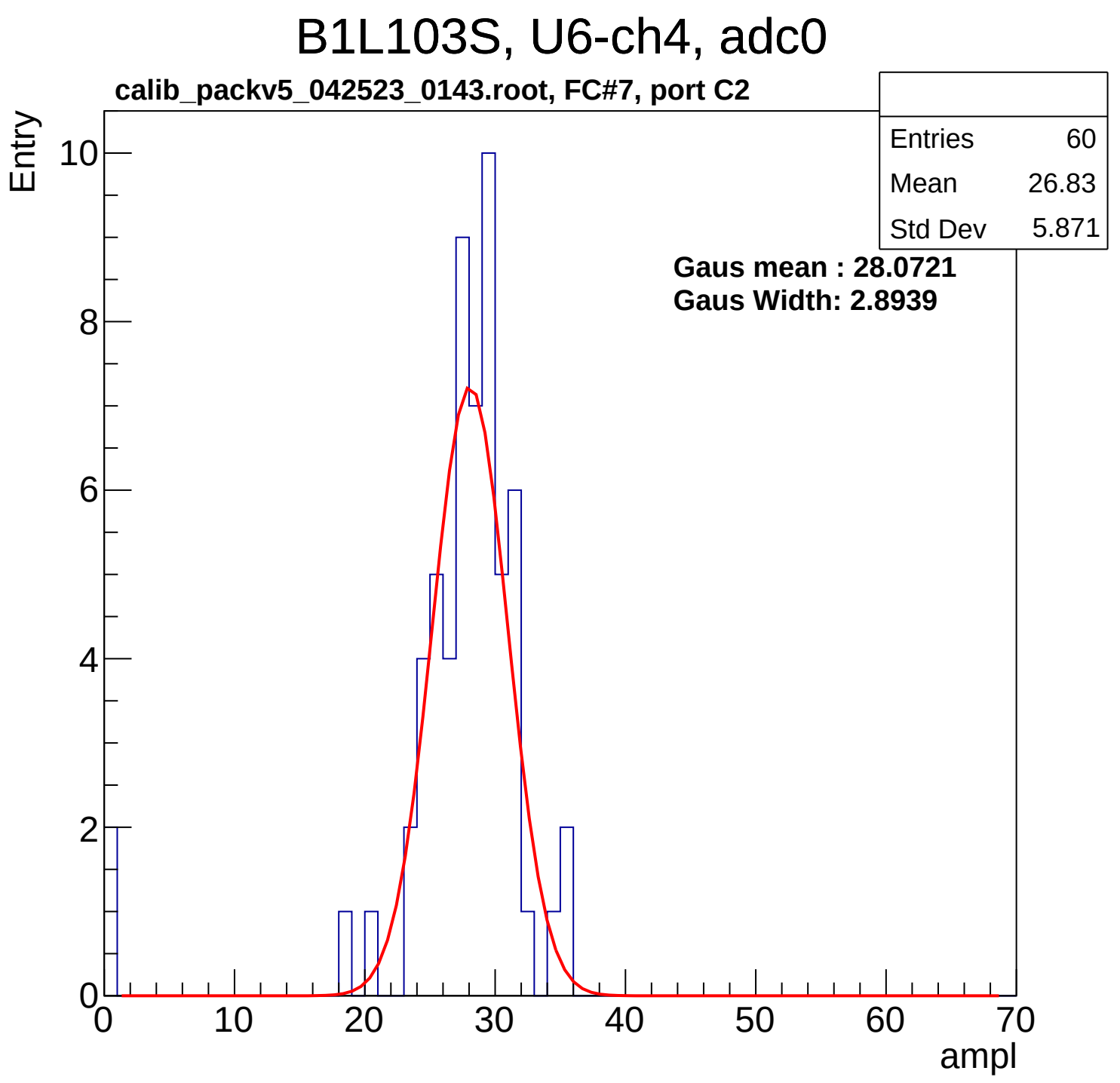
70
ampl


B1L103S, U6-ch4, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 26.83
Std Dev 5.871
Gaus mean : 28.0721
Gaus Width: 2.8939
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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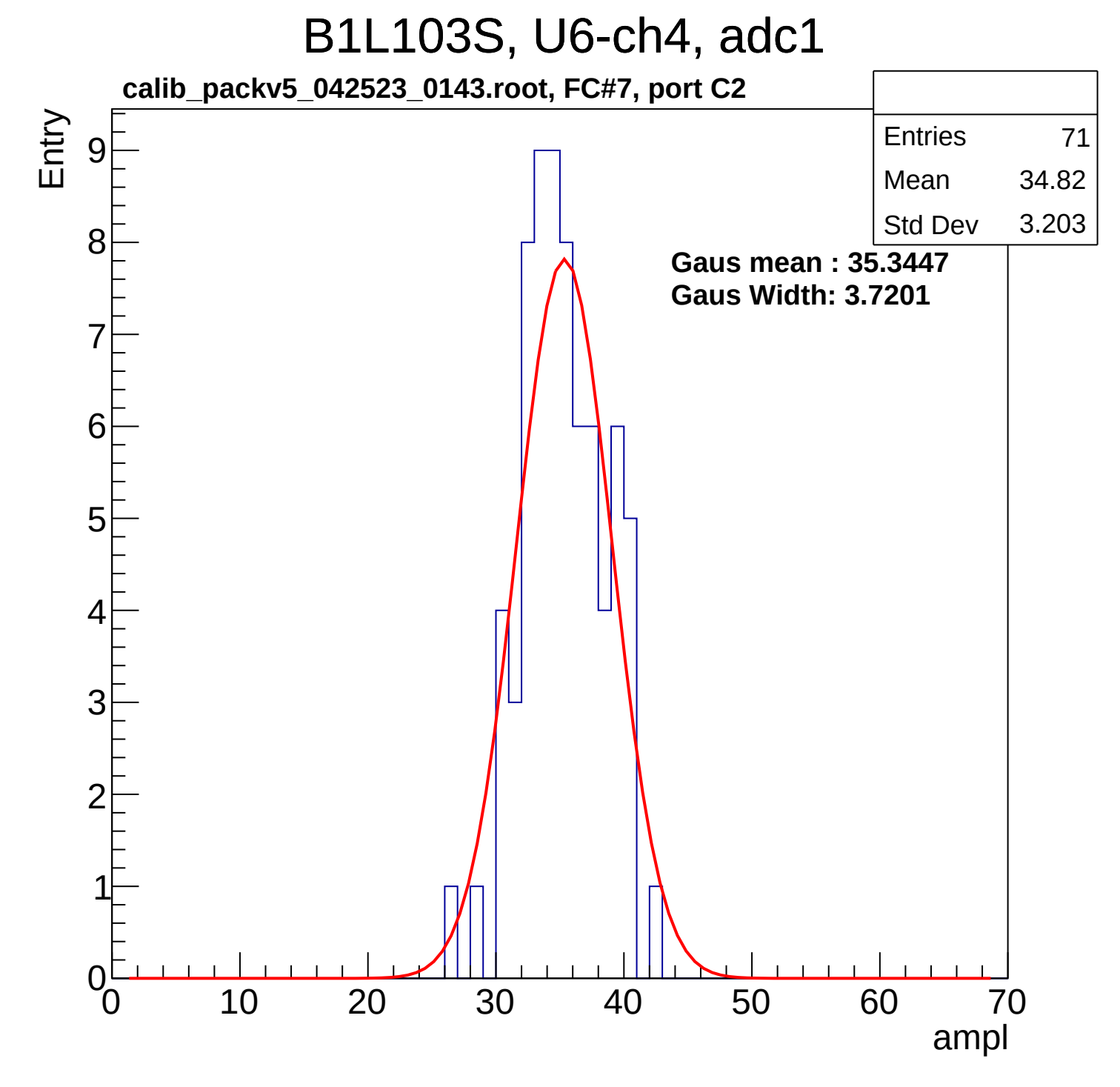
B1L103S, U6-ch4, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 34.82
Std Dev 3.203
Gaus mean : 35.3447
Gaus Width: 3.7201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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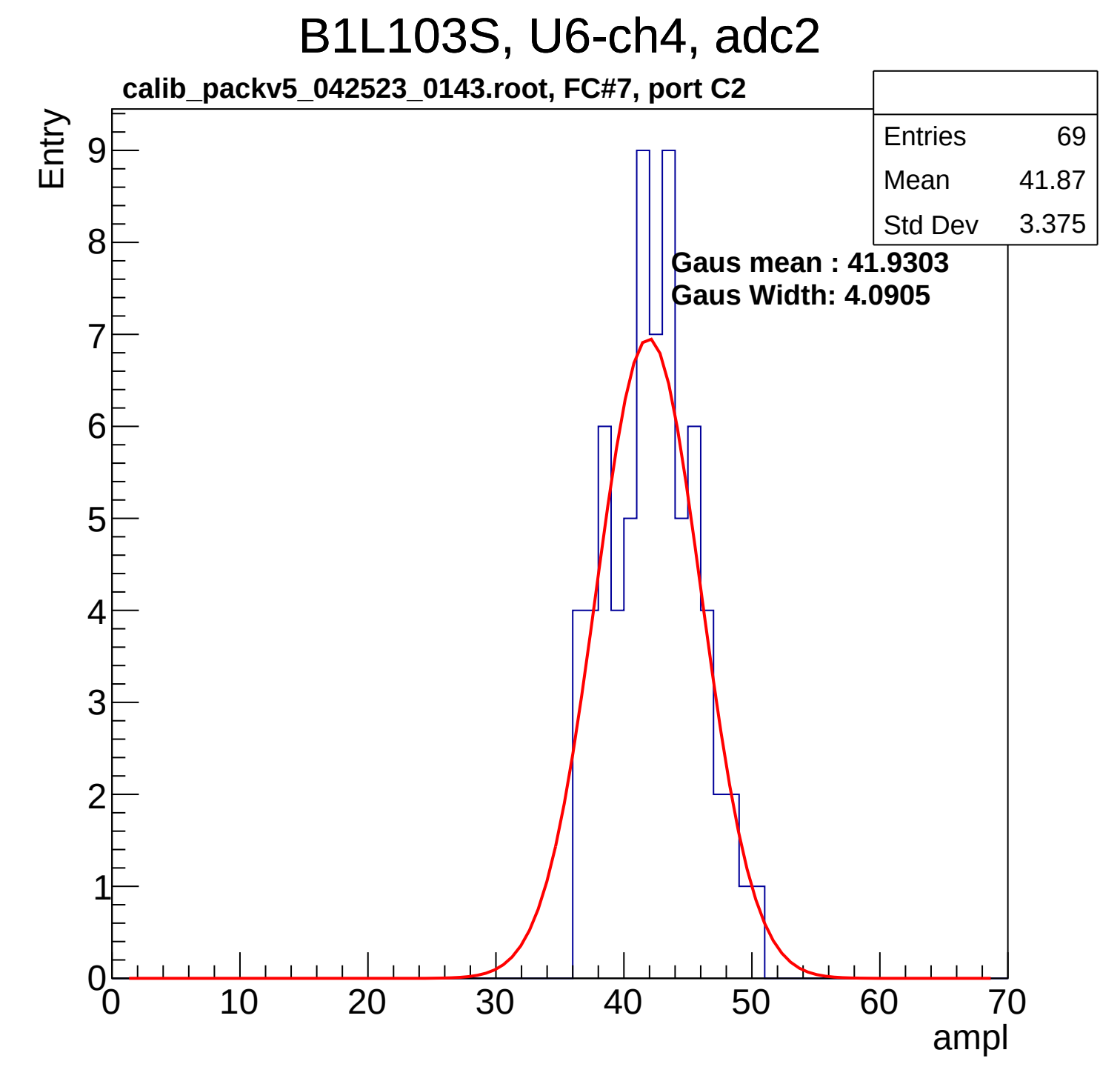
B1L103S, U6-ch4, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 41.87
Std Dev 3.375
Gaus mean : 41.9303
Gaus Width: 4.0905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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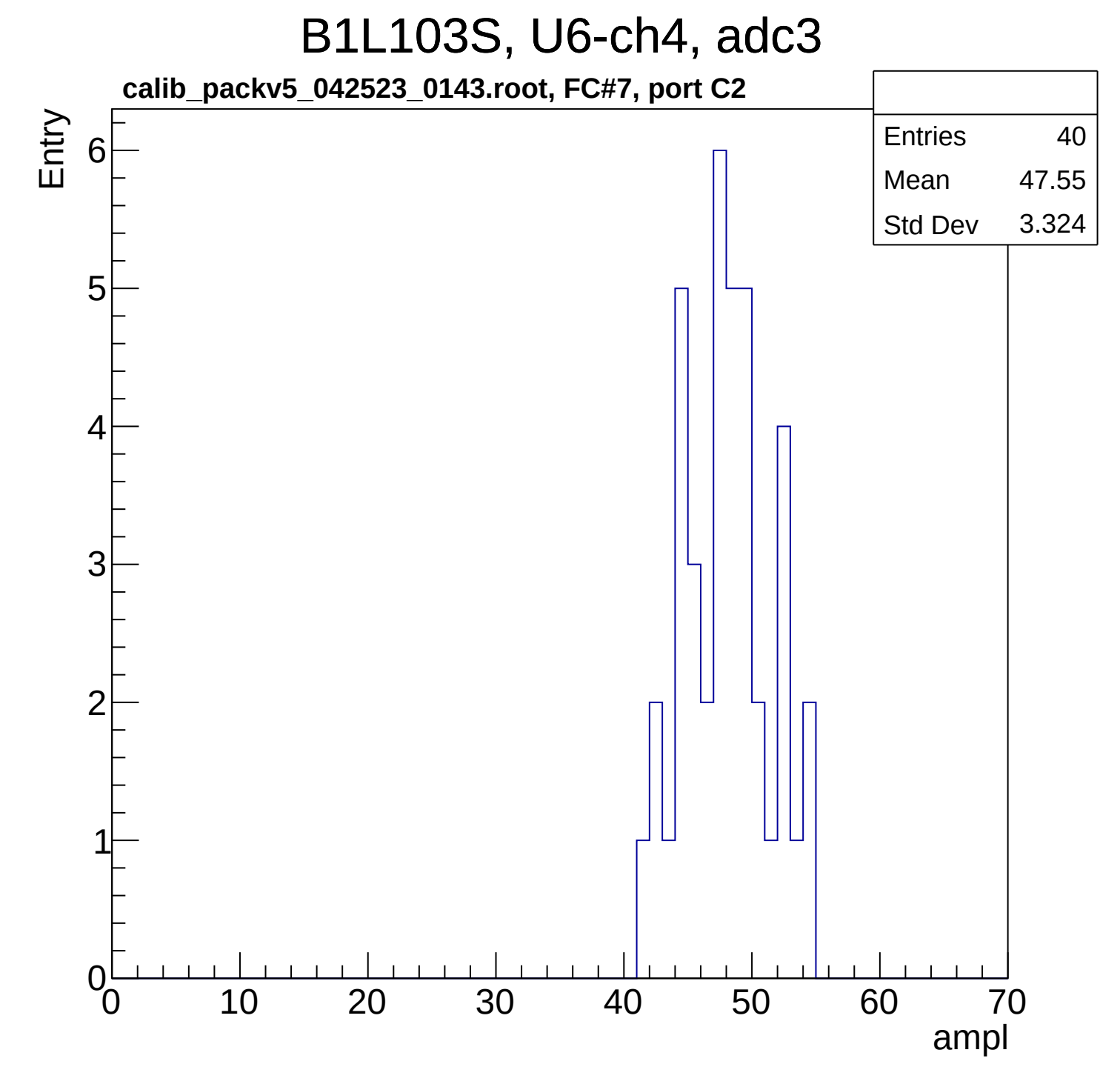
B1L103S, U6-ch4, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 47.55
Std Dev 3.324
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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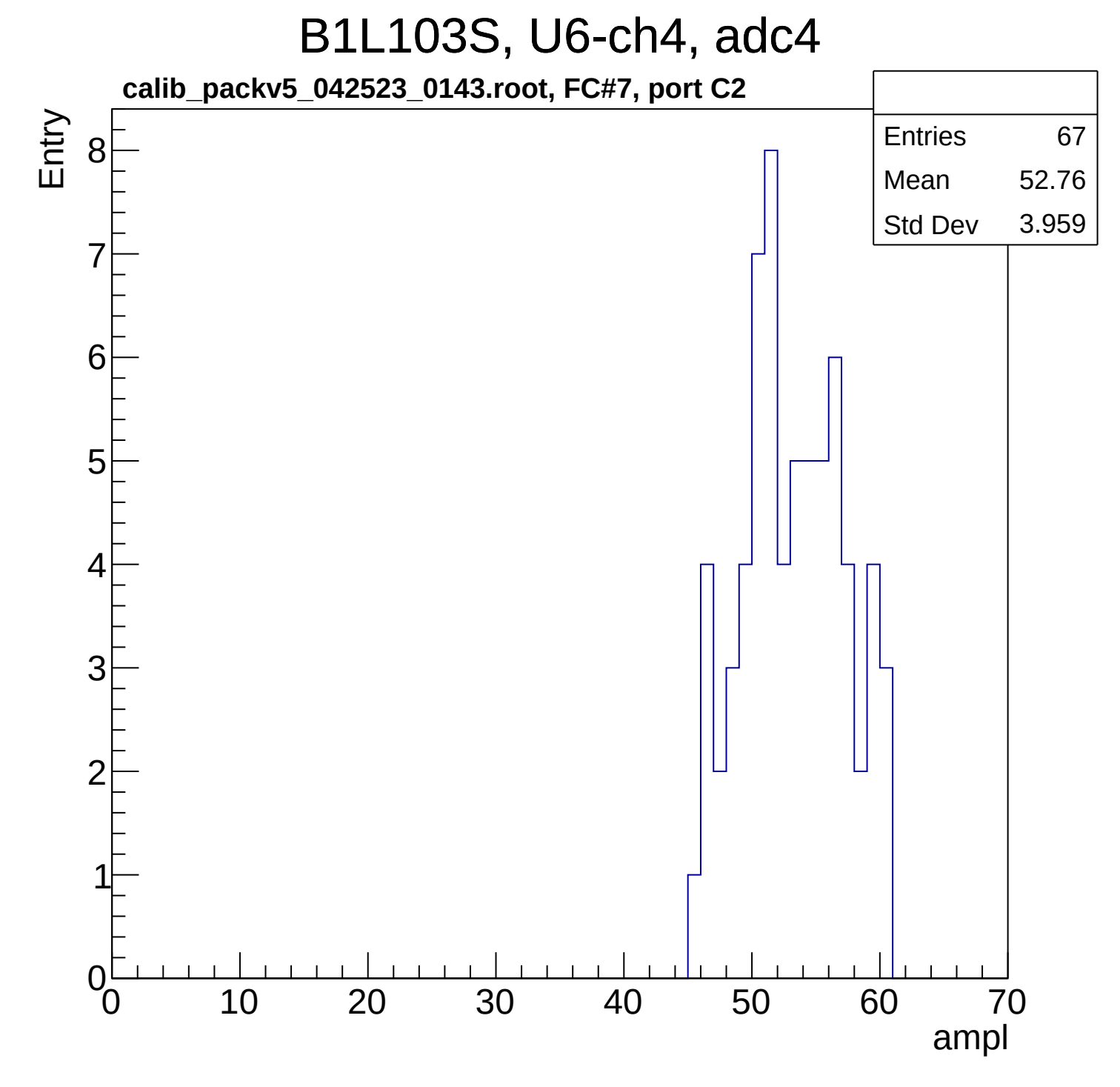
B1L103S, U6-ch4, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 52.76
Std Dev 3.959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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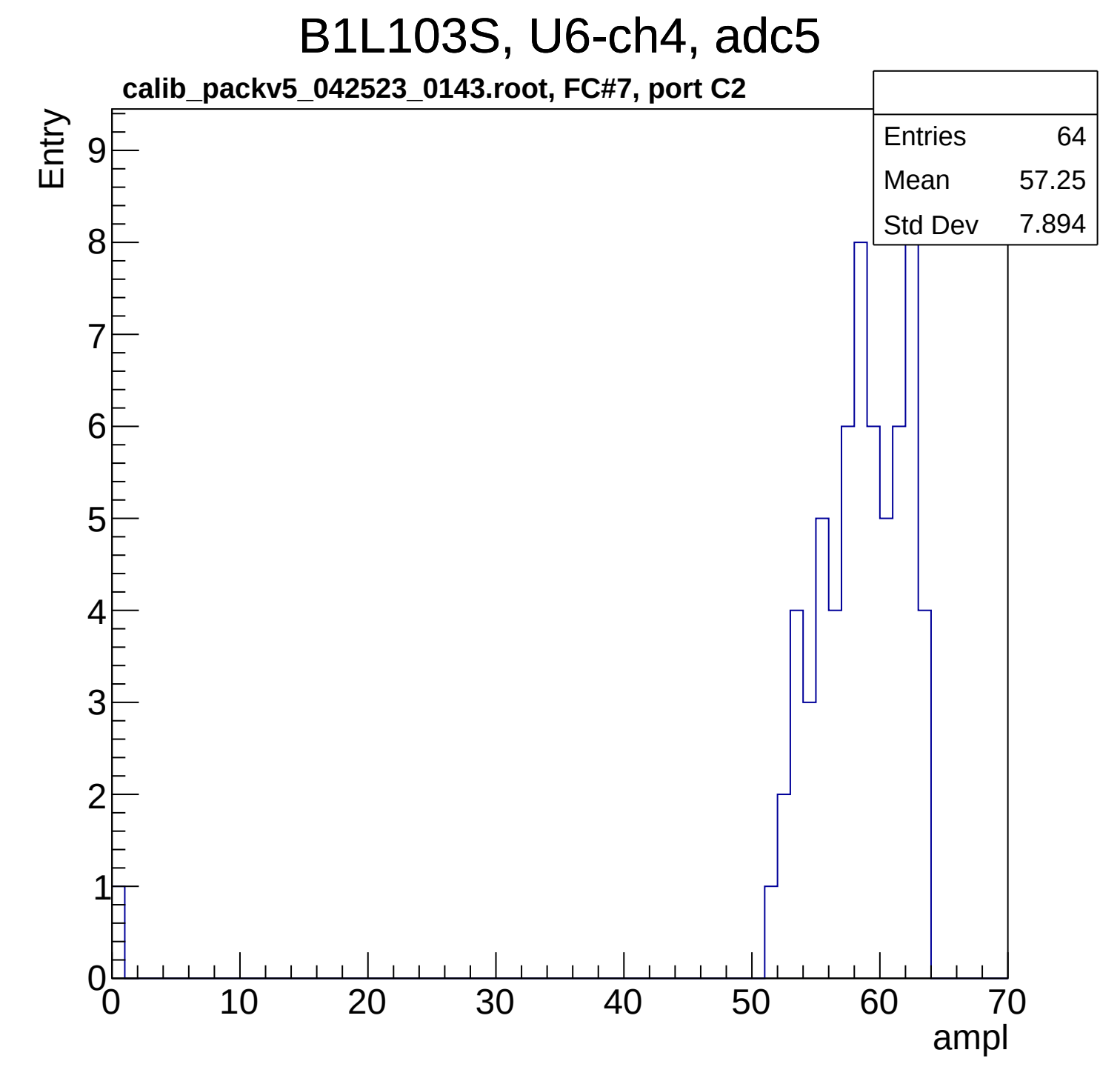
B1L103S, U6-ch4, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 57.25
Std Dev 7.894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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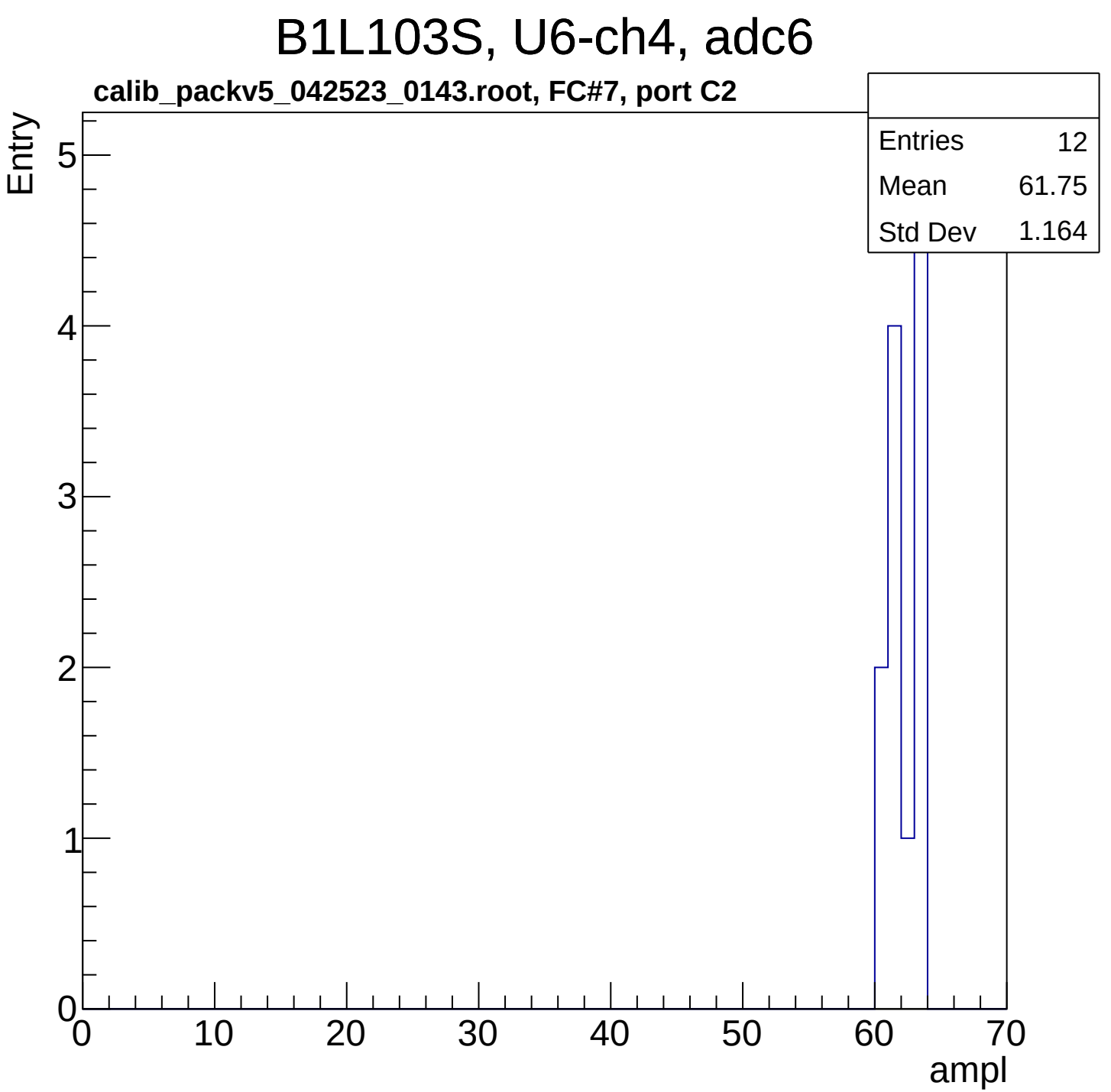
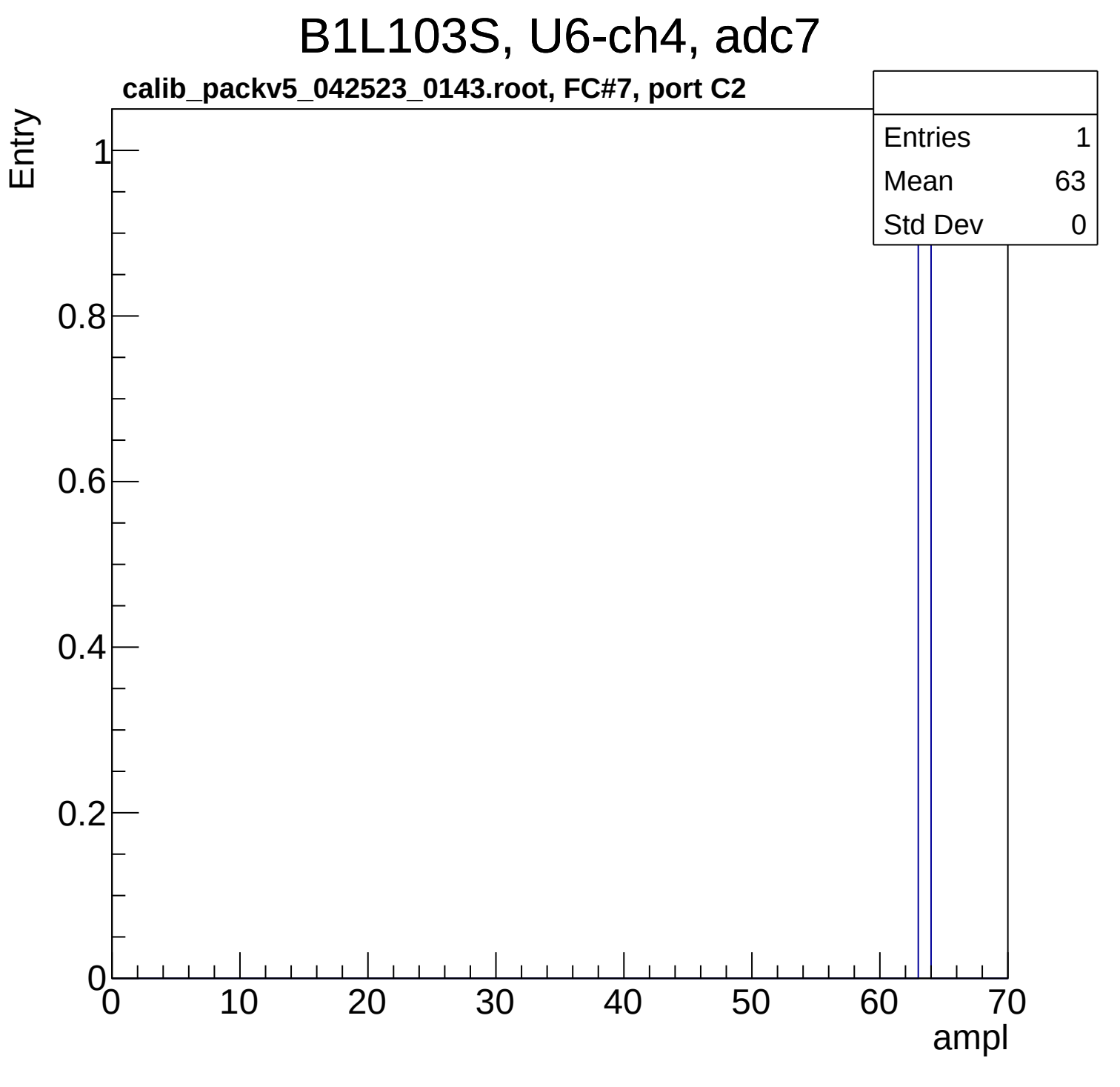

B1L103S, U6-ch4, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.75
Std Dev 1.164
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch4, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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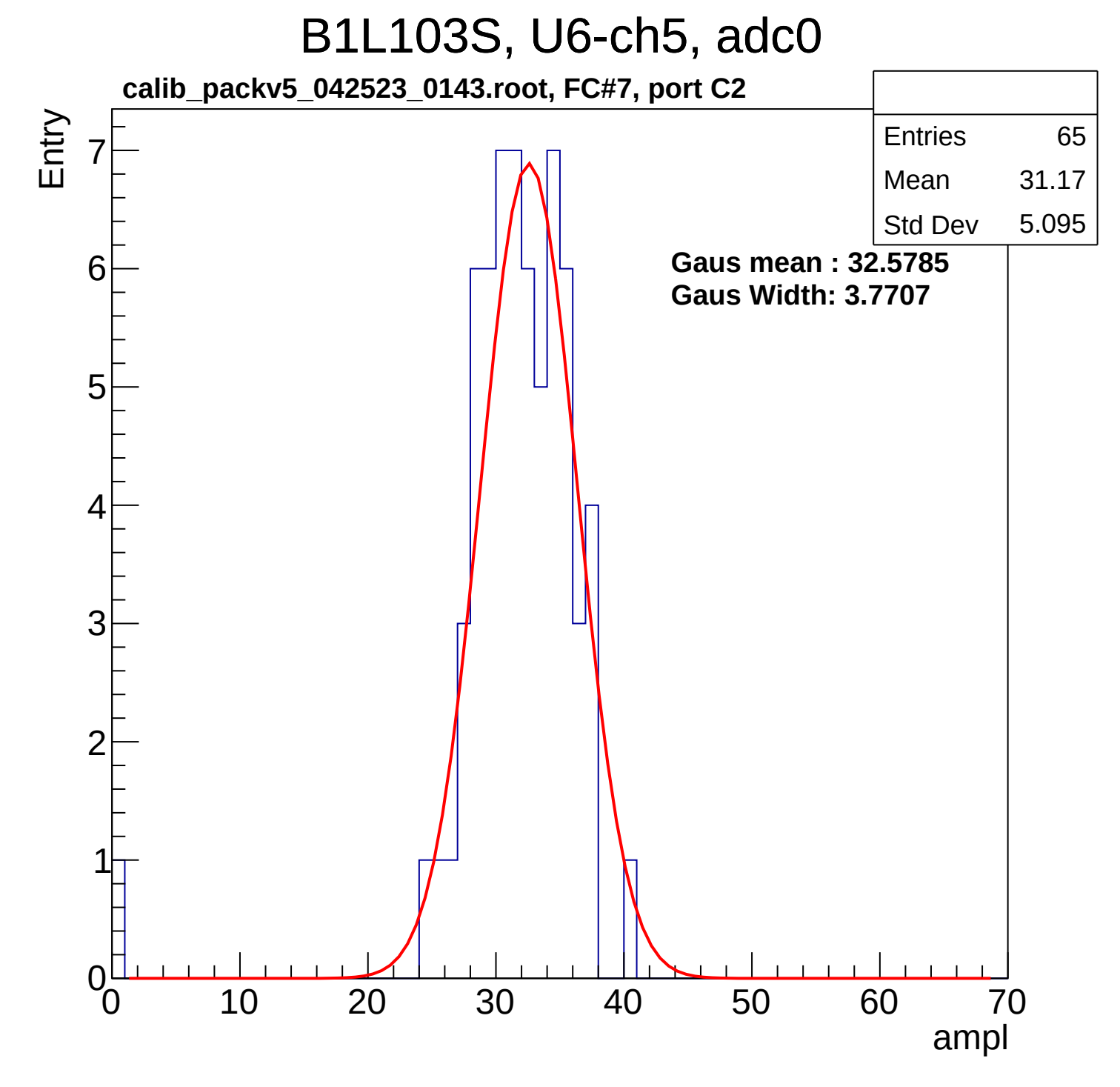
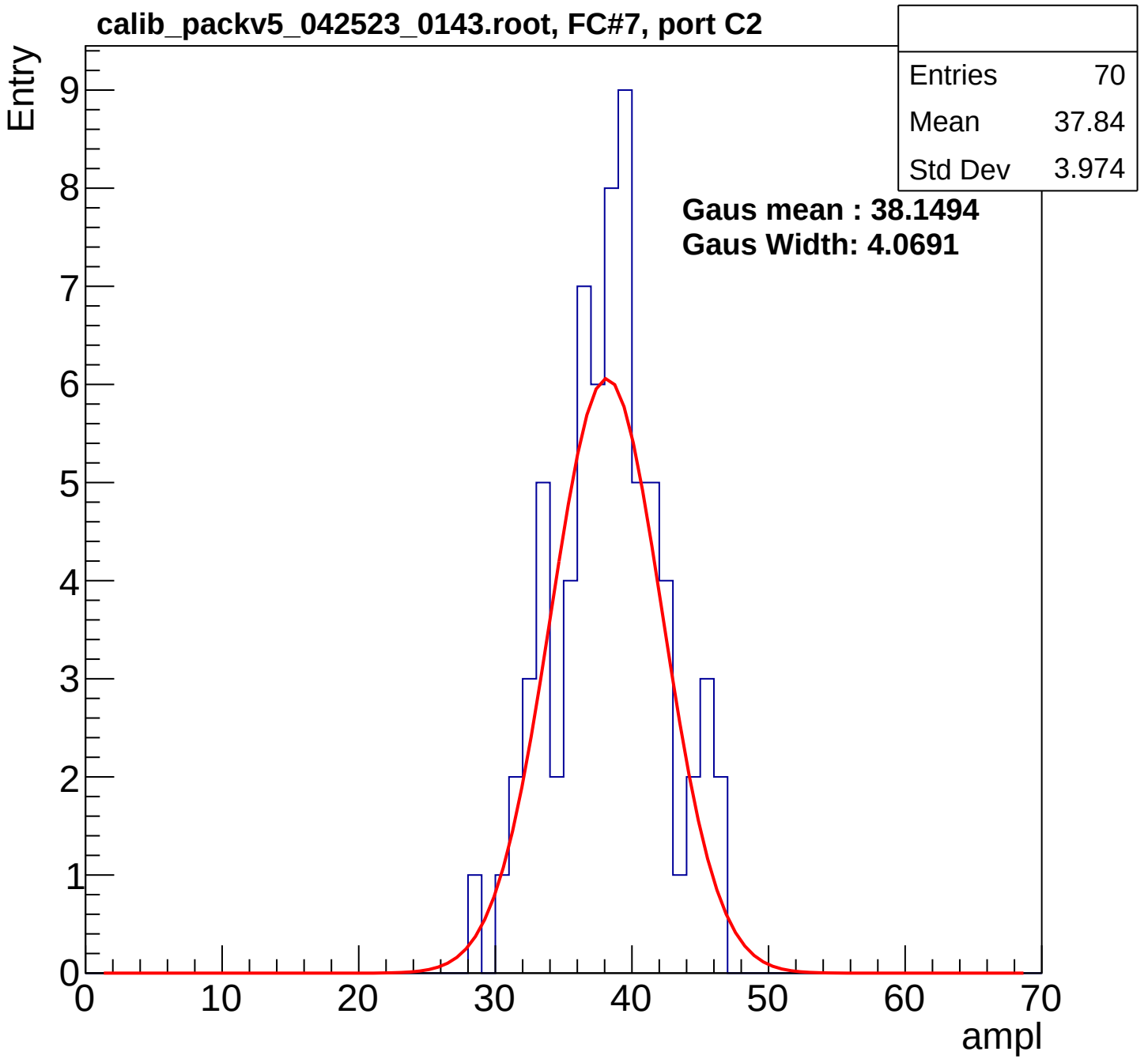
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch5, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 31.17
Std Dev 5.095
Gaus mean : 32.5785
Gaus Width: 3.7707
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch5, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 37.84
Std Dev 3.974
Gaus mean : 38.1494
Gaus Width: 4.0691
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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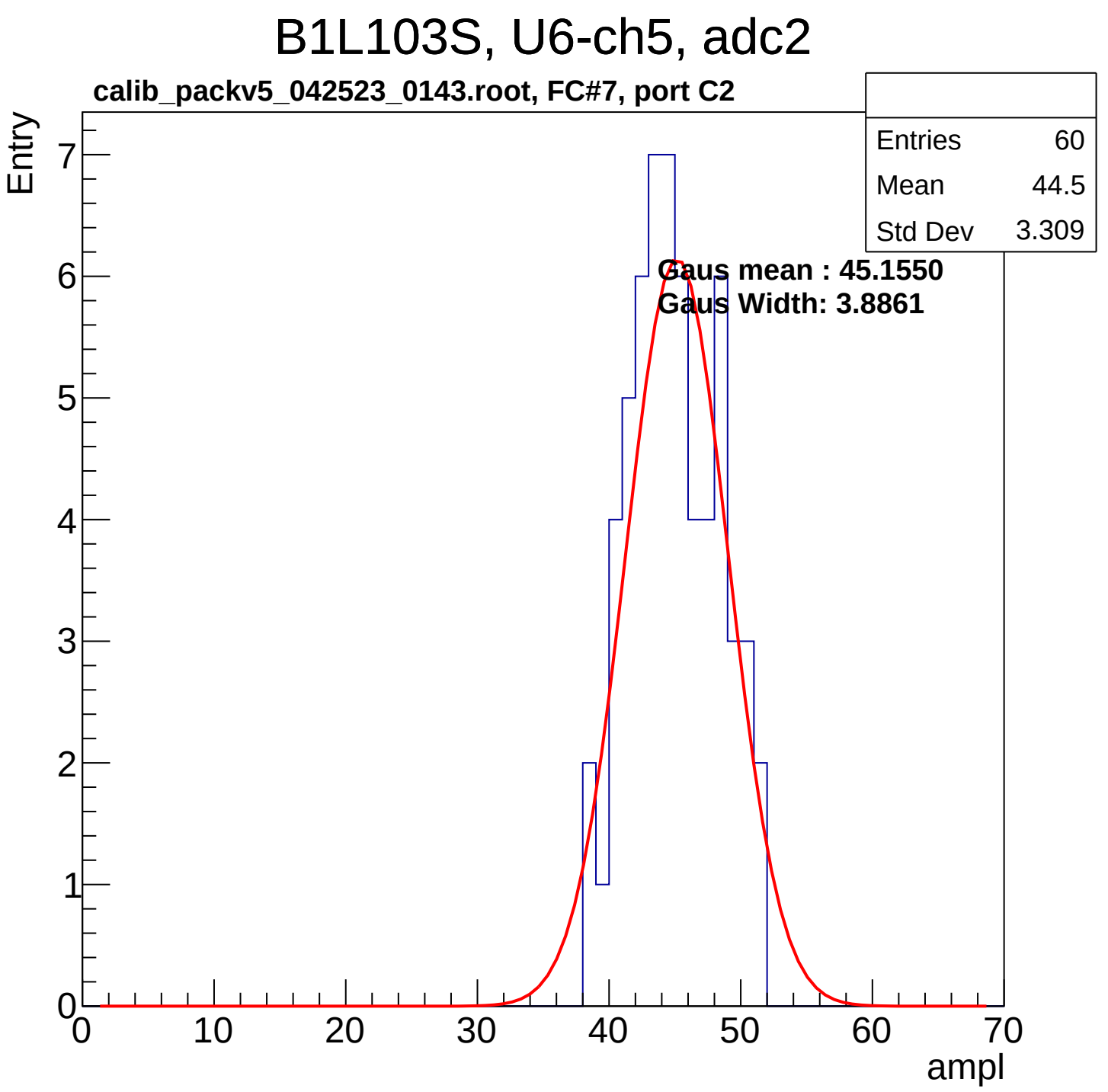
B1L103S, U6-ch5, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 44.5
Std Dev 3.309
Gaus mean : 45.1550
Gaus Width: 3.8861
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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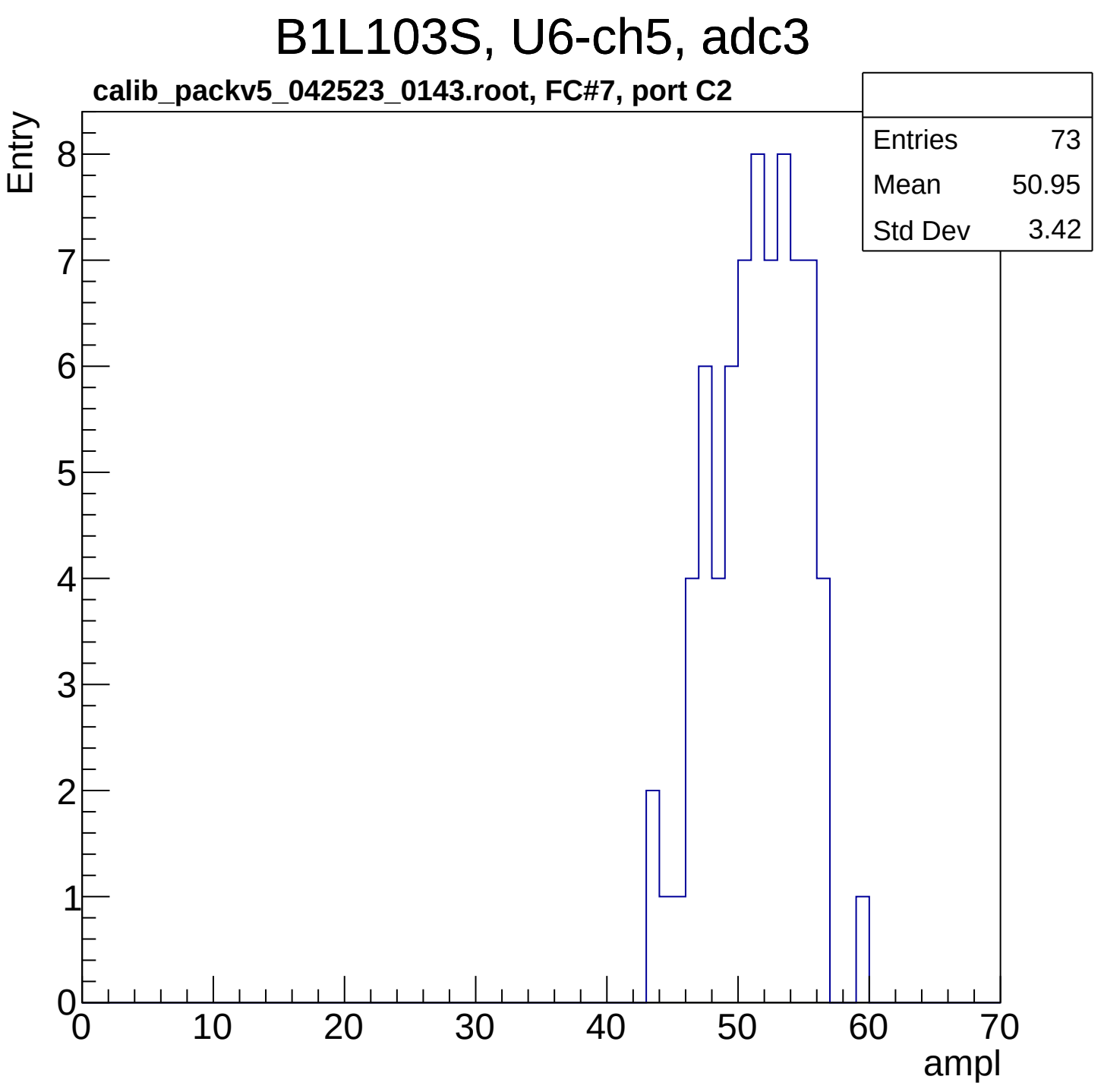
B1L103S, U6-ch5, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50.95
Std Dev 3.42
Entry
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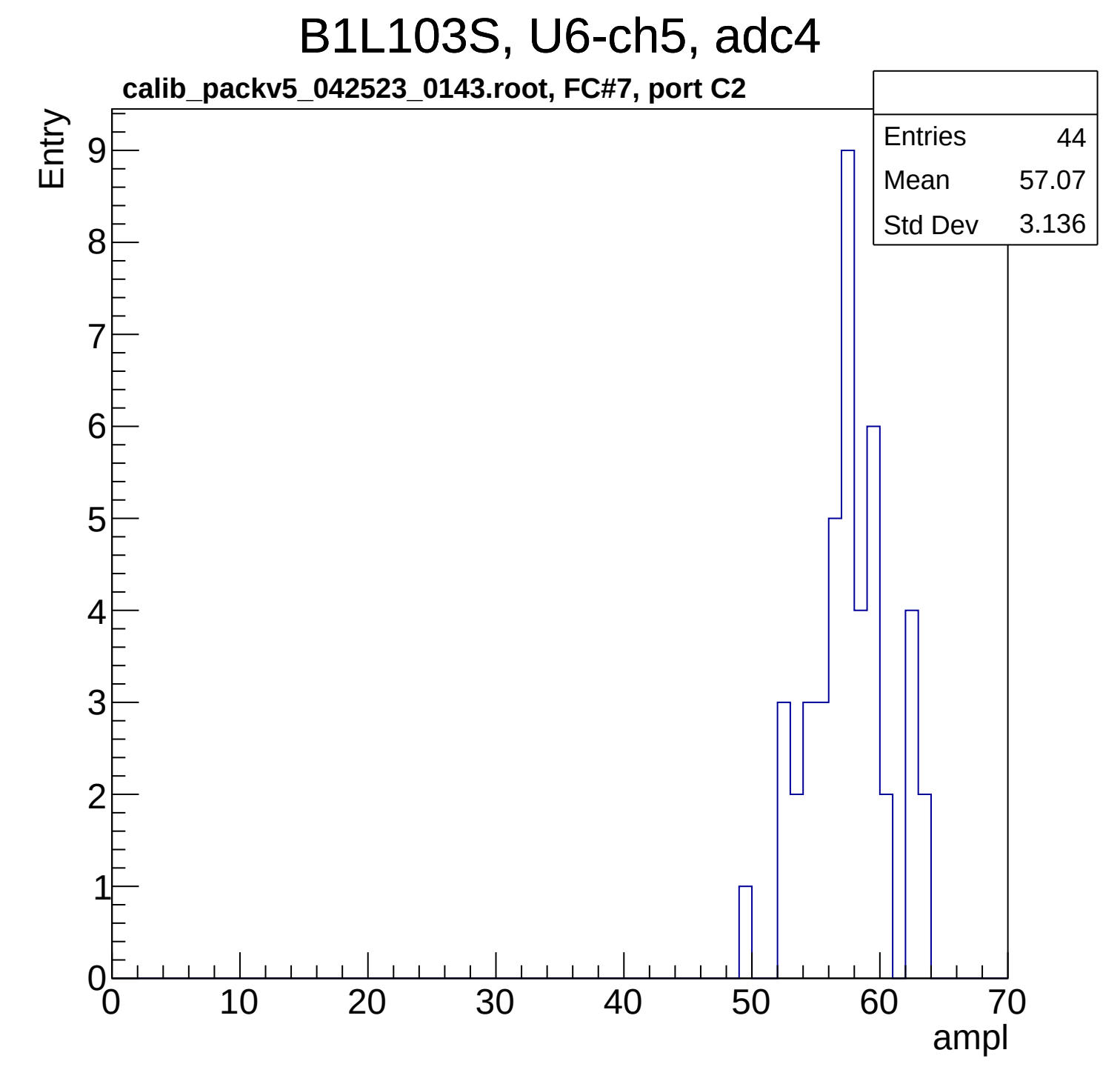
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch5, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 57.07
Std Dev 3.136
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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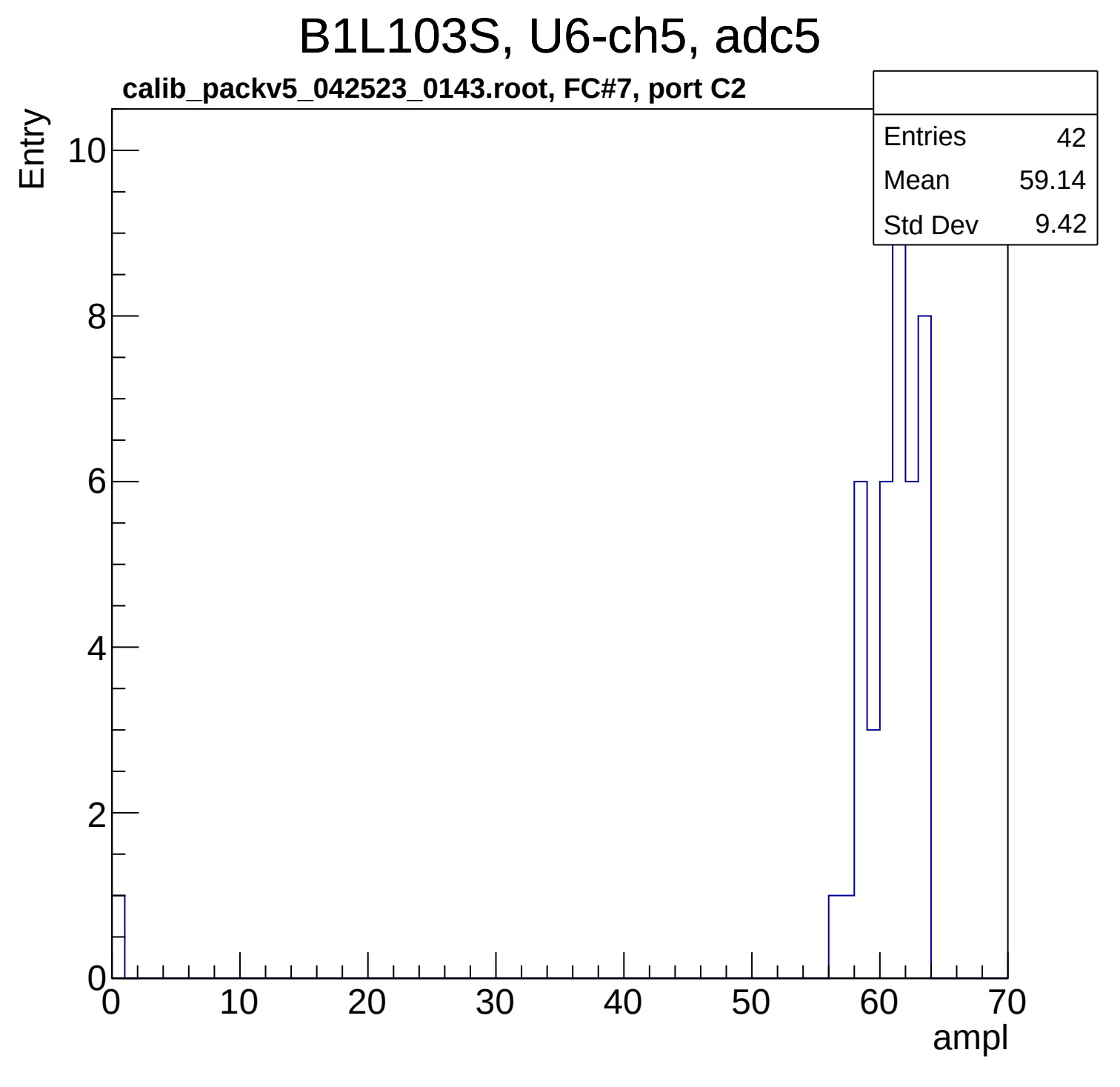
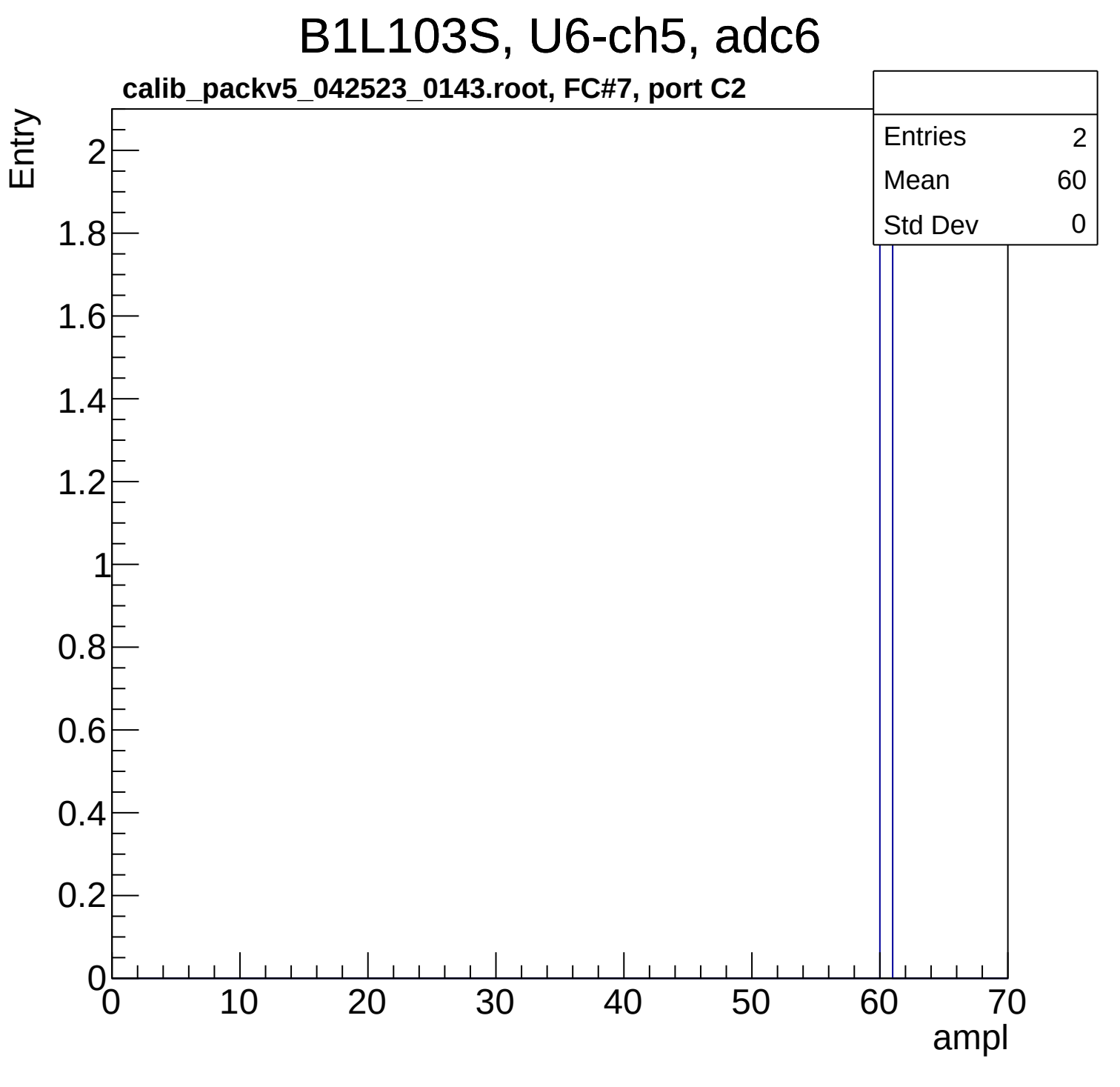
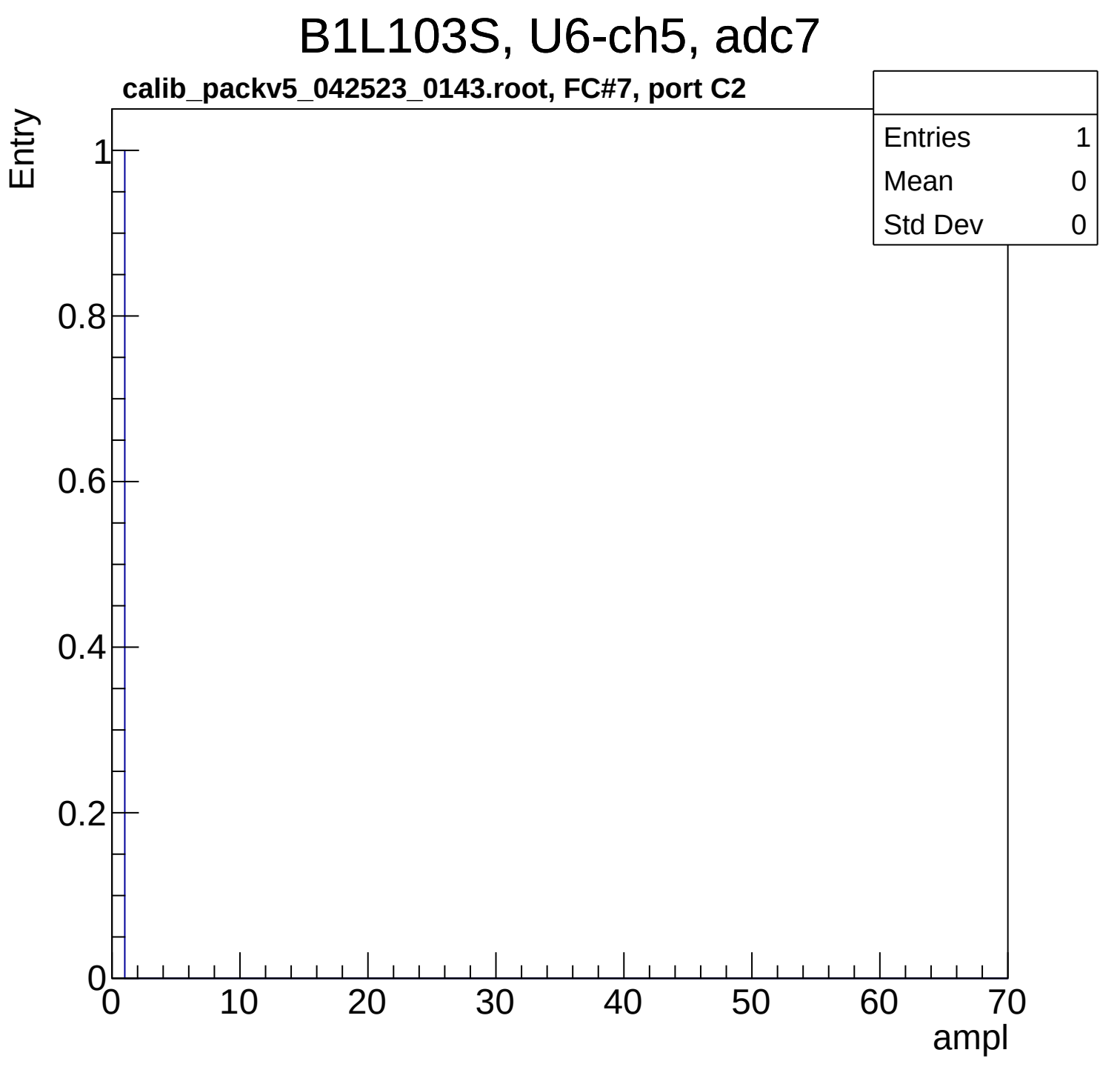
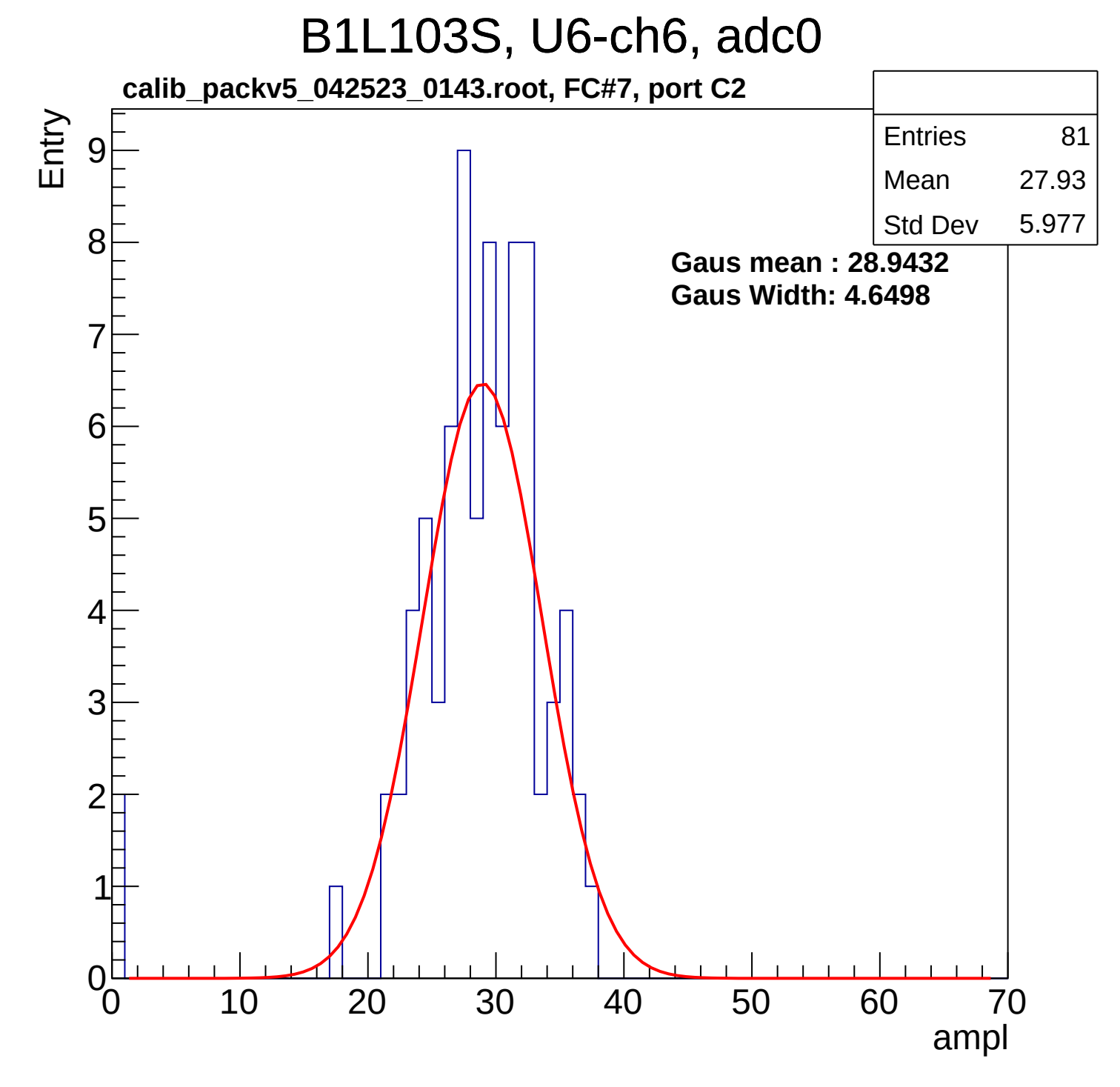

B1L103S, U6-ch5, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 59.14
Std Dev 9.42
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch5, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 60
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch5, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch6, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 27.93
Std Dev 5.977
Gaus mean : 28.9432
Gaus Width: 4.6498
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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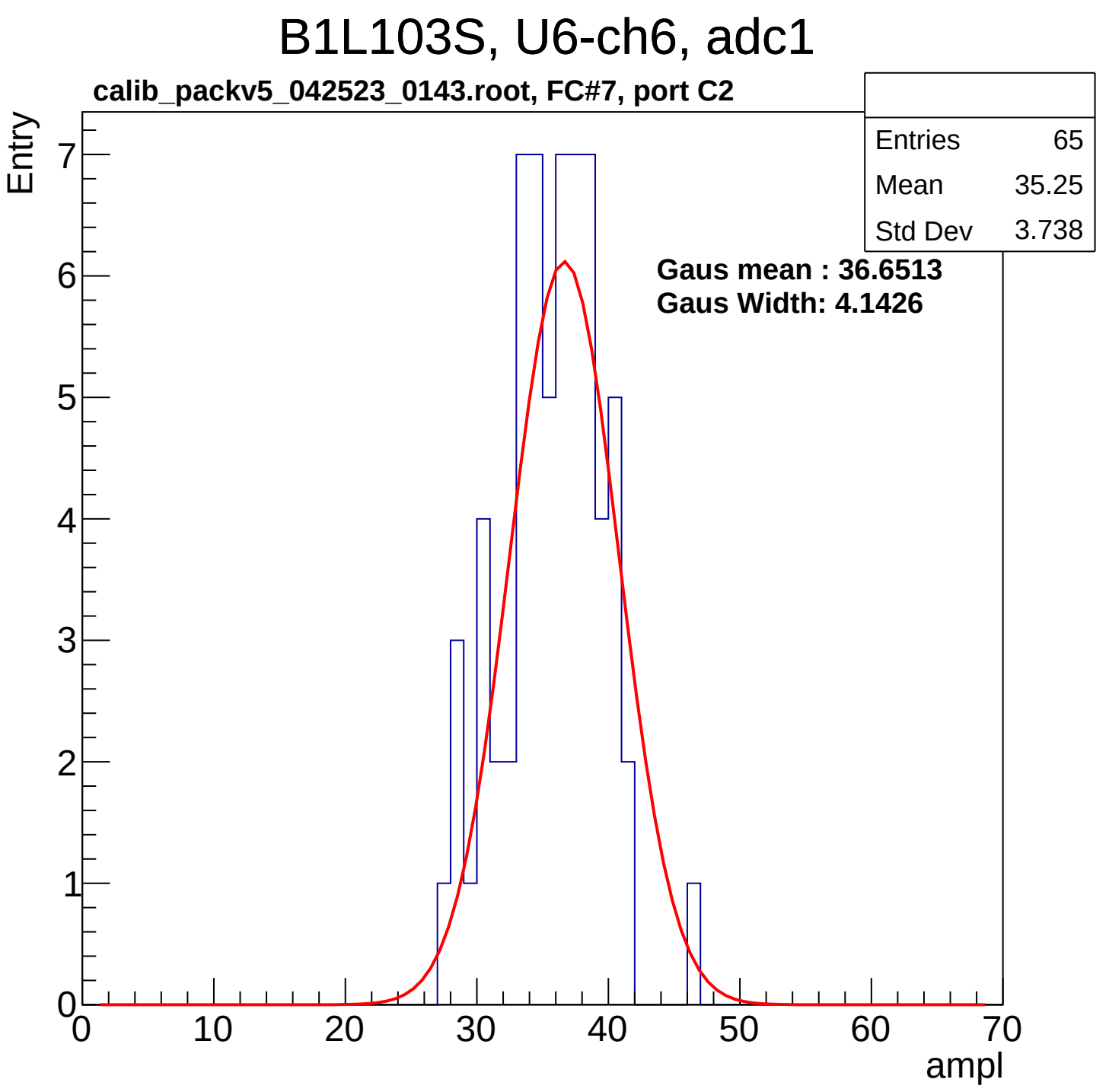
B1L103S, U6-ch6, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.25
Std Dev 3.738
Gaus mean : 36.6513
Gaus Width: 4.1426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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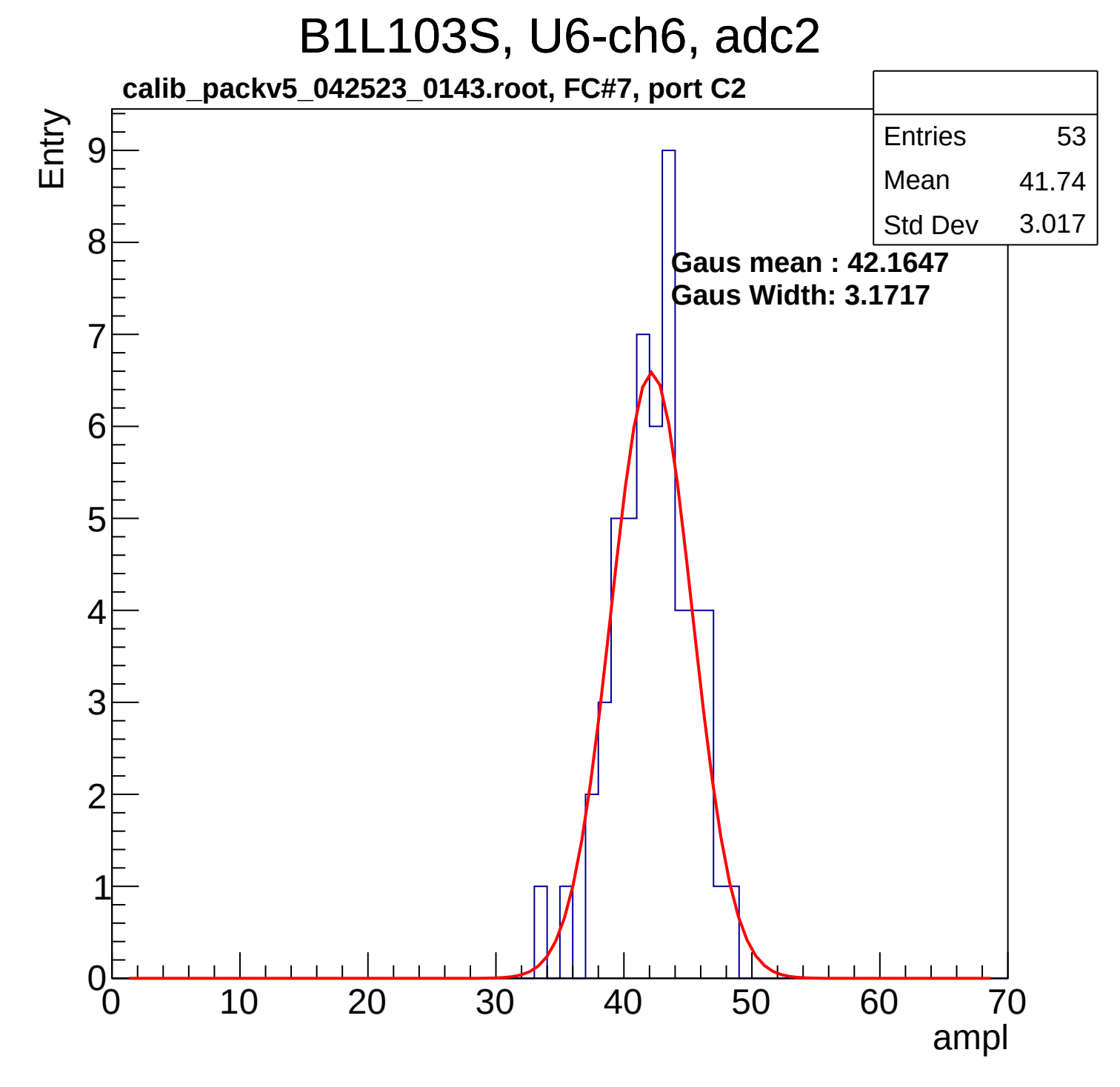
40
50
60
70


B1L103S, U6-ch6, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 41.74
Std Dev 3.017
Gaus mean : 42.1647
Gaus Width: 3.1717
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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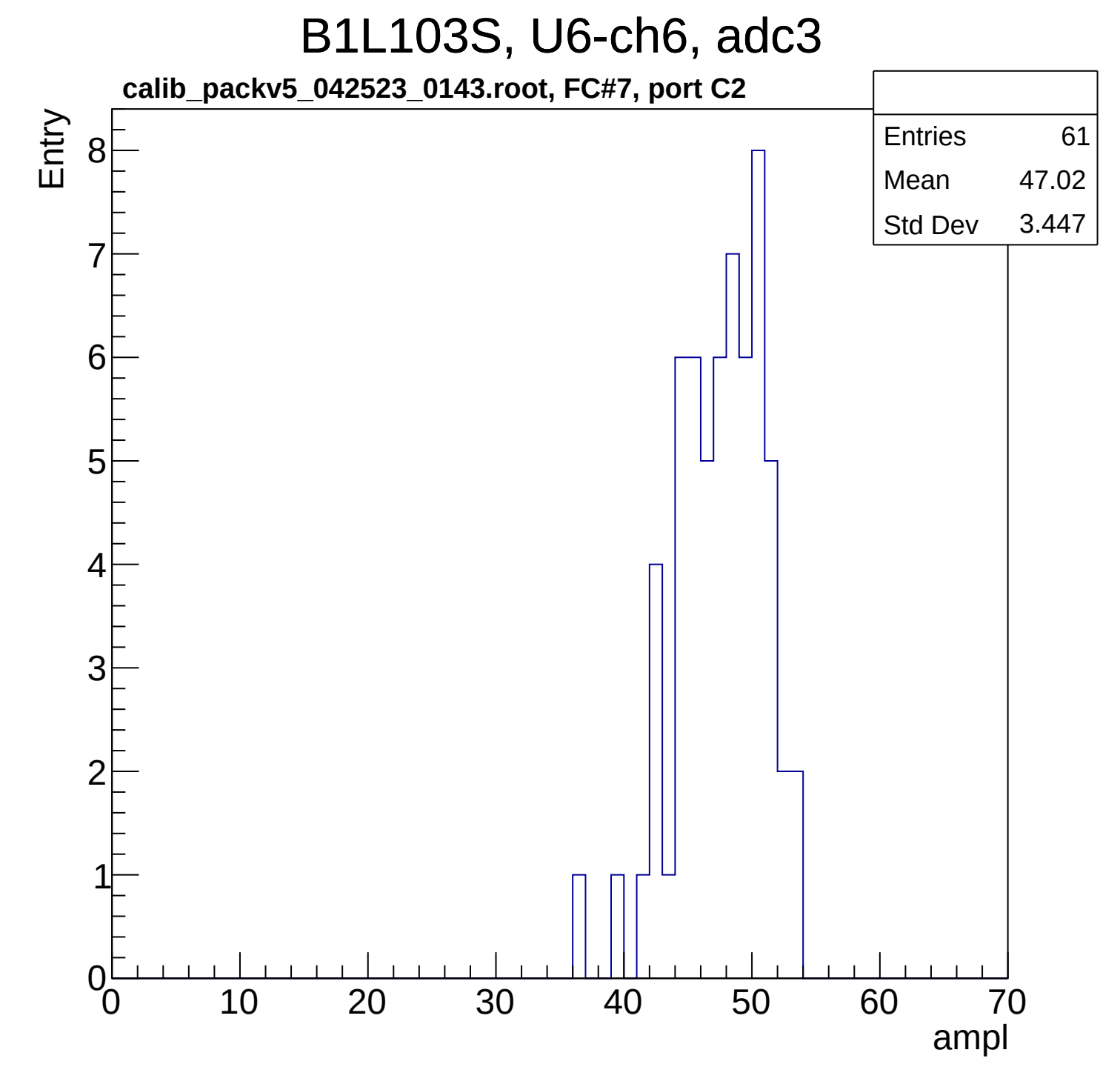
B1L103S, U6-ch6, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 47.02
Std Dev 3.447
Entry
ampl
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2
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4
5
6
7
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0
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40
50
60
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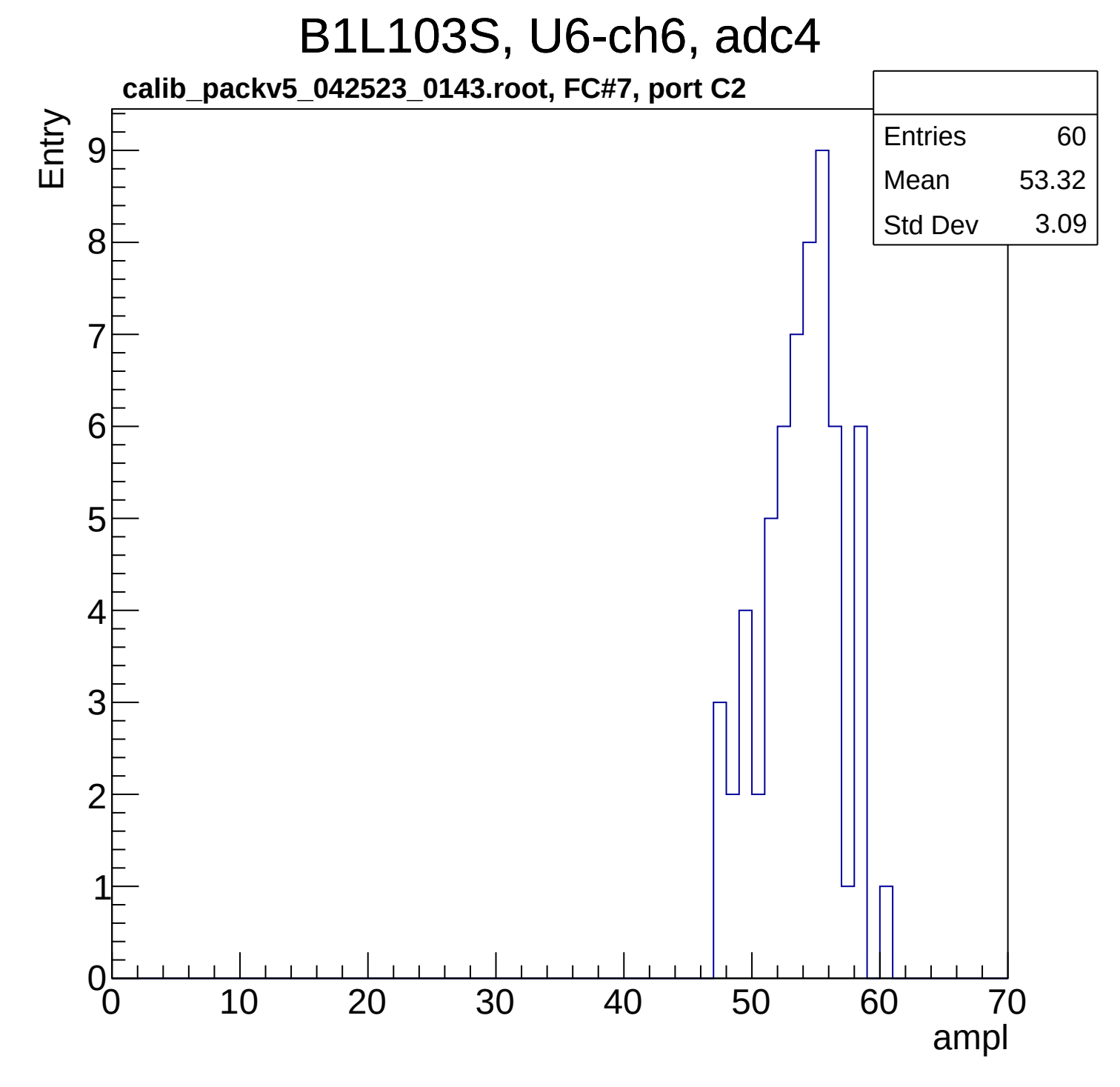
B1L103S, U6-ch6, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 53.32
Std Dev 3.09
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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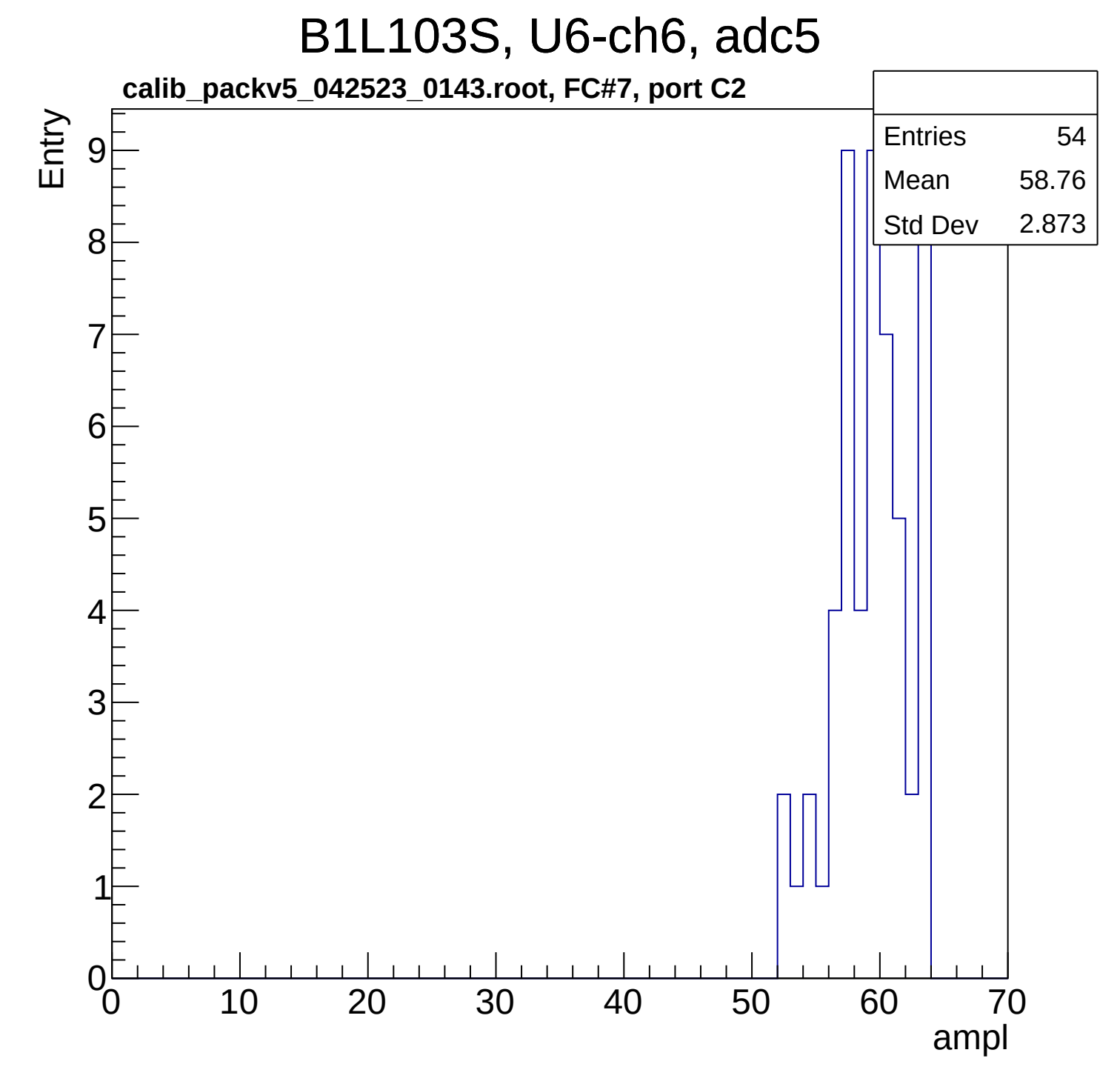
B1L103S, U6-ch6, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.76
Std Dev 2.873
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
50
60
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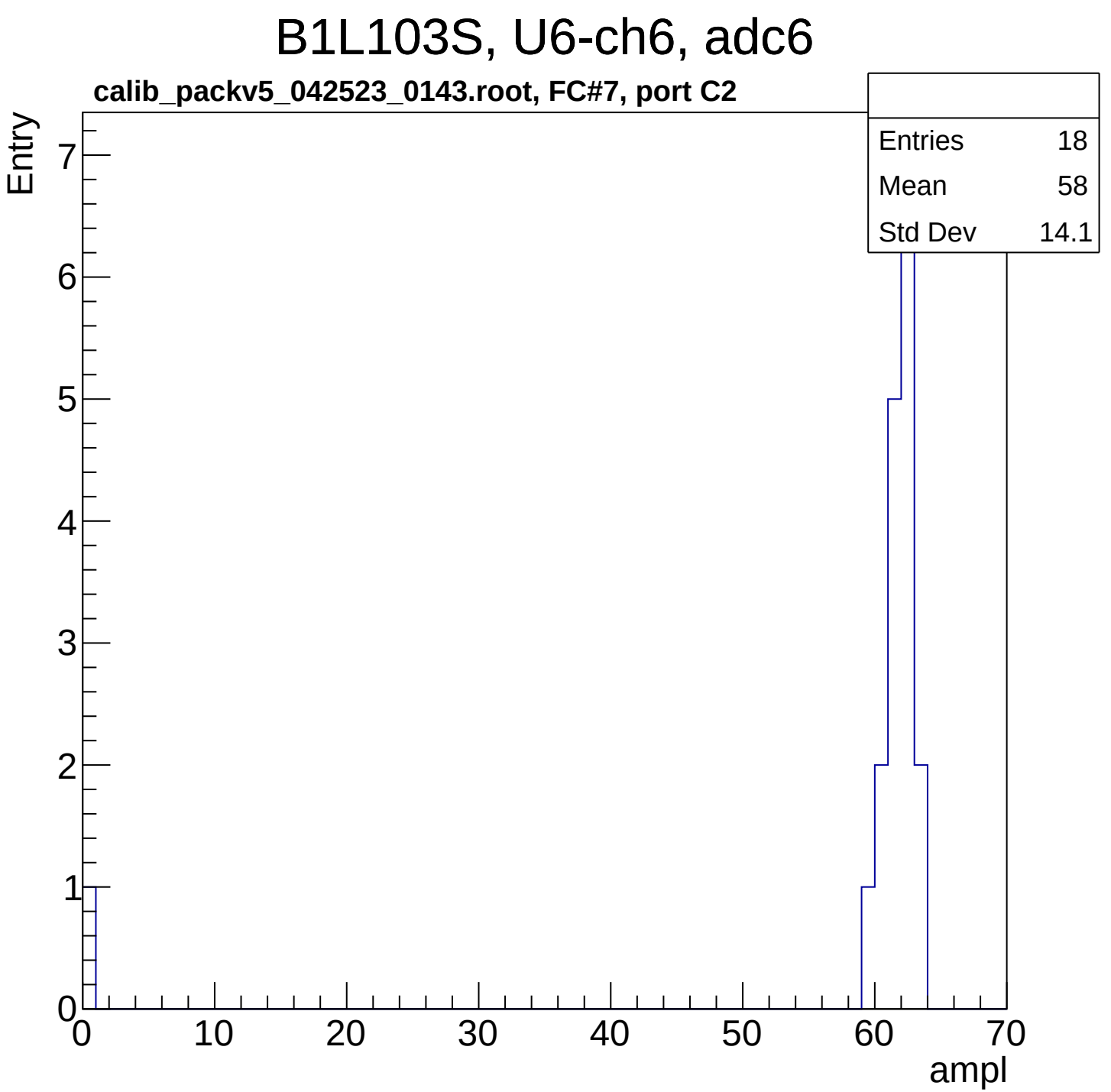
B1L103S, U6-ch6, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 58
Std Dev 14.1
Entry
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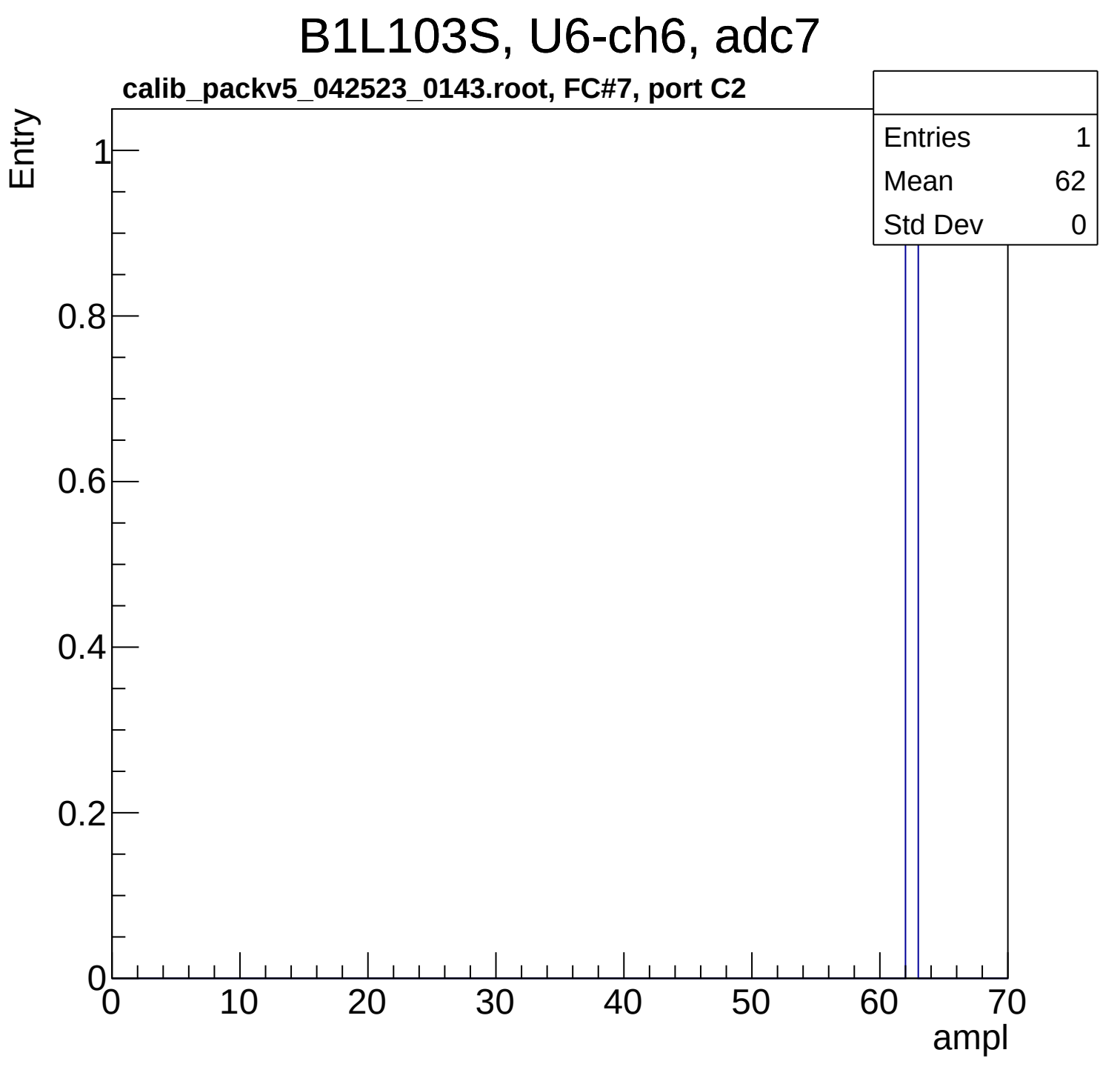
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch6, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
30
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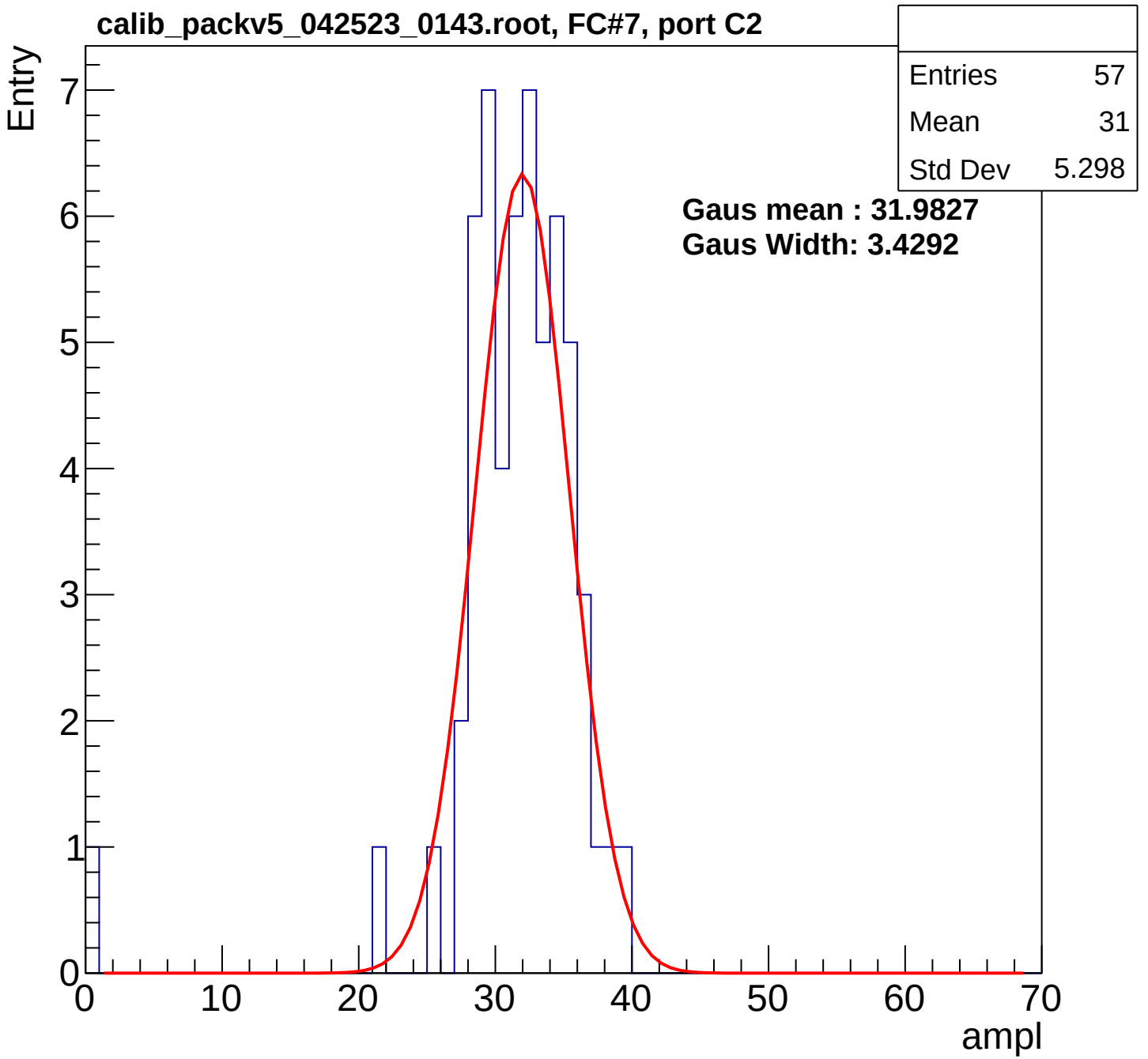
50
60
70
ampl


B1L103S, U6-ch7, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 31
Std Dev 5.298
Gaus mean : 31.9827
Gaus Width: 3.4292
Entry
ampl
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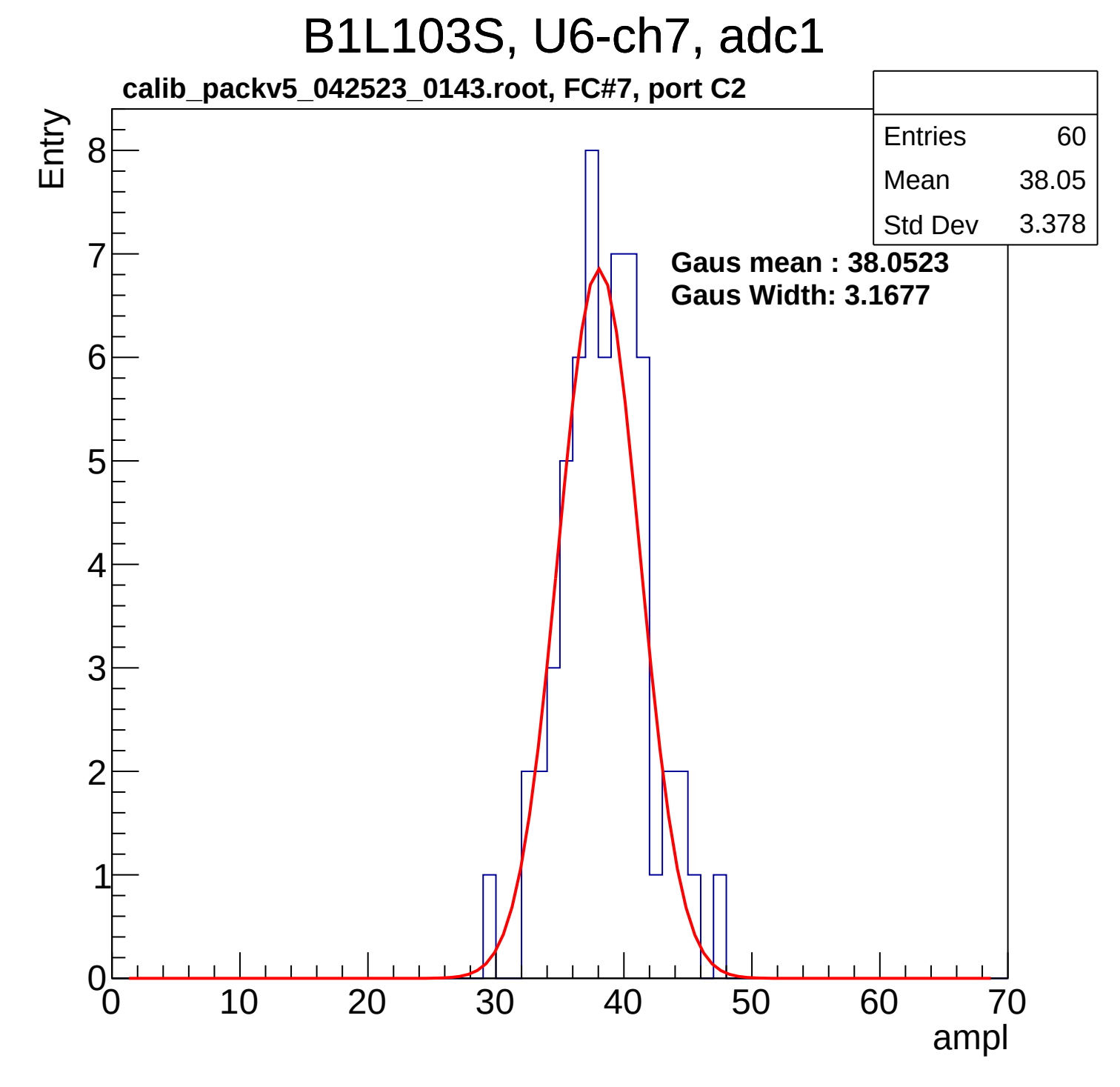
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch7, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 38.05
Std Dev 3.378
Gaus mean : 38.0523
Gaus Width: 3.1677
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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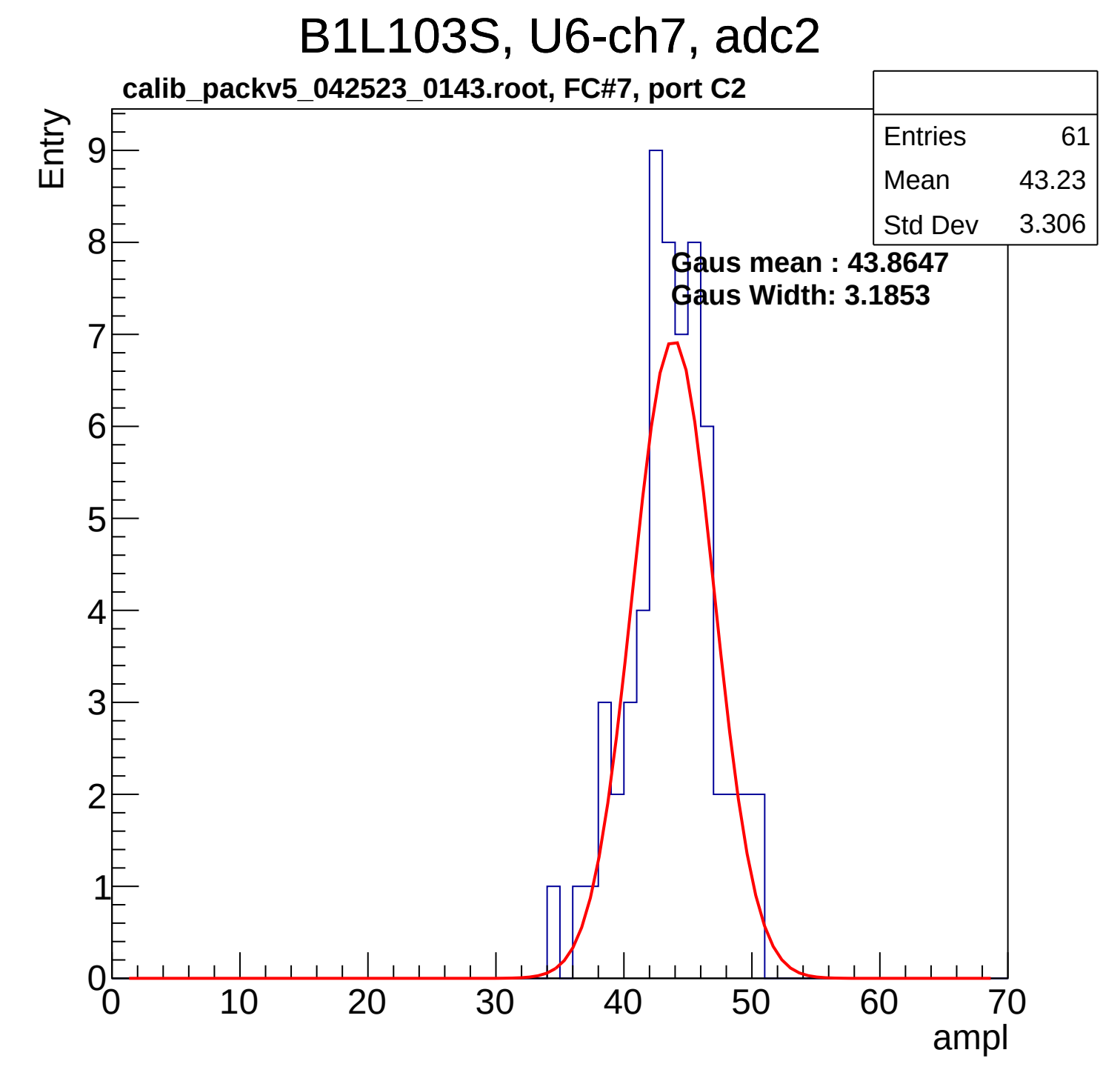
B1L103S, U6-ch7, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.23
Std Dev 3.306
Gaus mean : 43.8647
Gaus Width: 3.1853
Entry
ampl
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2
3
4
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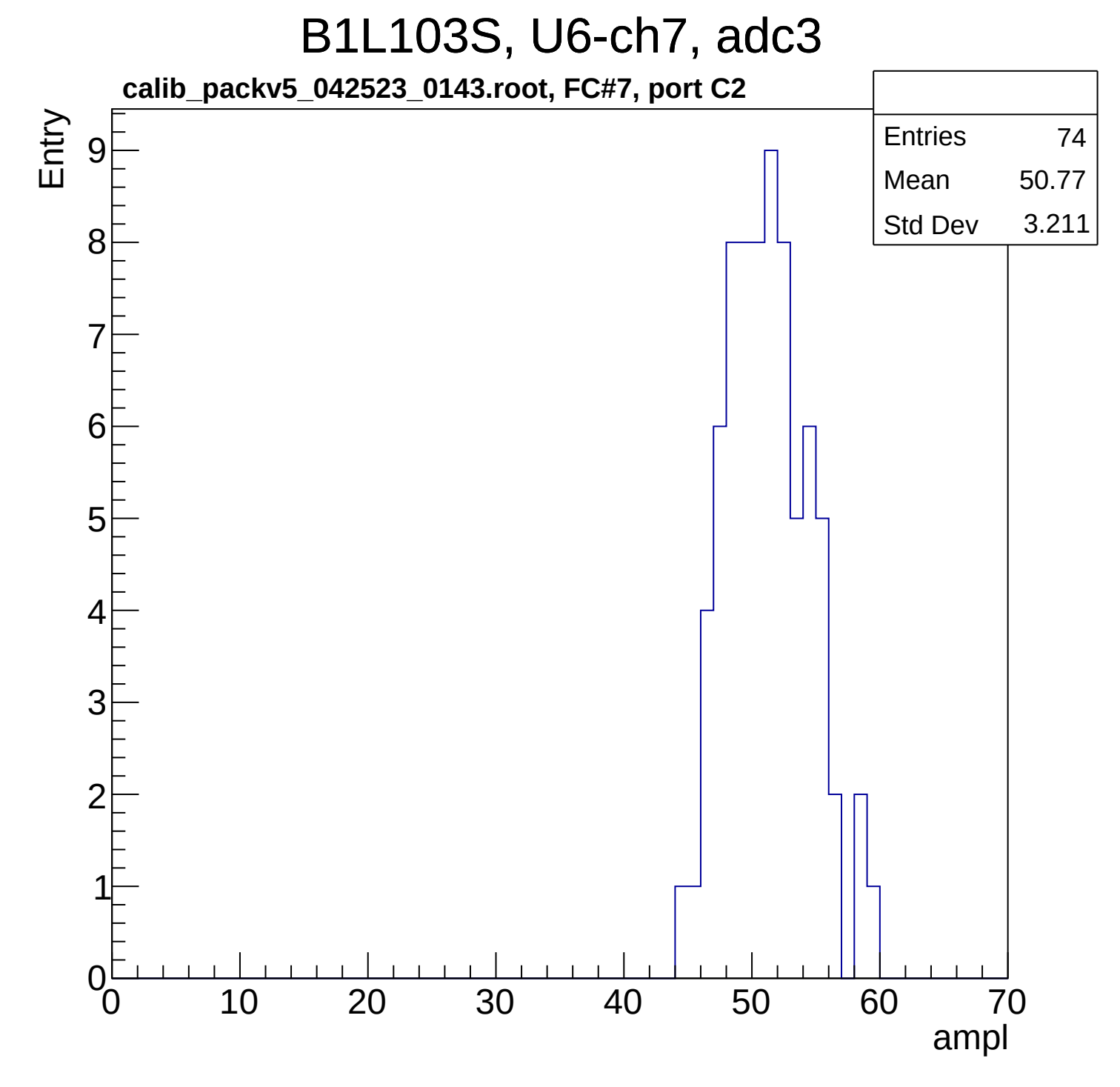
B1L103S, U6-ch7, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 50.77
Std Dev 3.211
Entry
ampl
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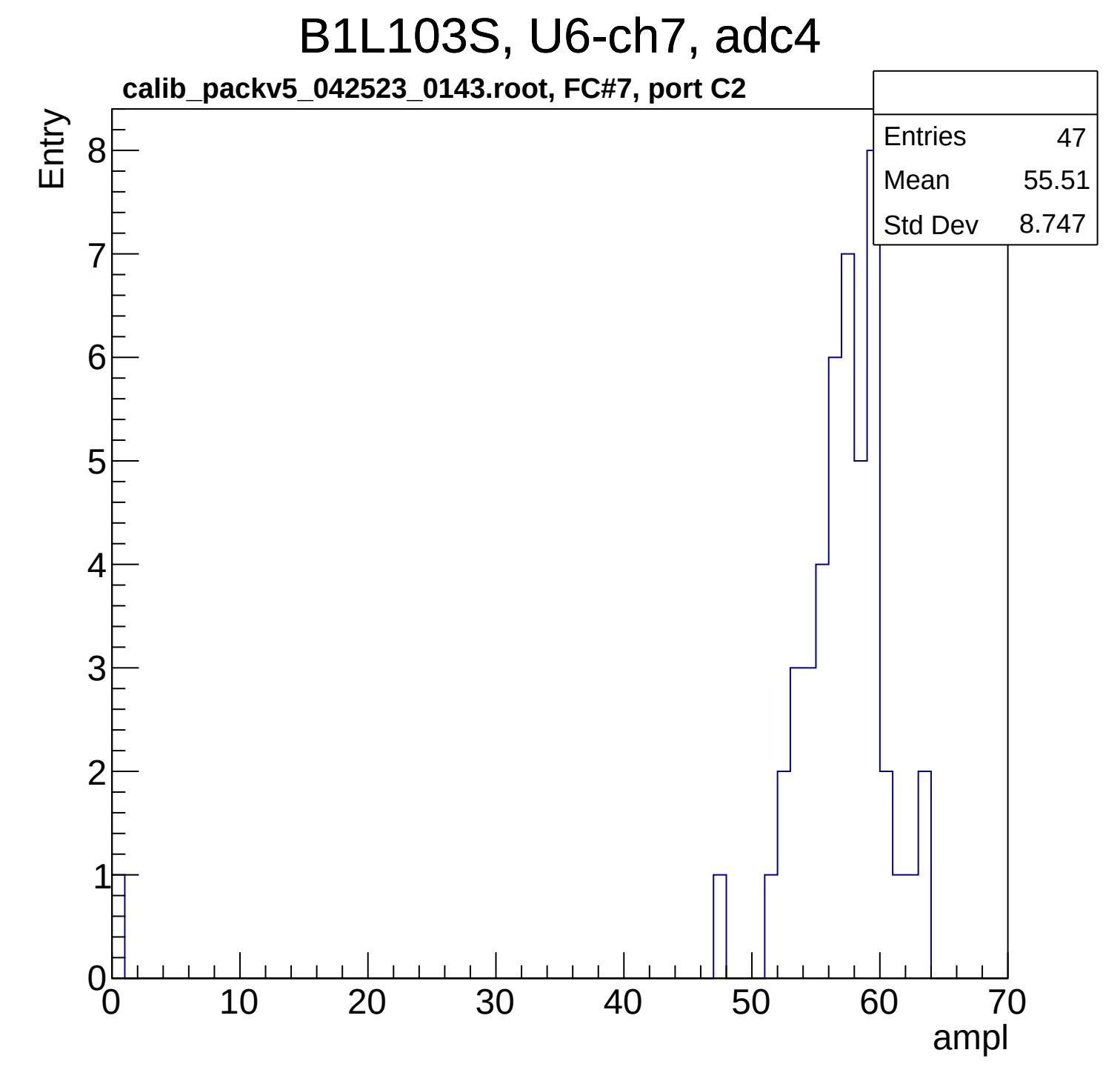
B1L103S, U6-ch7, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 55.51
Std Dev 8.747
Entry
ampl
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60
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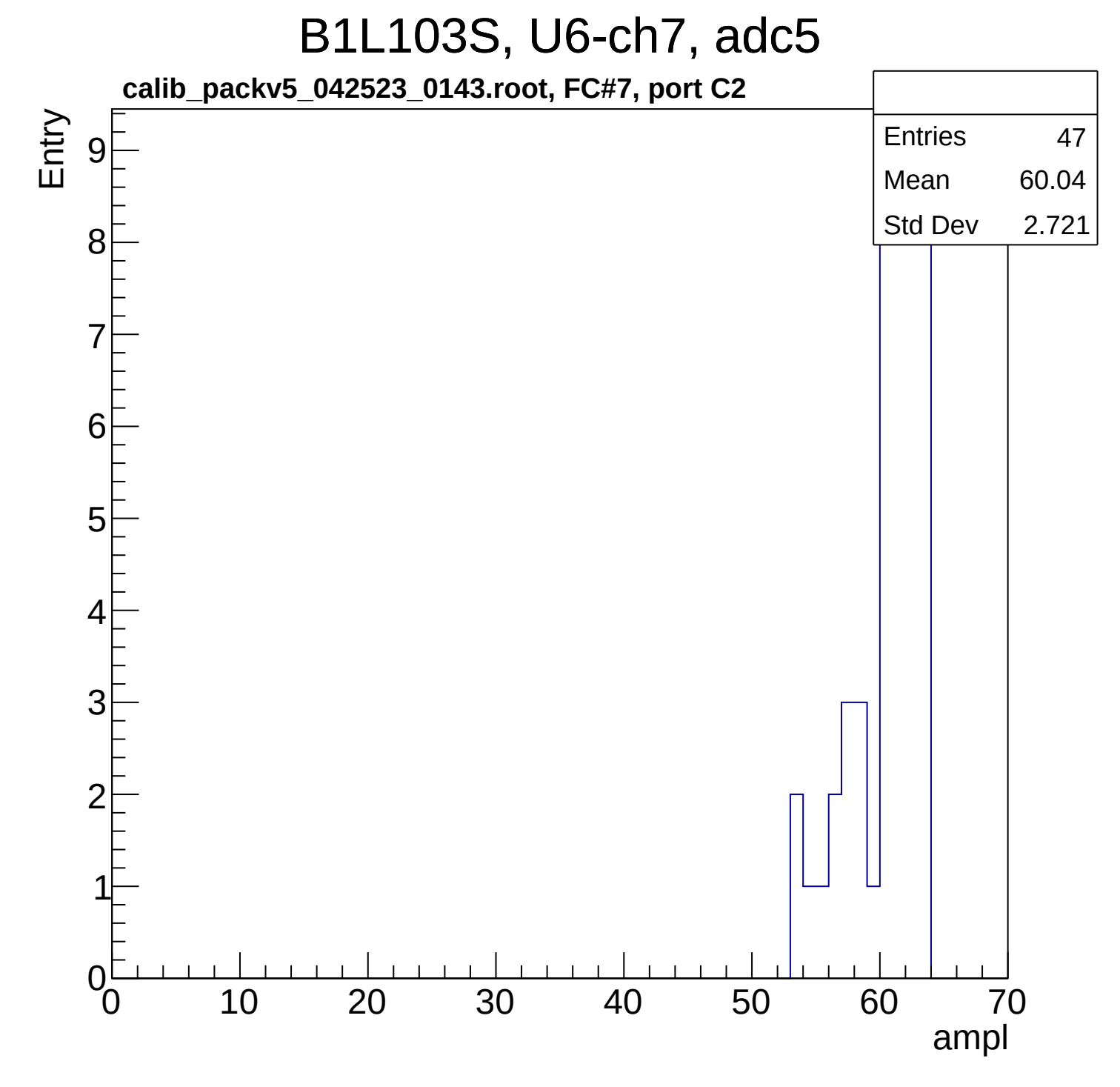
B1L103S, U6-ch7, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 60.04
Std Dev 2.721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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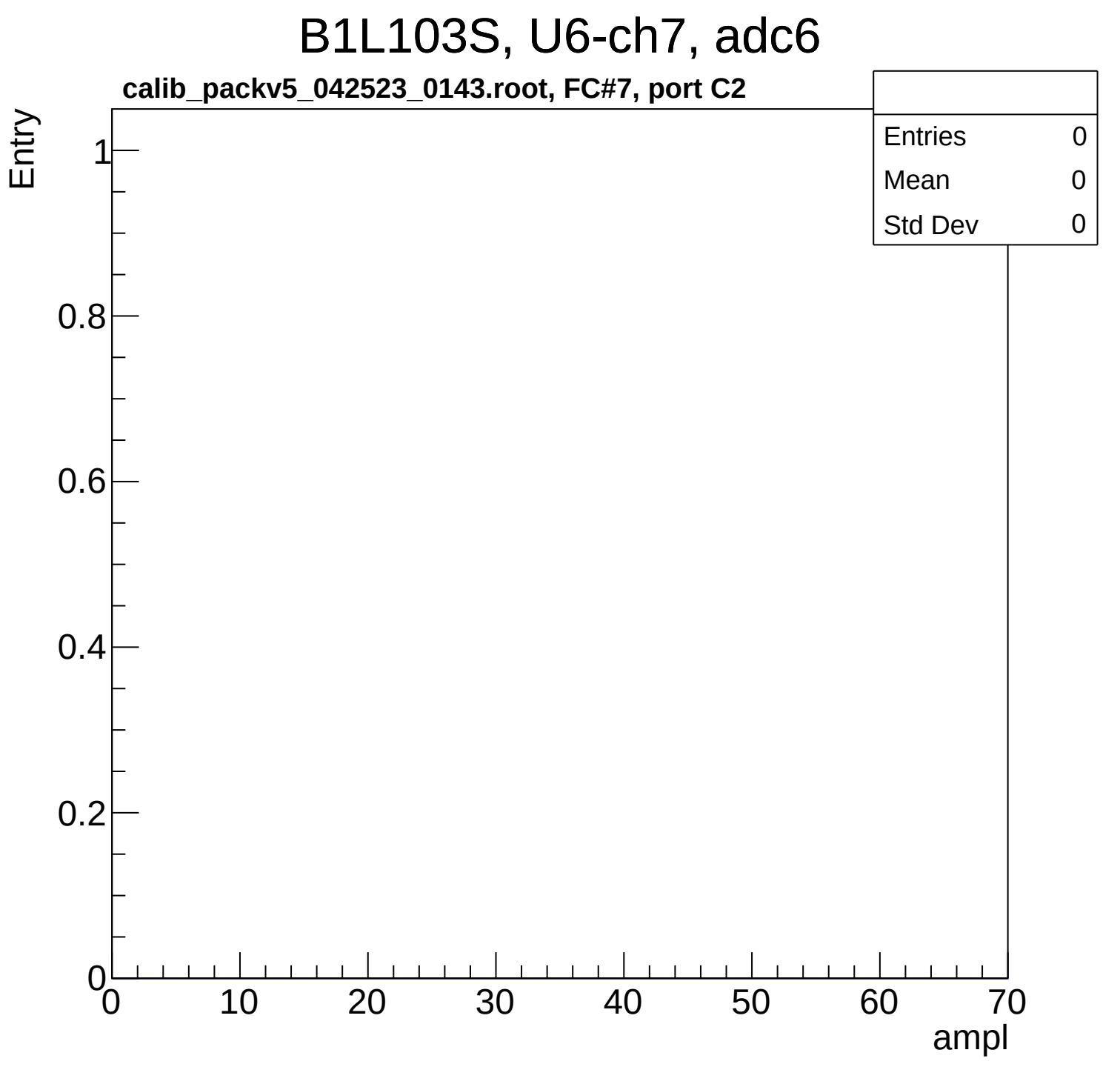
40
50
60
70


B1L103S, U6-ch7, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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10
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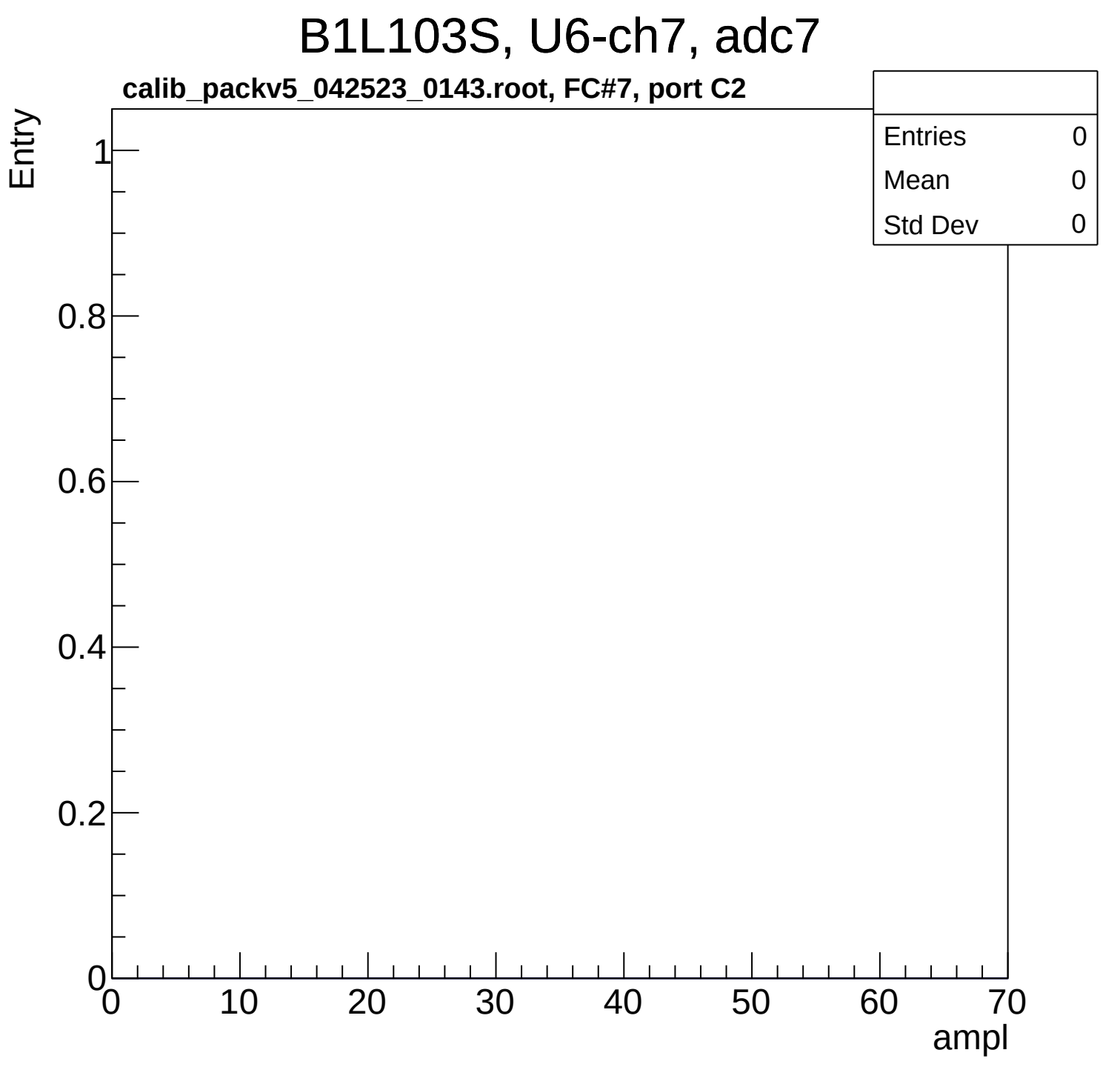
70
ampl


B1L103S, U6-ch7, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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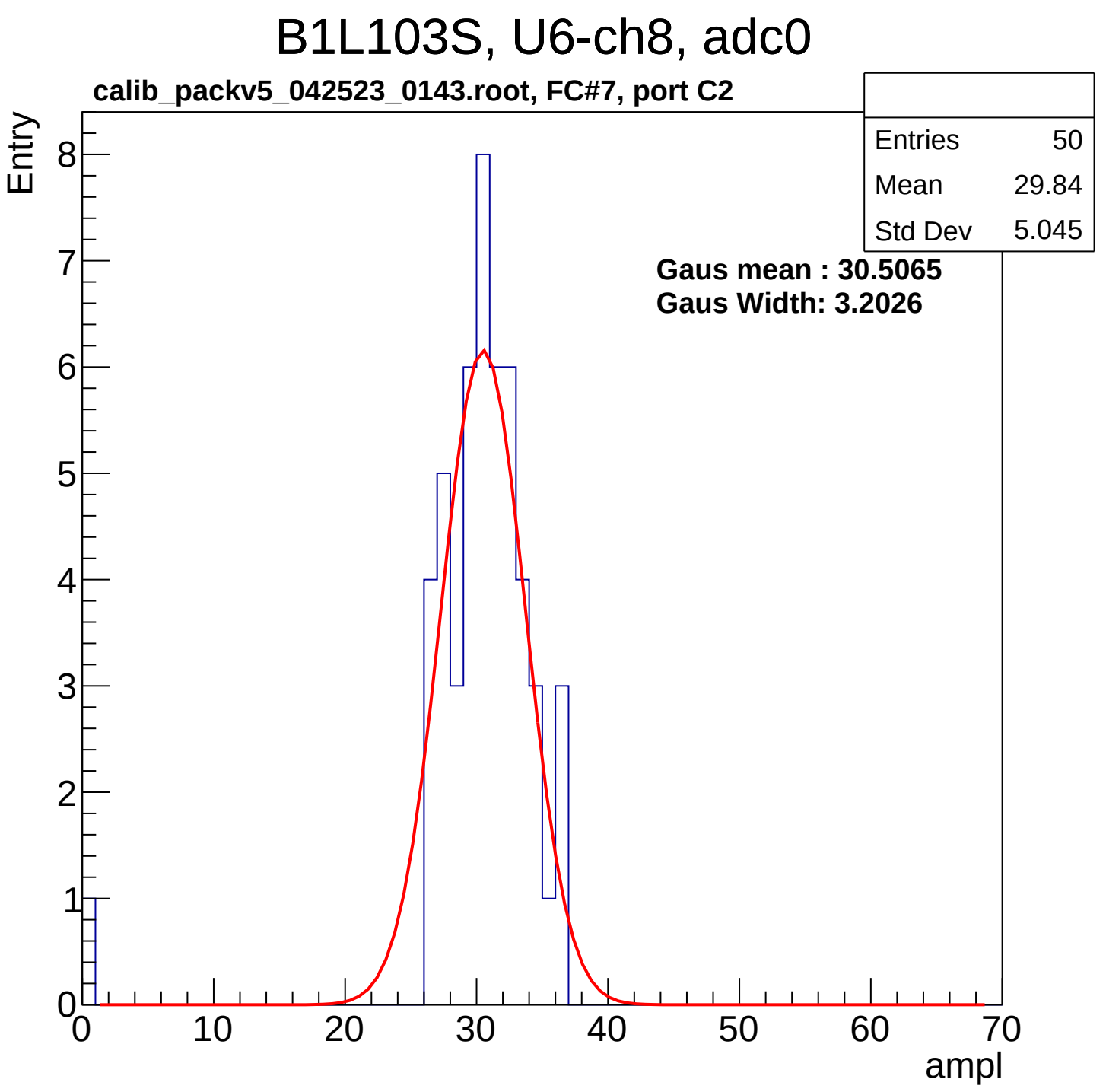
50
60
70
ampl


B1L103S, U6-ch8, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 29.84
Std Dev 5.045
Gaus mean : 30.5065
Gaus Width: 3.2026
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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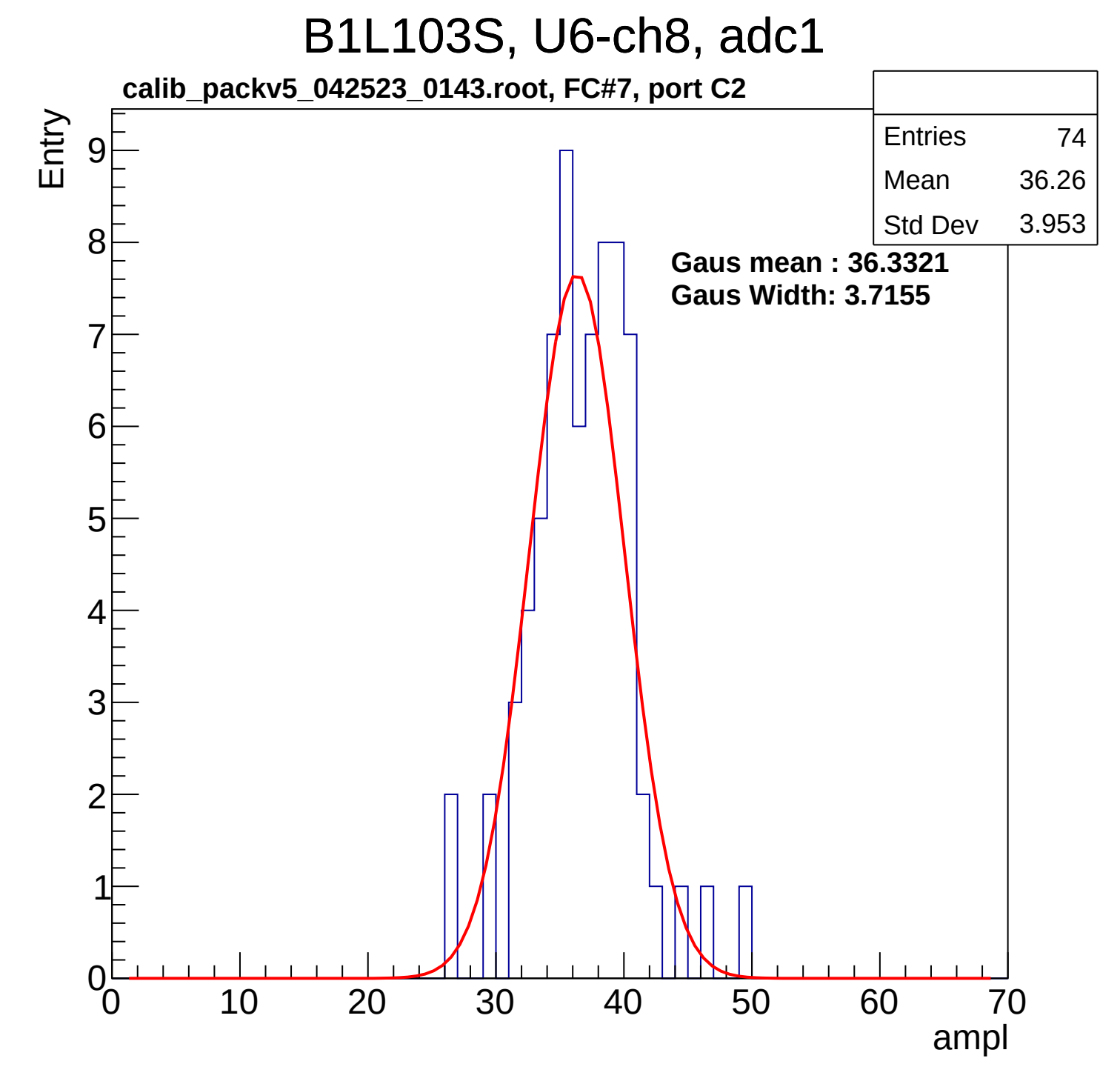
B1L103S, U6-ch8, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 36.26
Std Dev 3.953
Gaus mean : 36.3321
Gaus Width: 3.7155
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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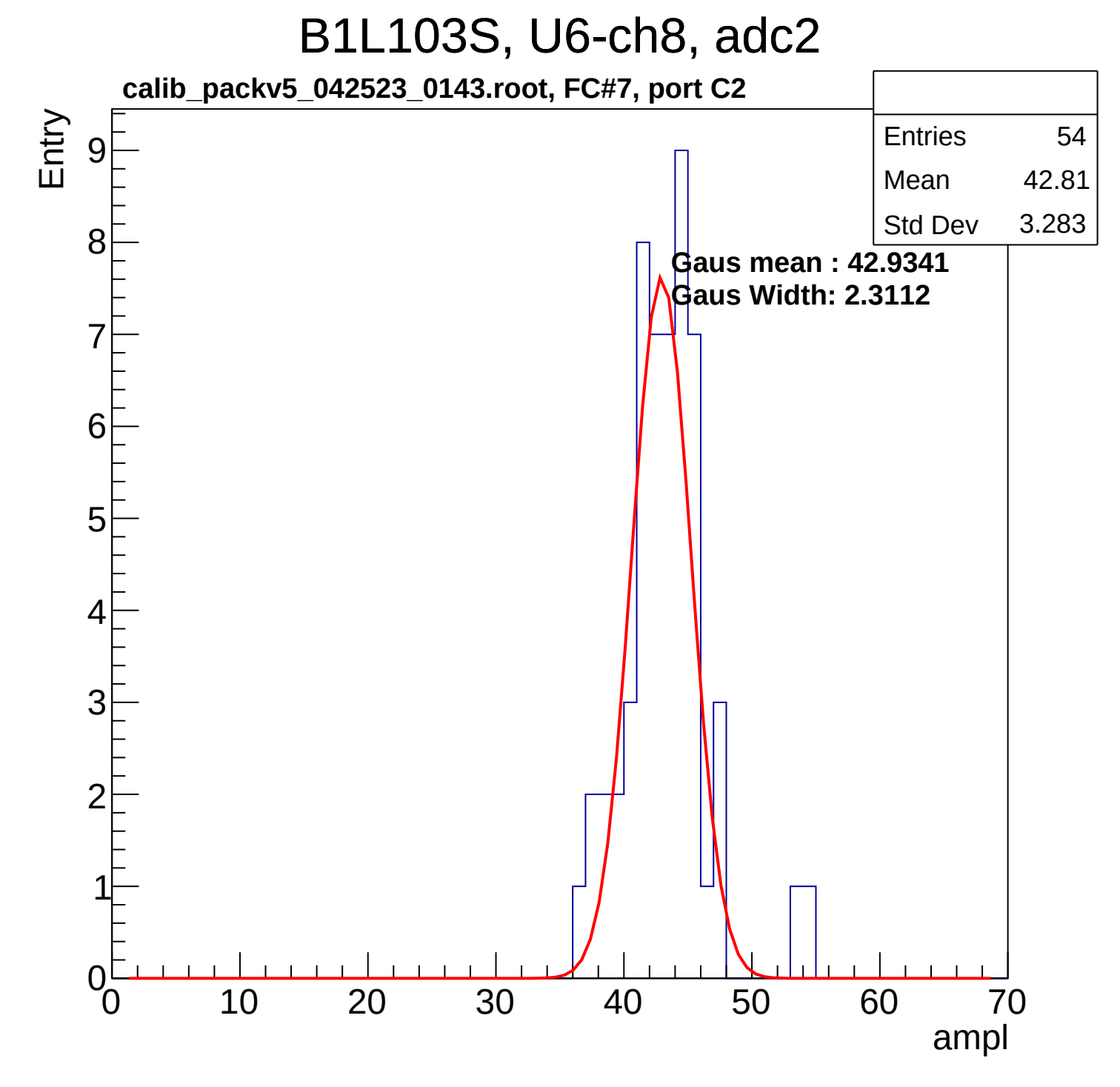
B1L103S, U6-ch8, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 42.81
Std Dev 3.283
Gaus mean : 42.9341
Gaus Width: 2.3112
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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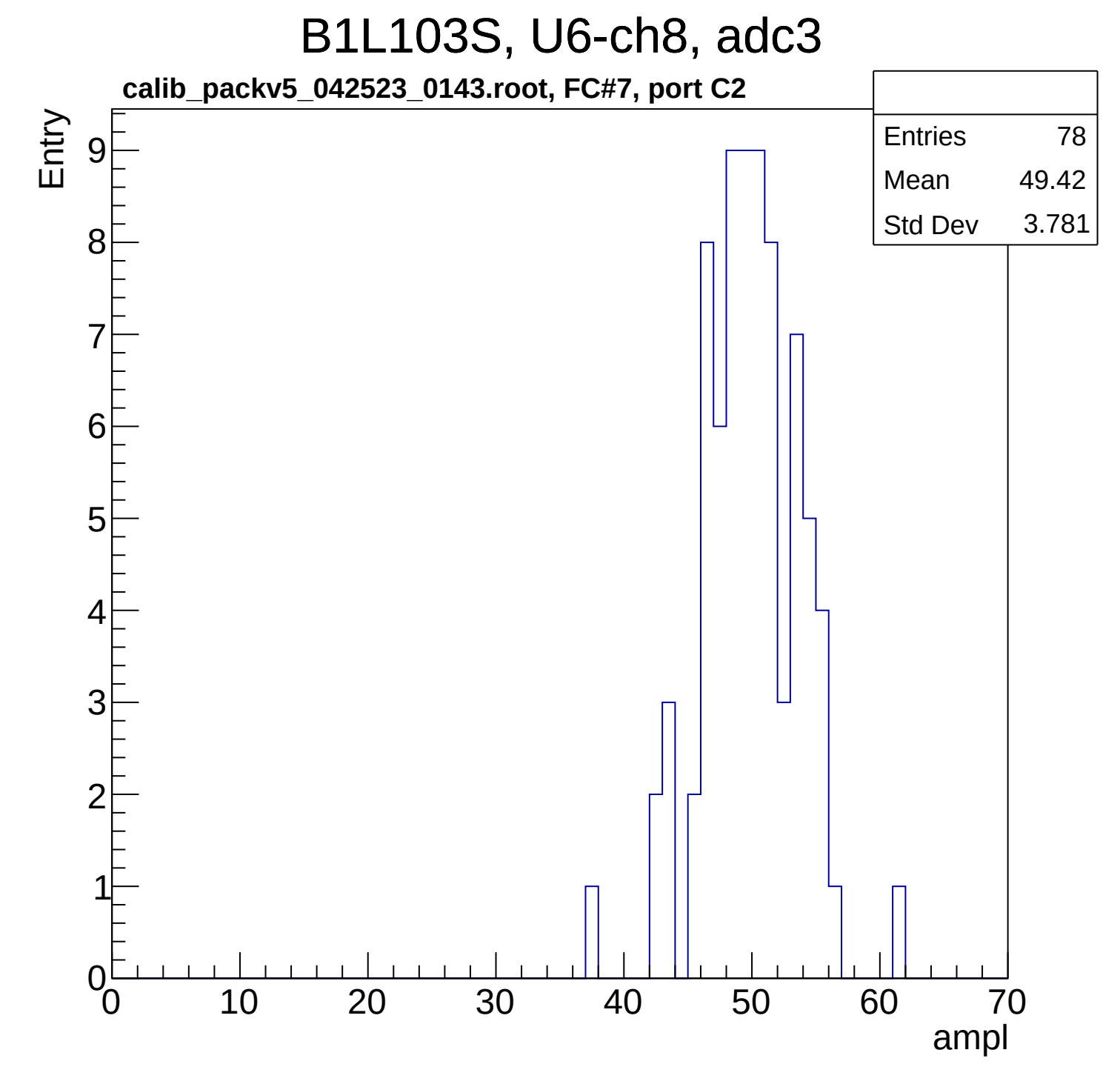
B1L103S, U6-ch8, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 49.42
Std Dev 3.781
Entry
ampl
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2
3
4
5
6
7
8
9
0
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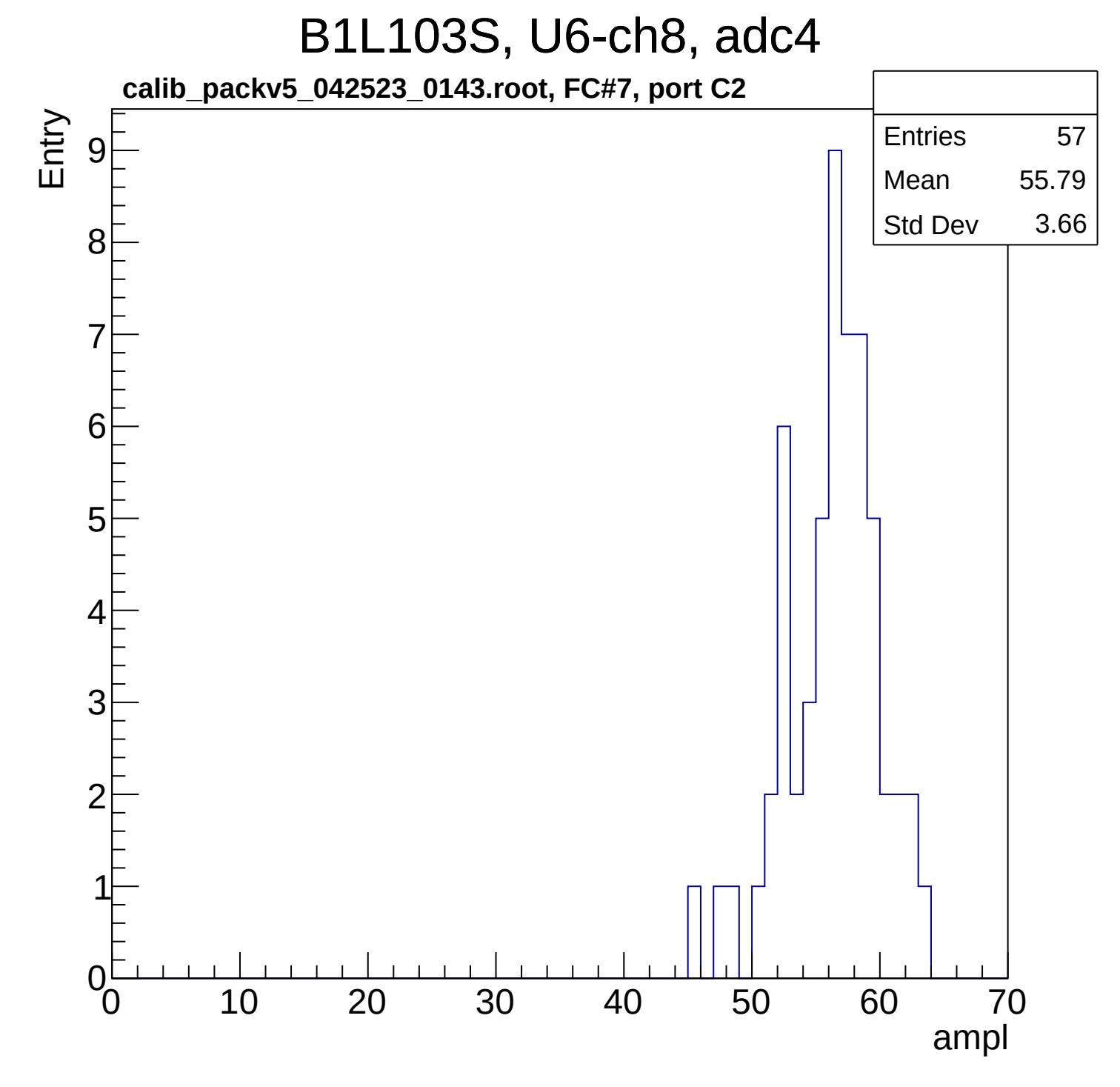
B1L103S, U6-ch8, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.79
Std Dev 3.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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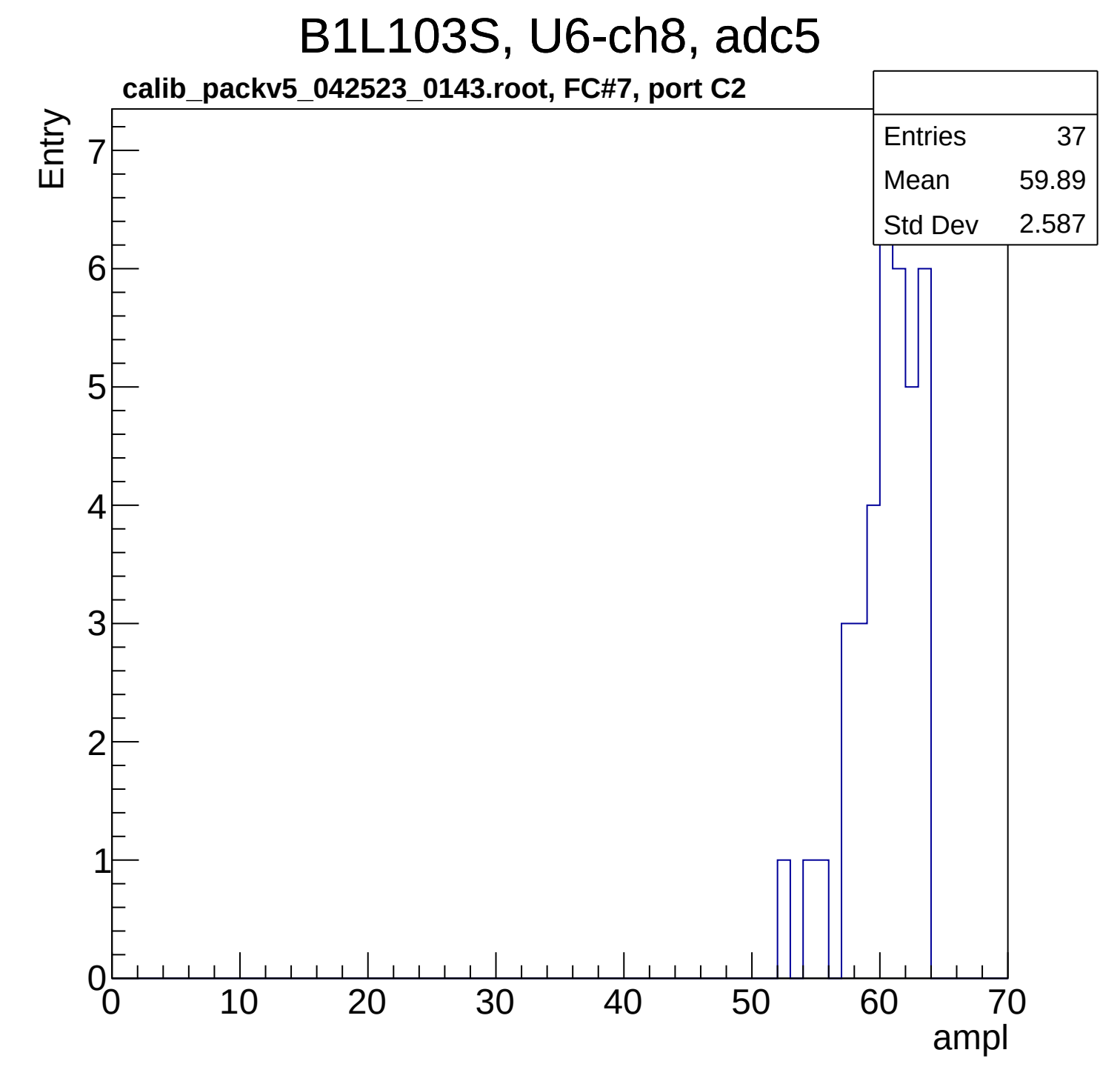
B1L103S, U6-ch8, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 59.89
Std Dev 2.587
Entry
ampl
0
1
2
3
4
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6
7
0
10
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60
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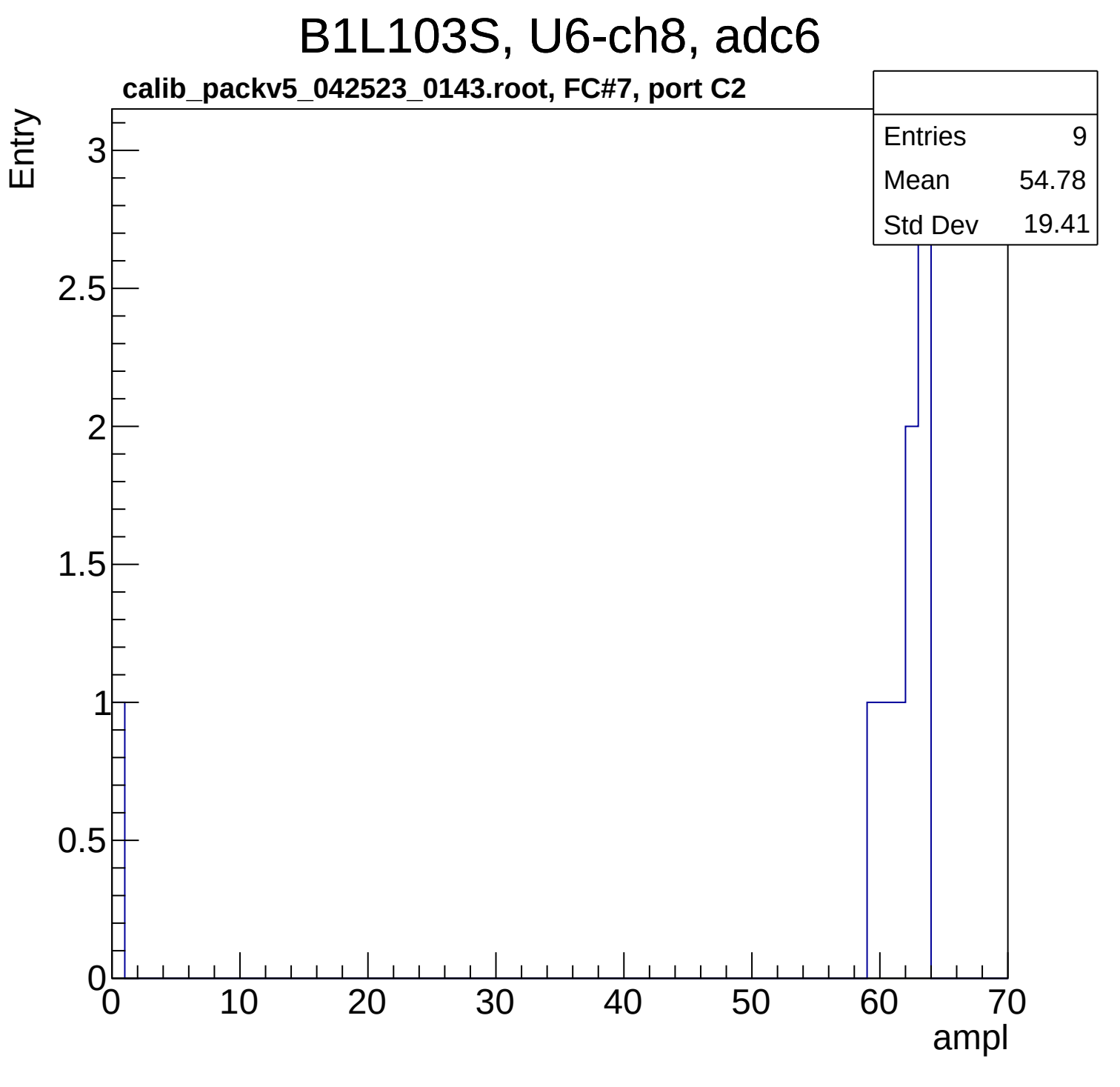
B1L103S, U6-ch8, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 54.78
Std Dev 19.41
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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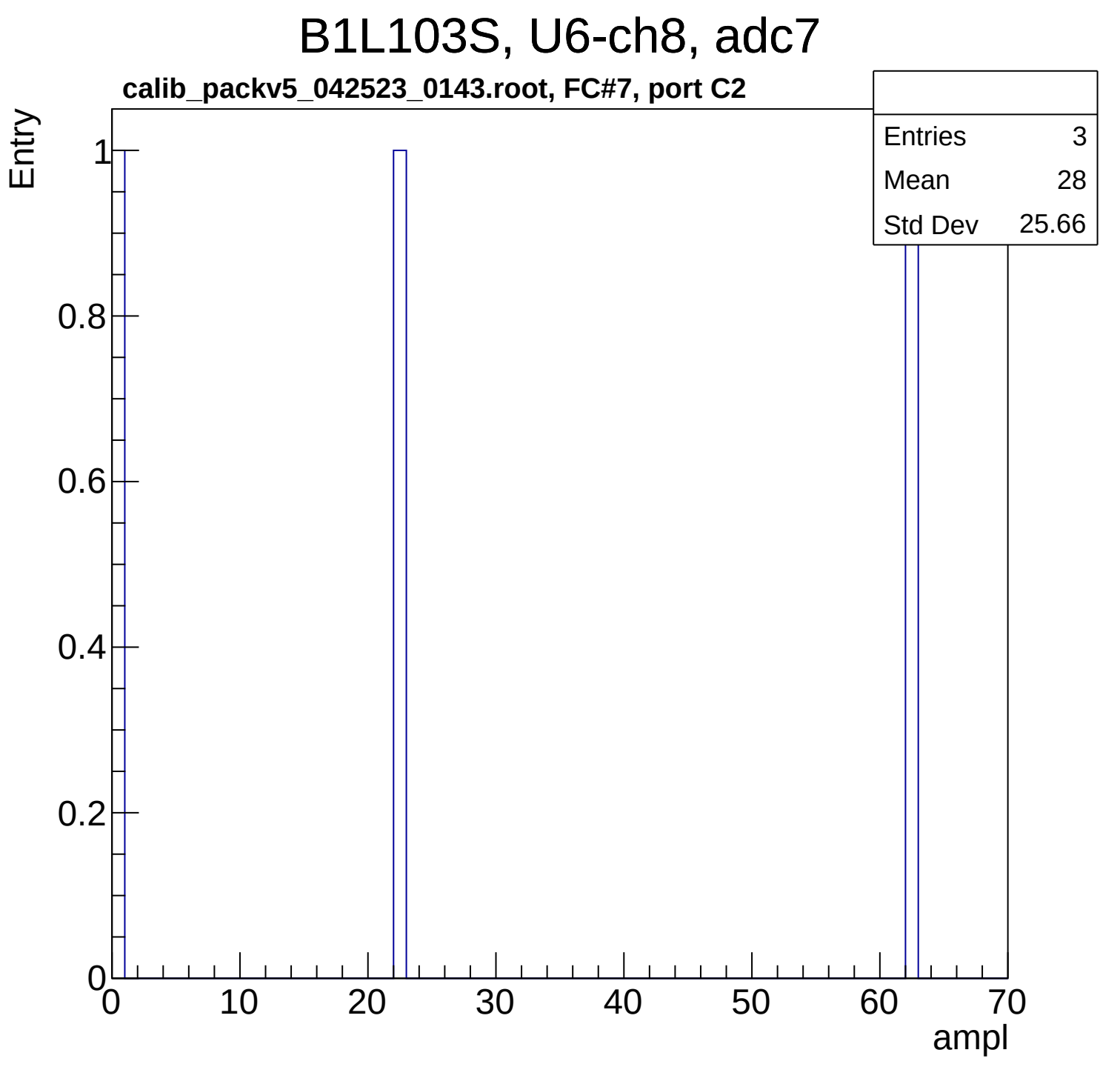
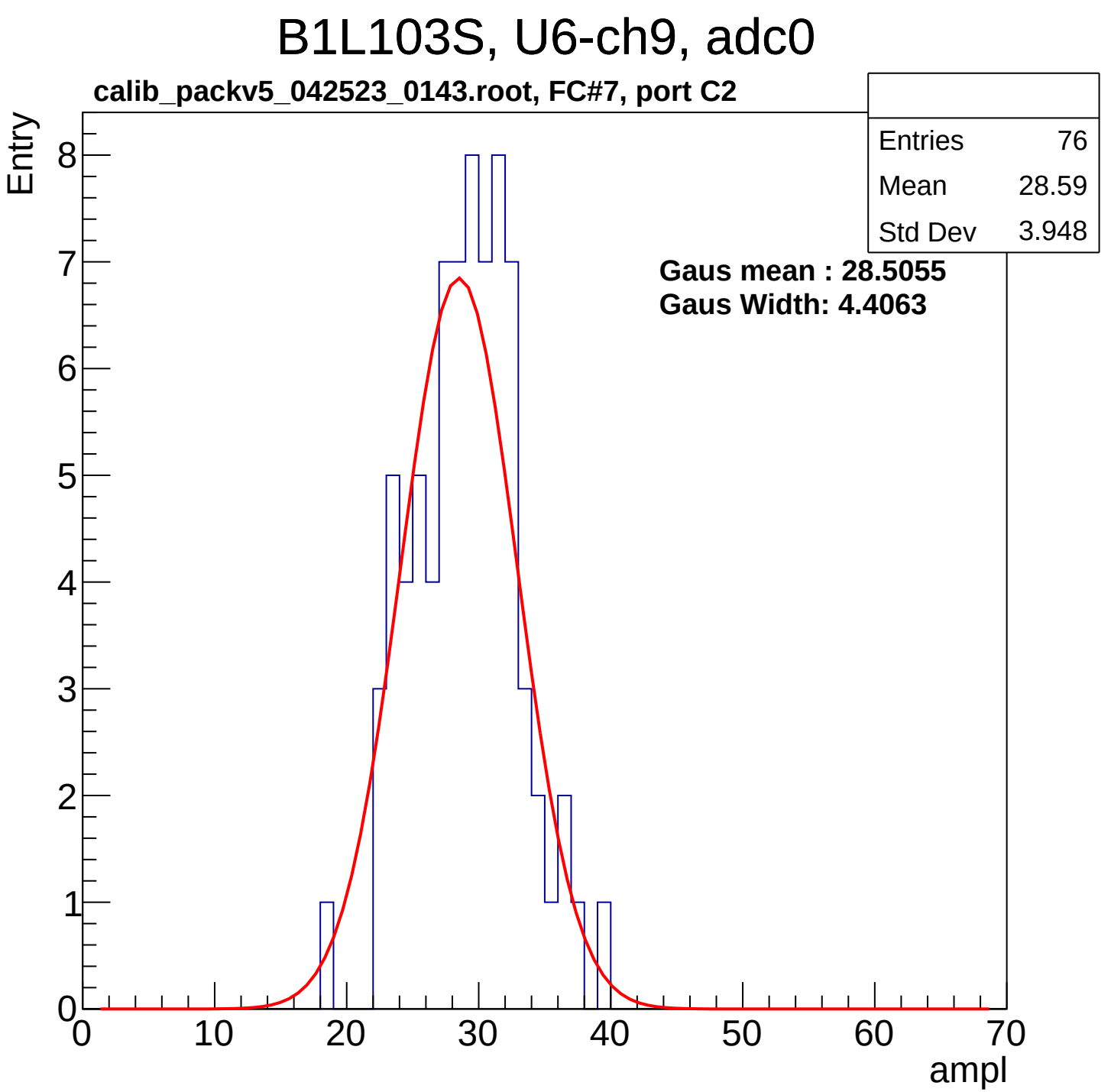

B1L103S, U6-ch8, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U6-ch9, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 28.59
Std Dev 3.948
Gaus mean : 28.5055
Gaus Width: 4.4063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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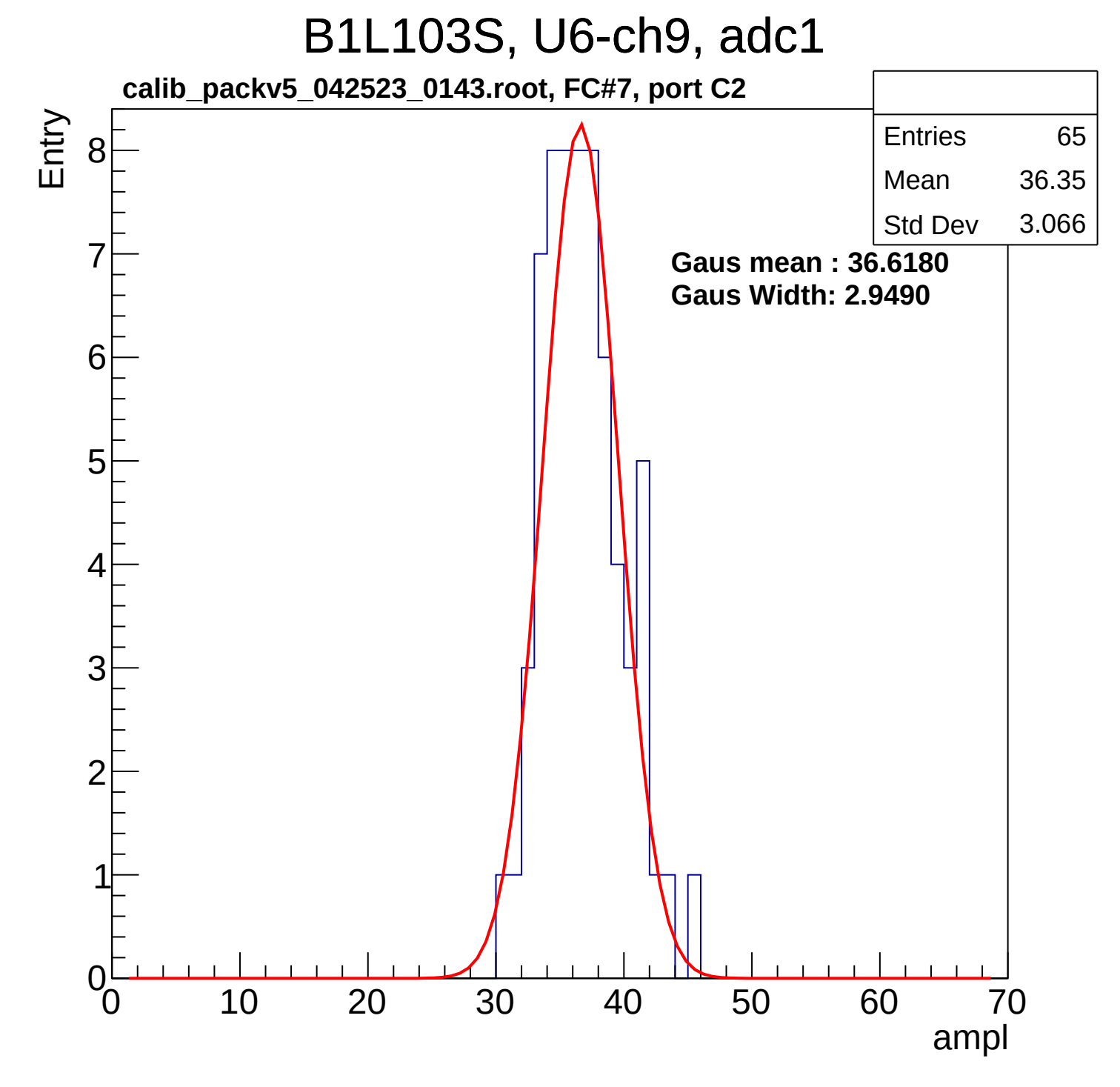
B1L103S, U6-ch9, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 36.35
Std Dev 3.066
Gaus mean : 36.6180
Gaus Width: 2.9490
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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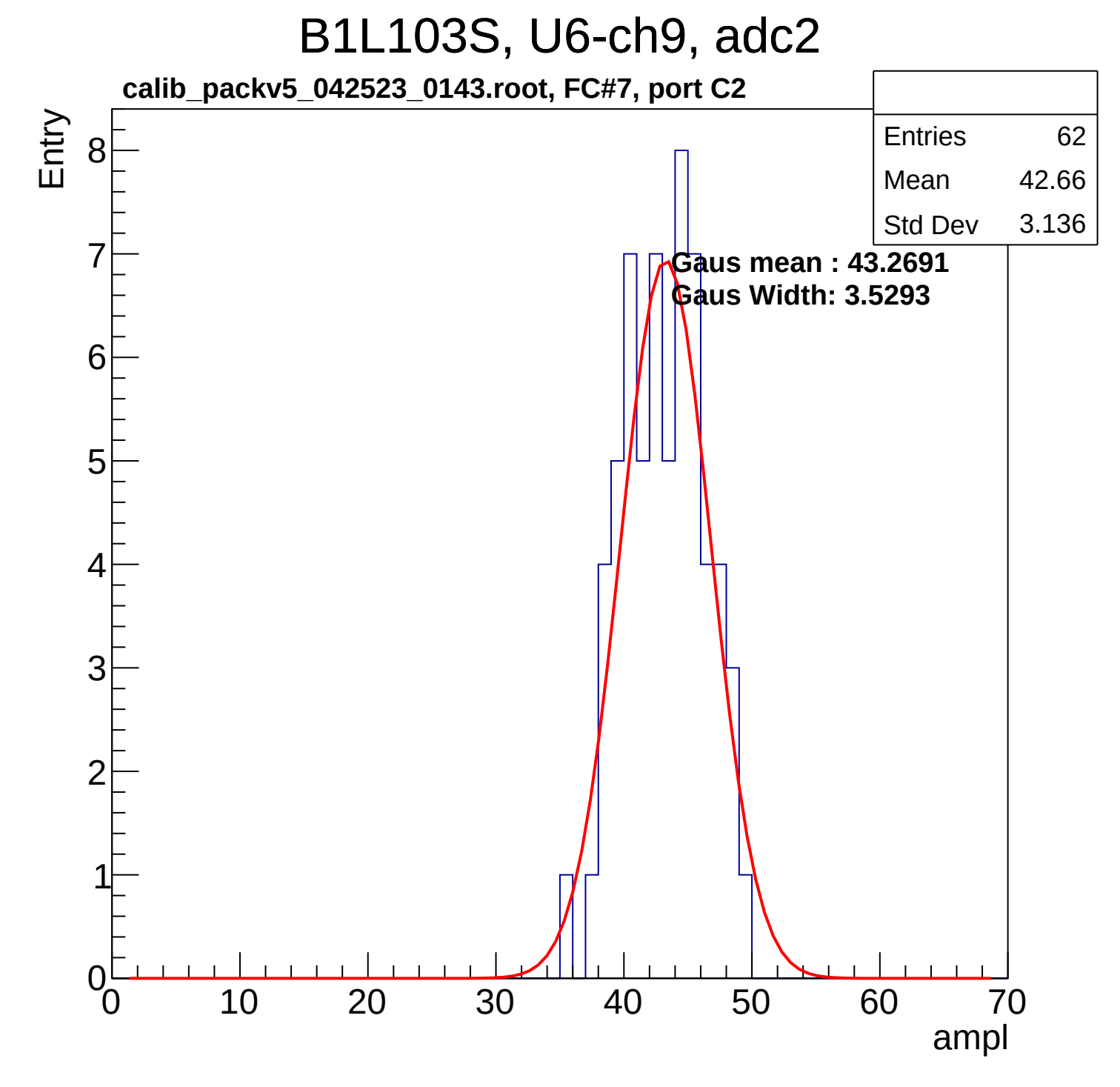
B1L103S, U6-ch9, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.66
Std Dev 3.136
Gaus mean : 43.2691
Gaus Width: 3.5293
Entry
ampl
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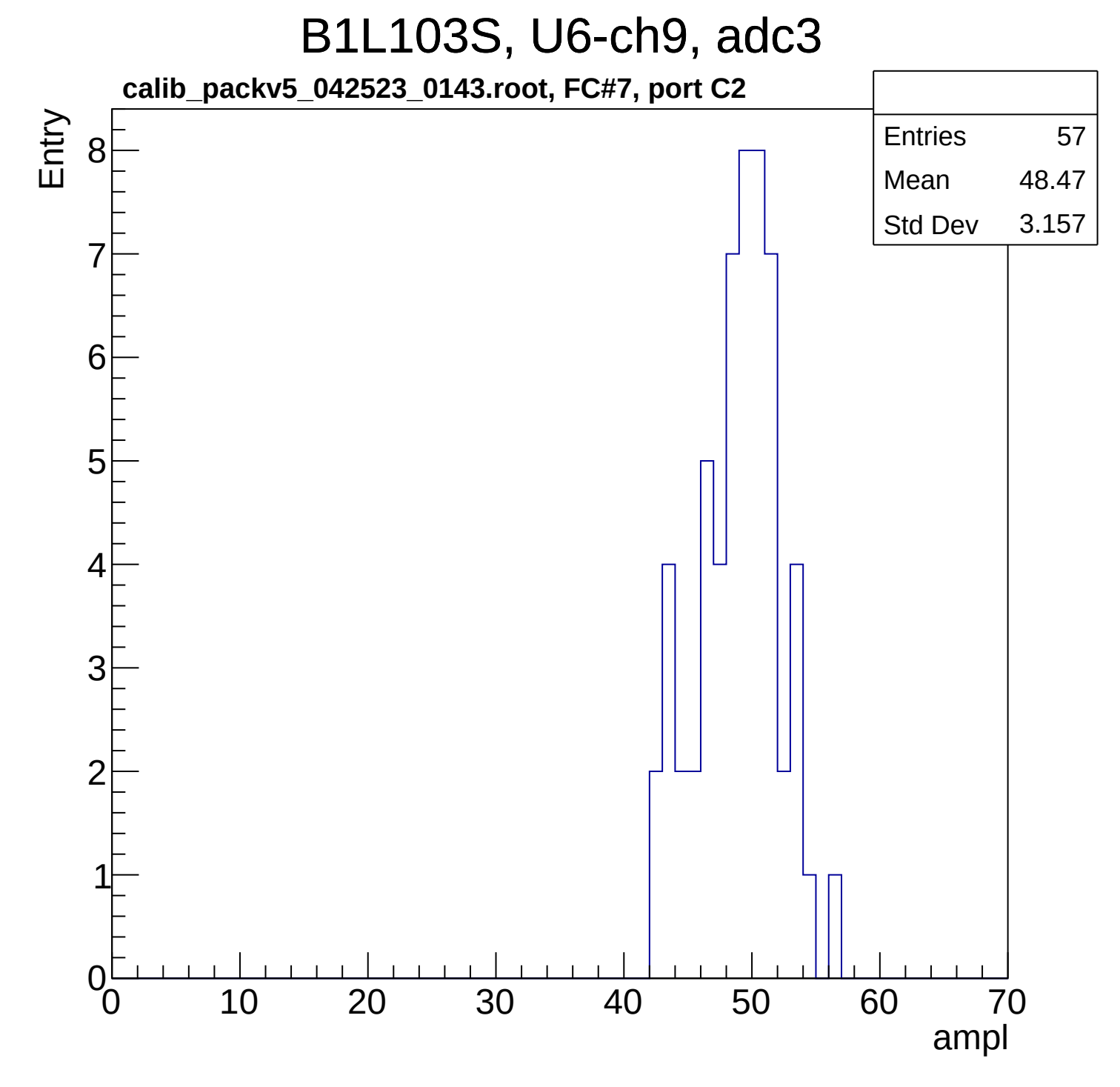
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch9, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 48.47
Std Dev 3.157
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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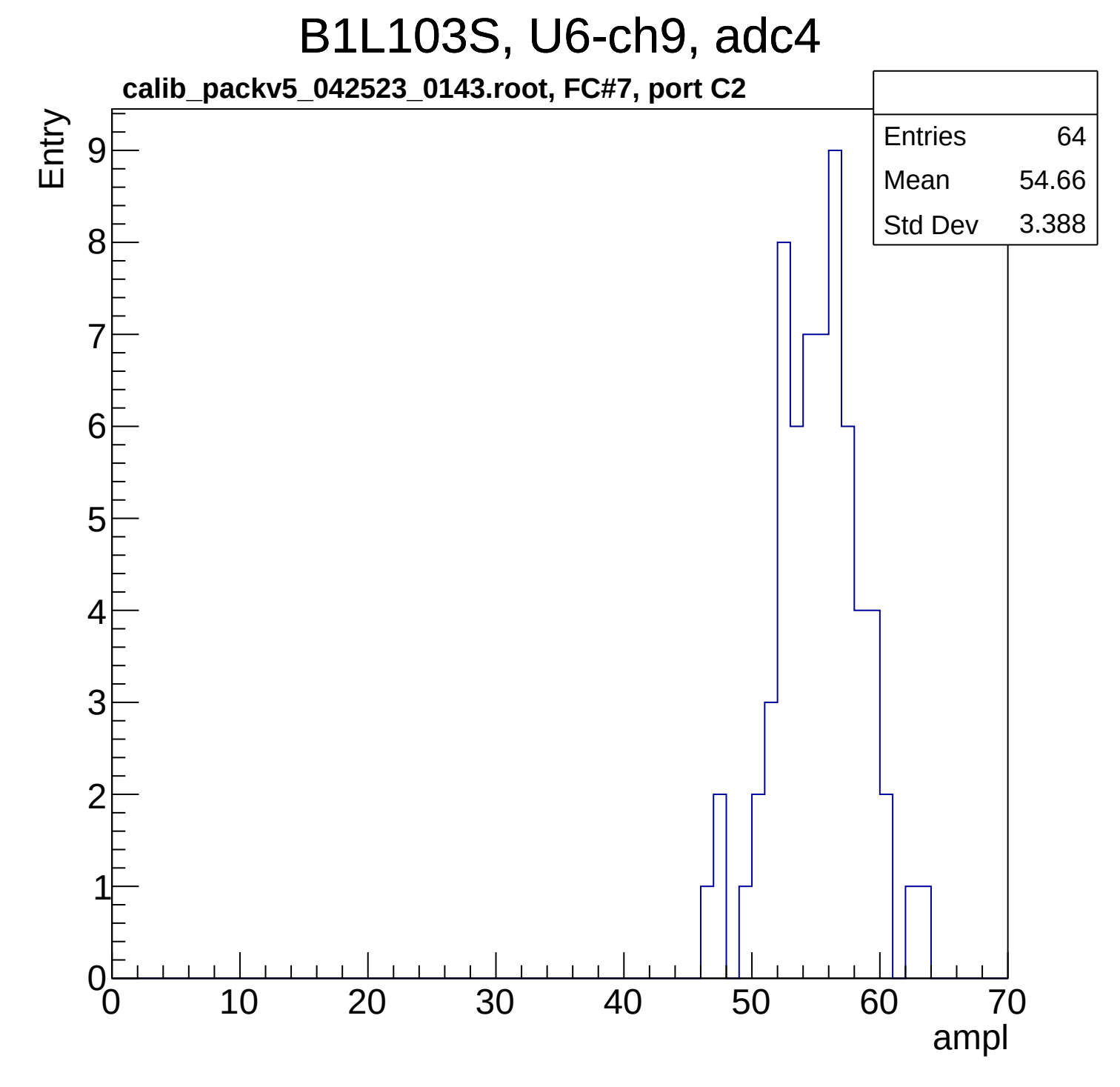
B1L103S, U6-ch9, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 54.66
Std Dev 3.388
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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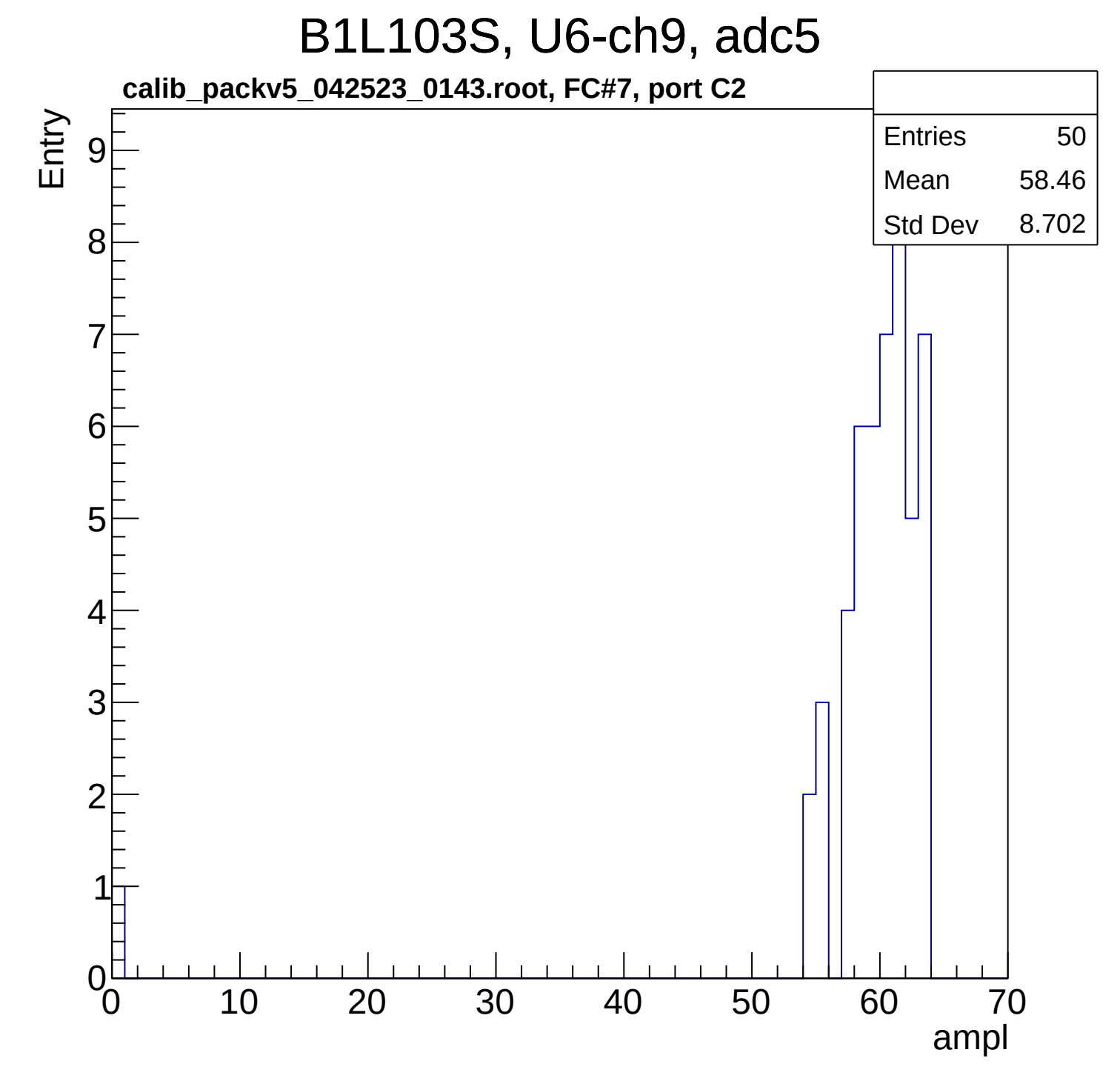
B1L103S, U6-ch9, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.46
Std Dev 8.702
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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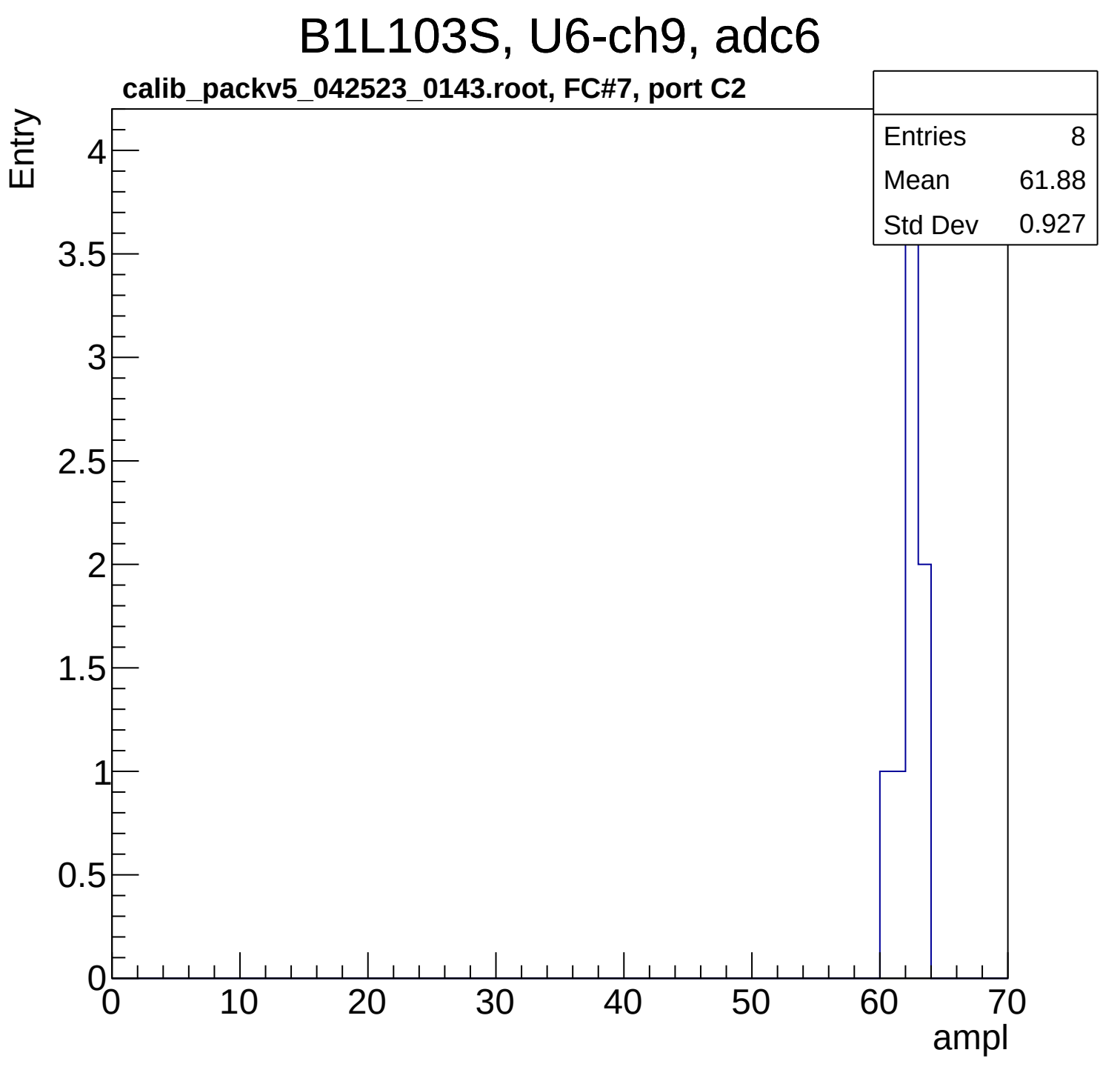
B1L103S, U6-ch9, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.88
Std Dev 0.927
Entry
ampl
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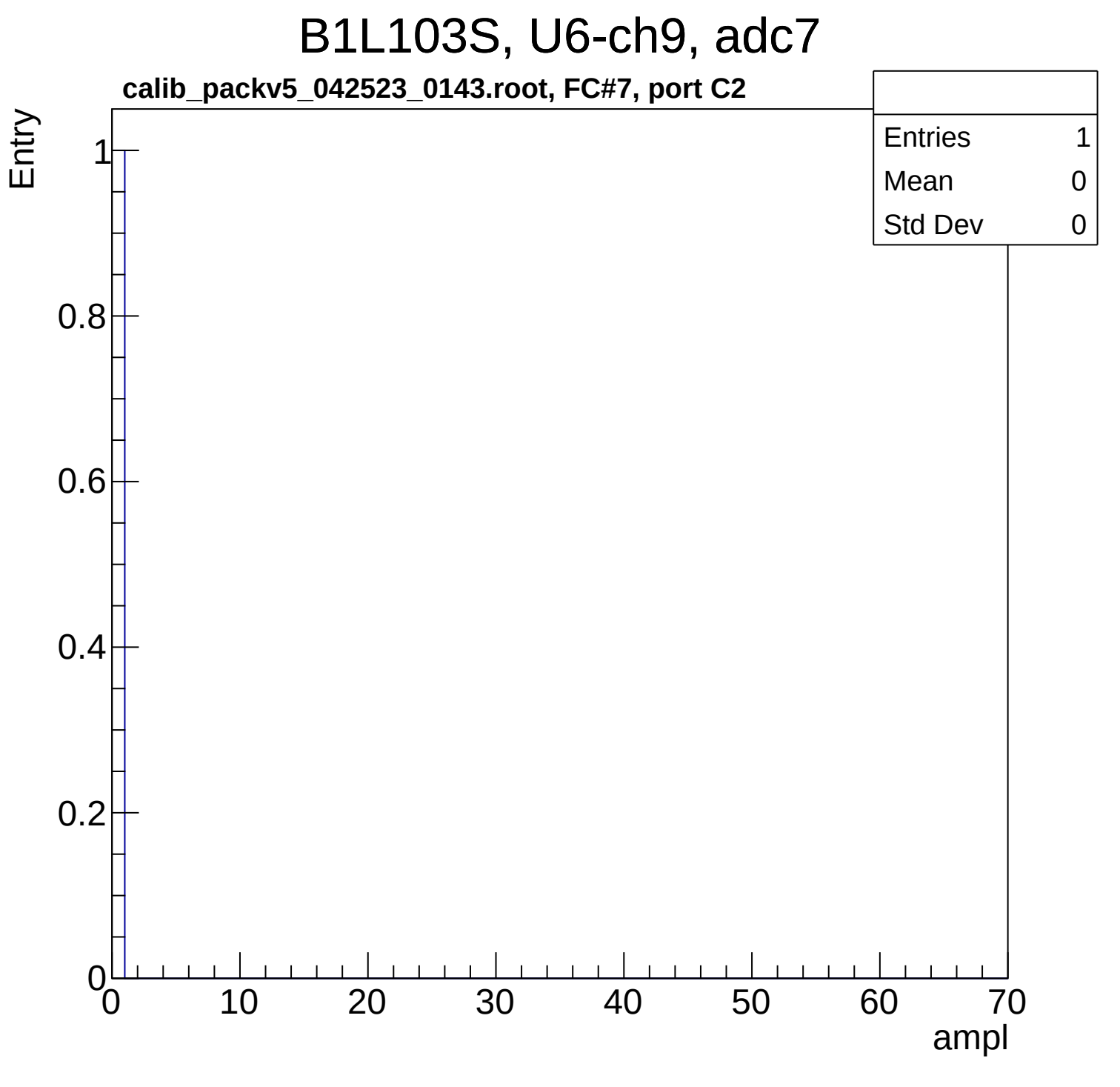
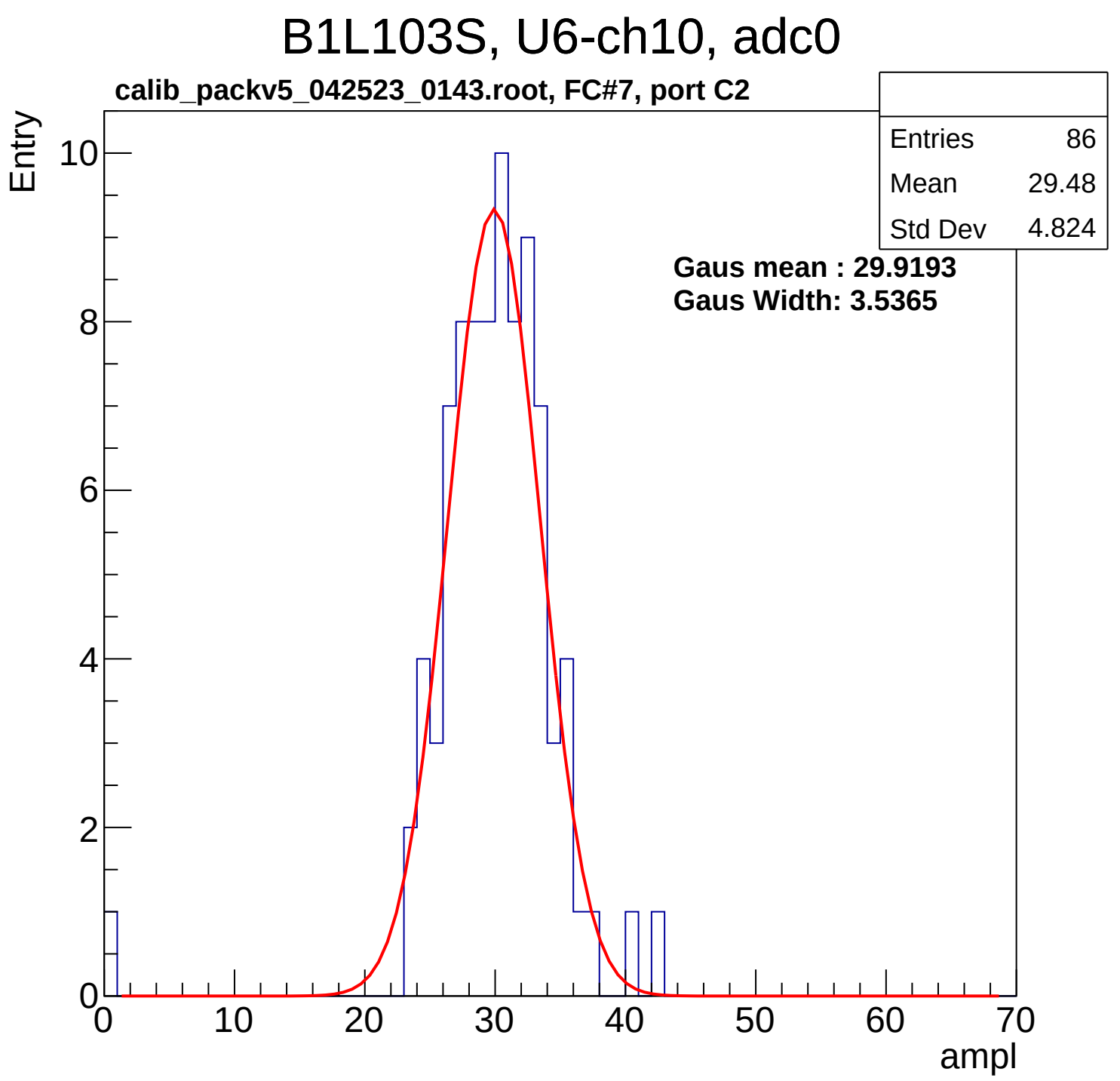
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch9, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch10, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 29.48
Std Dev 4.824
Gaus mean : 29.9193
Gaus Width: 3.5365
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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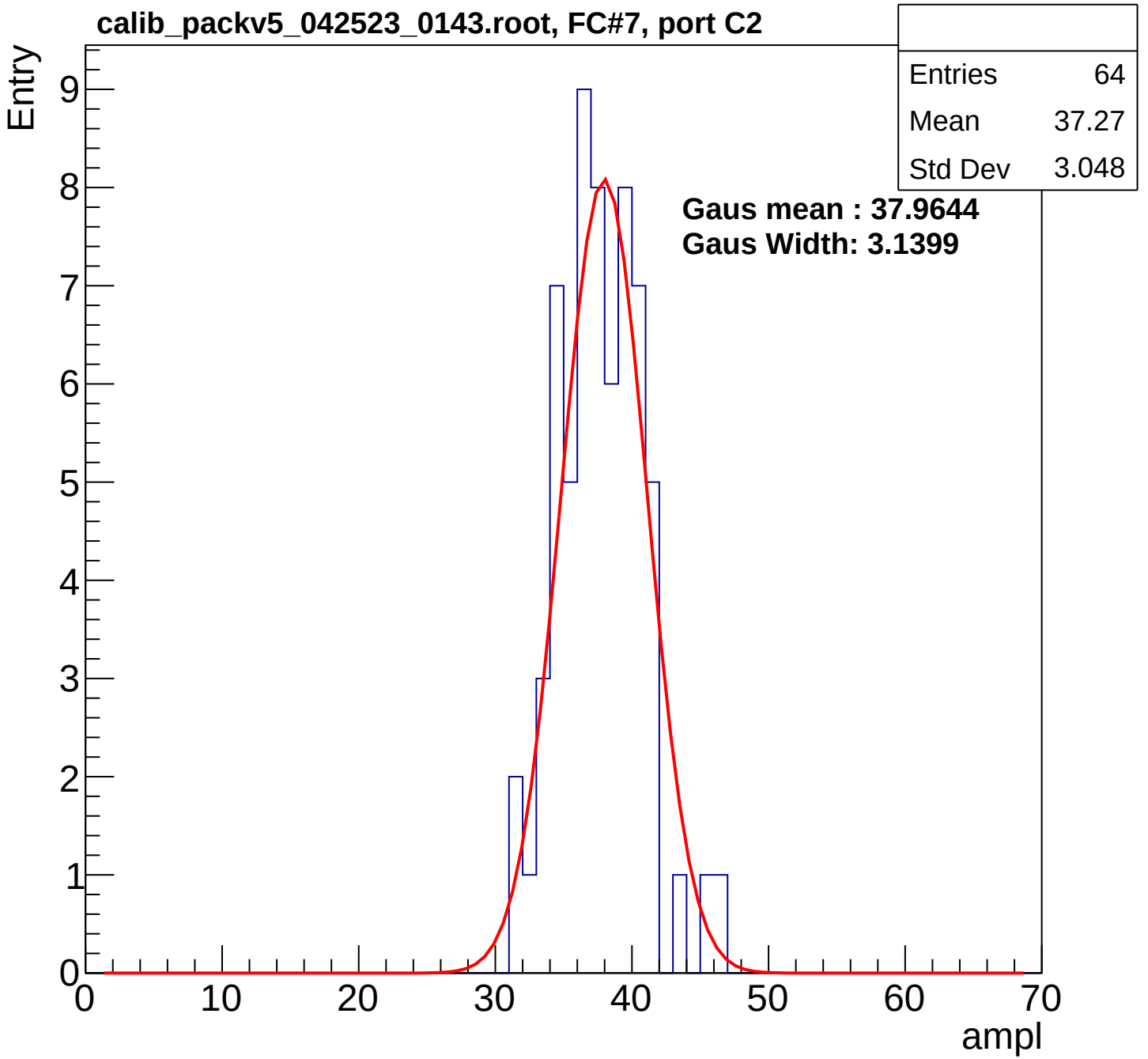
B1L103S, U6-ch10, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 37.27
Std Dev 3.048
Gaus mean : 37.9644
Gaus Width: 3.1399
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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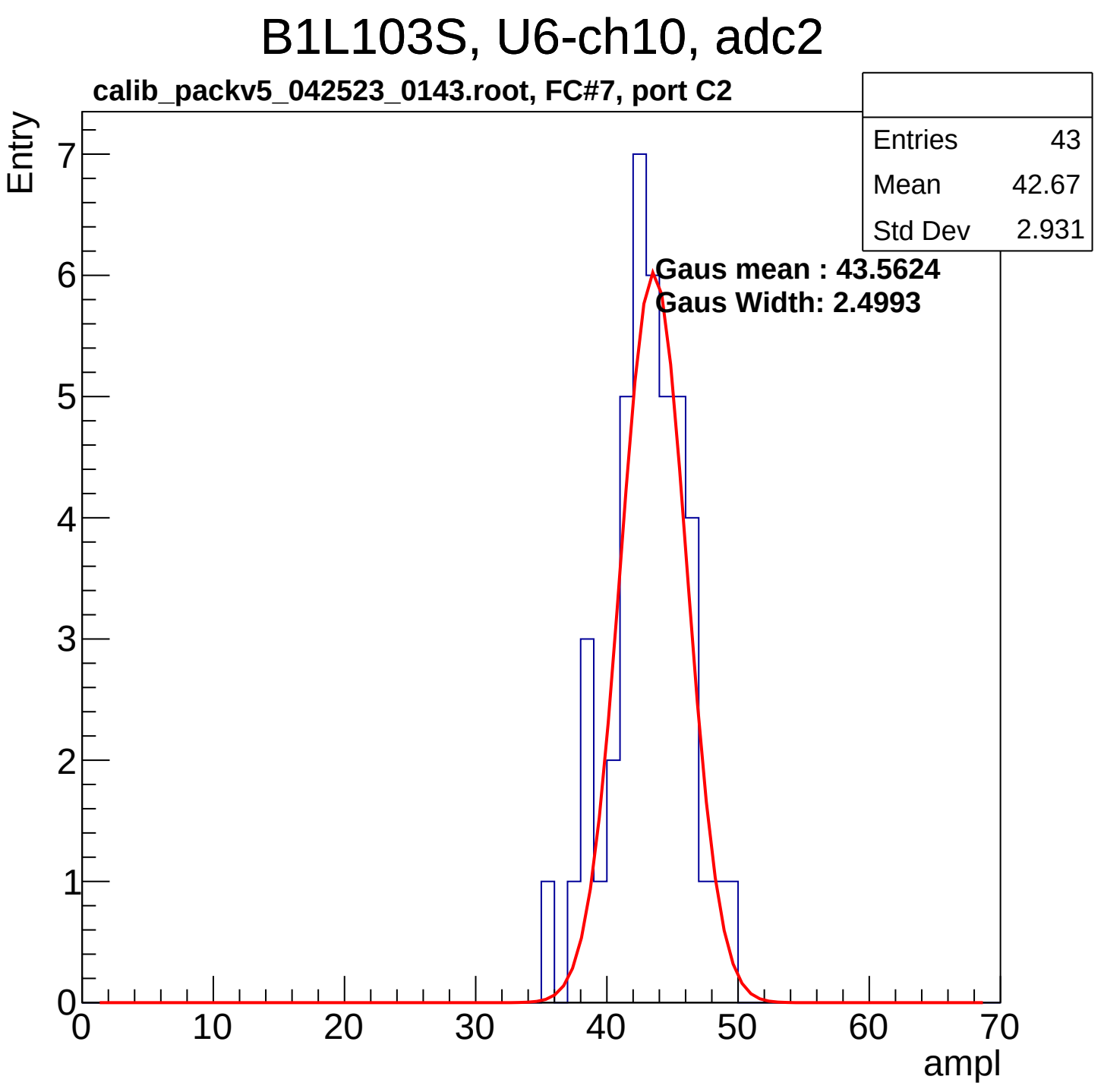
B1L103S, U6-ch10, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 42.67
Std Dev 2.931
Gaus mean : 43.5624
Gaus Width: 2.4993
Entry
ampl
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1
2
3
4
5
6
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60
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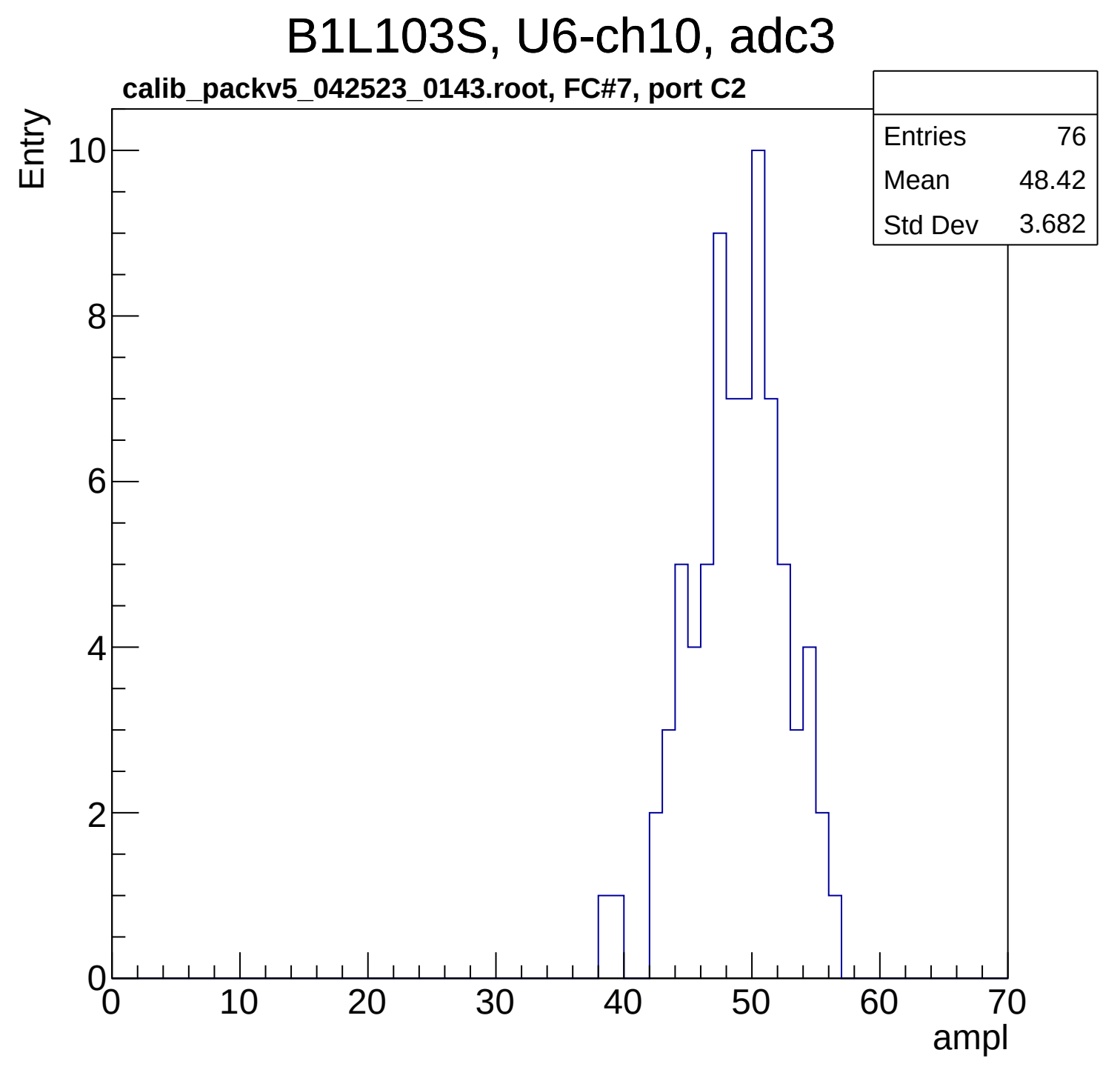
B1L103S, U6-ch10, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 48.42
Std Dev 3.682
Entry
ampl
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60
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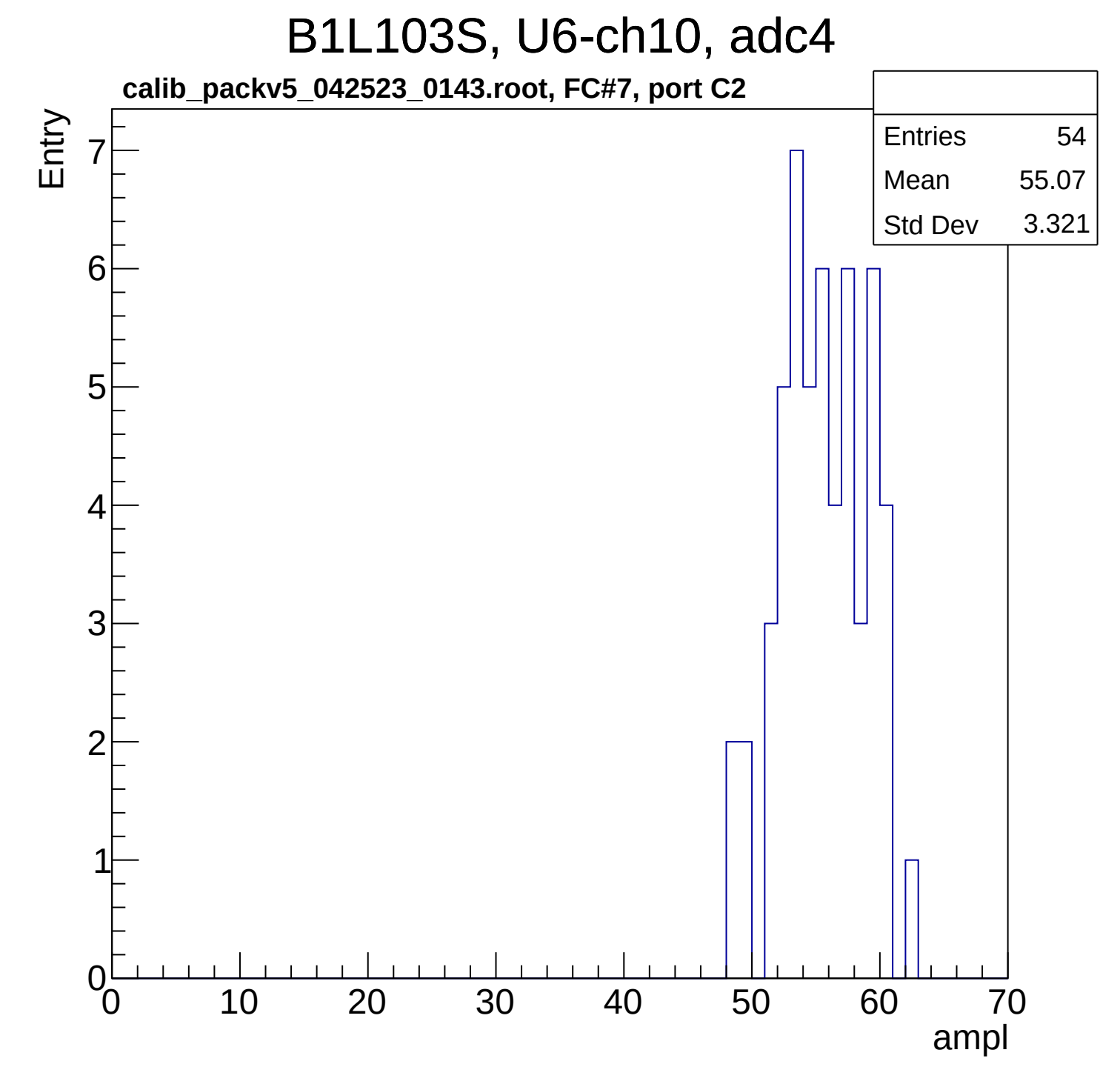
B1L103S, U6-ch10, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.07
Std Dev 3.321
Entry
ampl
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4
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6
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0
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40
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60
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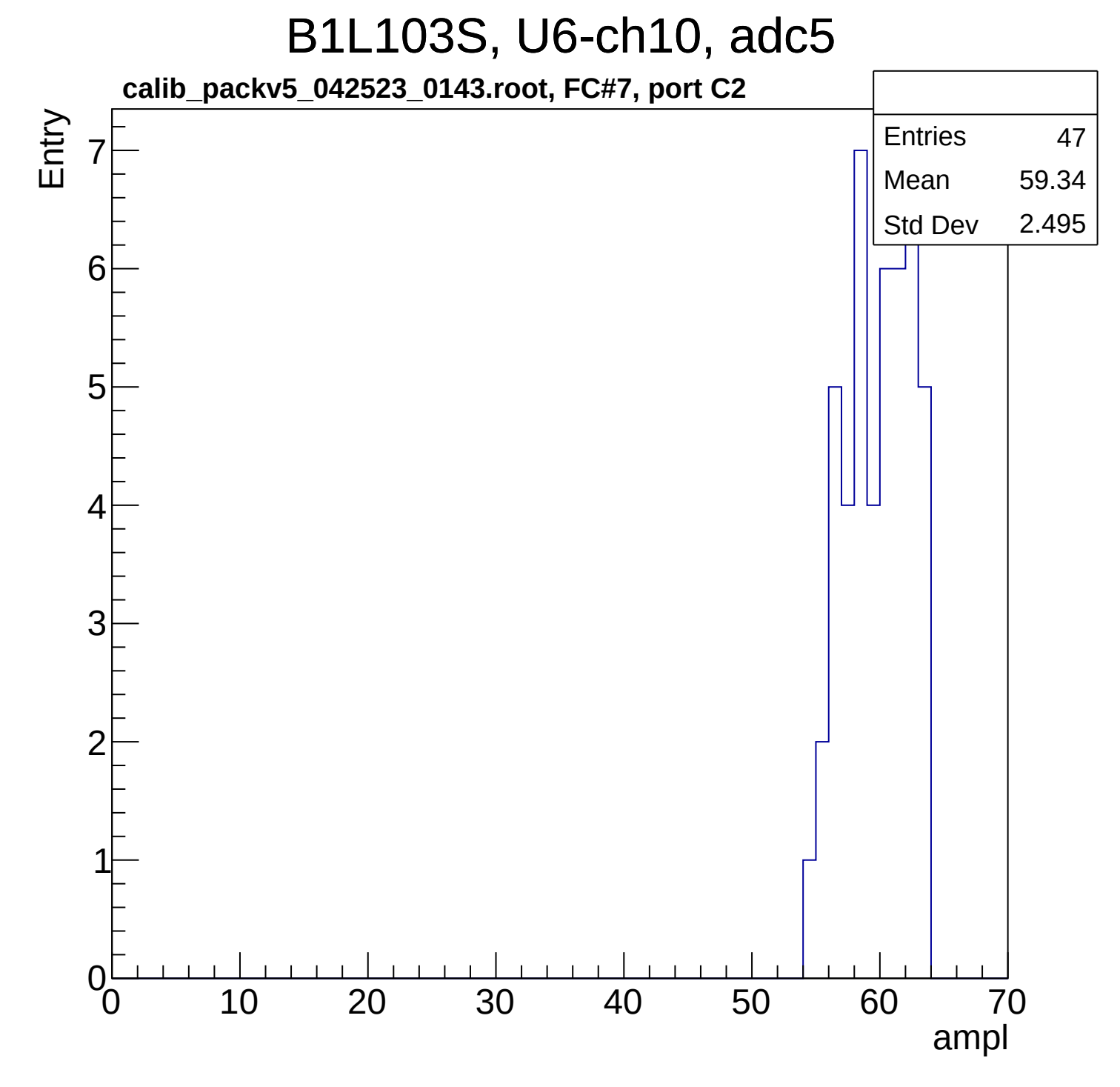
B1L103S, U6-ch10, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.34
Std Dev 2.495
Entry
ampl
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2
3
4
5
6
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0
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60
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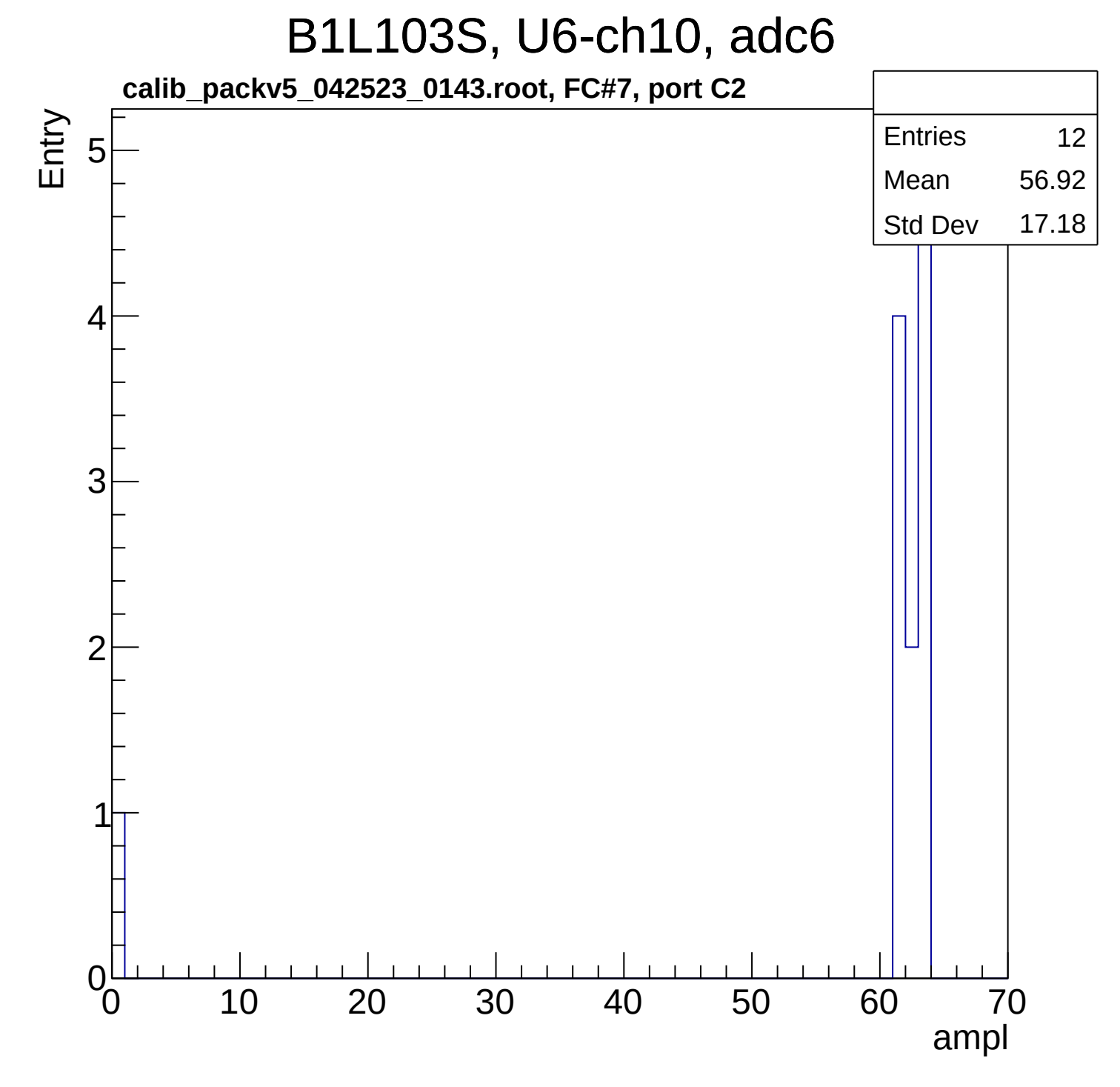
B1L103S, U6-ch10, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.92
Std Dev 17.18
Entry
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2
3
4
5
0
10
20
30
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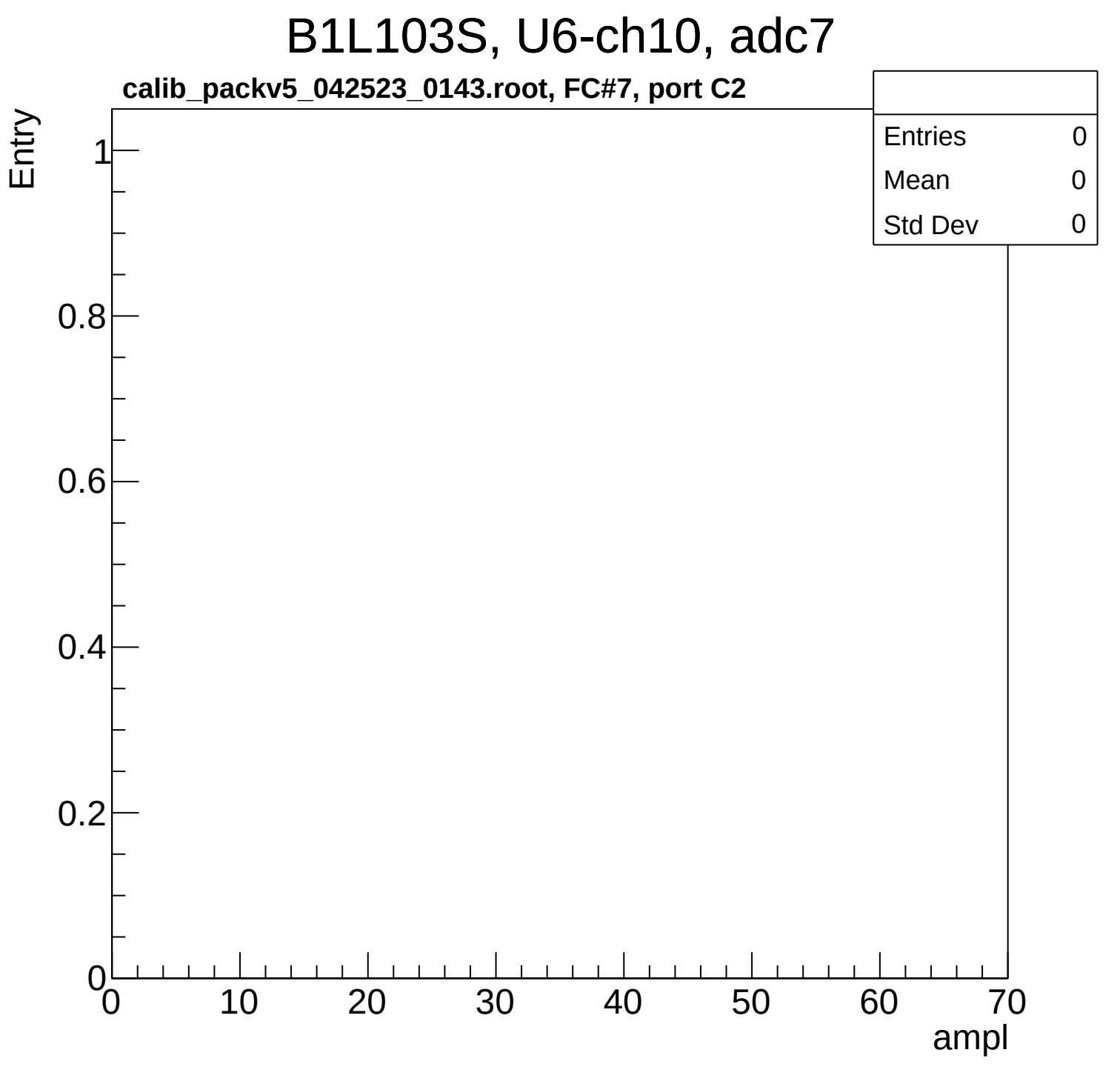
50
60
70
ampl


B1L103S, U6-ch10, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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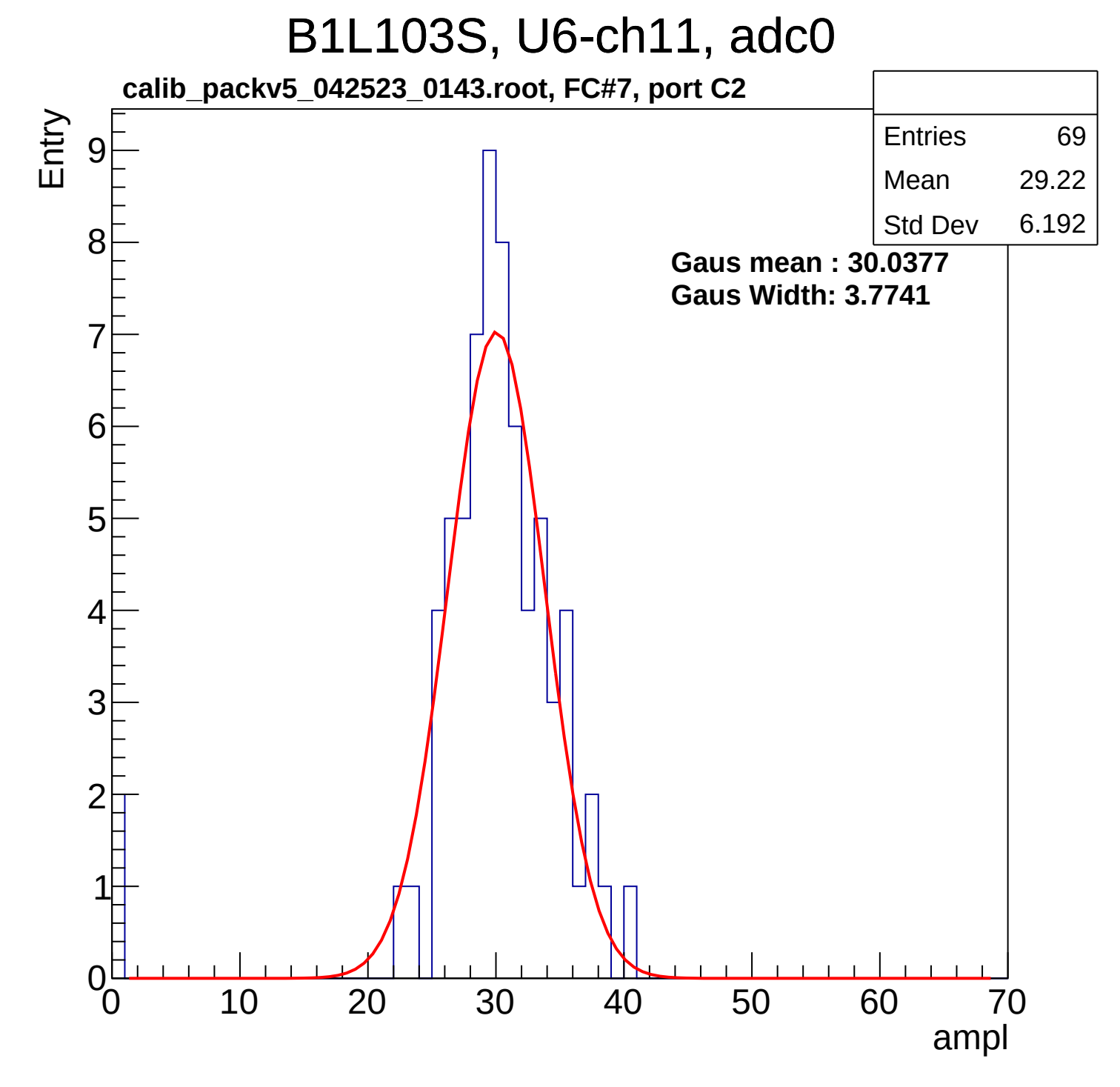
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch11, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 29.22
Std Dev 6.192
Gaus mean : 30.0377
Gaus Width: 3.7741
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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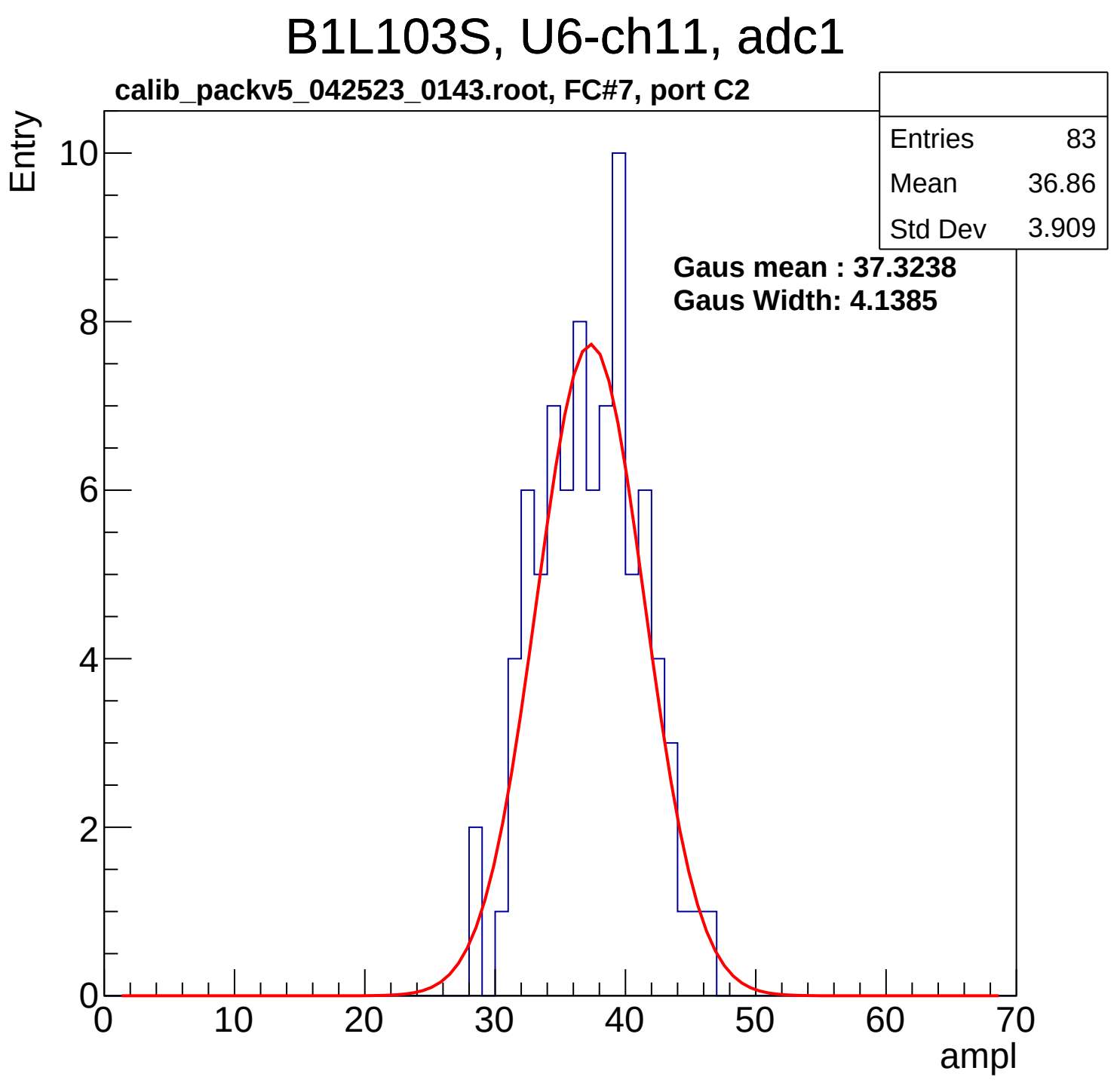
B1L103S, U6-ch11, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 36.86
Std Dev 3.909
Gaus mean : 37.3238
Gaus Width: 4.1385
Entry
ampl
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2
4
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8
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0
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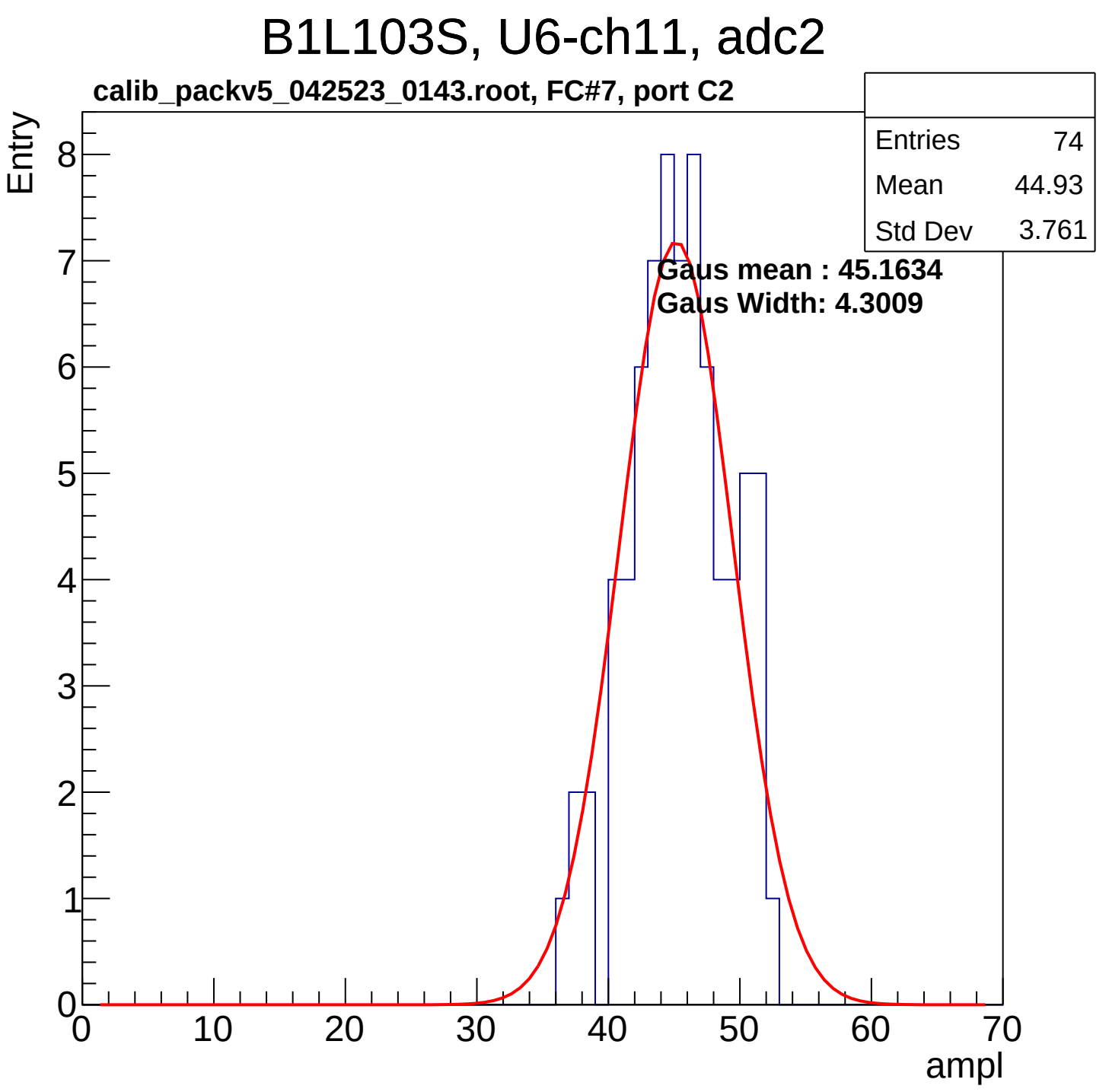
B1L103S, U6-ch11, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 44.93
Std Dev 3.761
Gaus mean : 45.1634
Gaus Width: 4.3009
Entry
ampl
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7
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0
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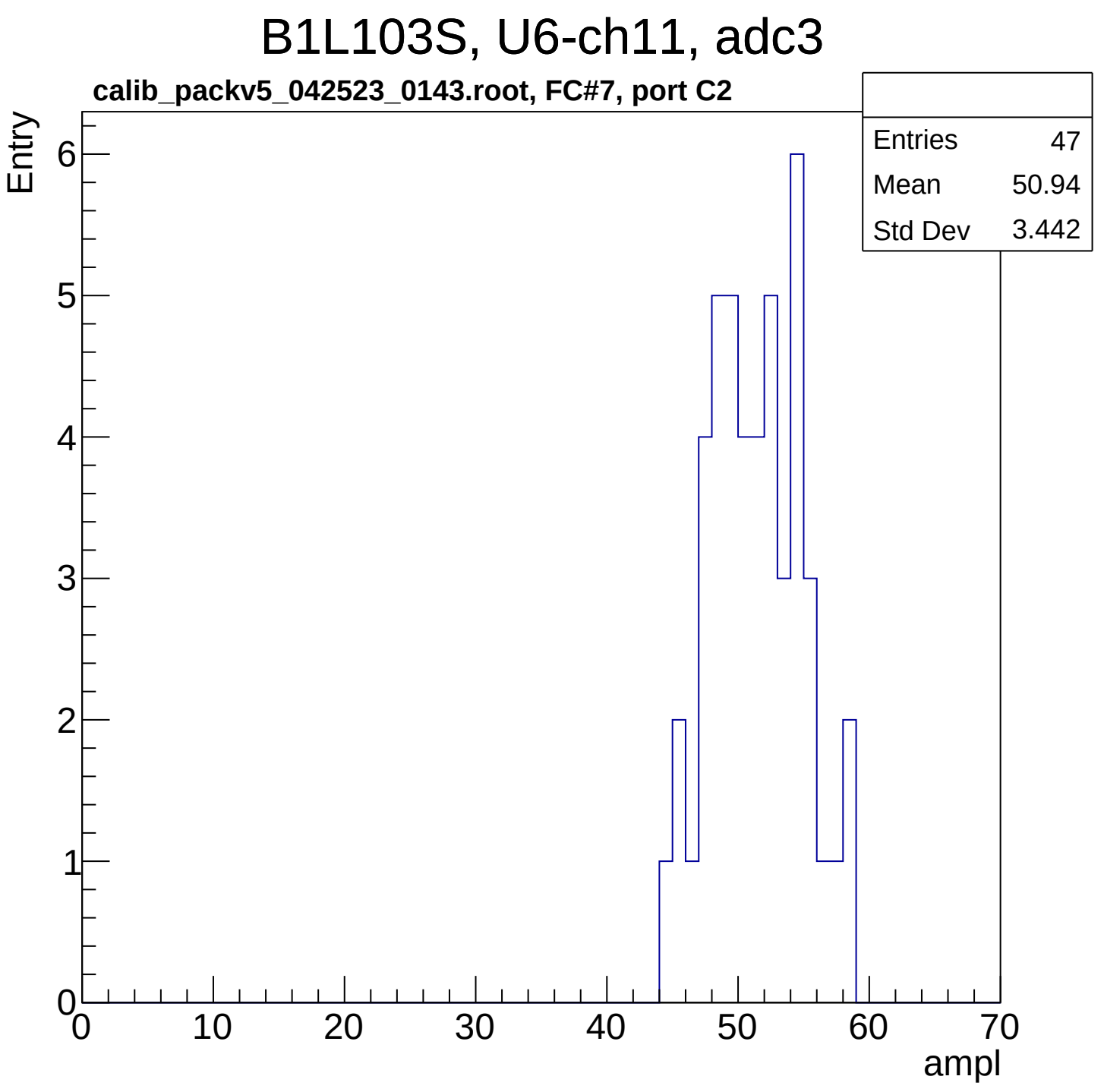
B1L103S, U6-ch11, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 50.94
Std Dev 3.442
Entry
ampl
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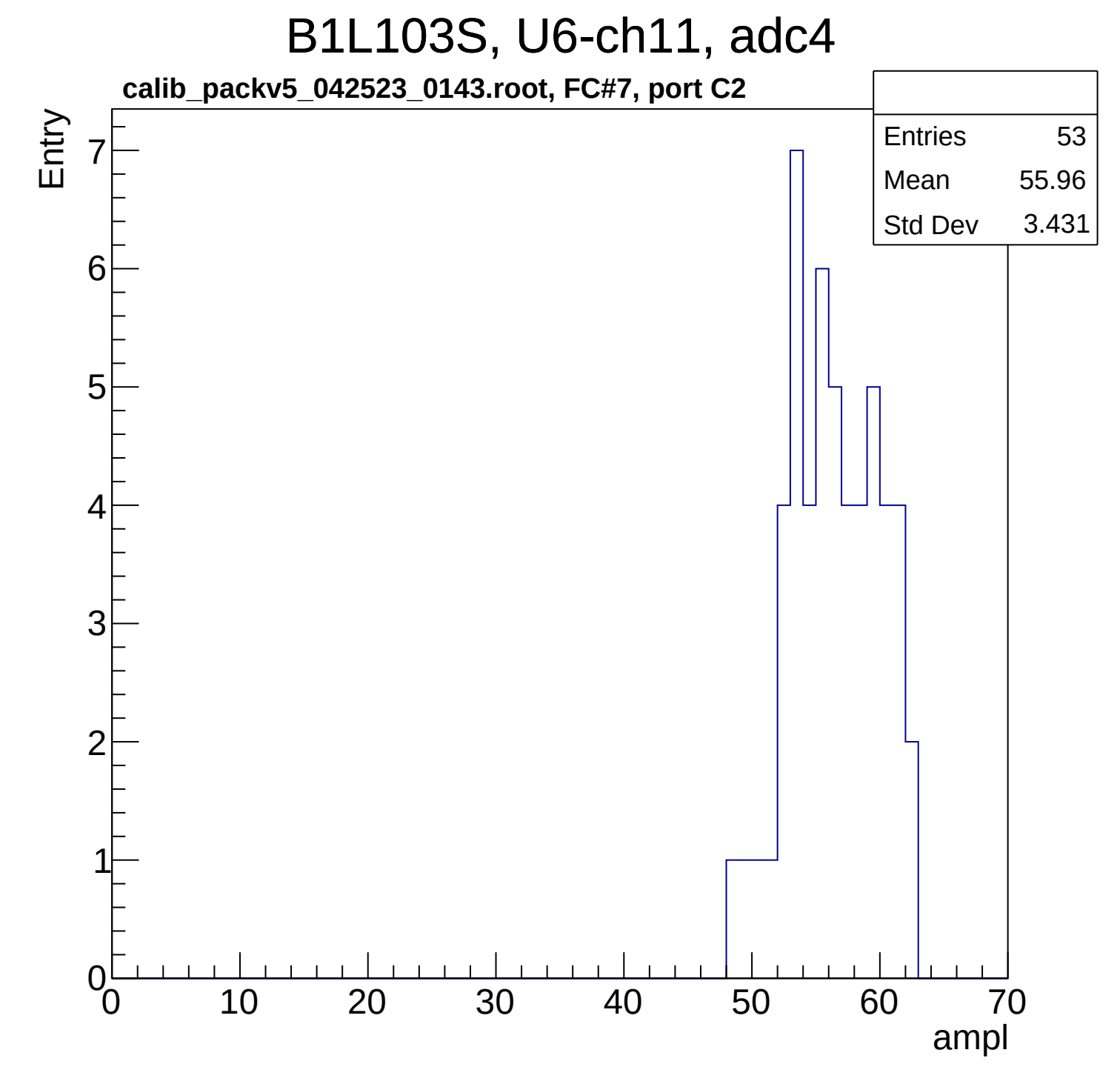
B1L103S, U6-ch11, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.96
Std Dev 3.431
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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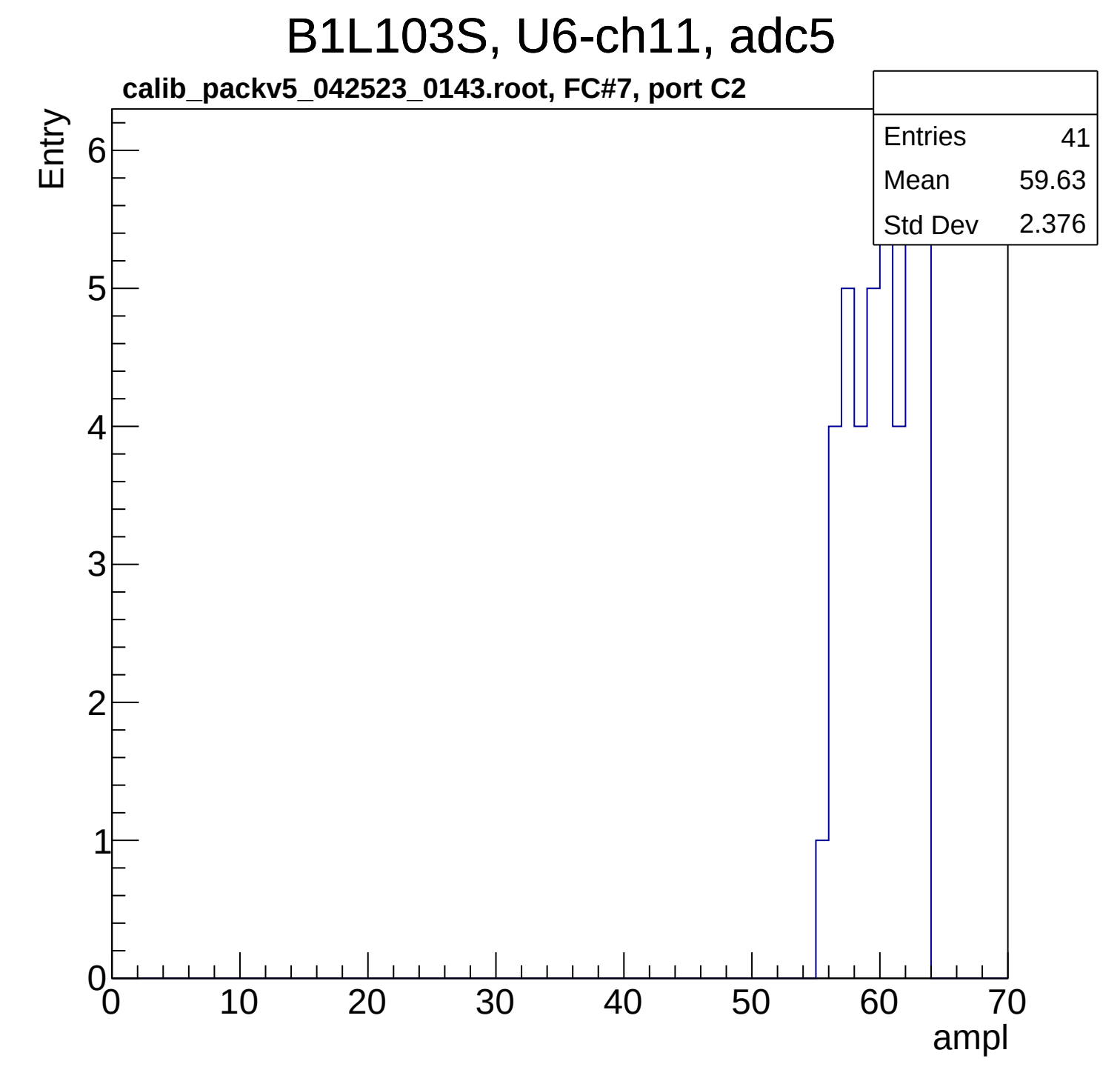
40
50
60
70


B1L103S, U6-ch11, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.63
Std Dev 2.376
Entry
ampl
0
1
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3
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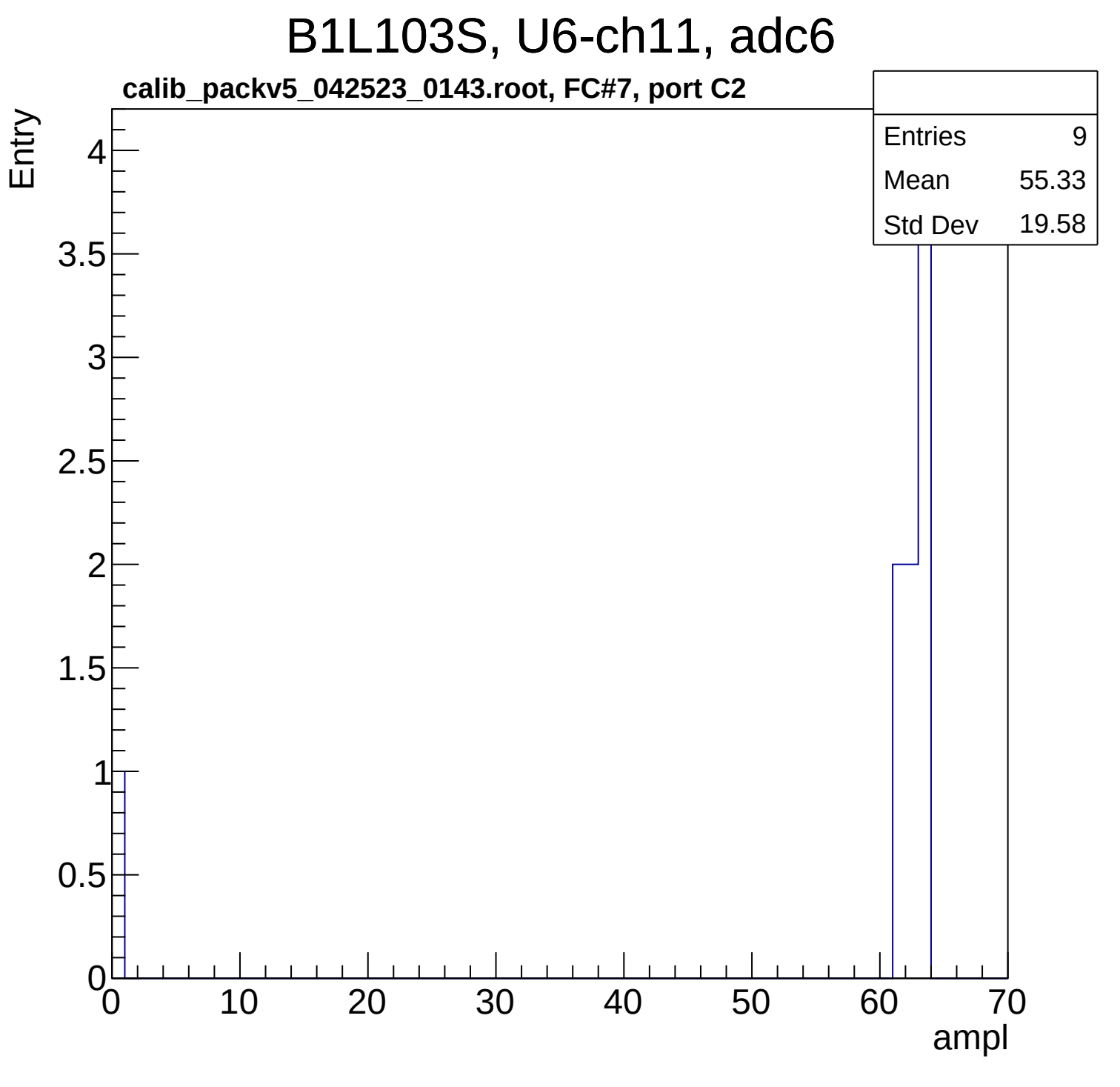
B1L103S, U6-ch11, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
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1.5
2
2.5
3
3.5
4
0
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60
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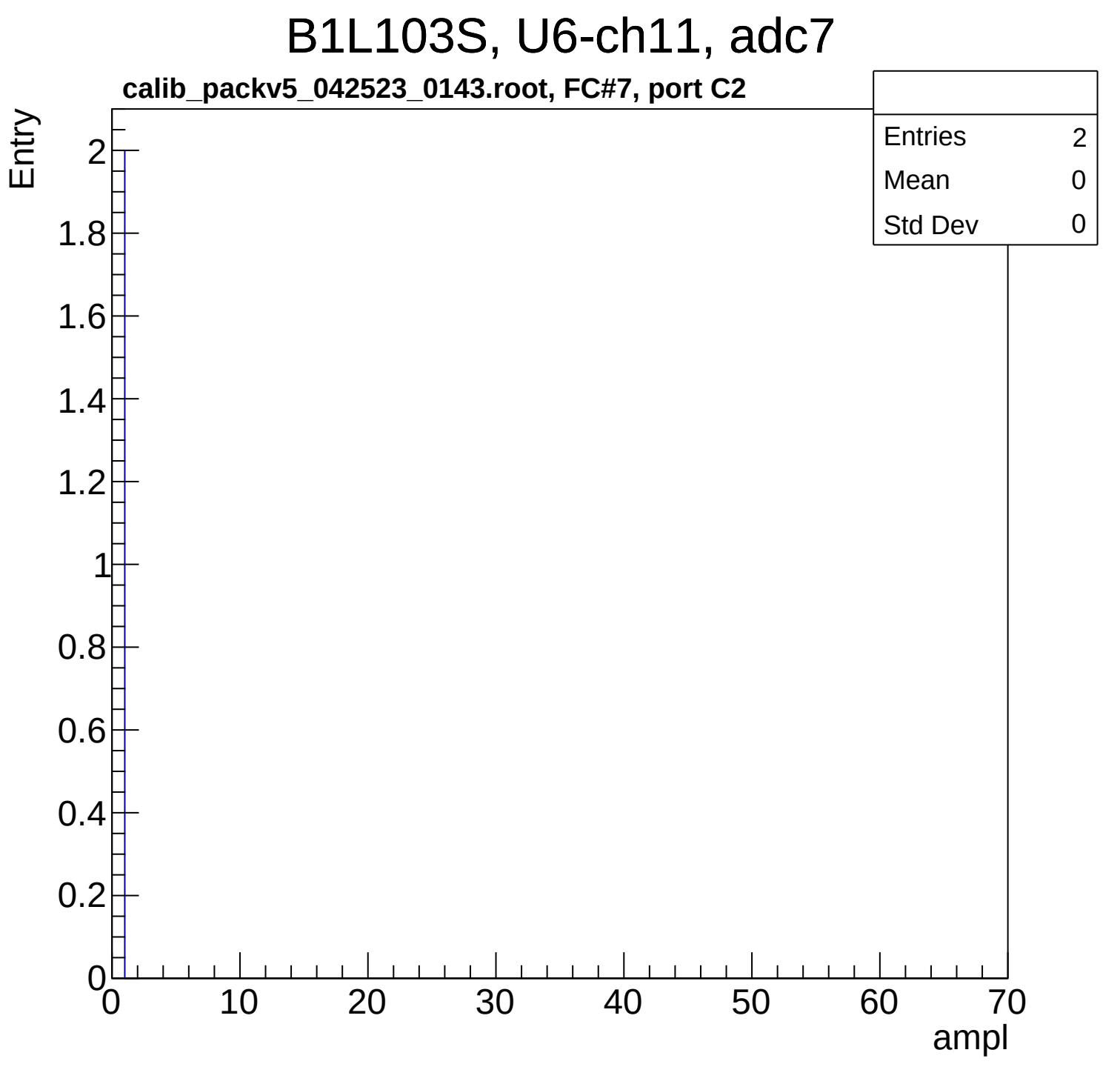
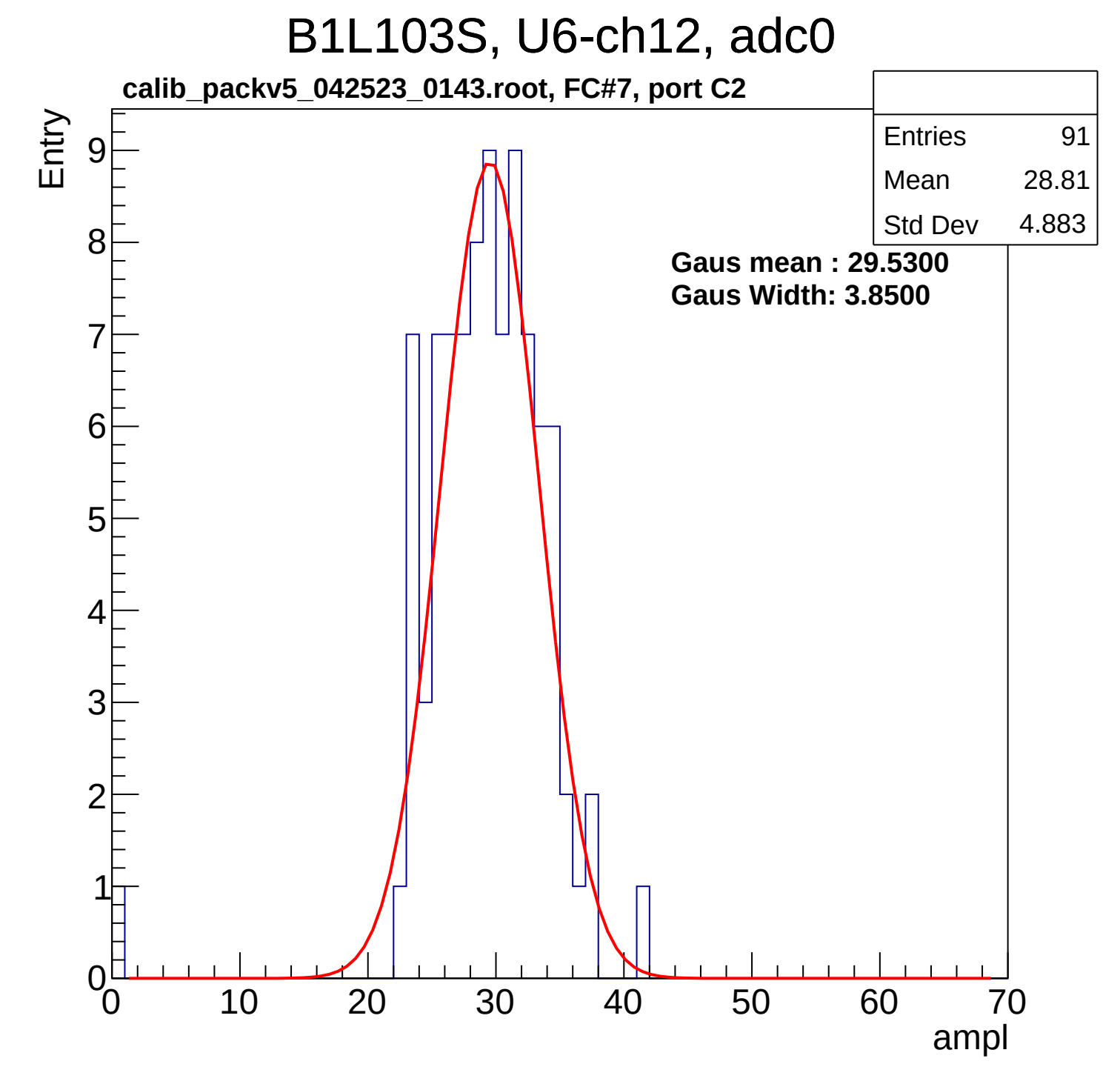

B1L103S, U6-ch11, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch12, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 28.81
Std Dev 4.883
Gaus mean : 29.5300
Gaus Width: 3.8500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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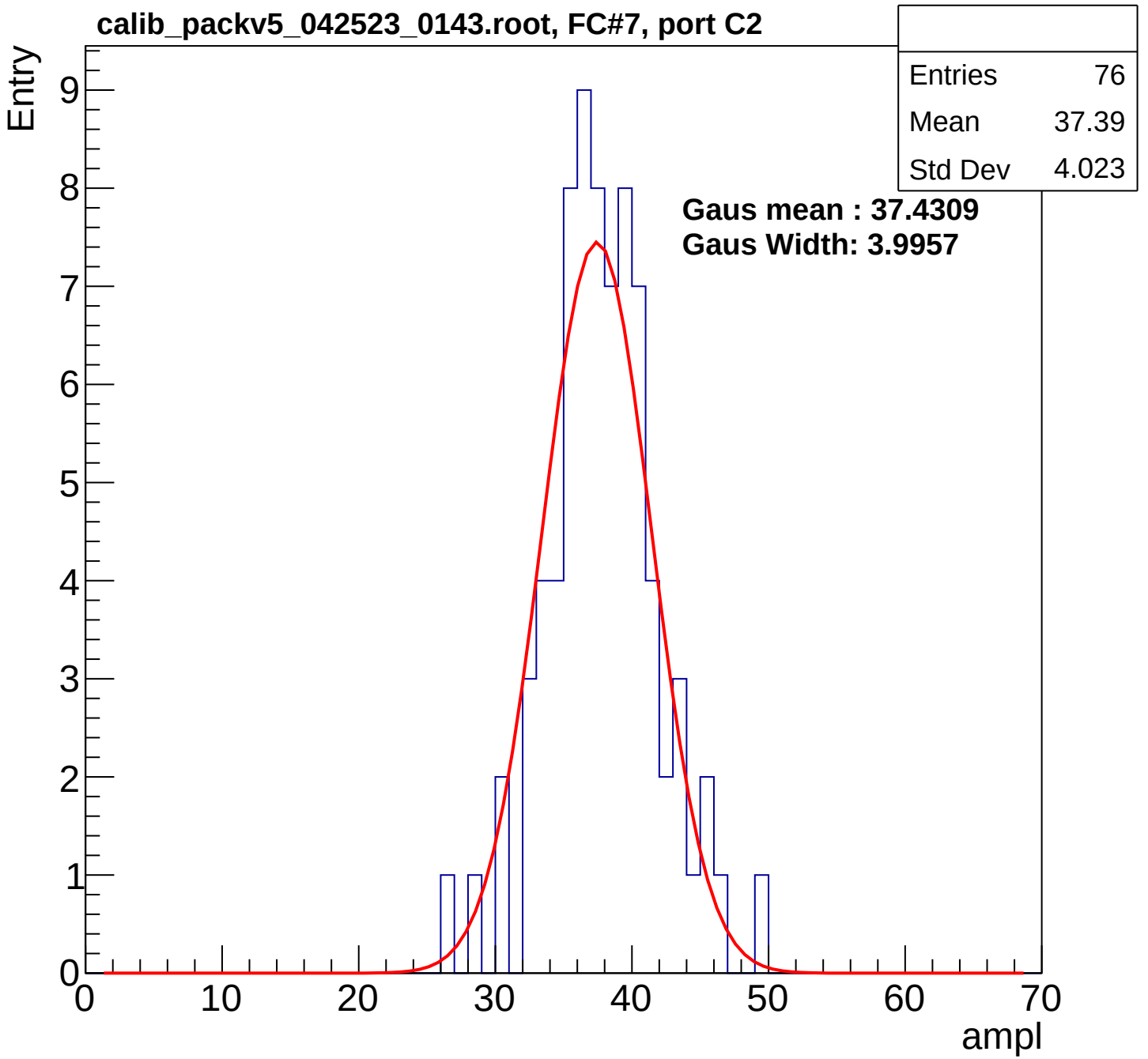
B1L103S, U6-ch12, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 37.39
Std Dev 4.023
Gaus mean : 37.4309
Gaus Width: 3.9957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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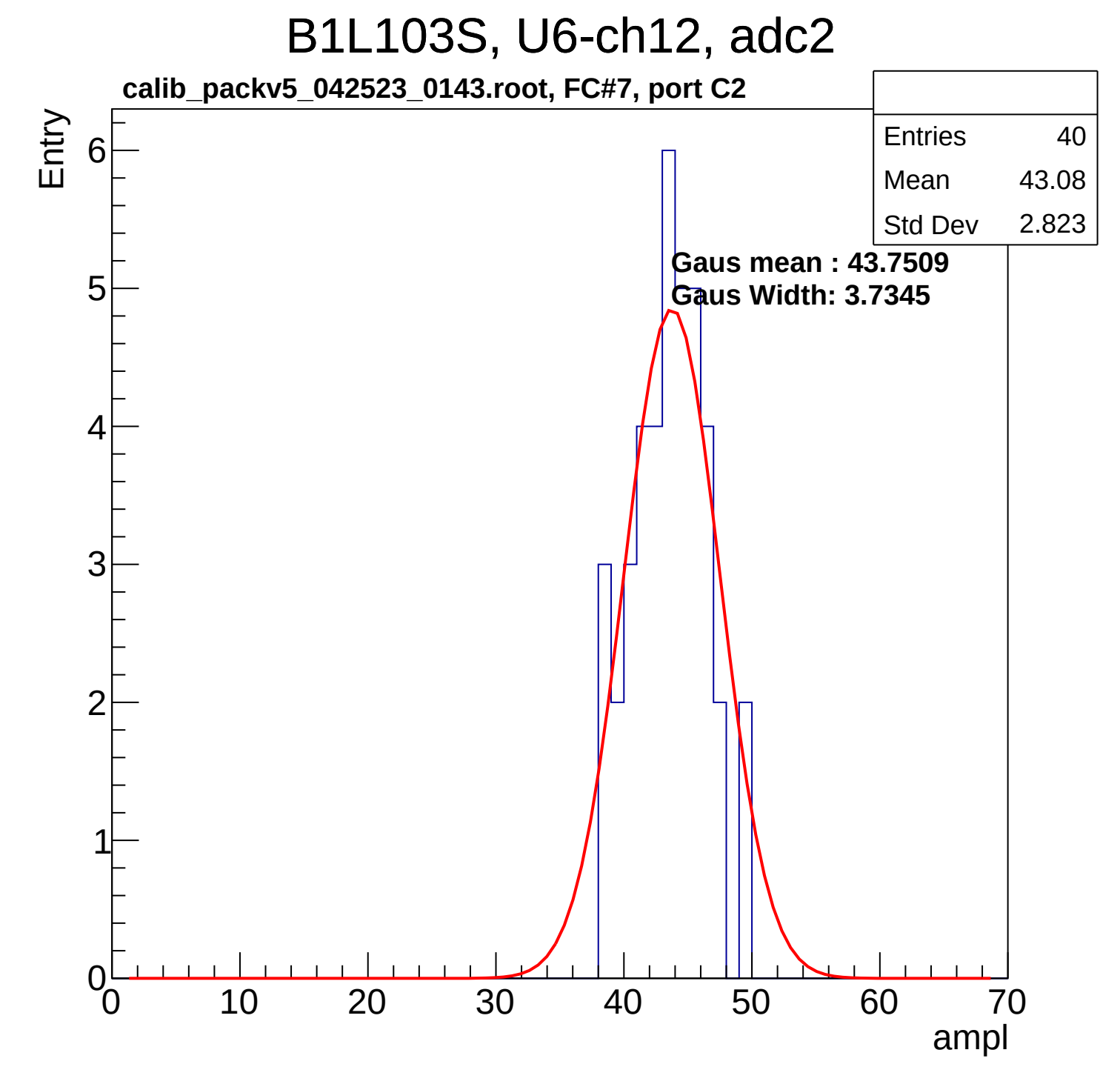
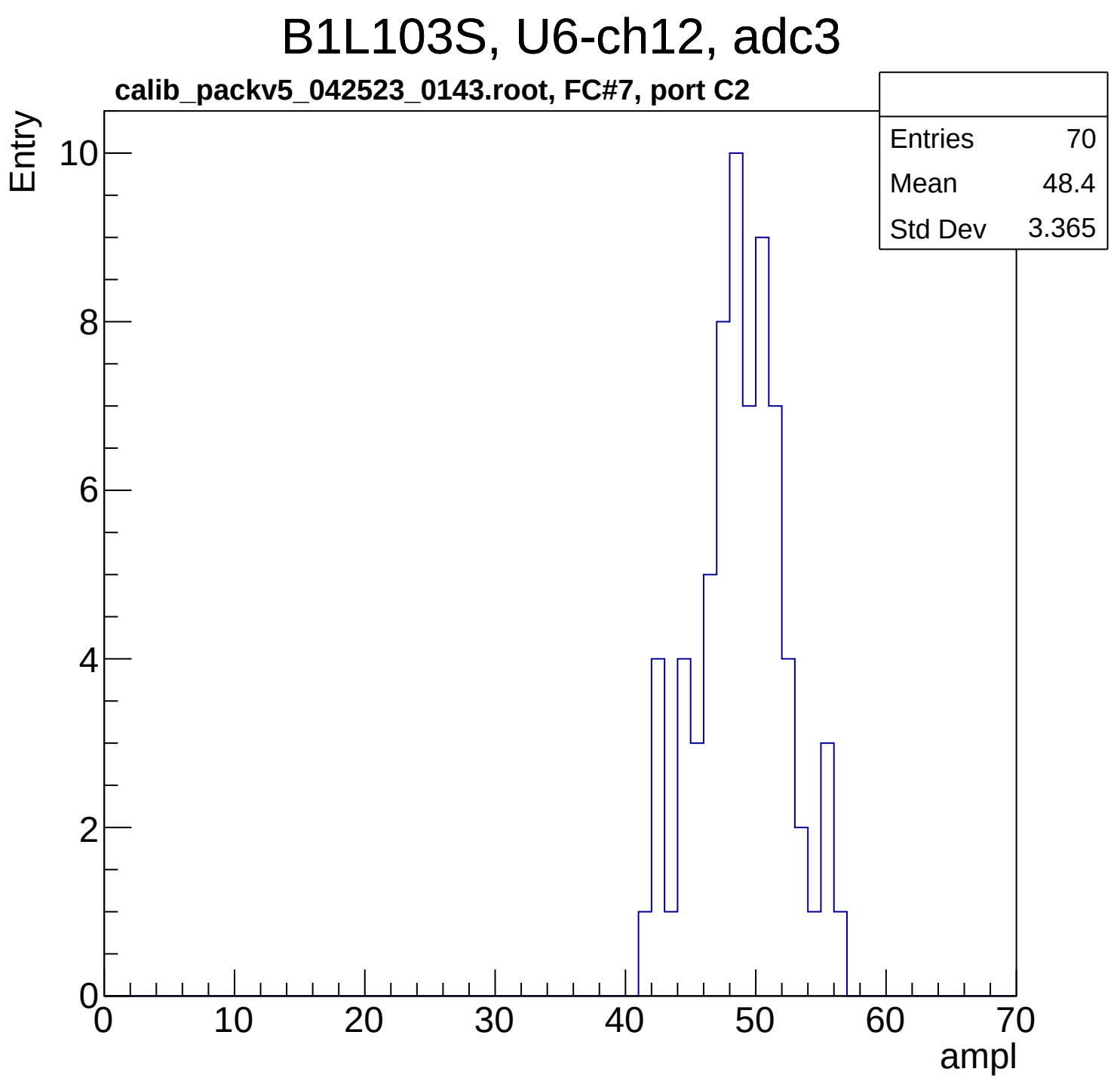
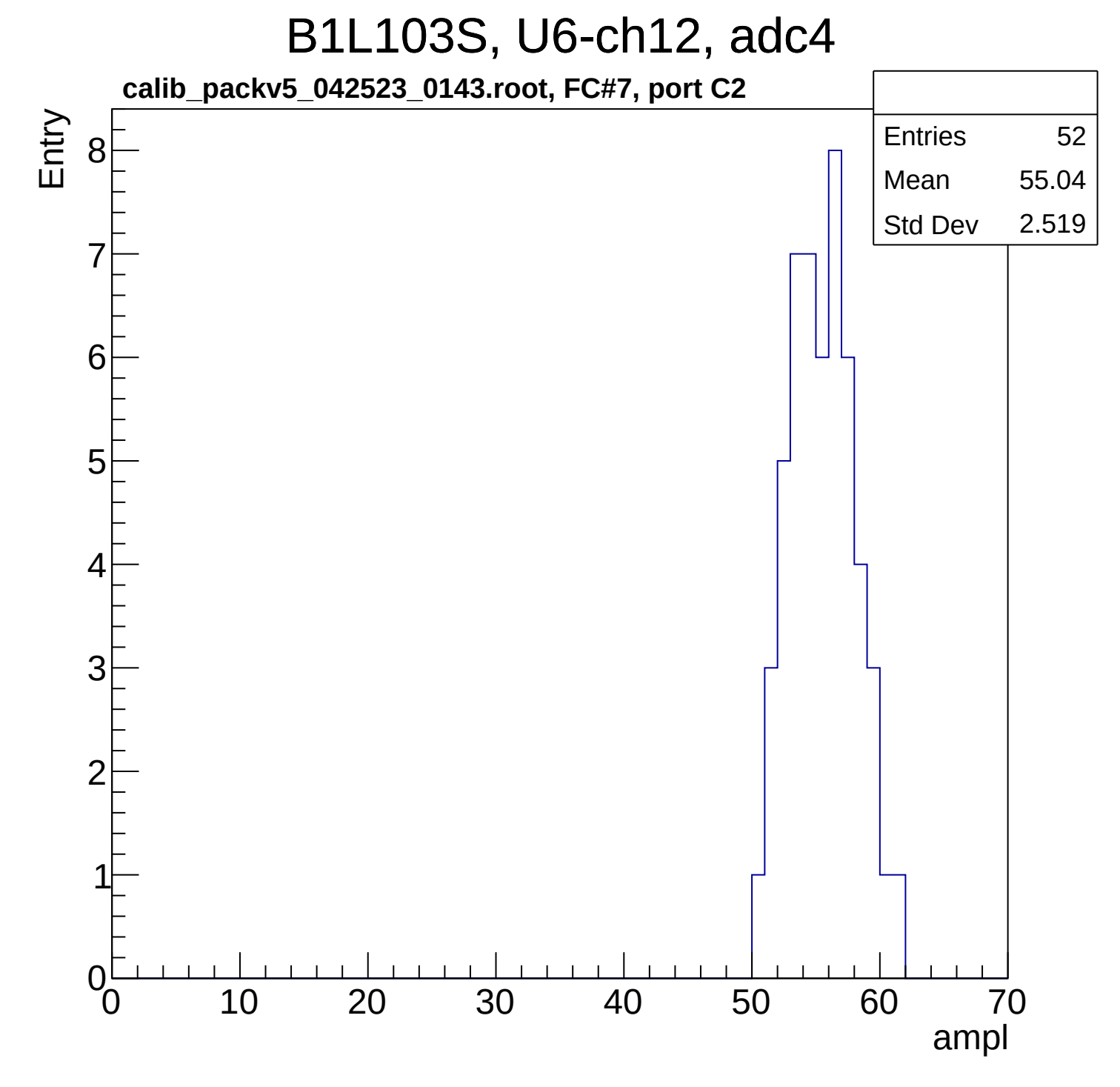

B1L103S, U6-ch12, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 43.08
Std Dev 2.823
Gaus mean : 43.7509
Gaus Width: 3.7345
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch12, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 48.4
Std Dev 3.365
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch12, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.04
Std Dev 2.519
Entry
ampl
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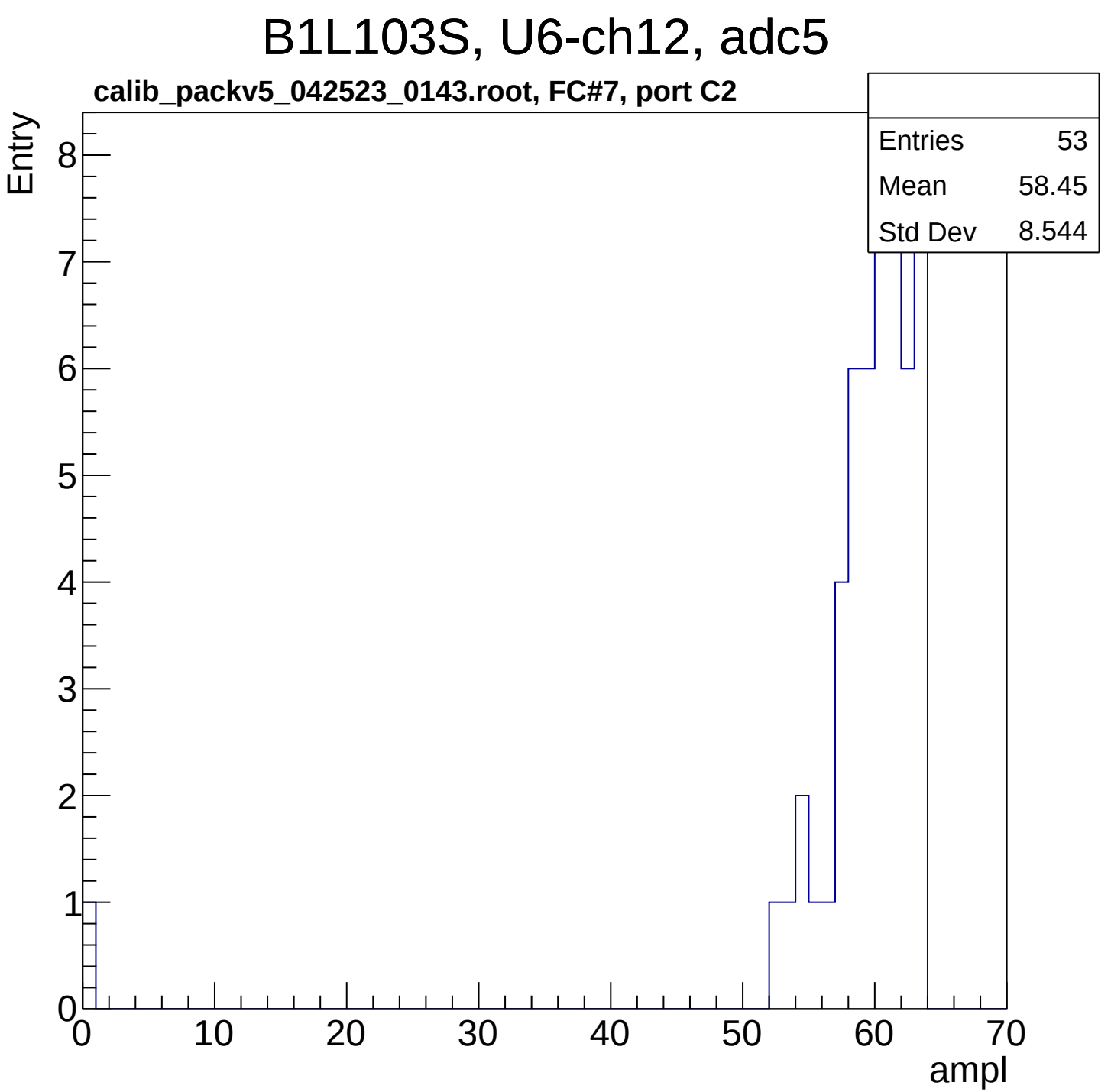
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch12, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.45
Std Dev 8.544
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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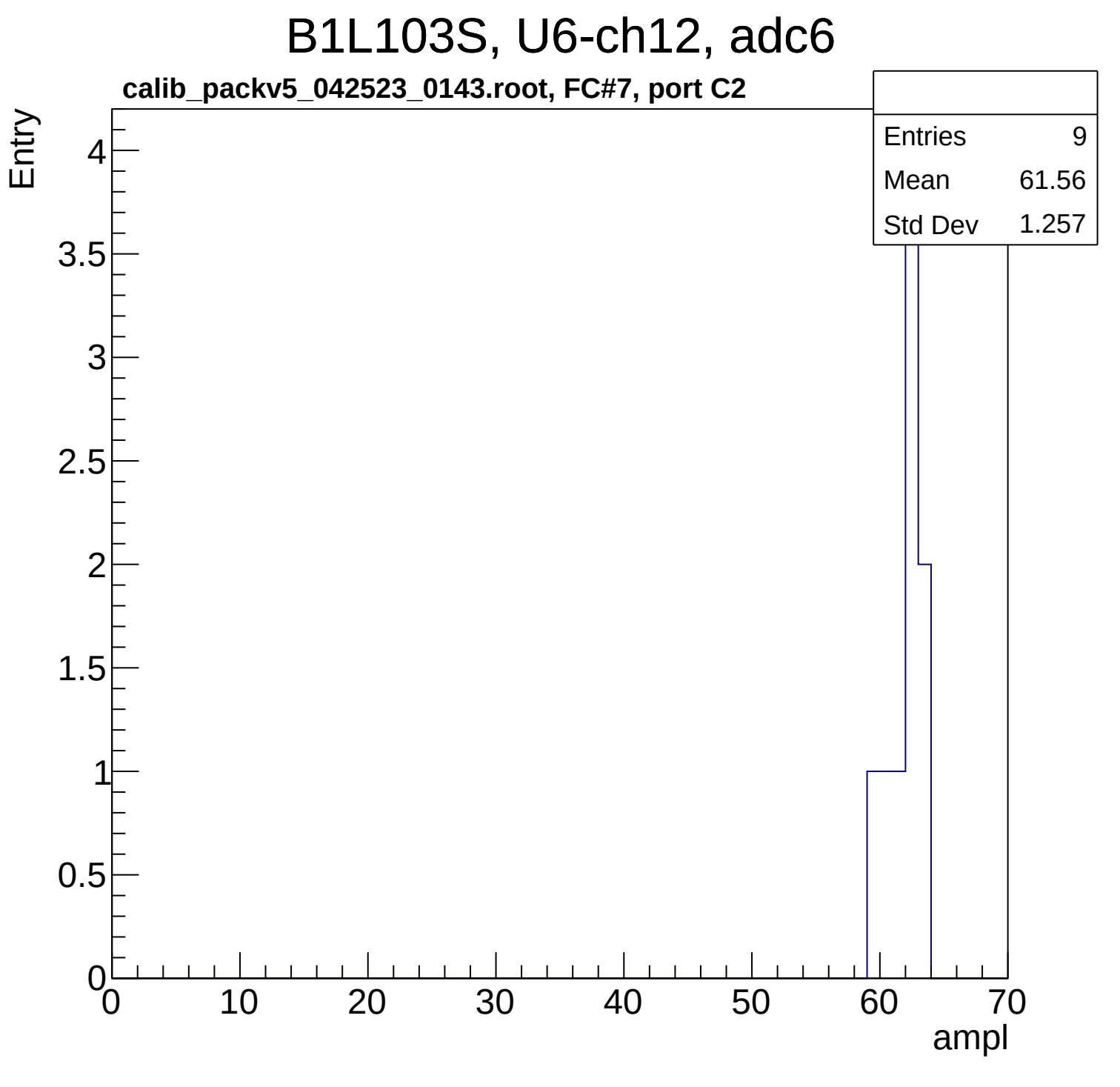
B1L103S, U6-ch12, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.56
Std Dev 1.257
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
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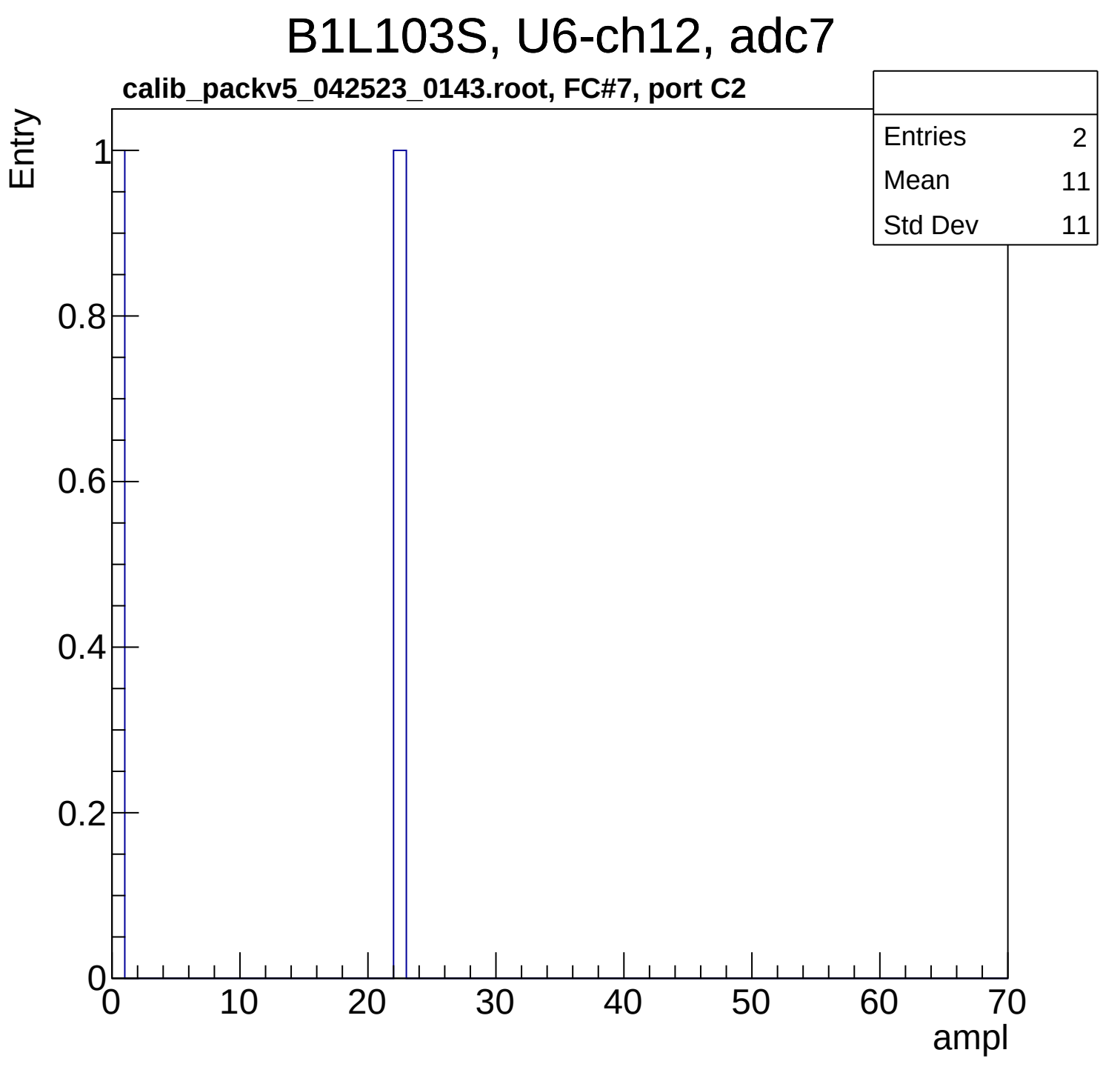
60
70


B1L103S, U6-ch12, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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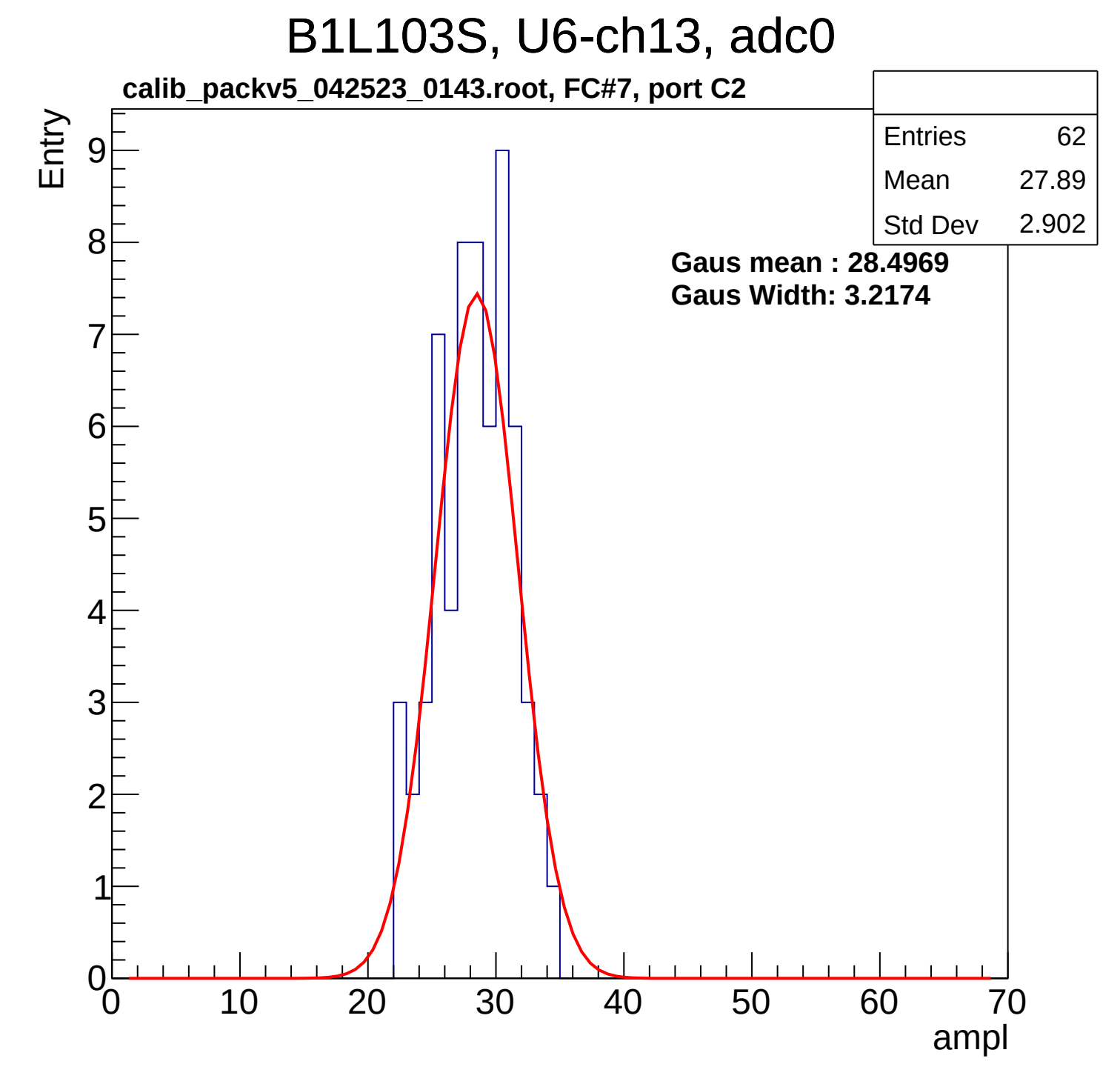
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch13, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 27.89
Std Dev 2.902
Gaus mean : 28.4969
Gaus Width: 3.2174
Entry
ampl
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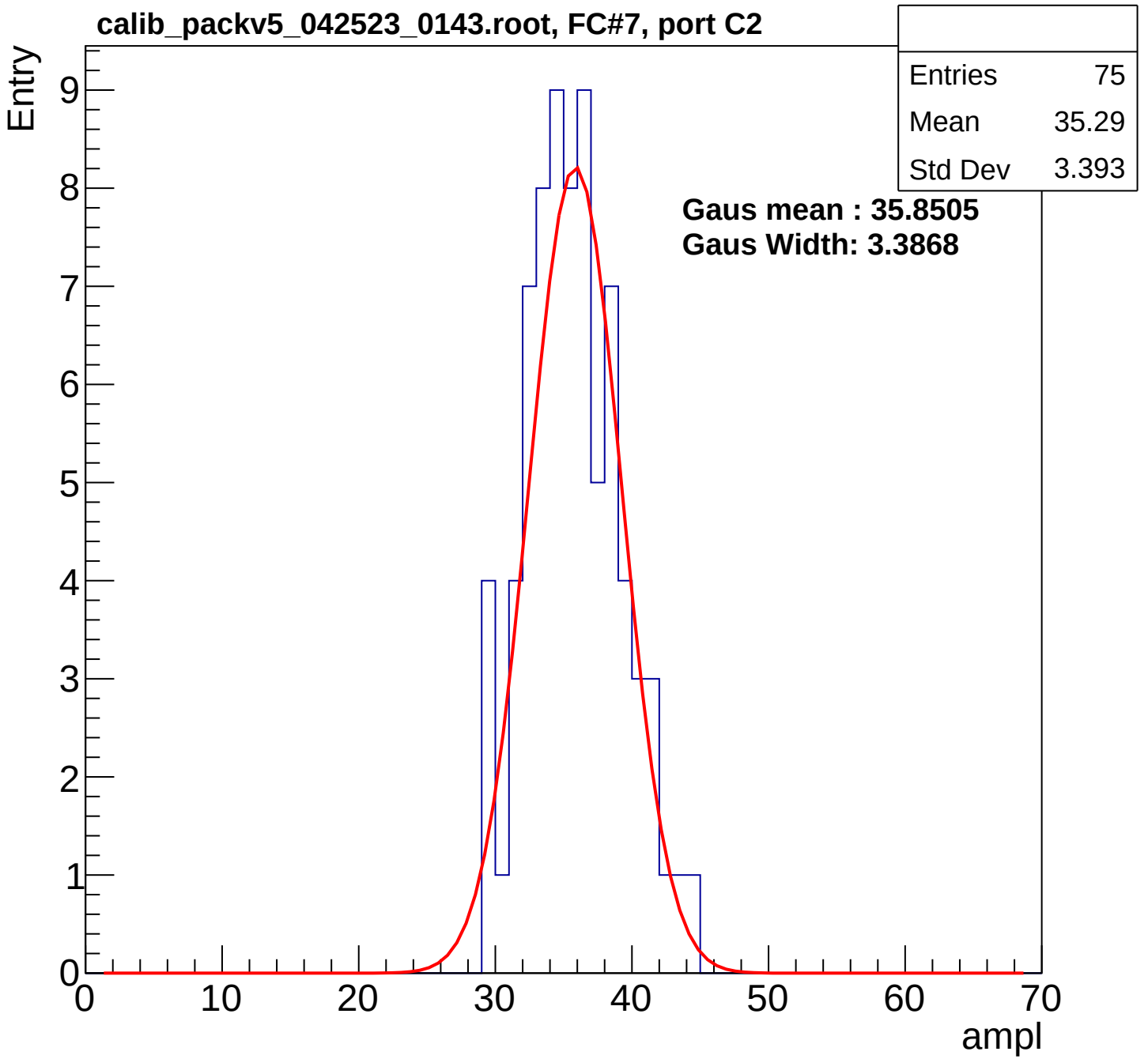
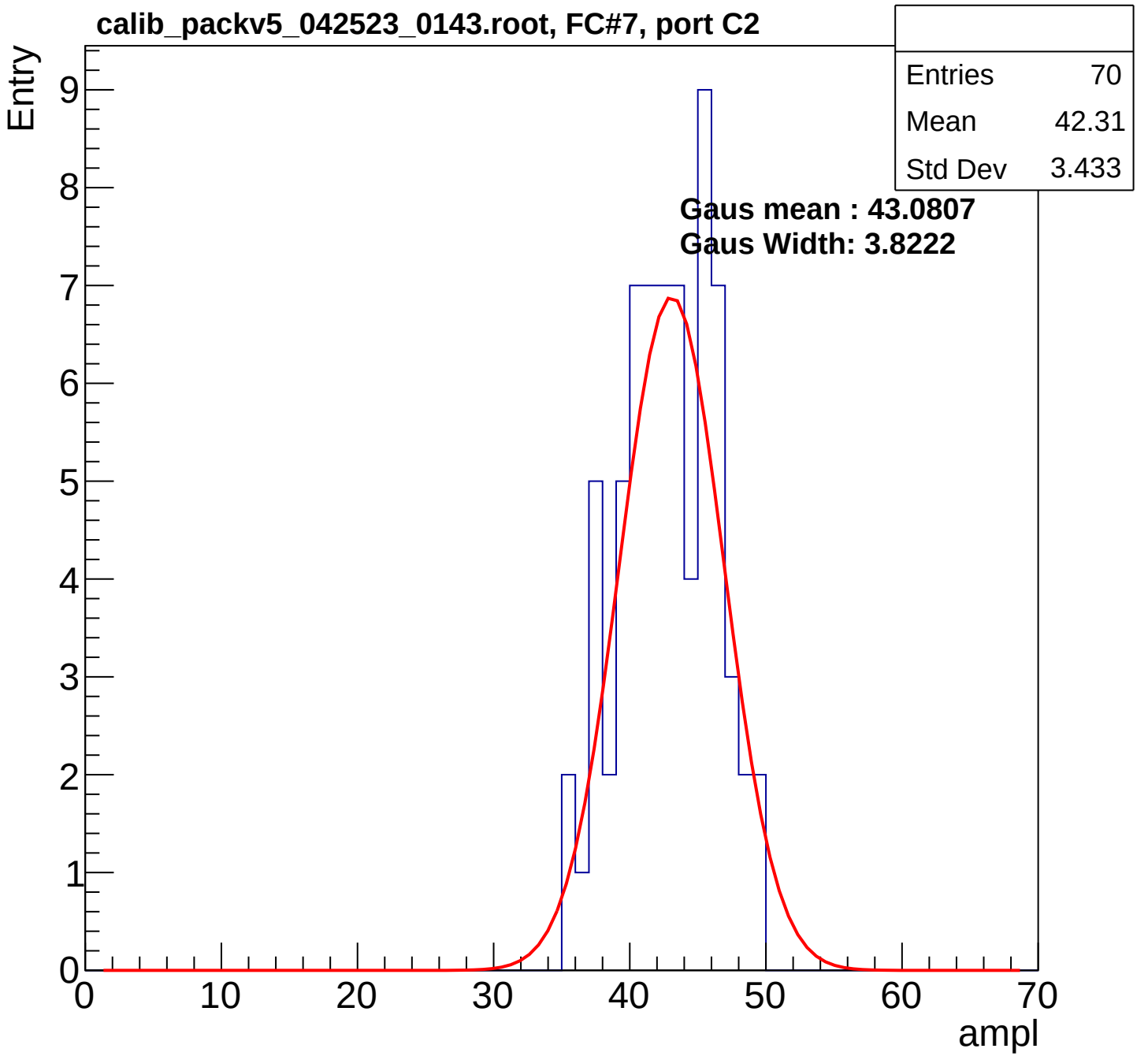
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch13, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 35.29
Std Dev 3.393
Gaus mean : 35.8505
Gaus Width: 3.3868
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U6-ch13, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.31
Std Dev 3.433
Gaus mean : 43.0807
Gaus Width: 3.8222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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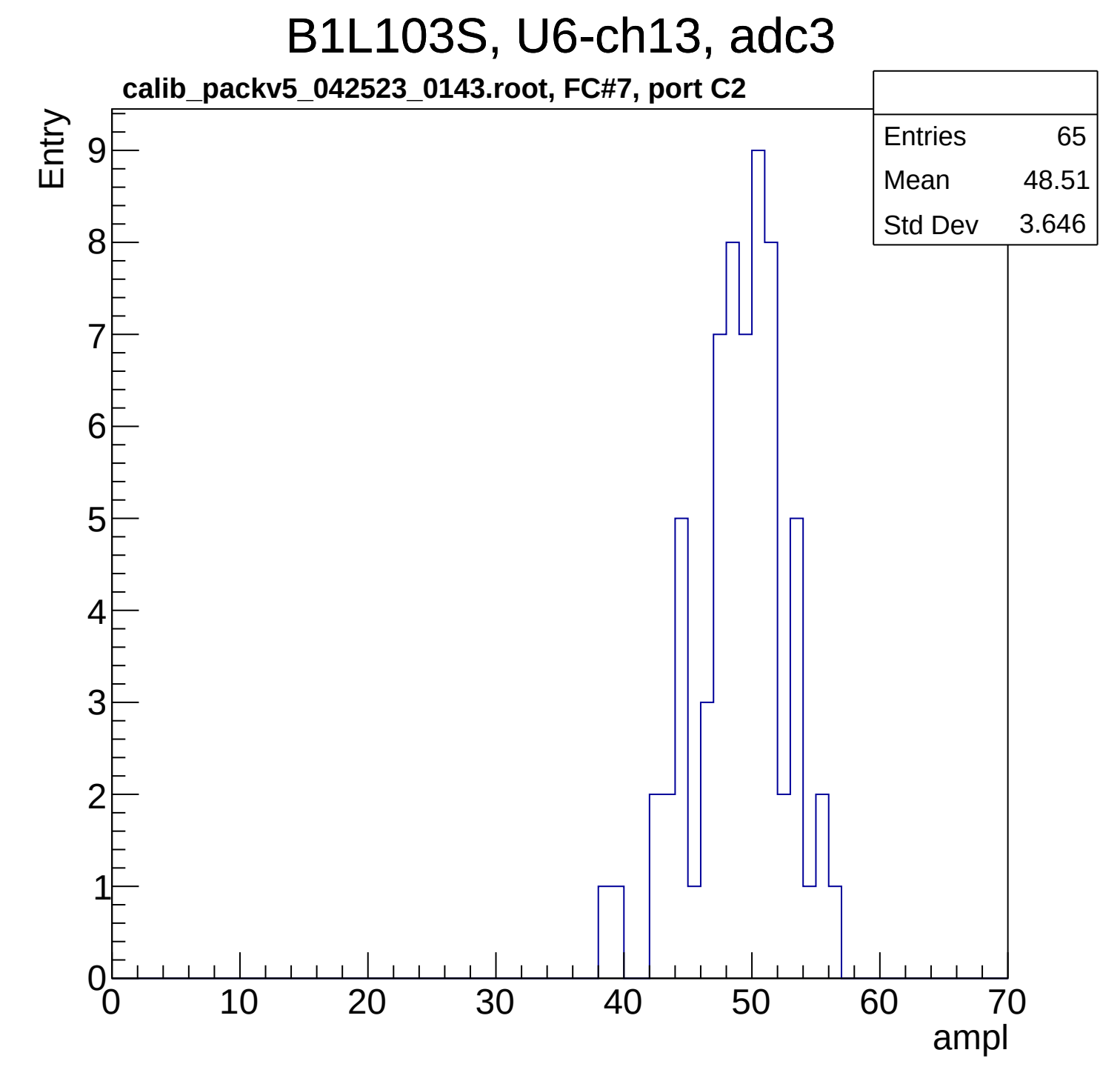
B1L103S, U6-ch13, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.51
Std Dev 3.646
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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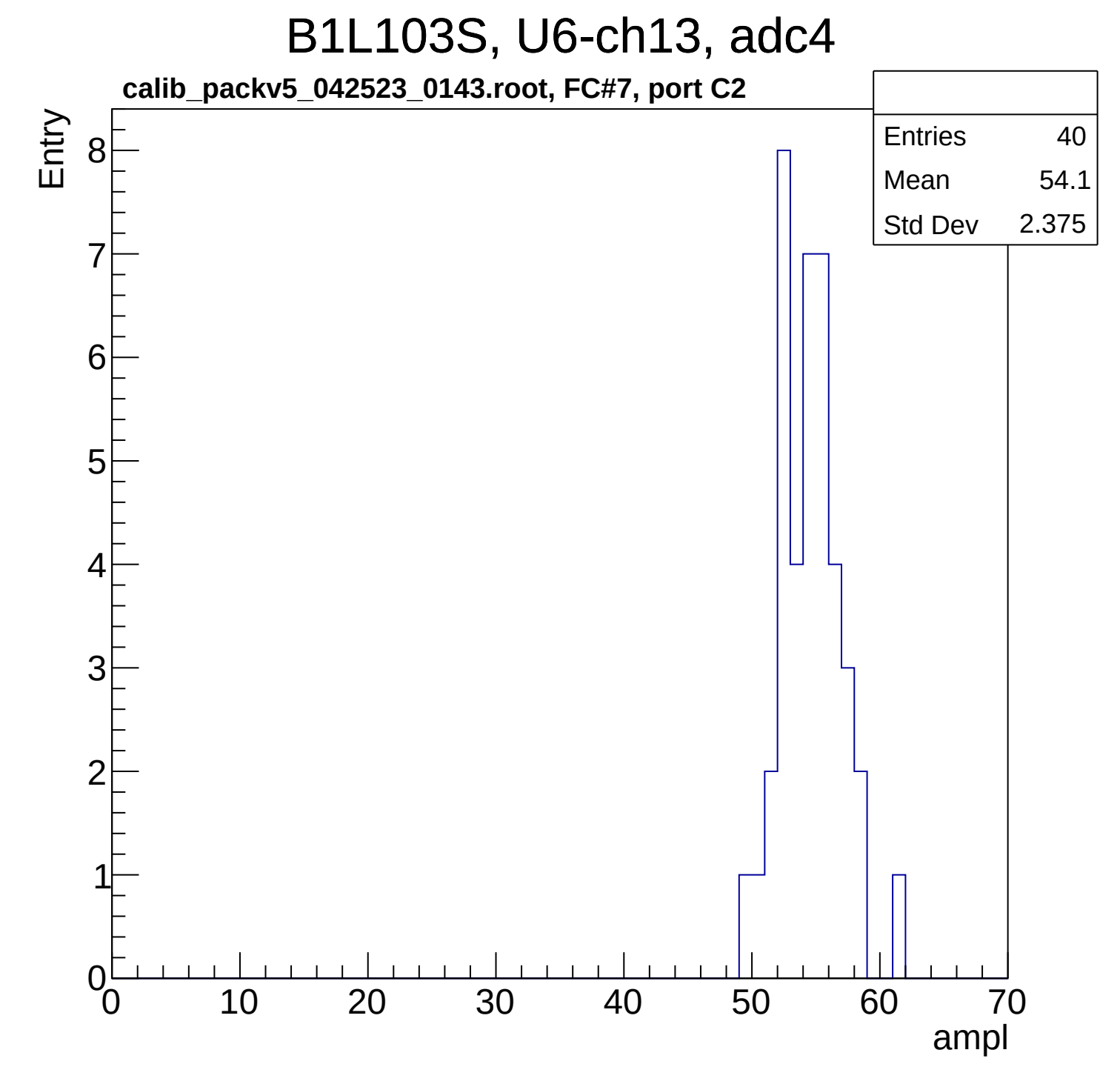
B1L103S, U6-ch13, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 54.1
Std Dev 2.375
Entry
ampl
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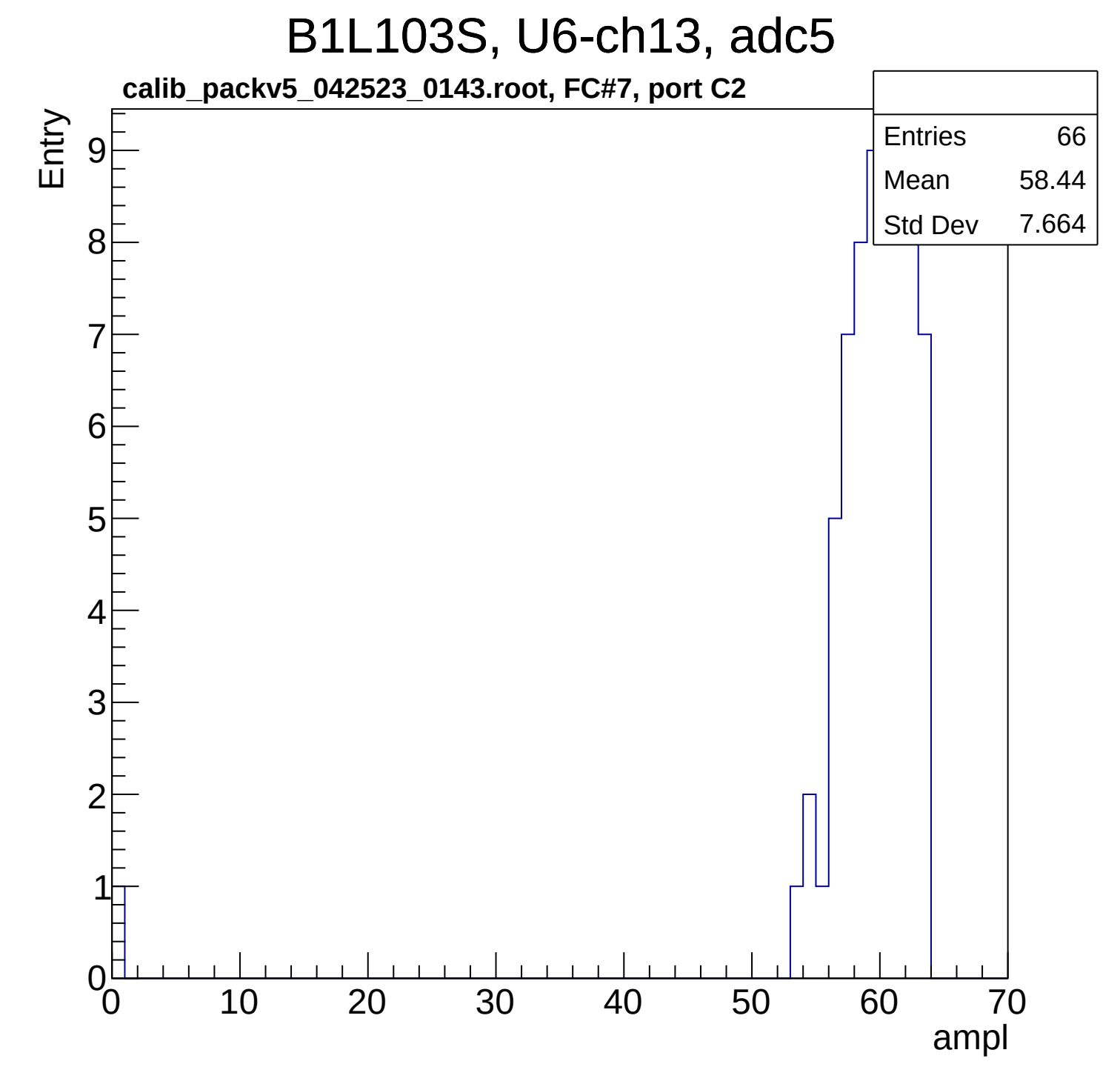
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch13, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 58.44
Std Dev 7.664
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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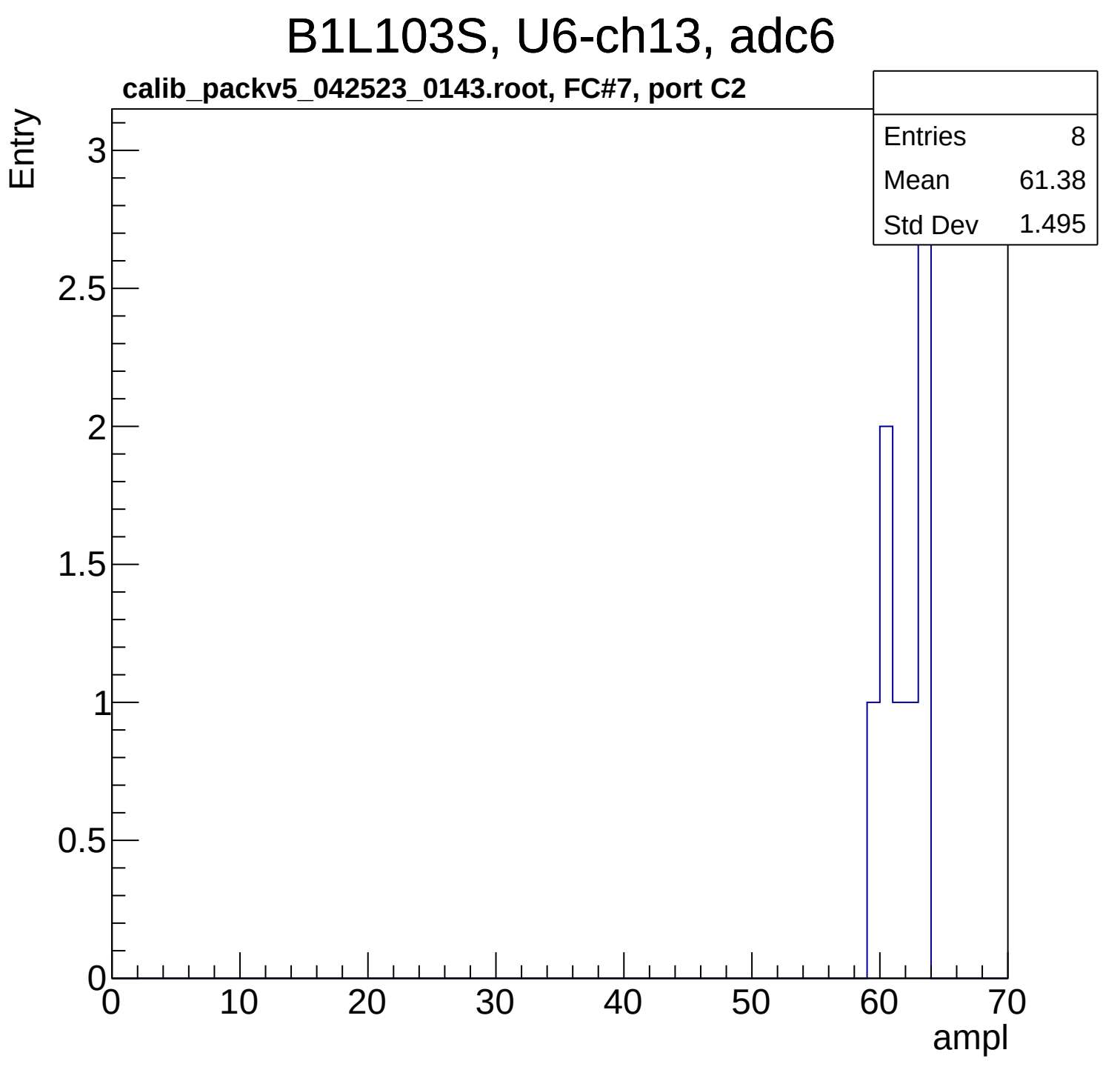
B1L103S, U6-ch13, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.38
Std Dev 1.495
Entry
ampl
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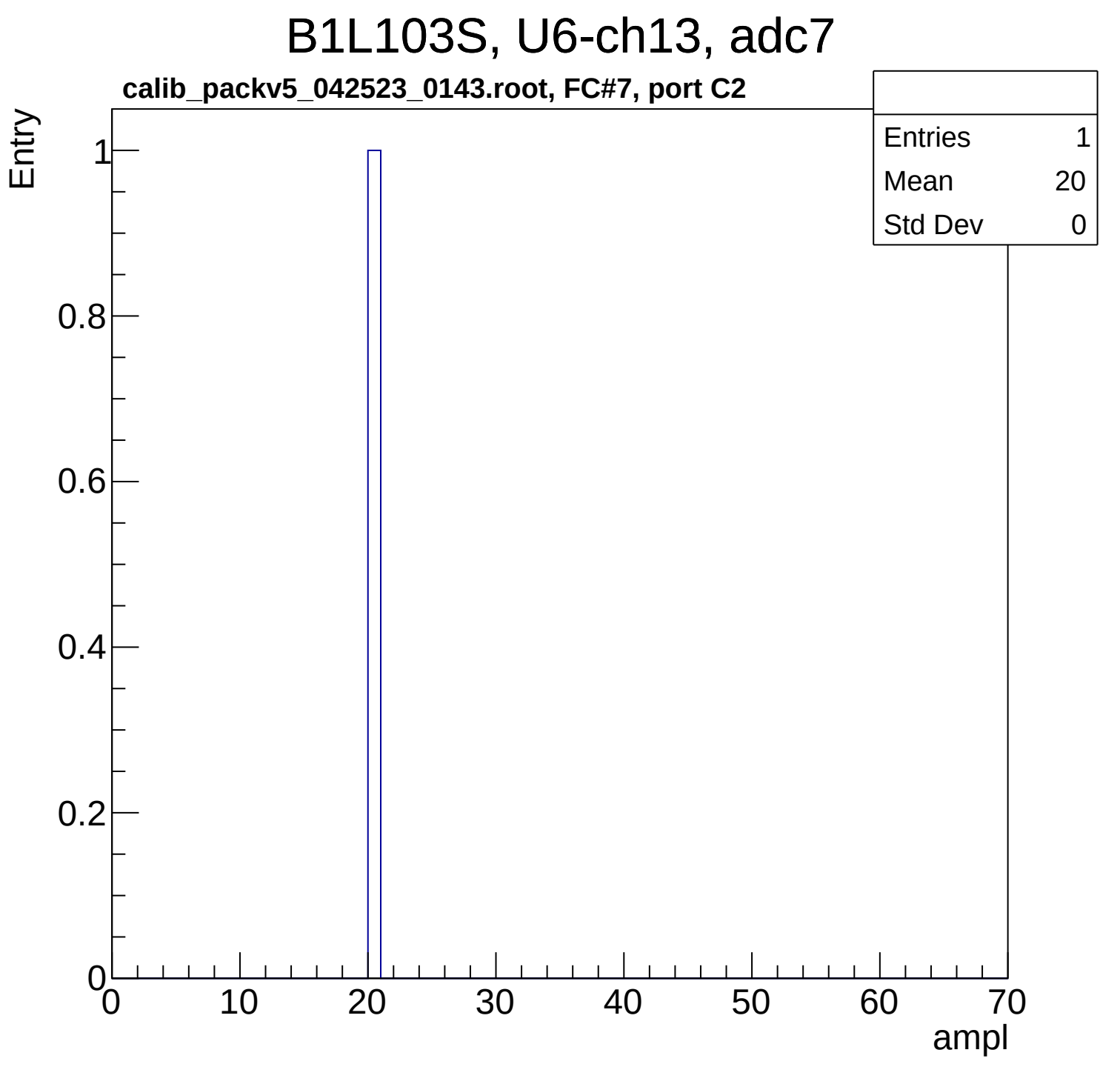
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch13, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
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0.8
0.6
0.4
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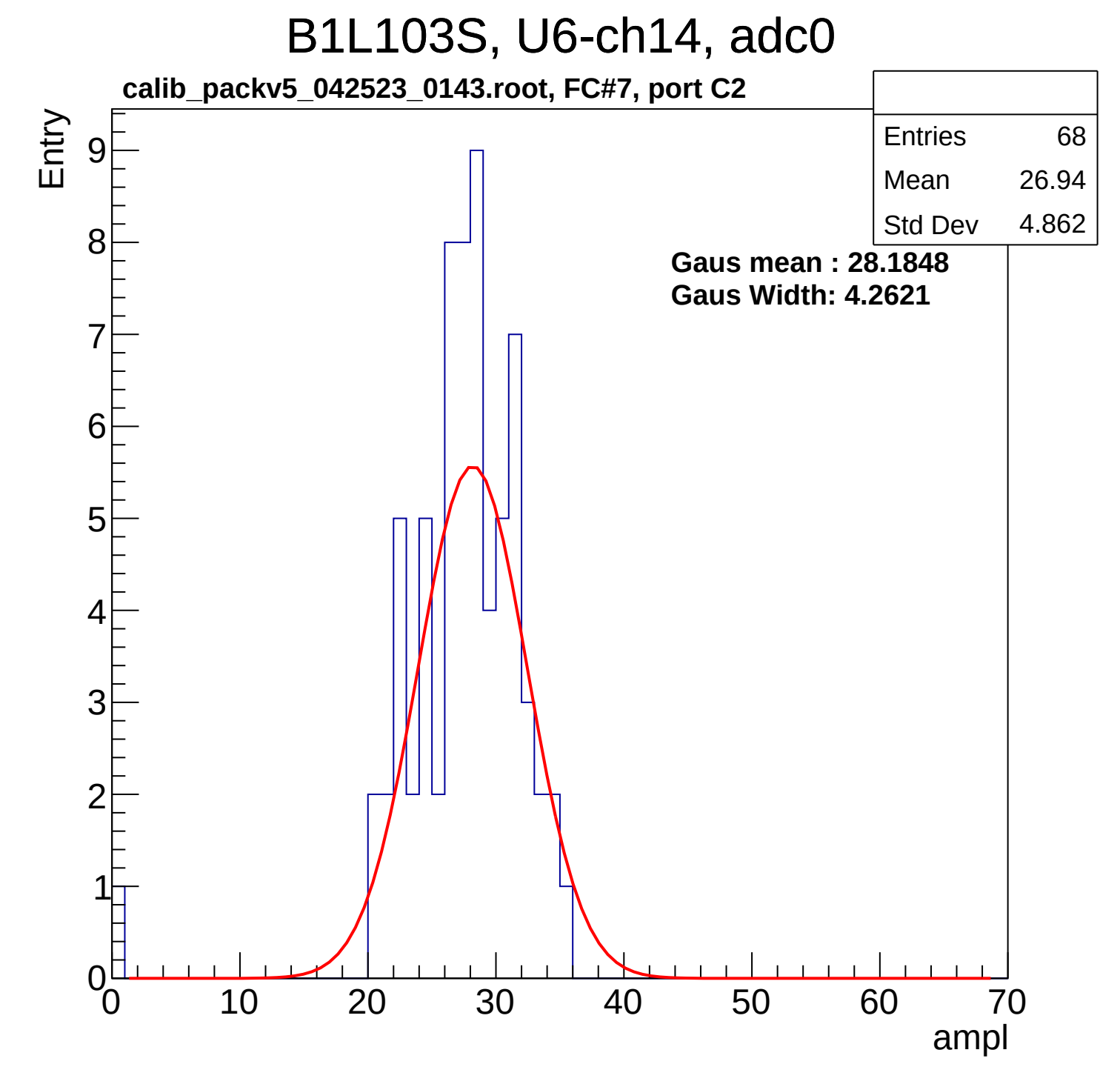
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch14, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 26.94
Std Dev 4.862
Gaus mean : 28.1848
Gaus Width: 4.2621
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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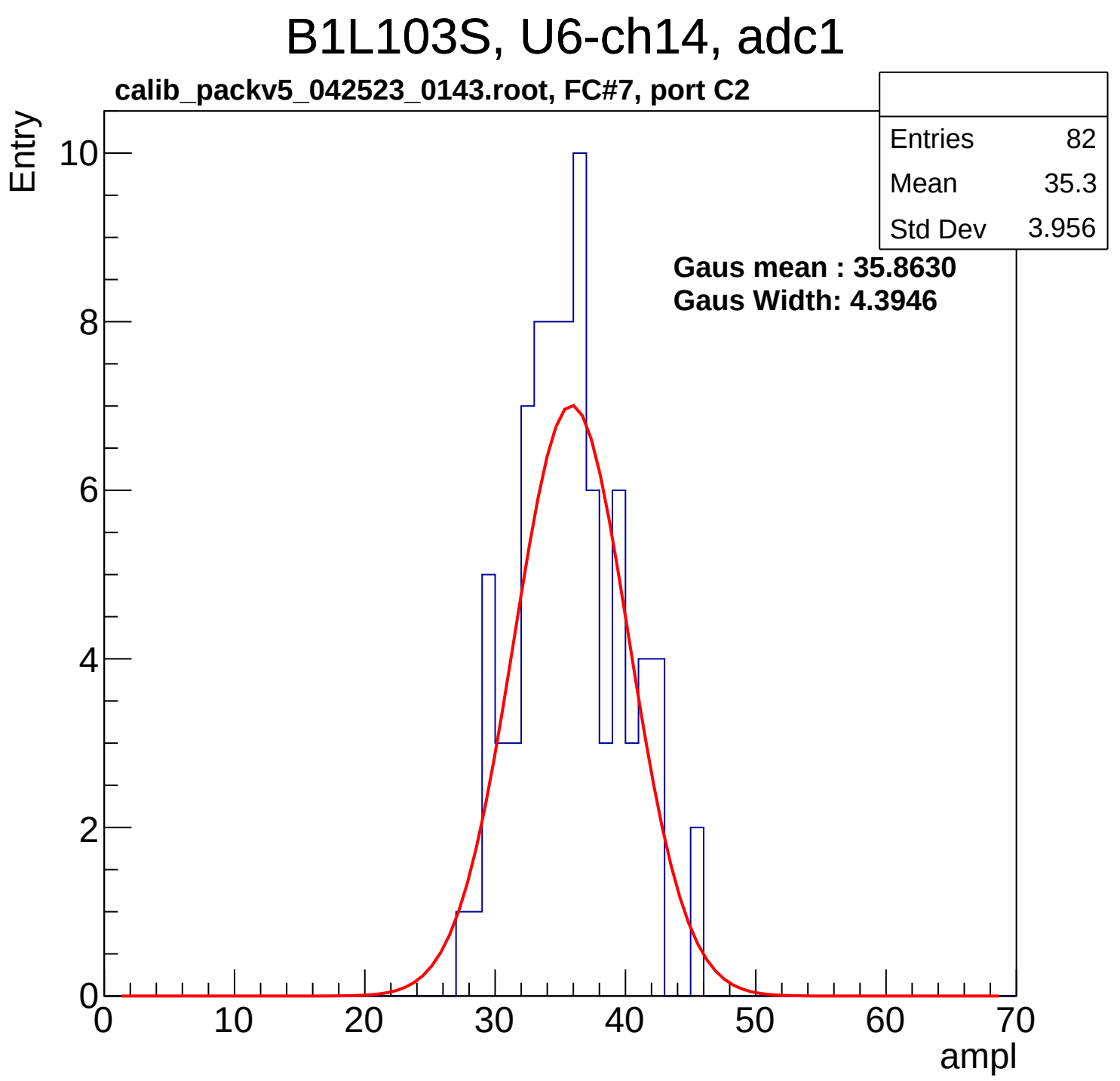
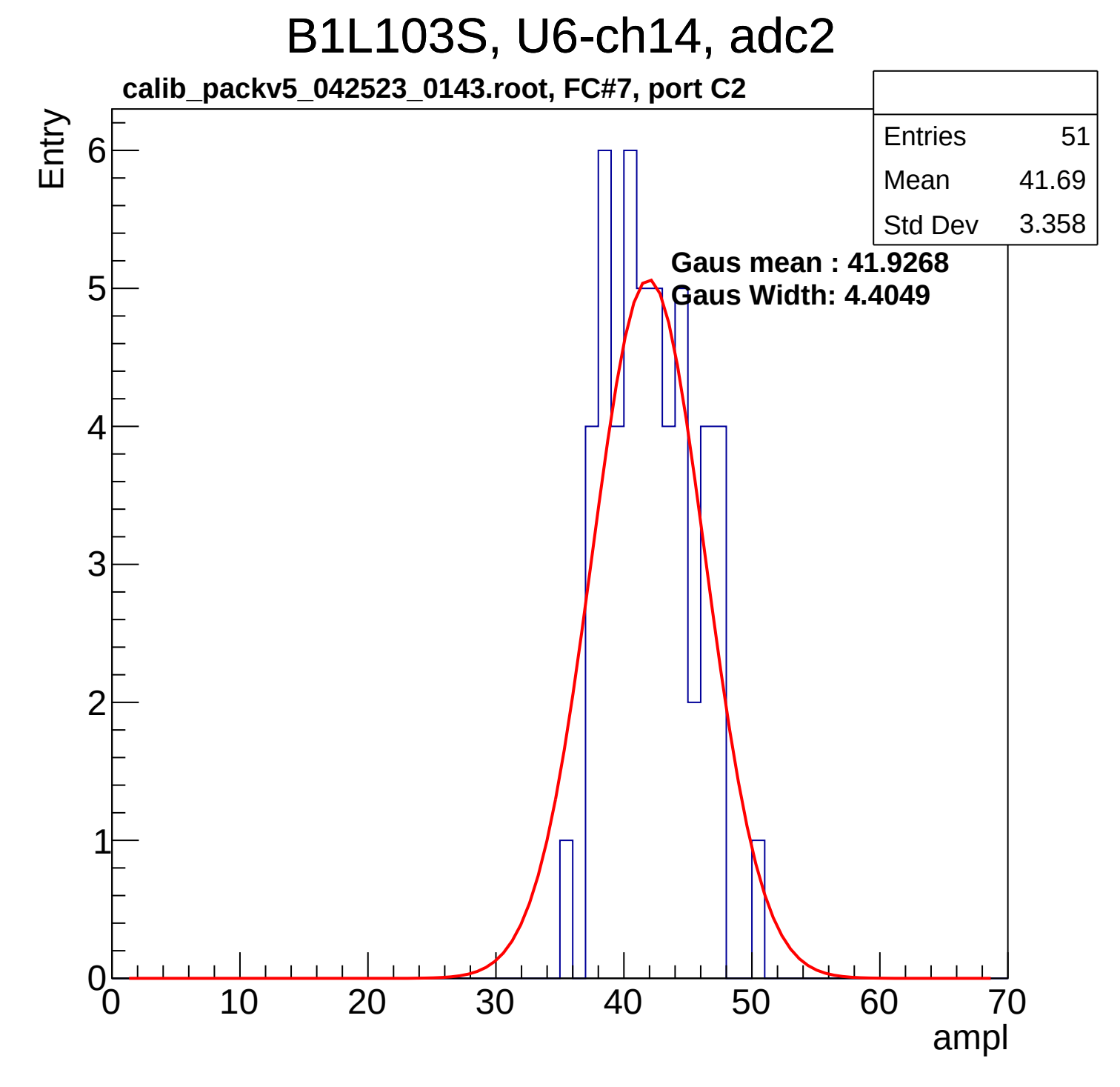

B1L103S, U6-ch14, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 35.3
Std Dev 3.956
Gaus mean : 35.8630
Gaus Width: 4.3946
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch14, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 41.69
Std Dev 3.358
Gaus mean : 41.9268
Gaus Width: 4.4049
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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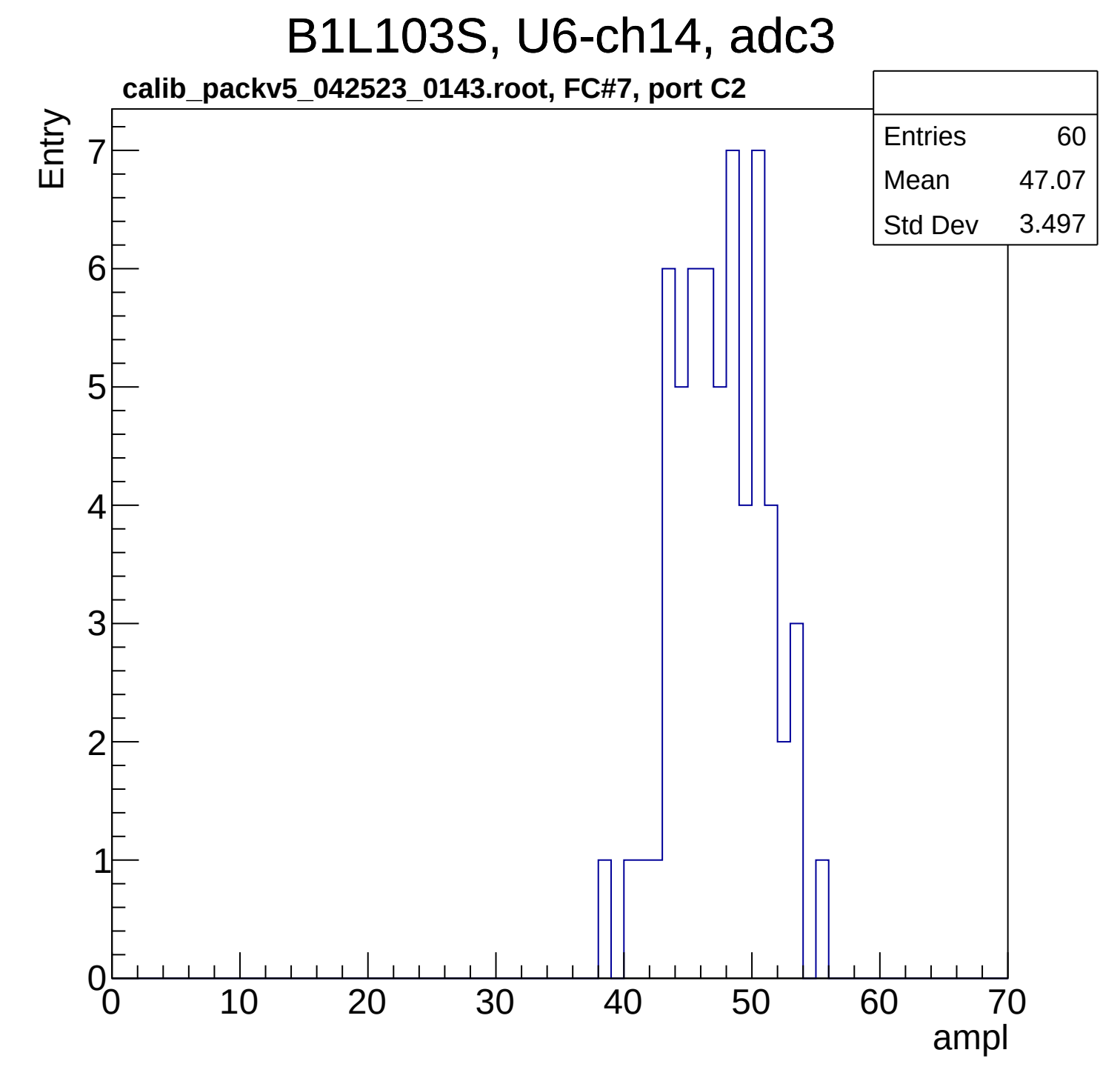
B1L103S, U6-ch14, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 47.07
Std Dev 3.497
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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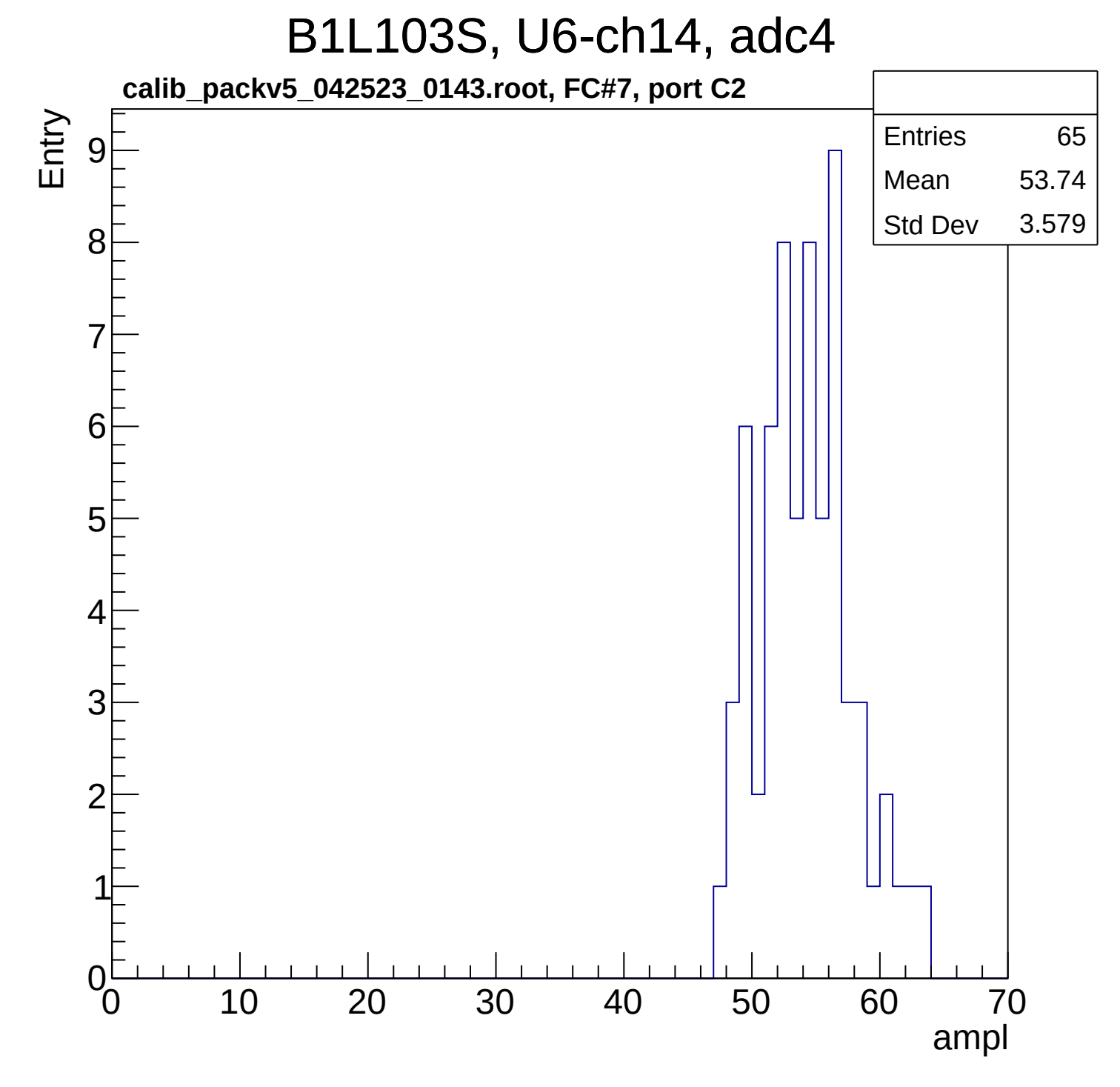
B1L103S, U6-ch14, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 53.74
Std Dev 3.579
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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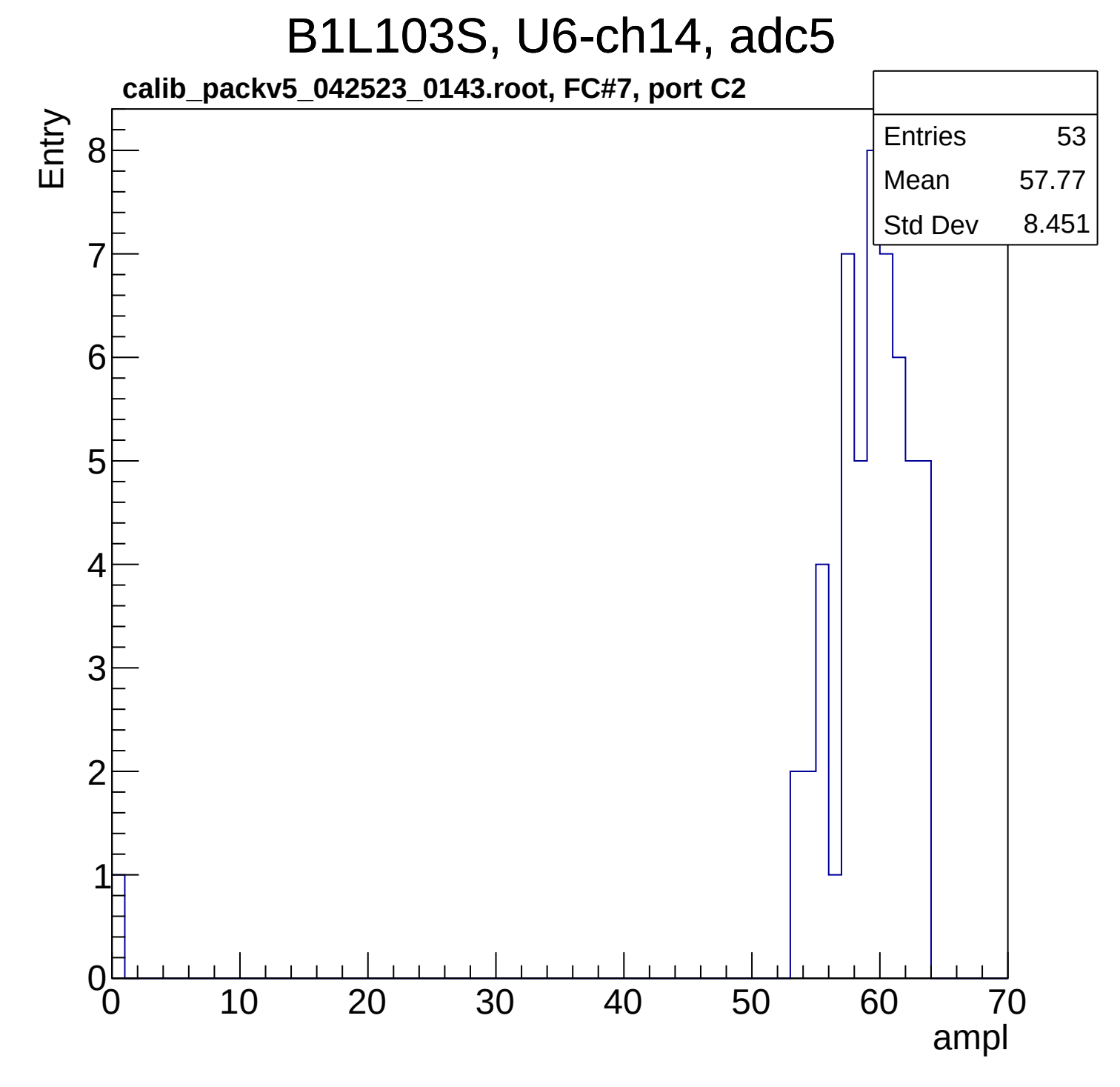
B1L103S, U6-ch14, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 57.77
Std Dev 8.451
Entry
ampl
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1
2
3
4
5
6
7
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60
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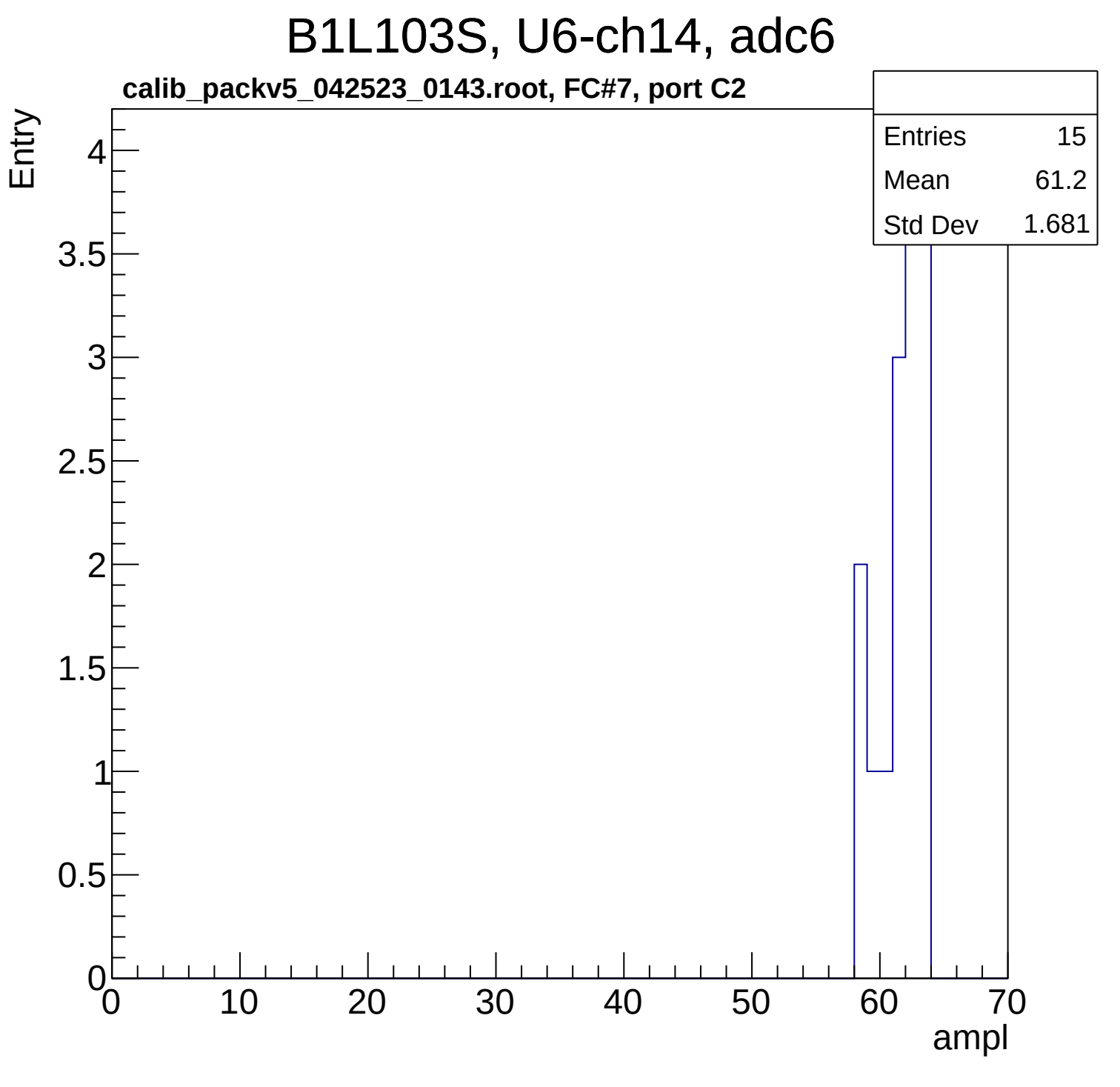
B1L103S, U6-ch14, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 61.2
Std Dev 1.681
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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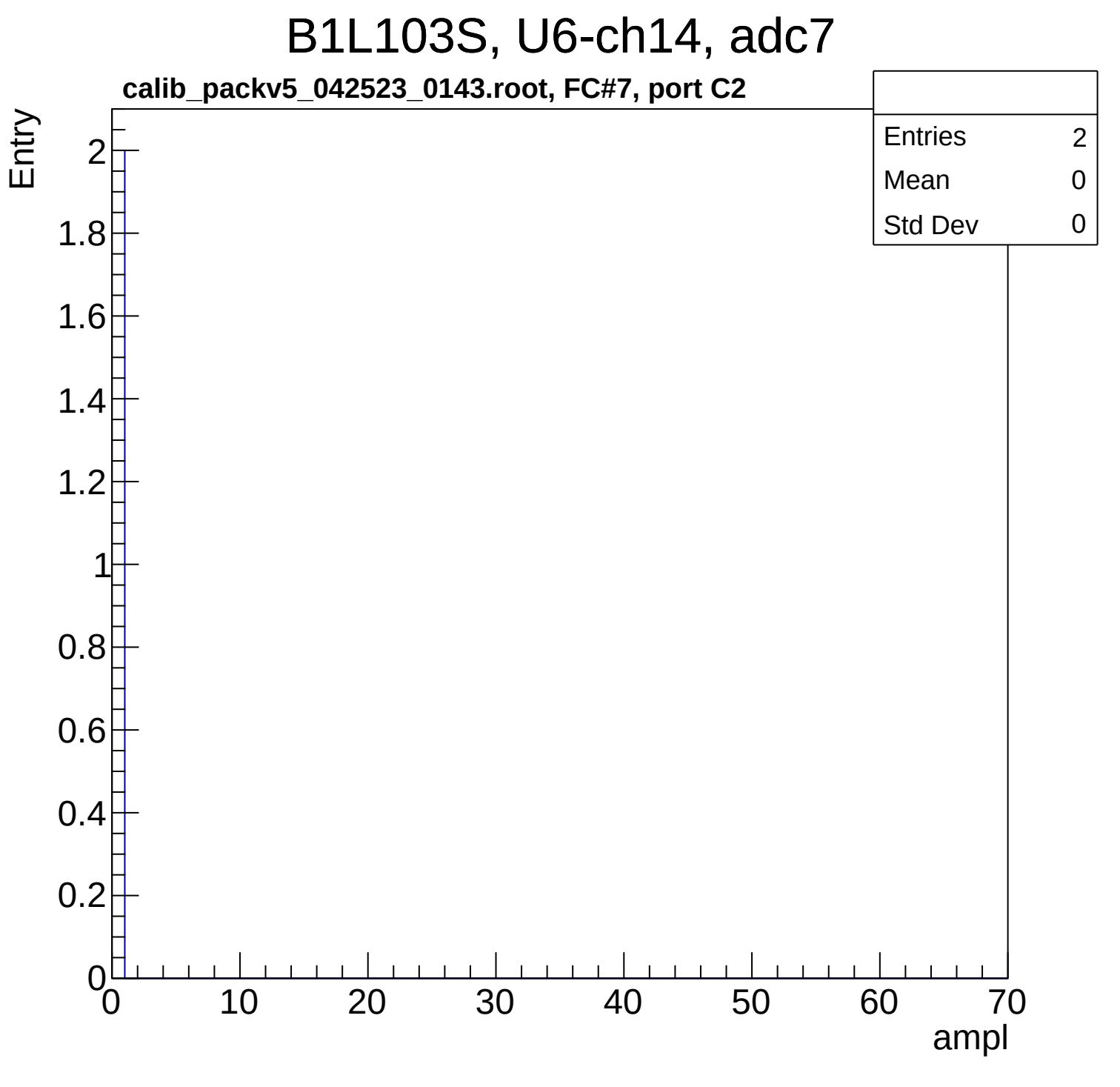
50
60
70


B1L103S, U6-ch14, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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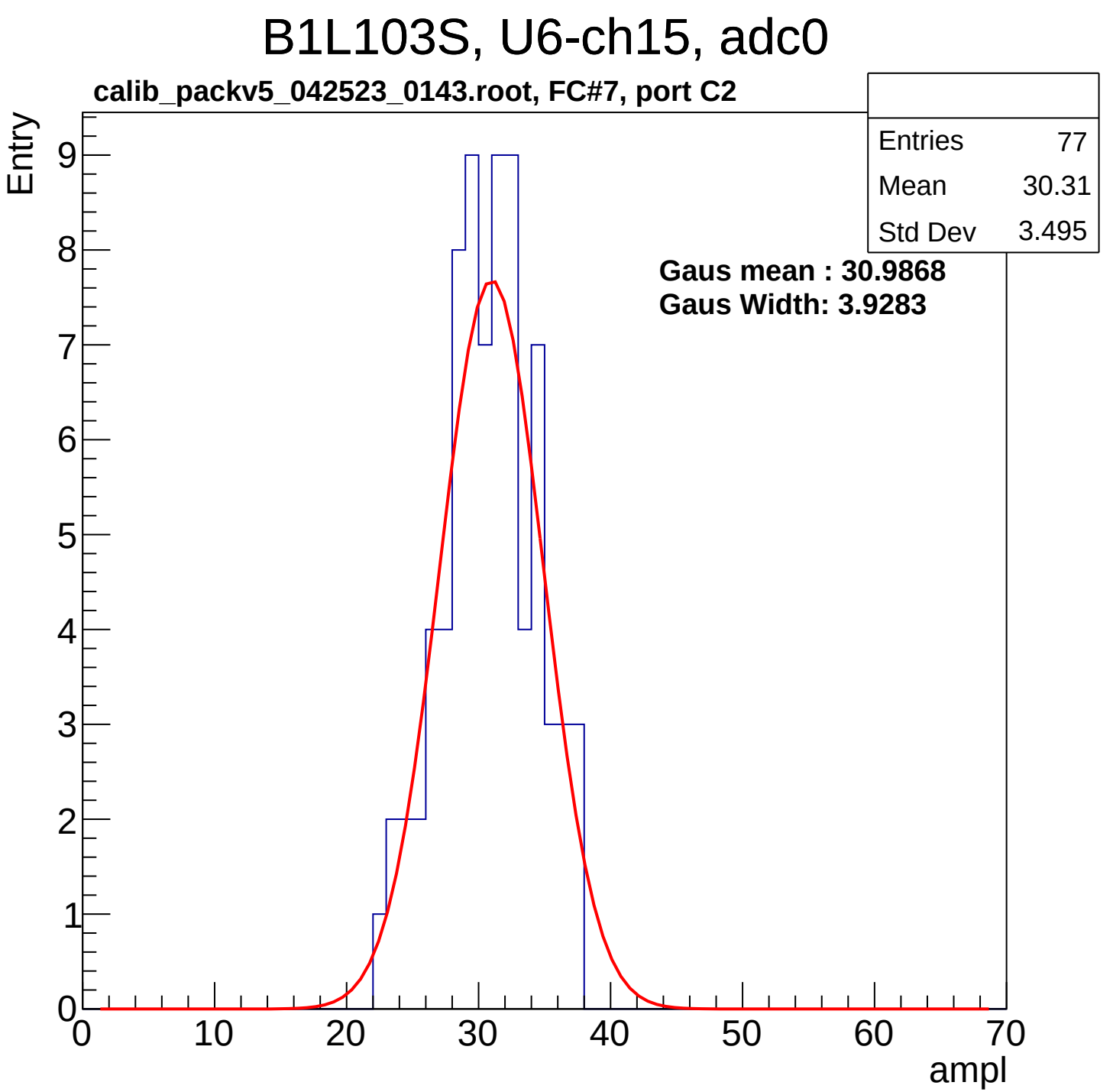
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch15, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.31
Std Dev 3.495
Gaus mean : 30.9868
Gaus Width: 3.9283
Entry
ampl
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10
20
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1
2
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4
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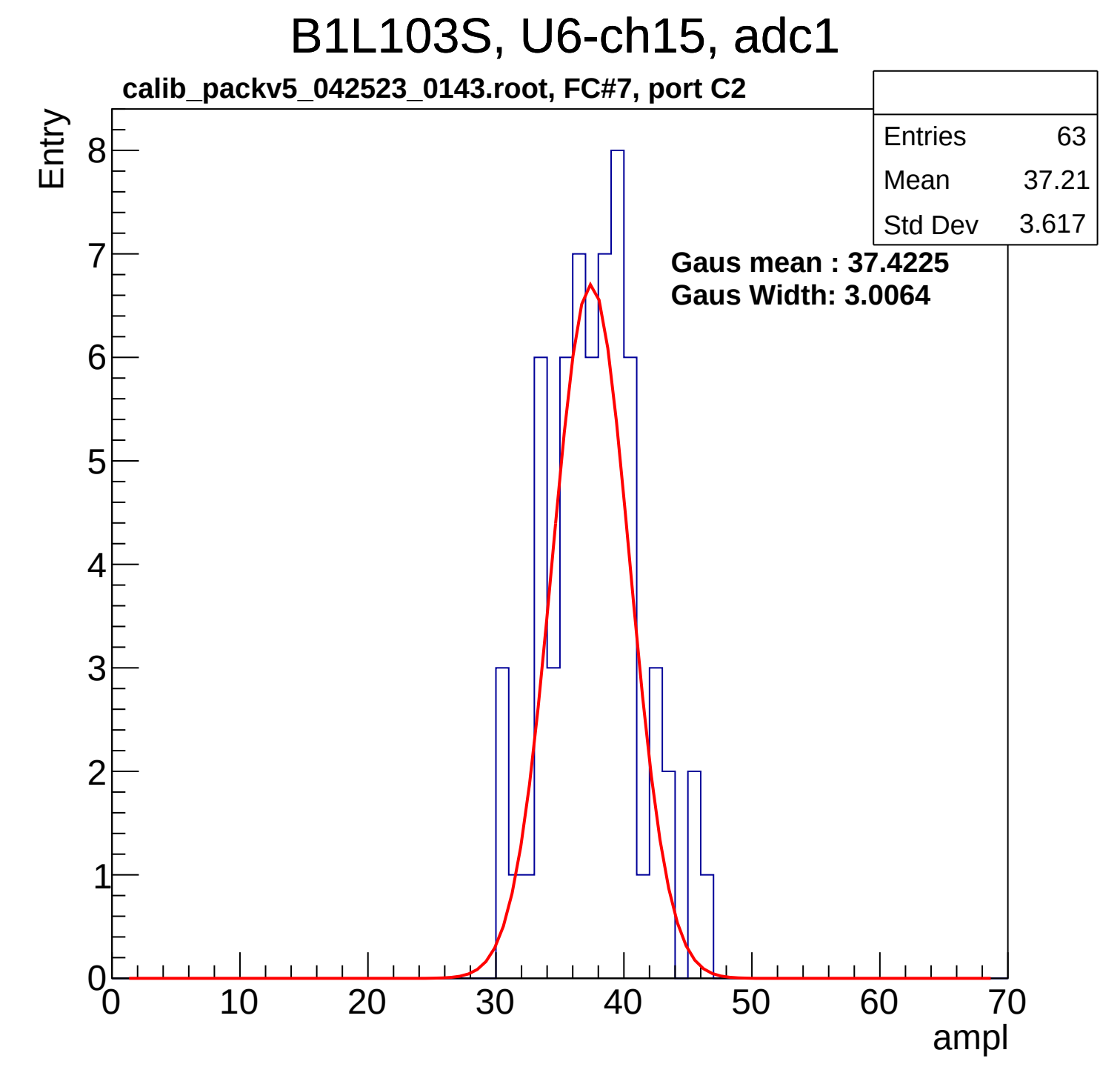
6
7
8
9


B1L103S, U6-ch15, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 37.21
Std Dev 3.617
Gaus mean : 37.4225
Gaus Width: 3.0064
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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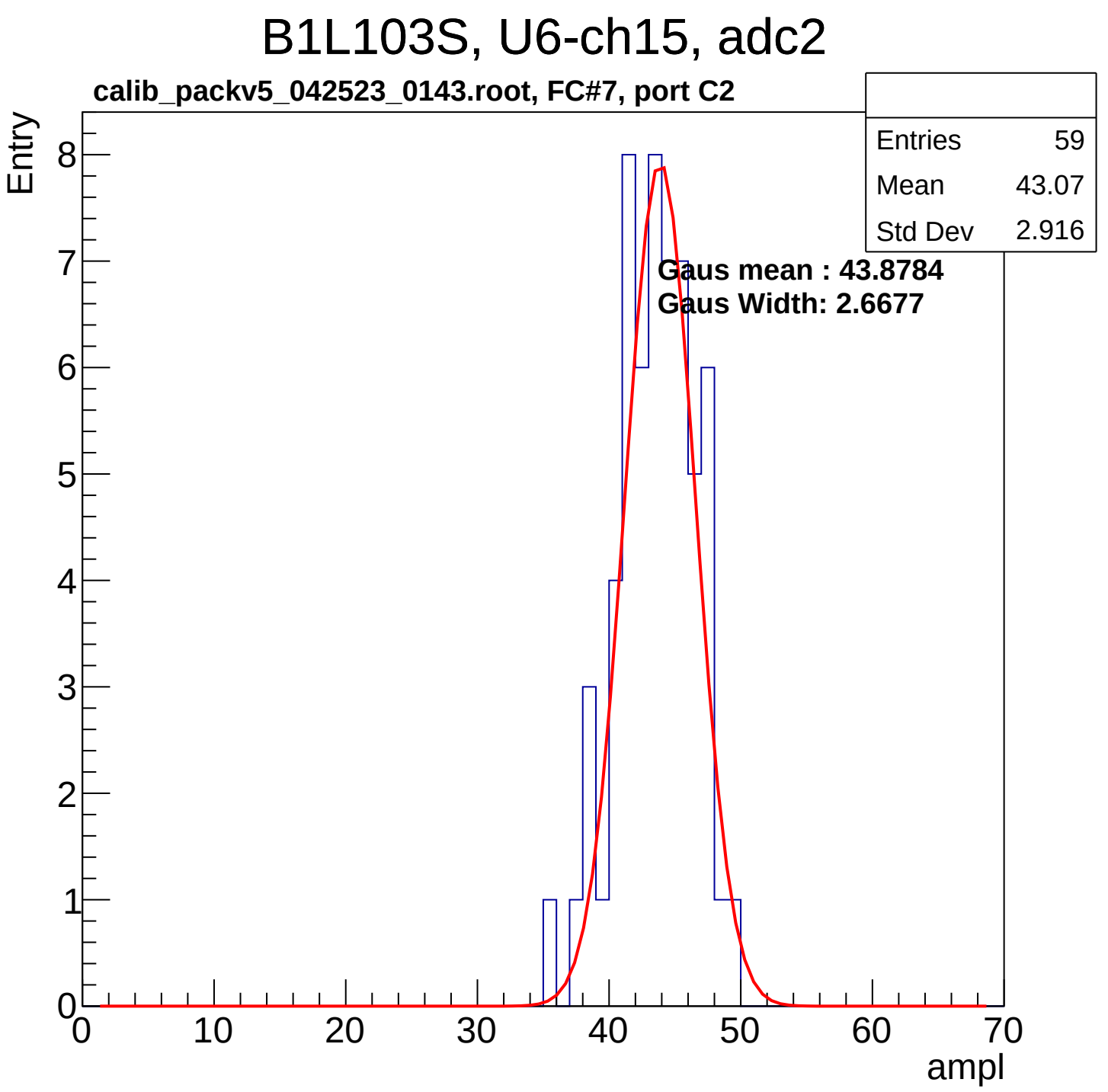
B1L103S, U6-ch15, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 43.07
Std Dev 2.916
Gaus mean : 43.8784
Gaus Width: 2.6677
Entry
ampl
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1
2
3
4
5
6
7
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0
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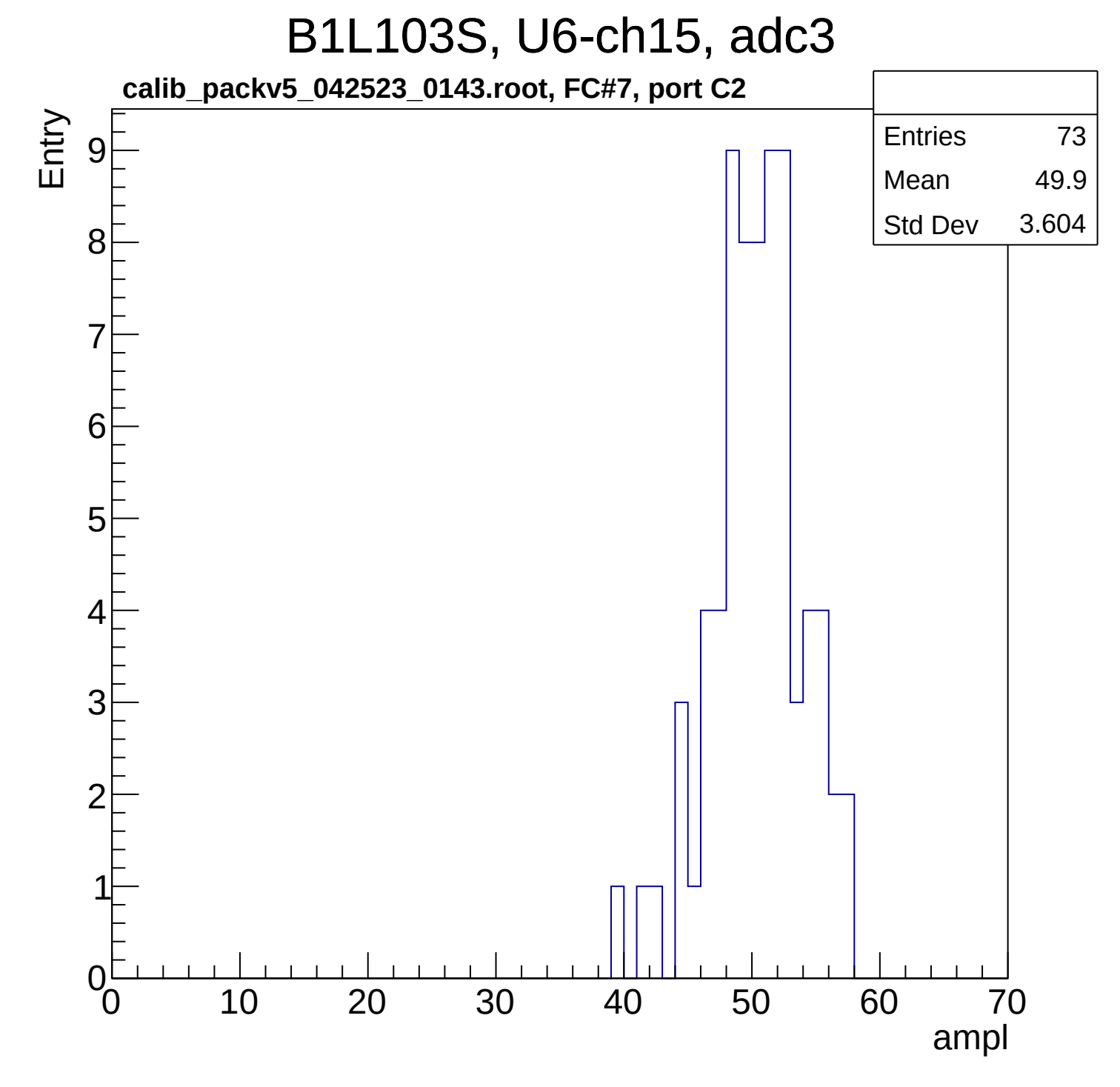
B1L103S, U6-ch15, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.9
Std Dev 3.604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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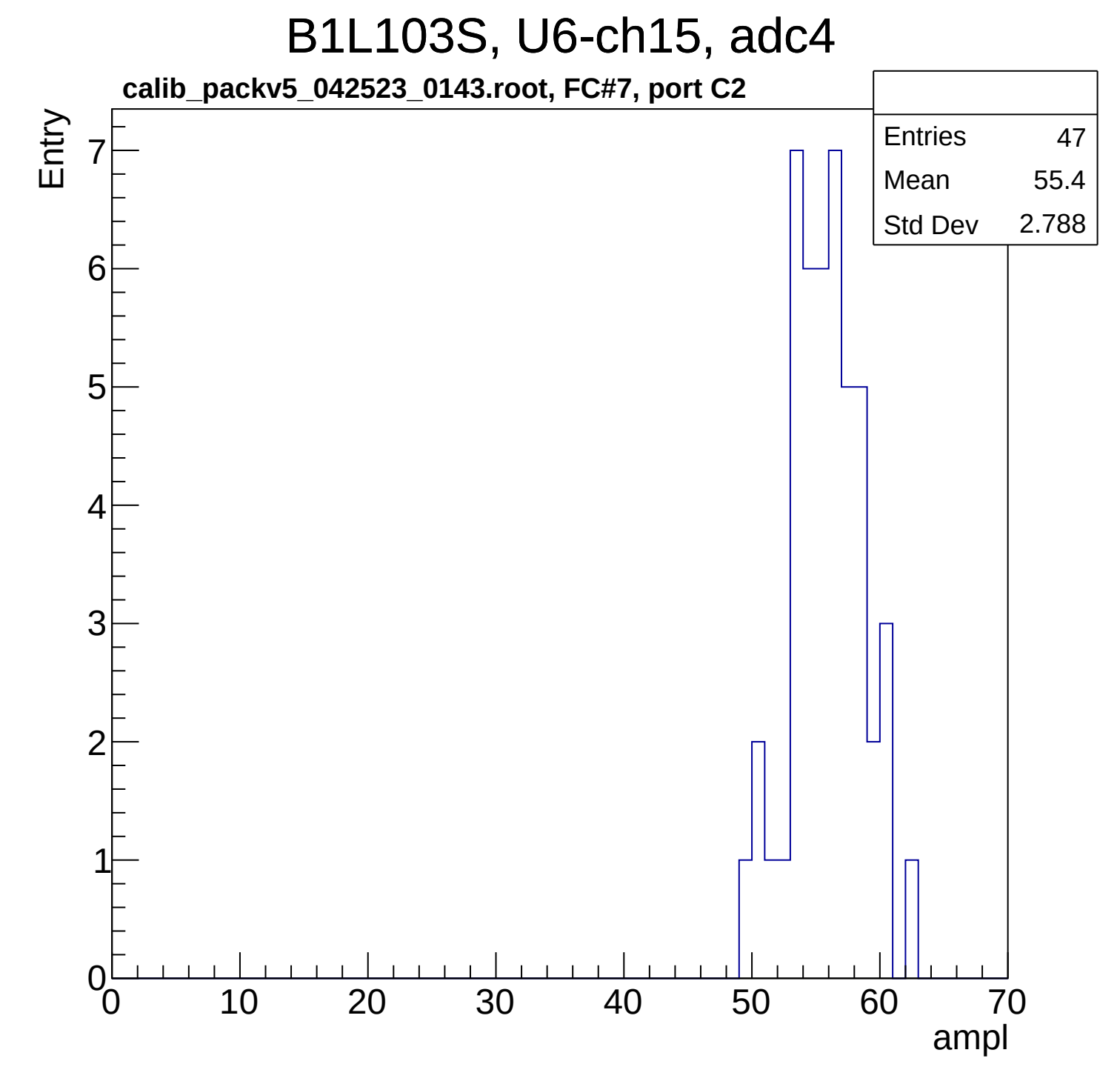
B1L103S, U6-ch15, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 55.4
Std Dev 2.788
Entry
ampl
0
1
2
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4
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60
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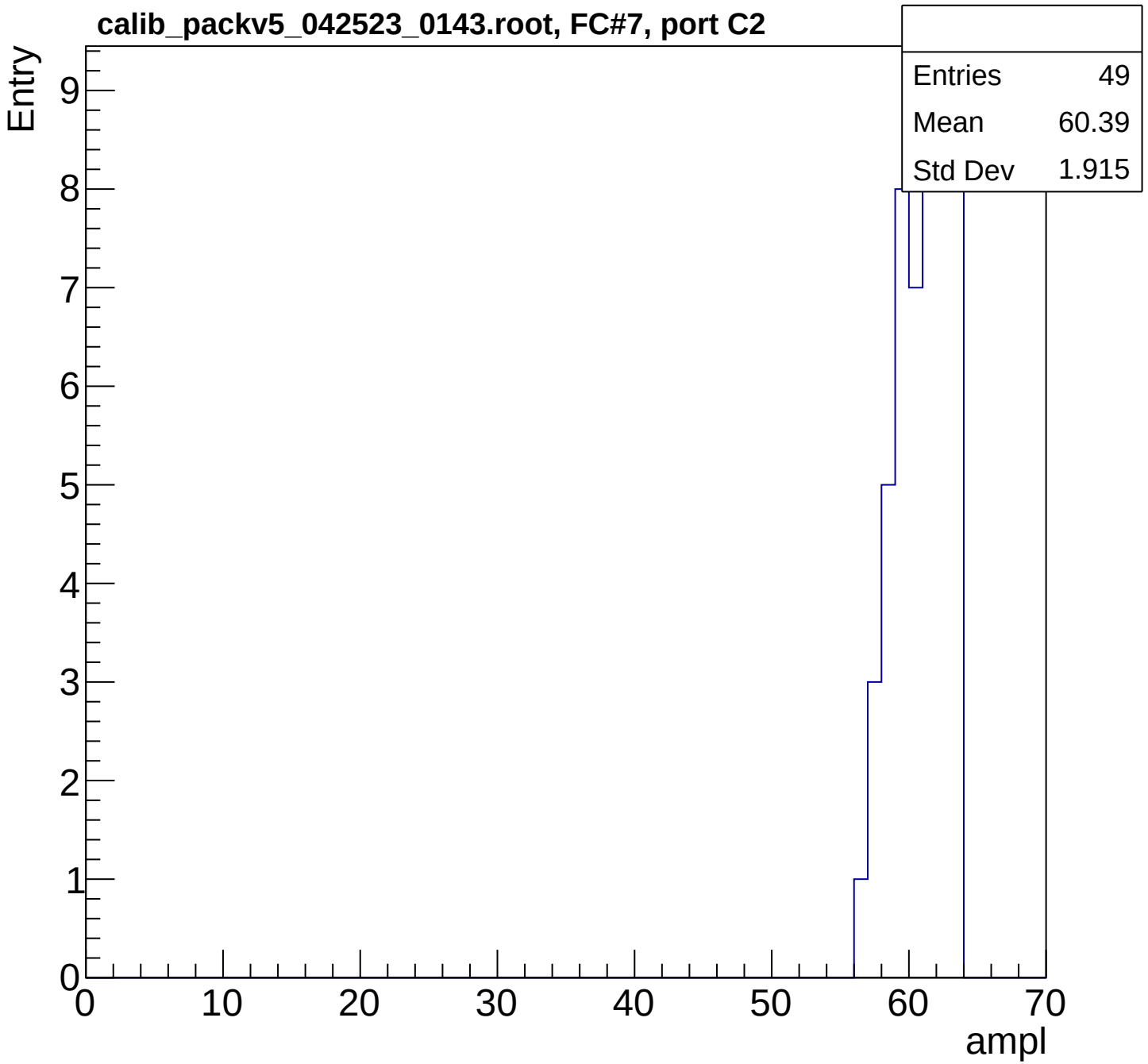
B1L103S, U6-ch15, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 60.39
Std Dev 1.915
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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60
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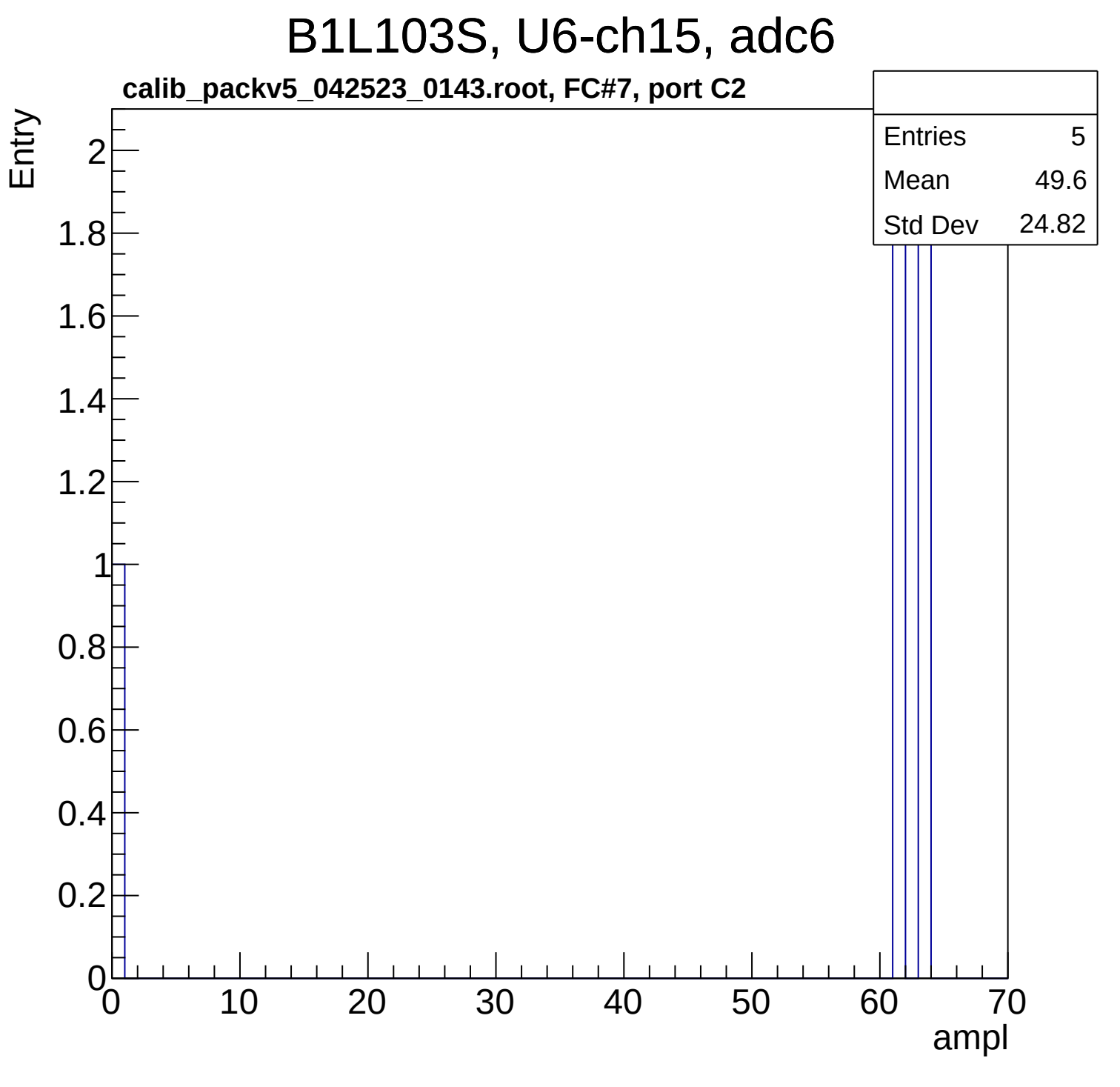
B1L103S, U6-ch15, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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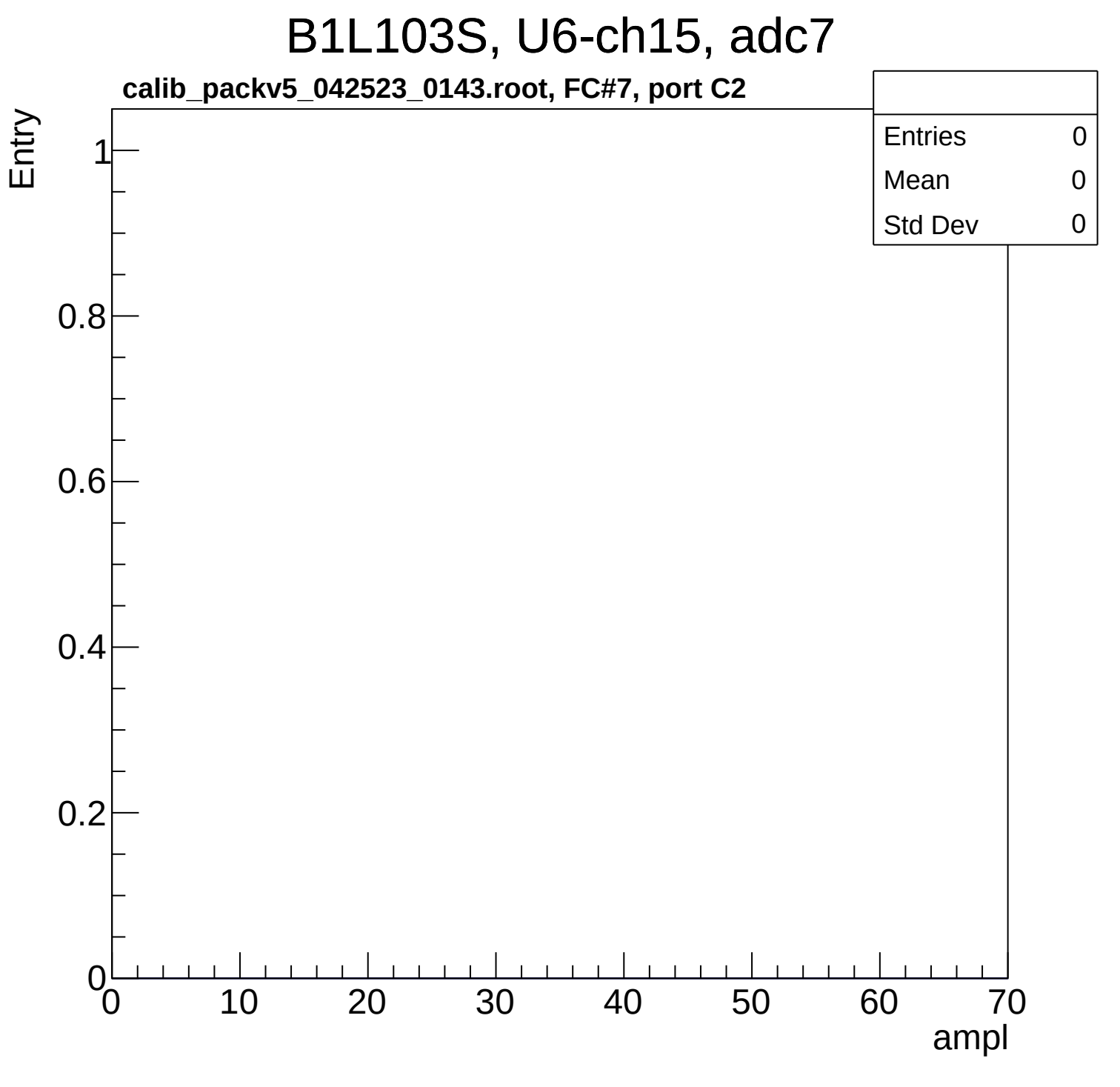
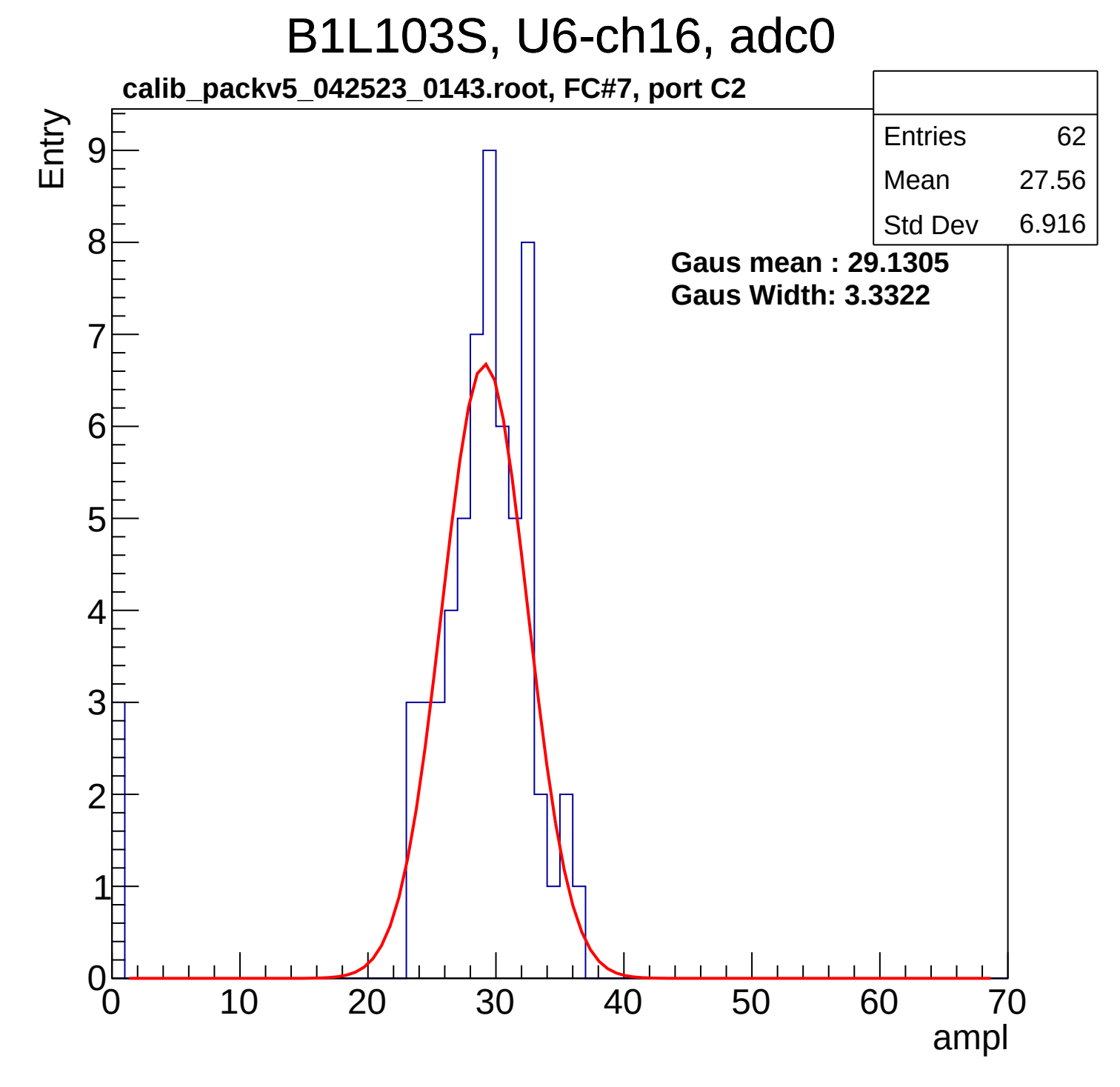

B1L103S, U6-ch15, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch16, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 27.56
Std Dev 6.916
Gaus mean : 29.1305
Gaus Width: 3.3322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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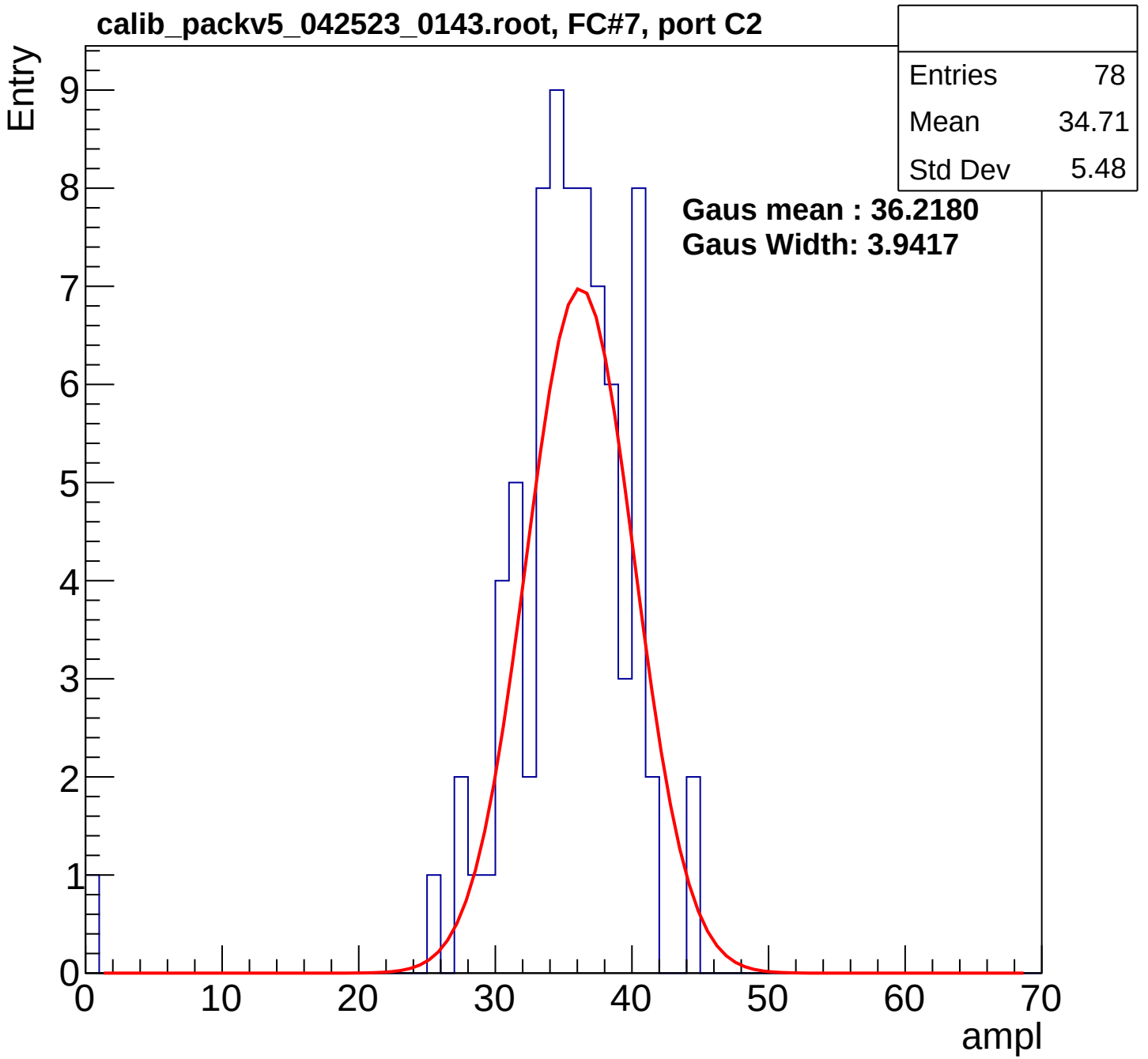
B1L103S, U6-ch16, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 34.71
Std Dev 5.48
Gaus mean : 36.2180
Gaus Width: 3.9417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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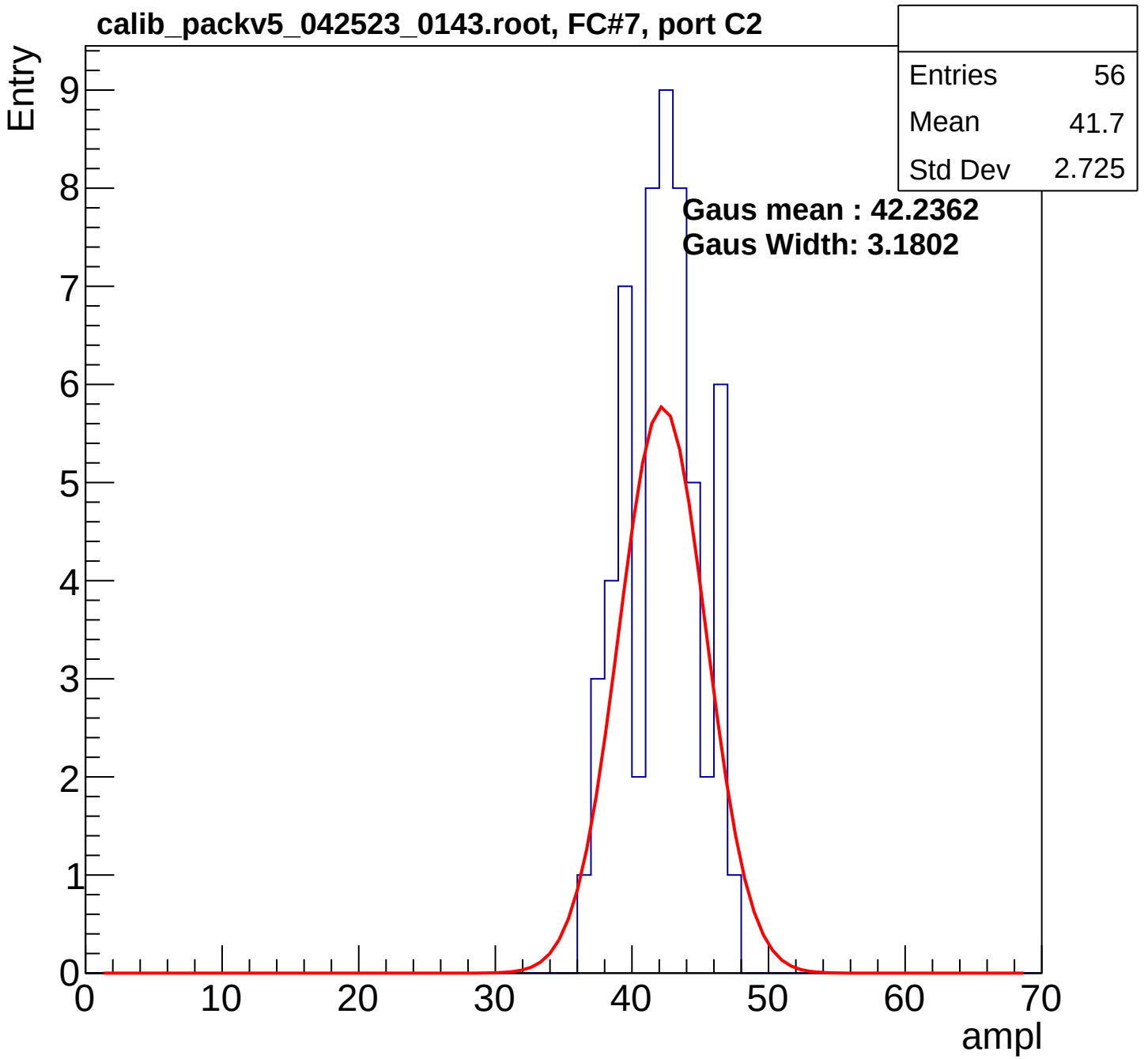
B1L103S, U6-ch16, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 41.7
Std Dev 2.725
Gaus mean : 42.2362
Gaus Width: 3.1802
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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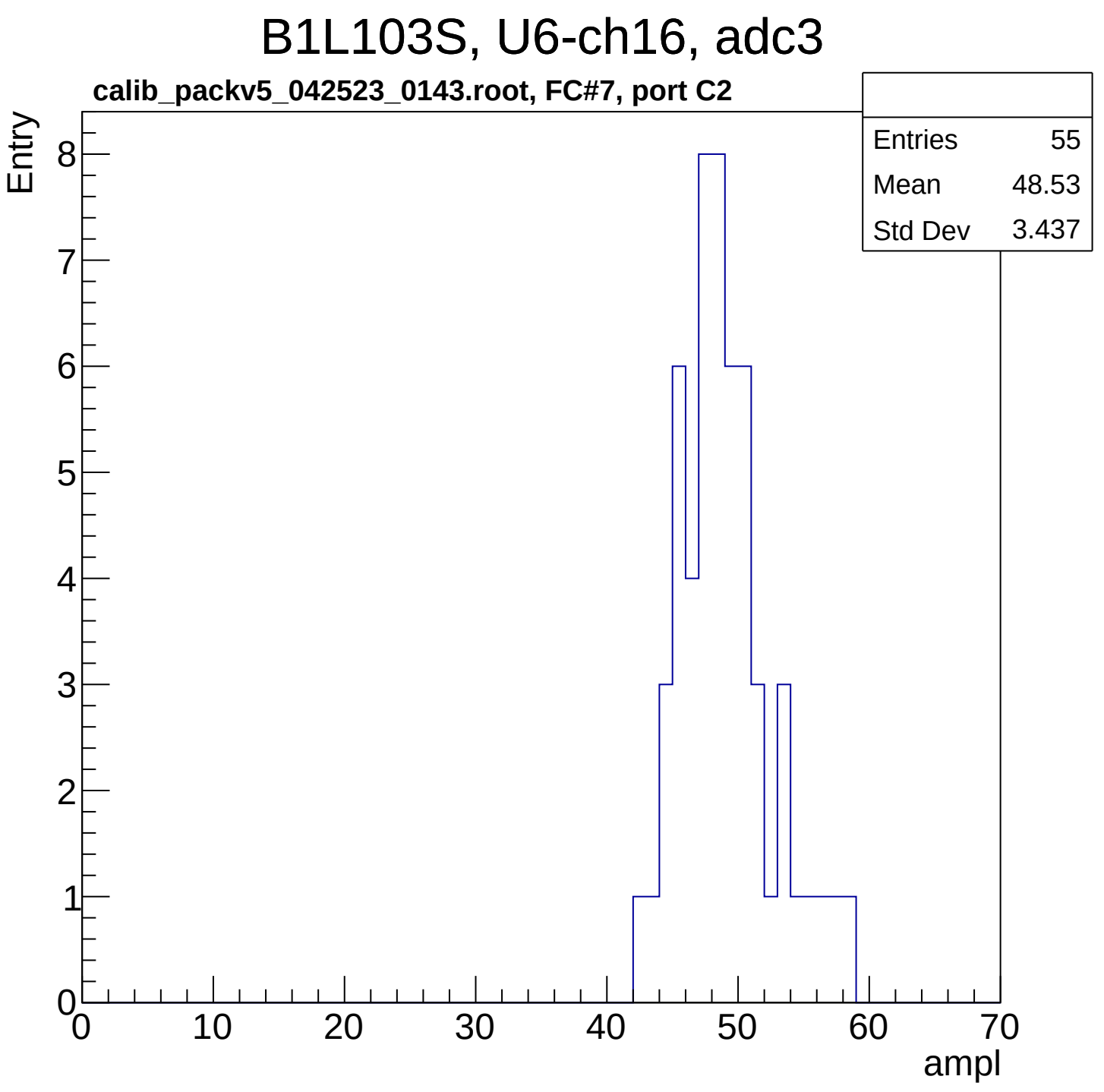
B1L103S, U6-ch16, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 48.53
Std Dev 3.437
Entry
ampl
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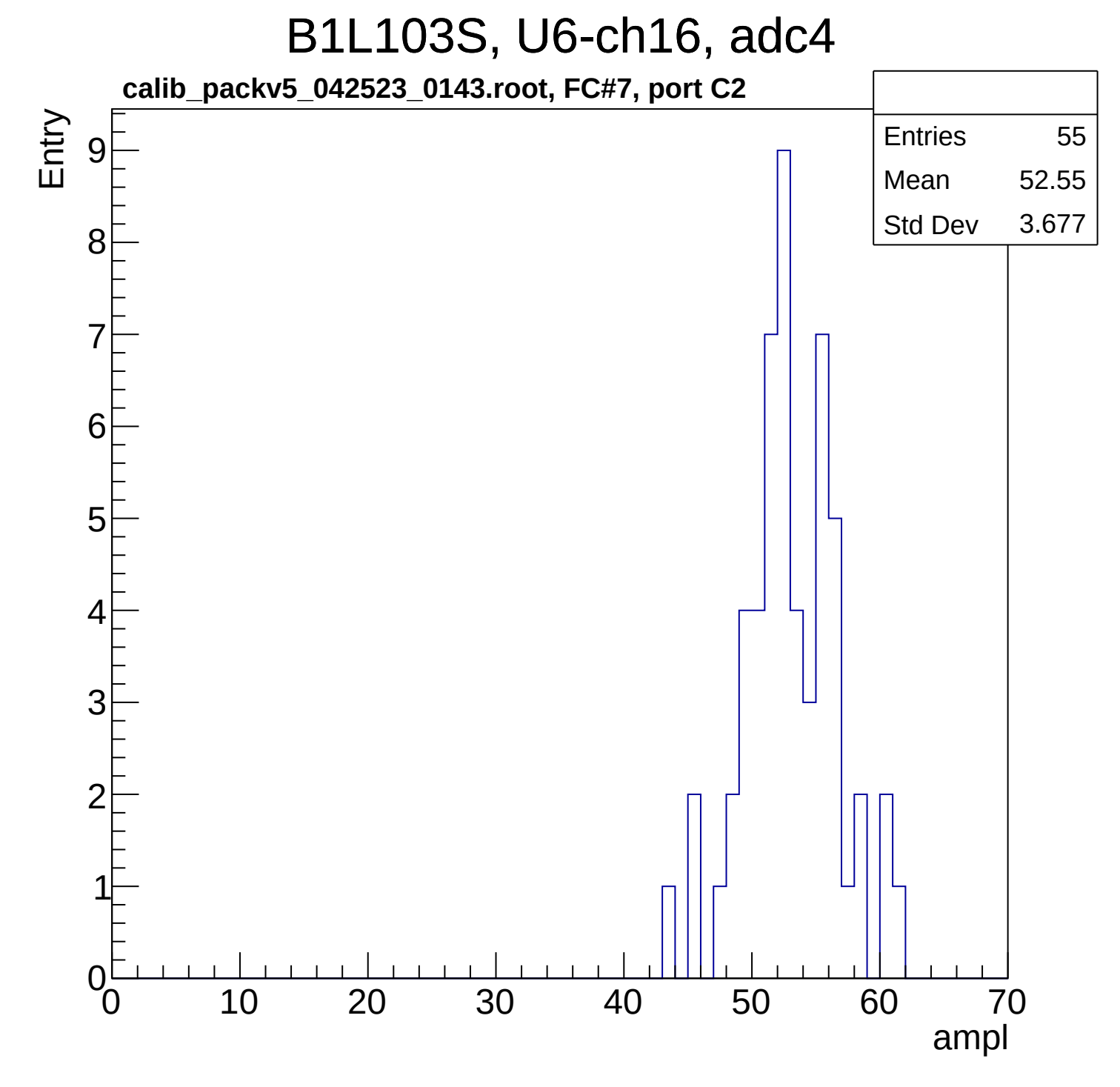
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch16, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 52.55
Std Dev 3.677
Entry
ampl
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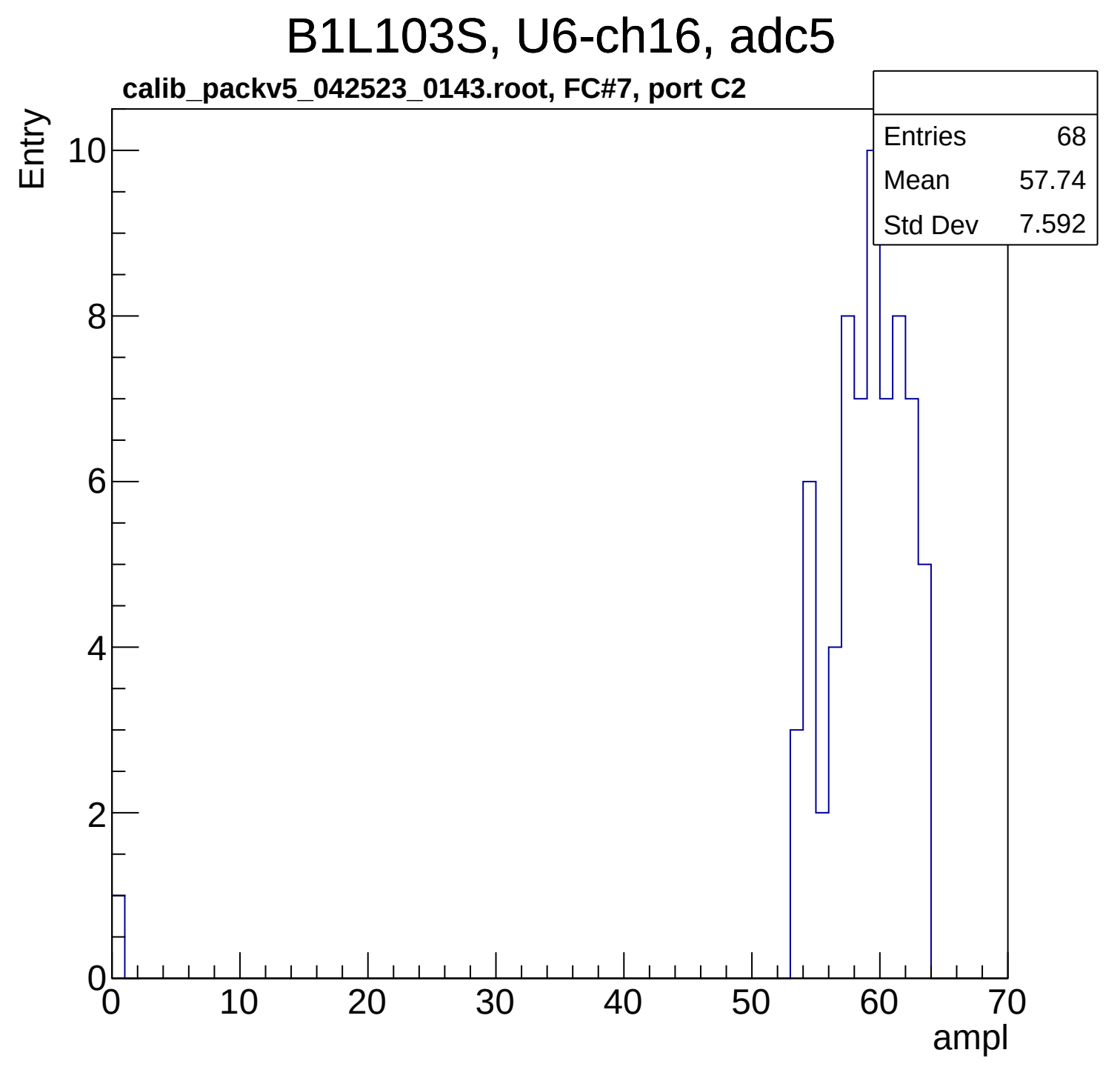
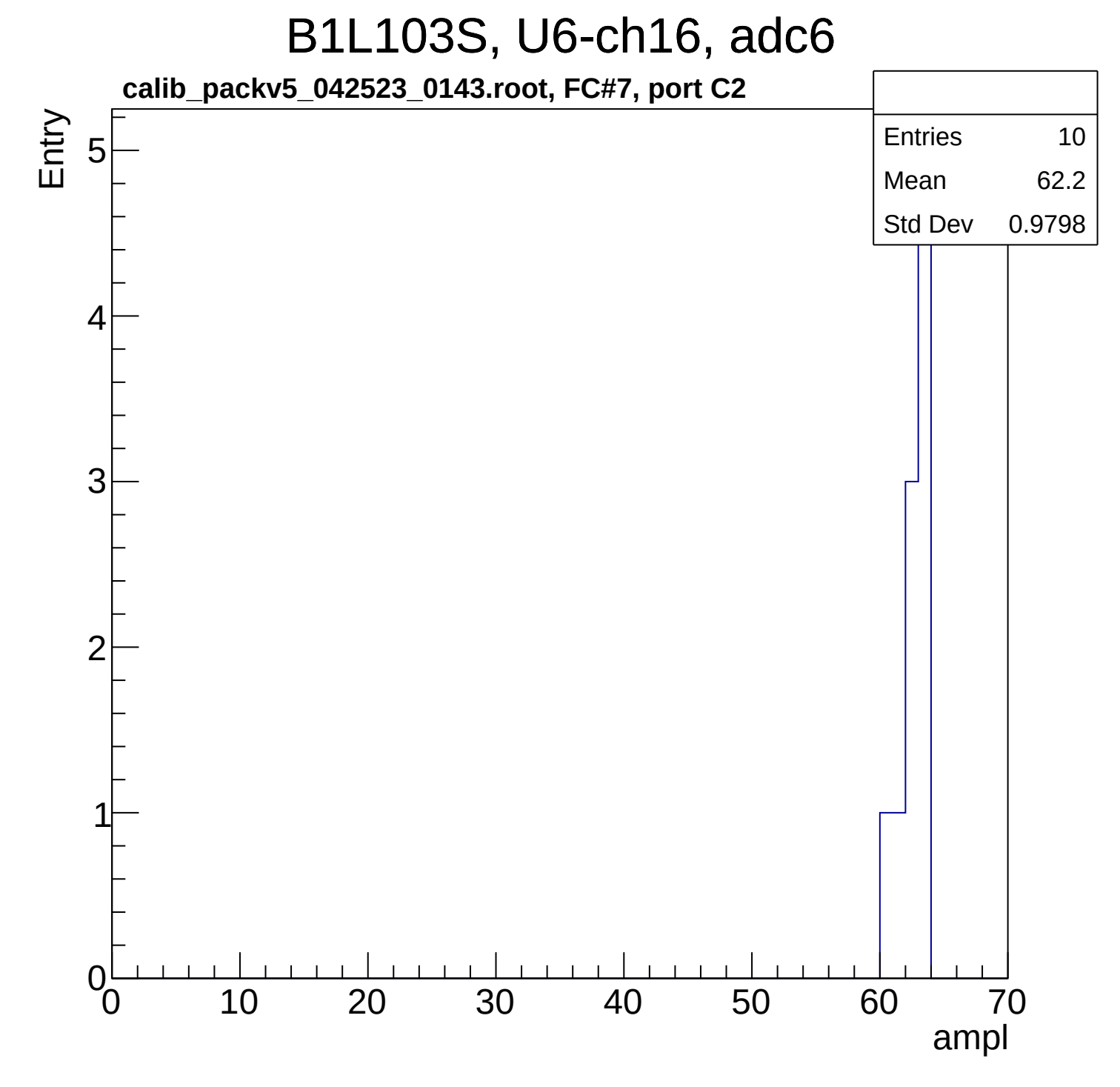
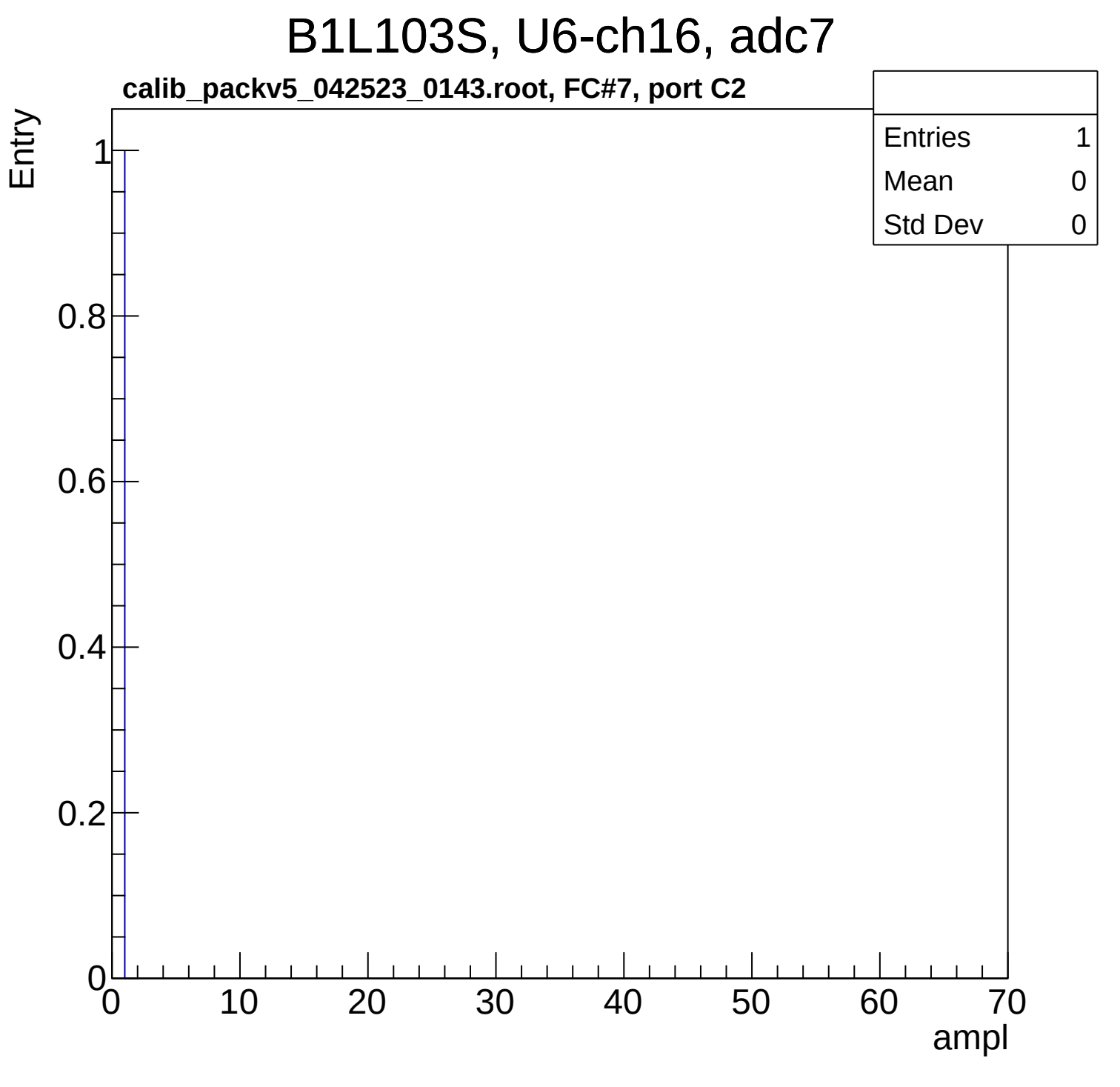
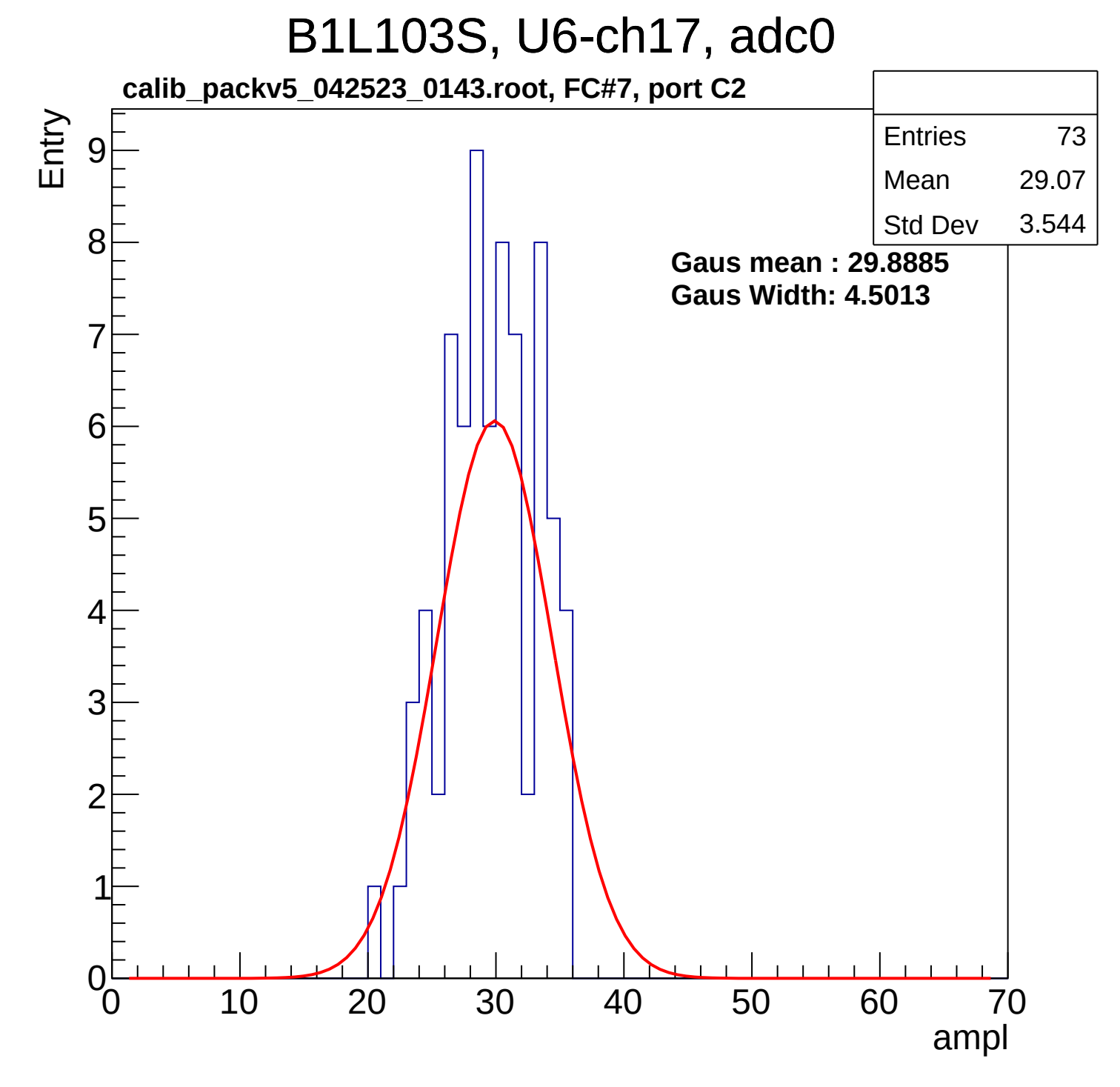
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch16, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 57.74
Std Dev 7.592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch16, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 62.2
Std Dev 0.9798
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch16, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch17, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.07
Std Dev 3.544
Gaus mean : 29.8885
Gaus Width: 4.5013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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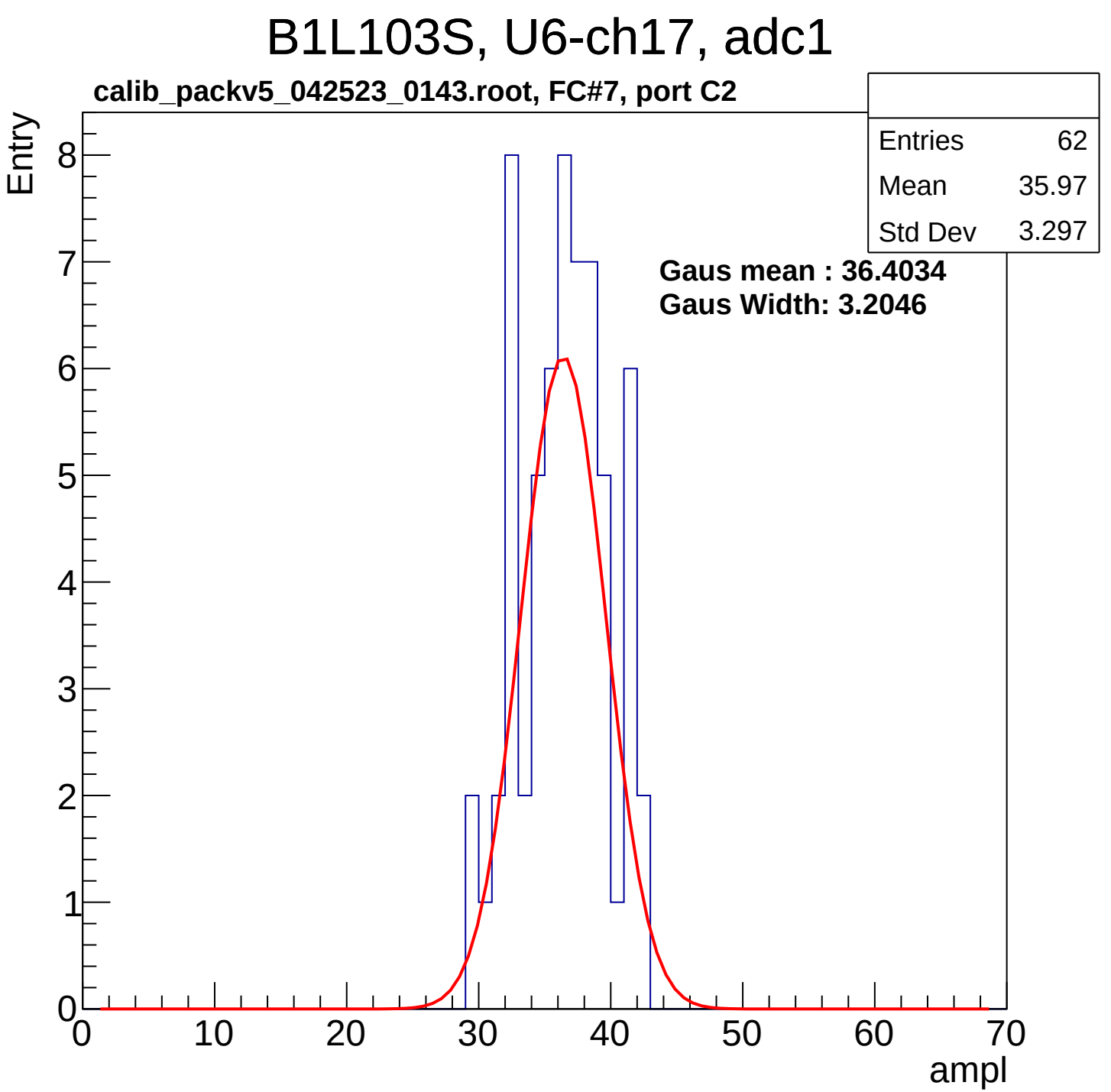
60
70


B1L103S, U6-ch17, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.97
Std Dev 3.297
Gaus mean : 36.4034
Gaus Width: 3.2046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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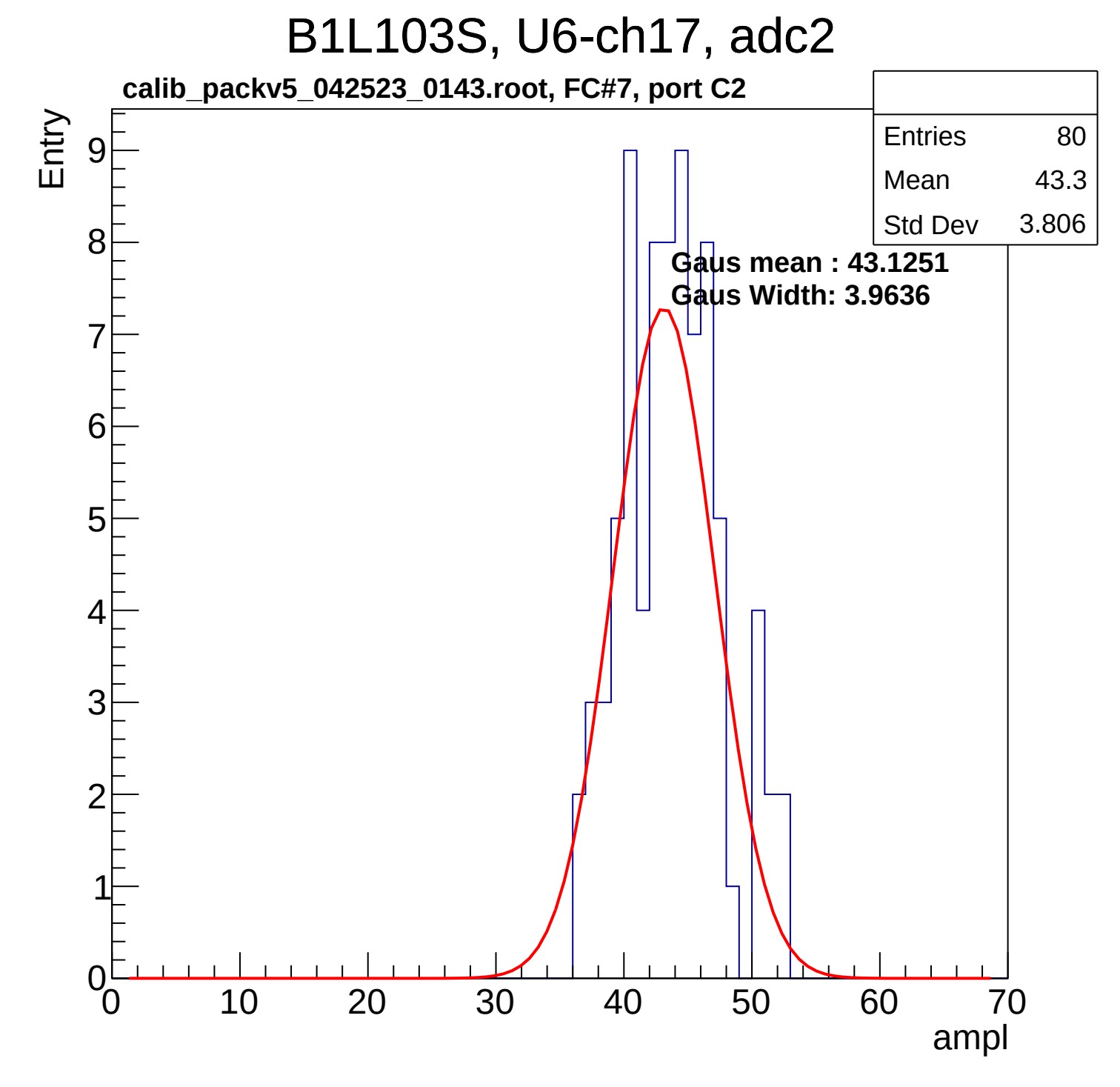
B1L103S, U6-ch17, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 43.3
Std Dev 3.806
Gaus mean : 43.1251
Gaus Width: 3.9636
Entry
ampl
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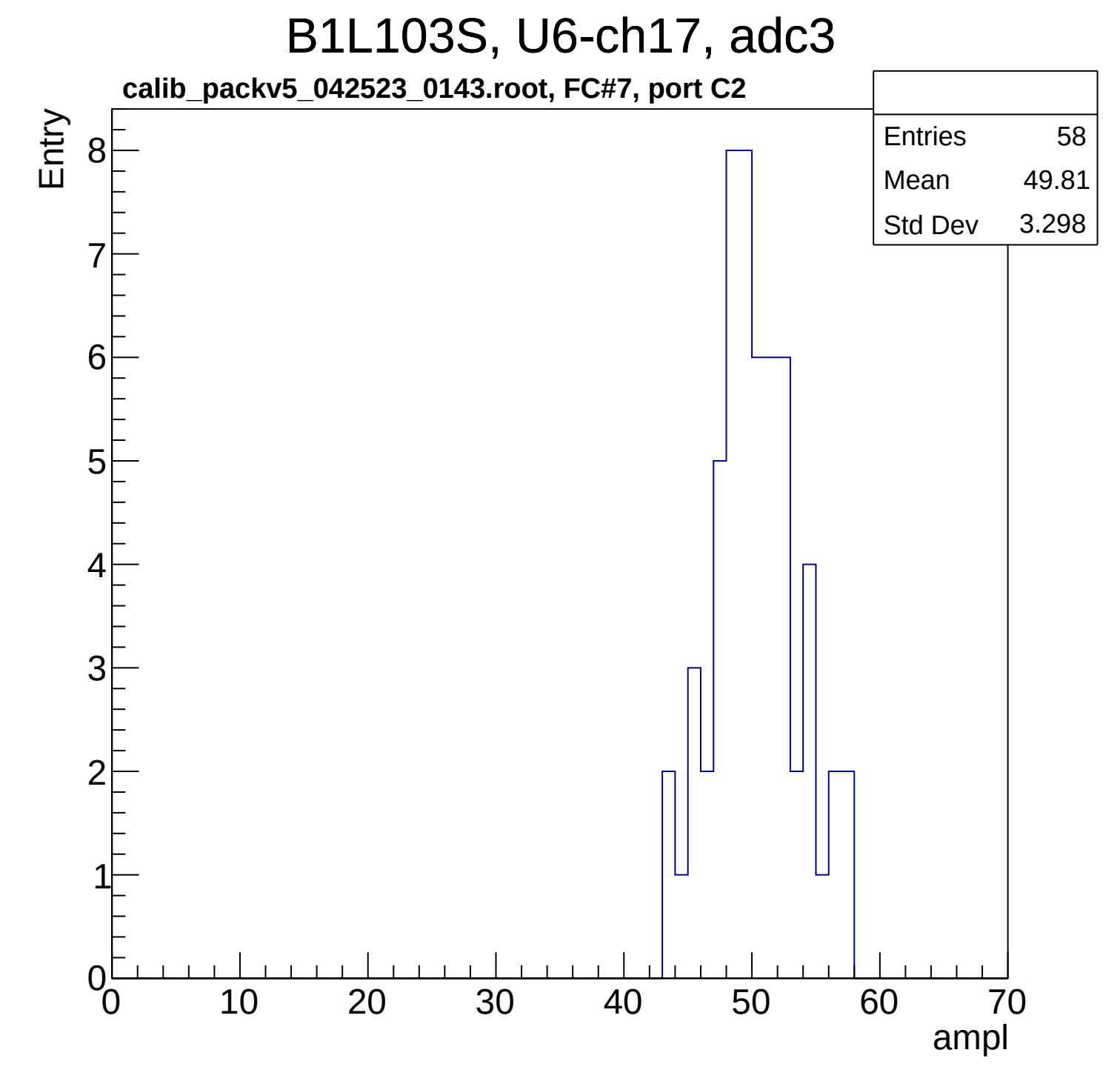
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U6-ch17, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.81
Std Dev 3.298
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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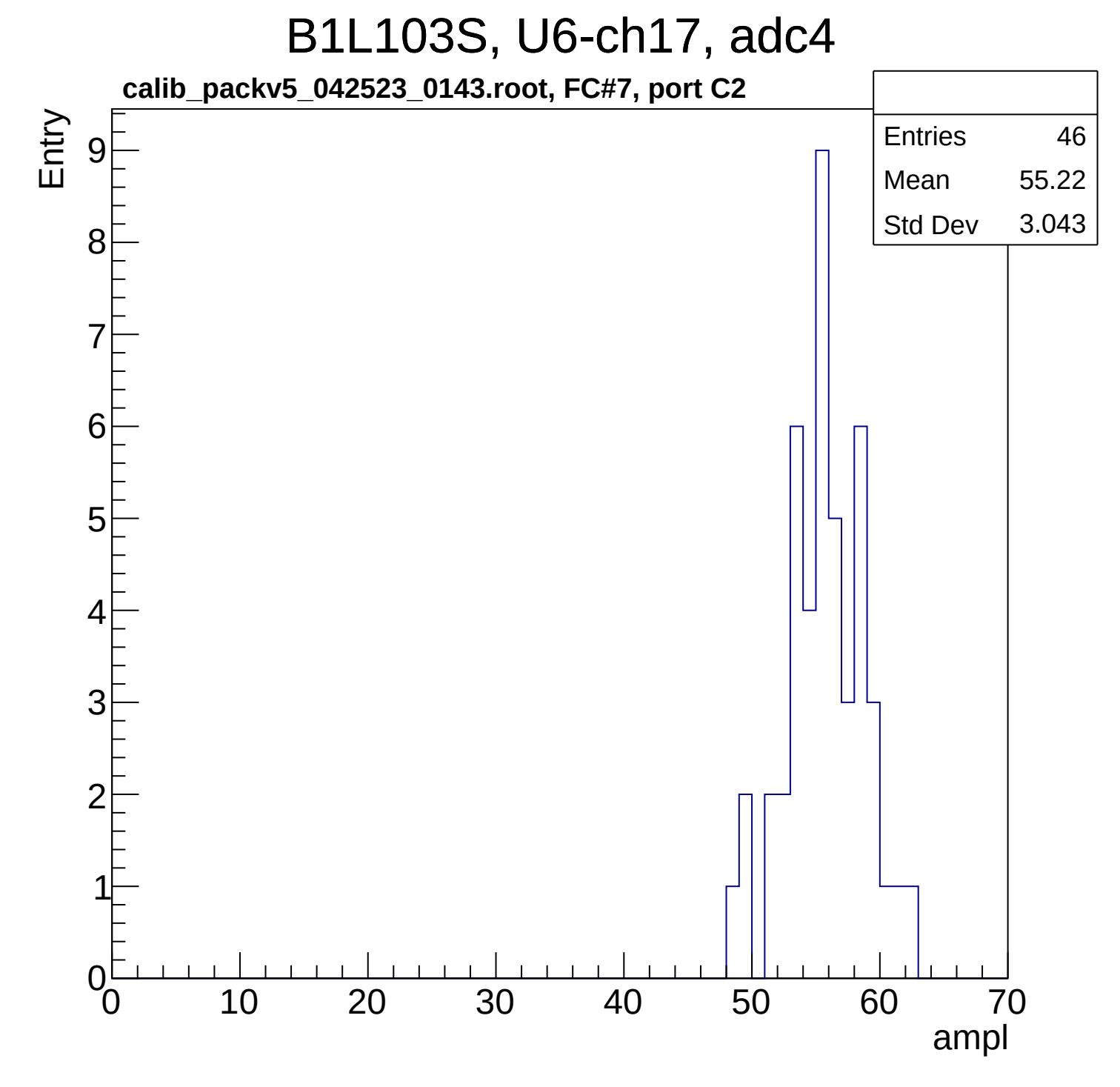
B1L103S, U6-ch17, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 55.22
Std Dev 3.043
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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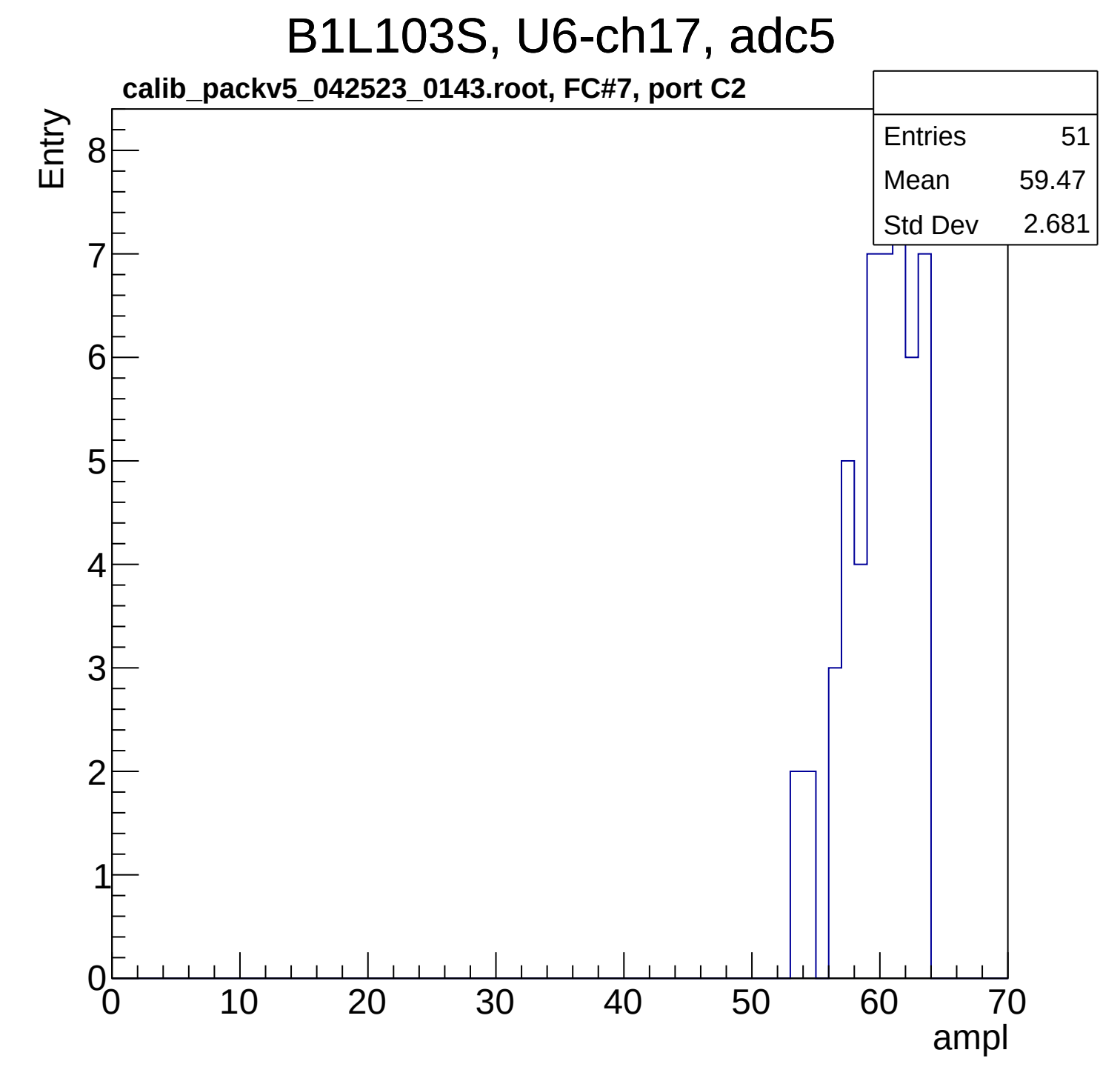
B1L103S, U6-ch17, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 59.47
Std Dev 2.681
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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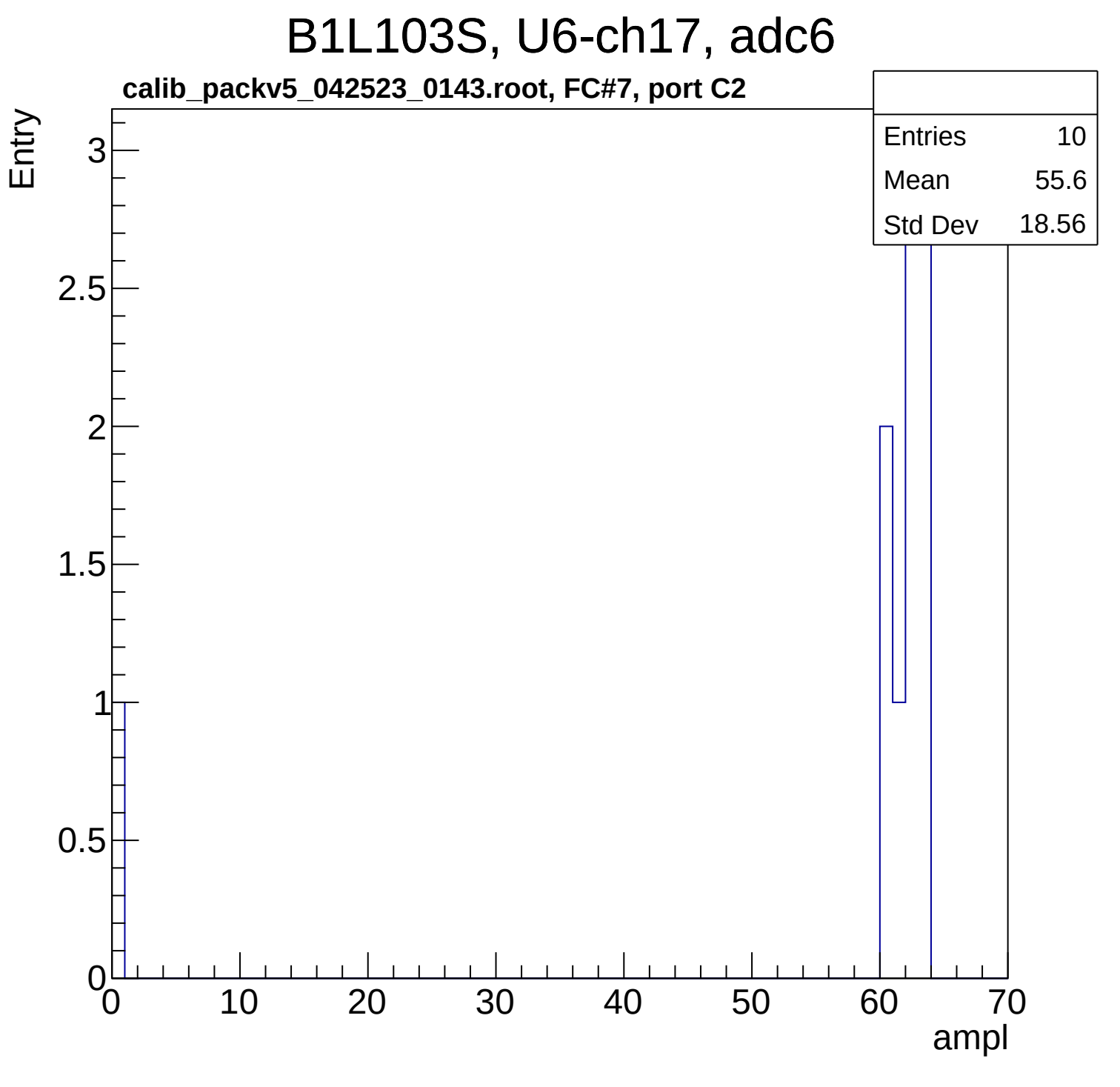
B1L103S, U6-ch17, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 55.6
Std Dev 18.56
Entry
ampl
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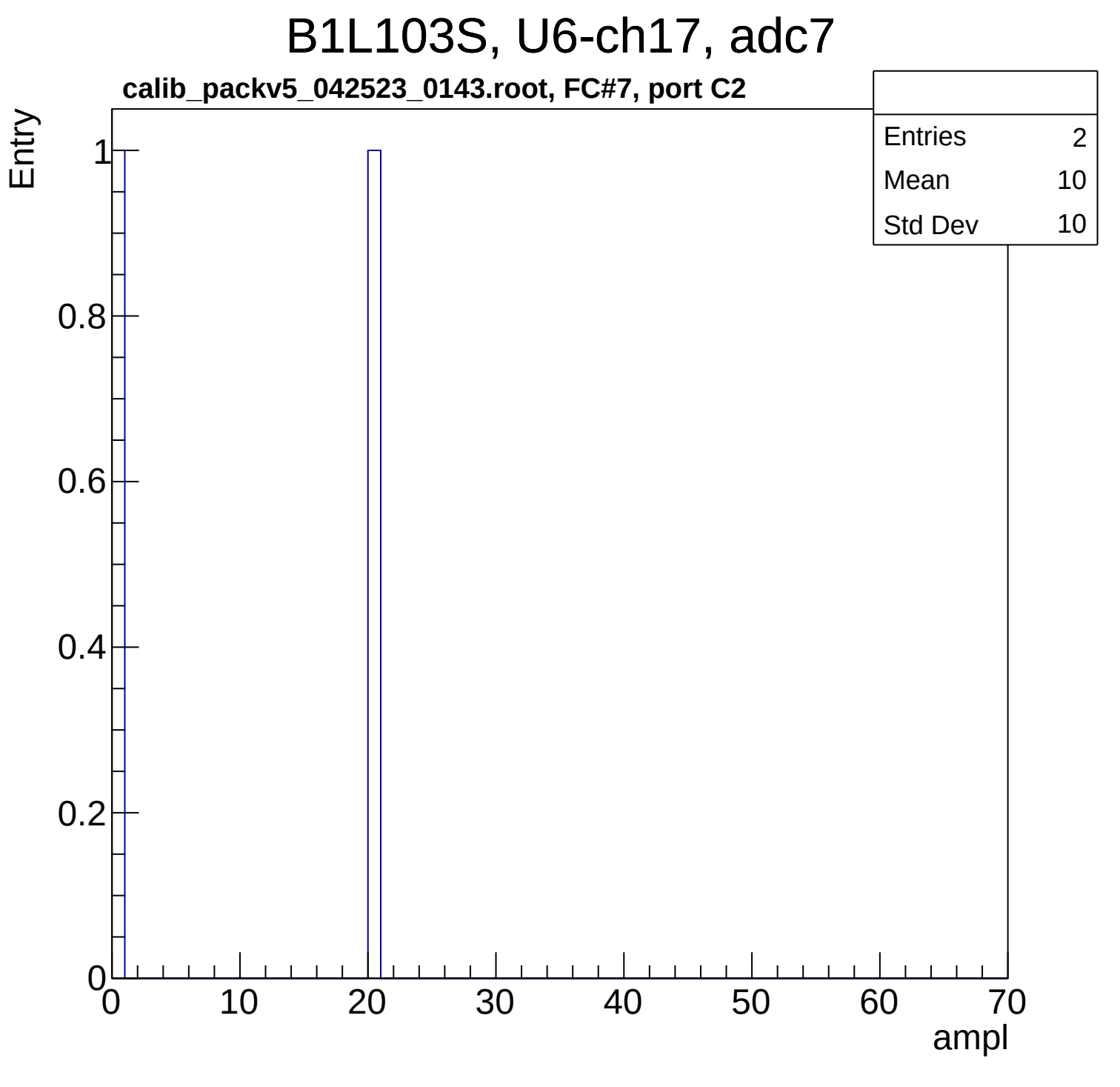
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch17, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10
Std Dev 10
Entry
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0.8
0.6
0.4
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0
10
20
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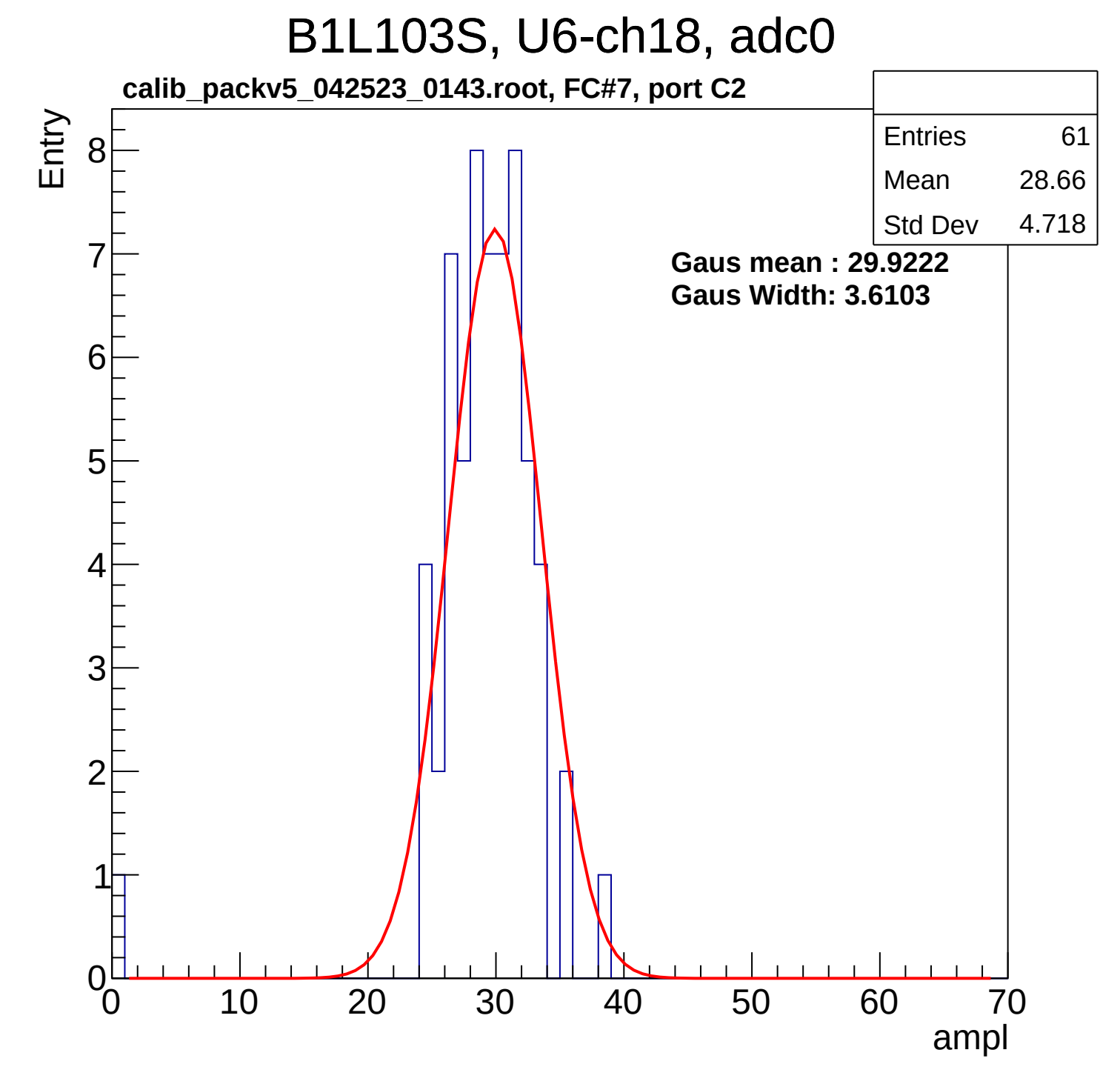
50
60
70
ampl


B1L103S, U6-ch18, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 28.66
Std Dev 4.718
Gaus mean : 29.9222
Gaus Width: 3.6103
Entry
ampl
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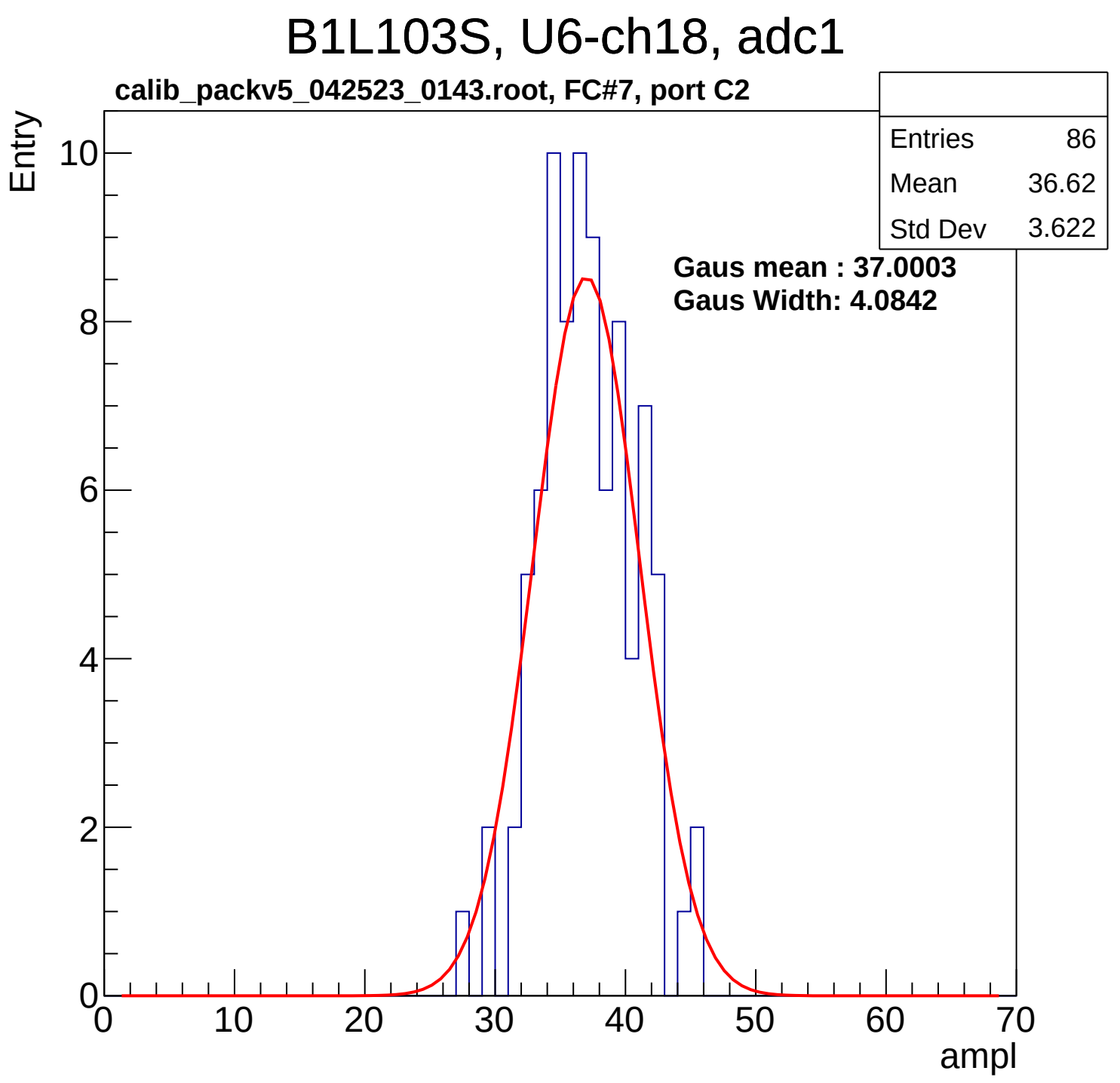
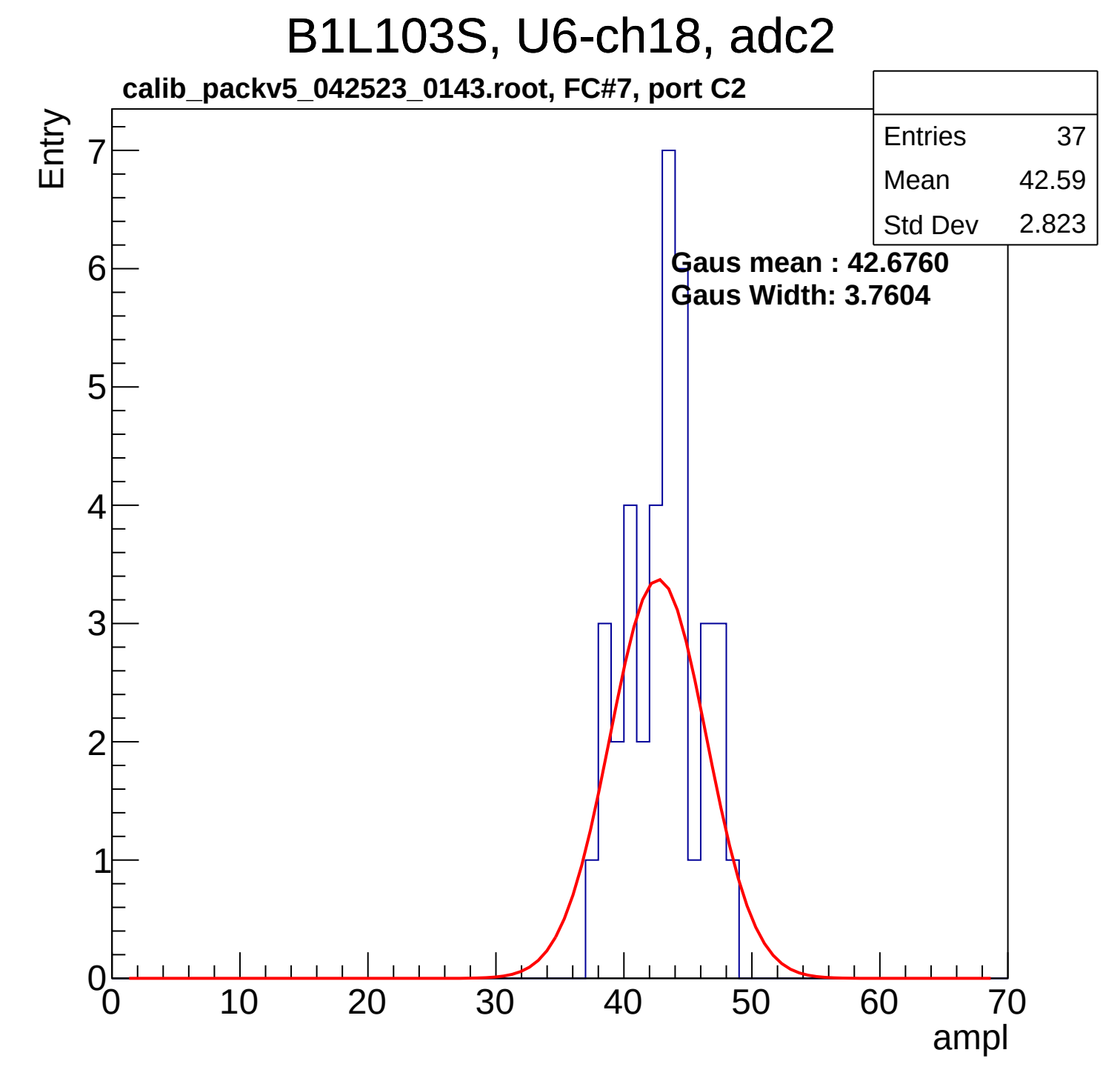
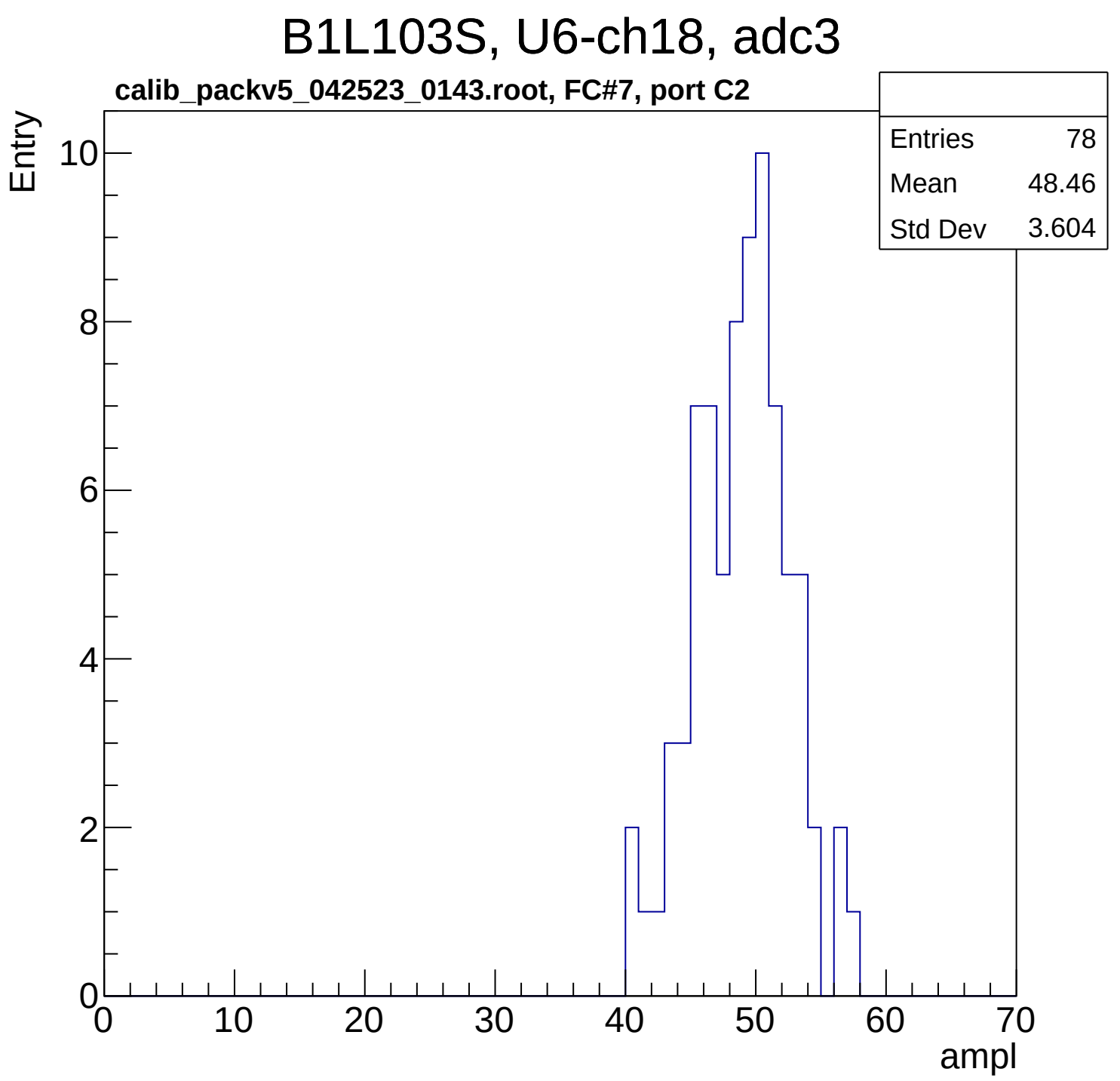
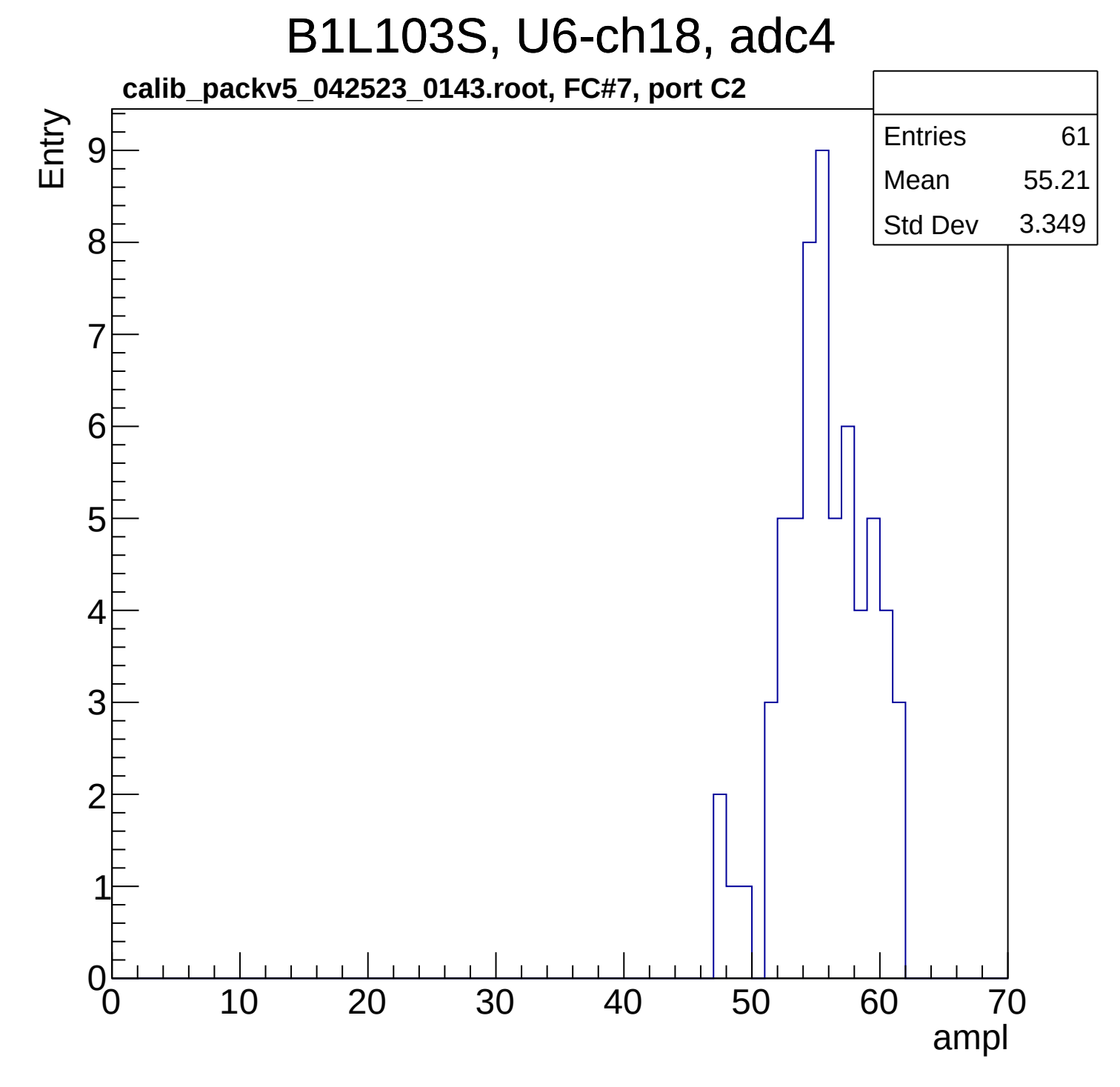
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch18, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 36.62
Std Dev 3.622
Gaus mean : 37.0003
Gaus Width: 4.0842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch18, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 42.59
Std Dev 2.823
Gaus mean : 42.6760
Gaus Width: 3.7604
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch18, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 48.46
Std Dev 3.604
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch18, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 55.21
Std Dev 3.349
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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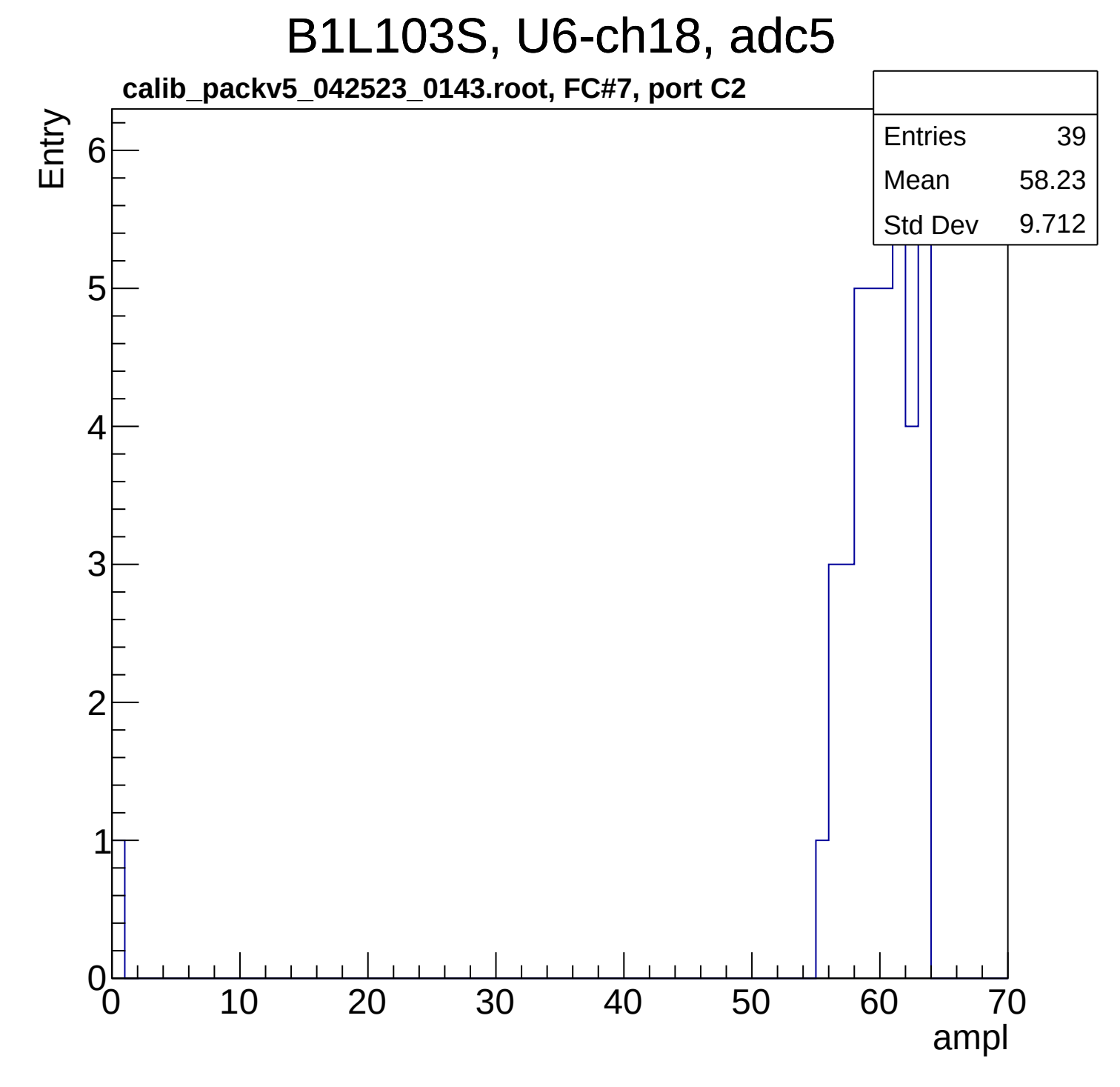
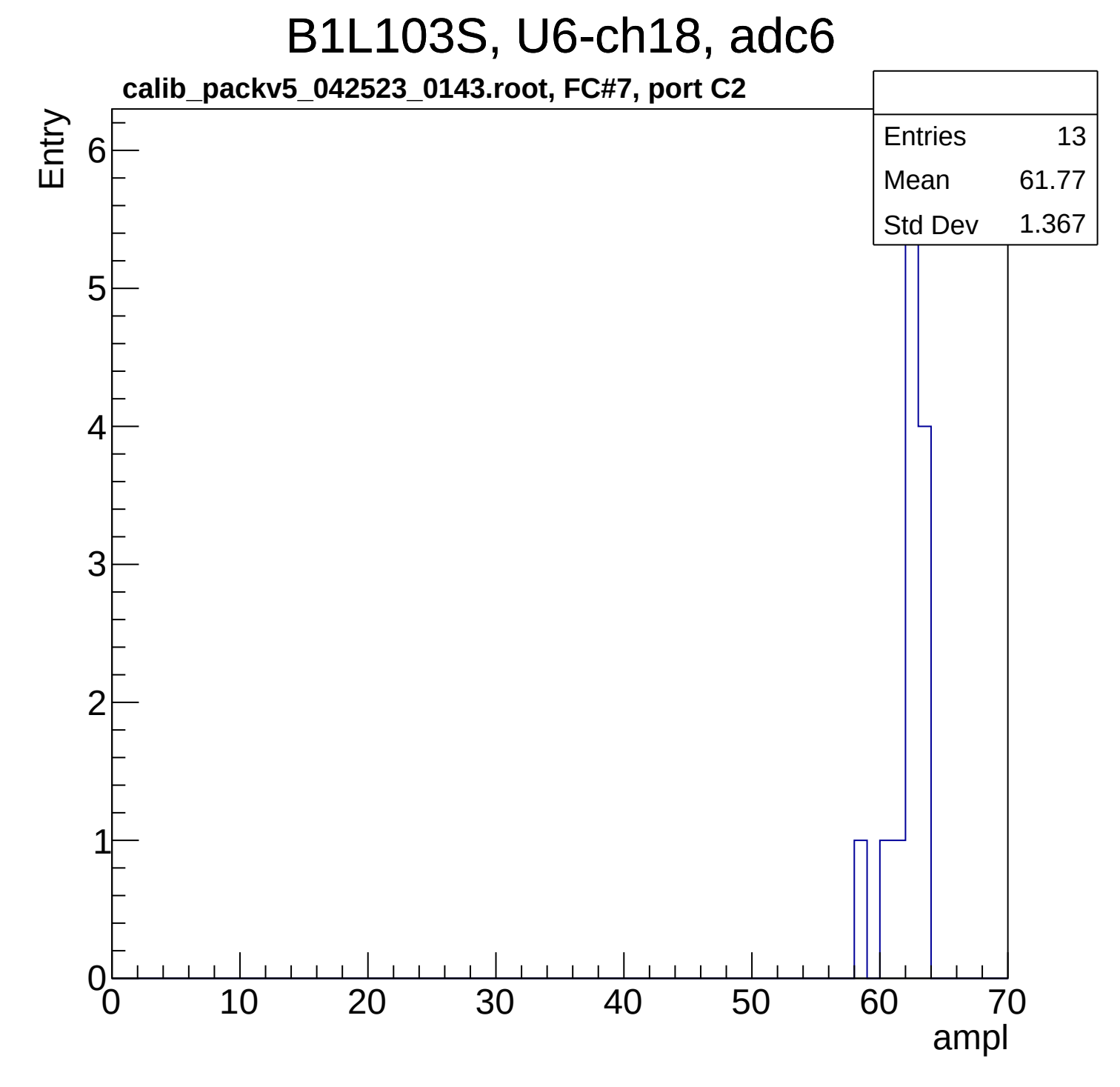
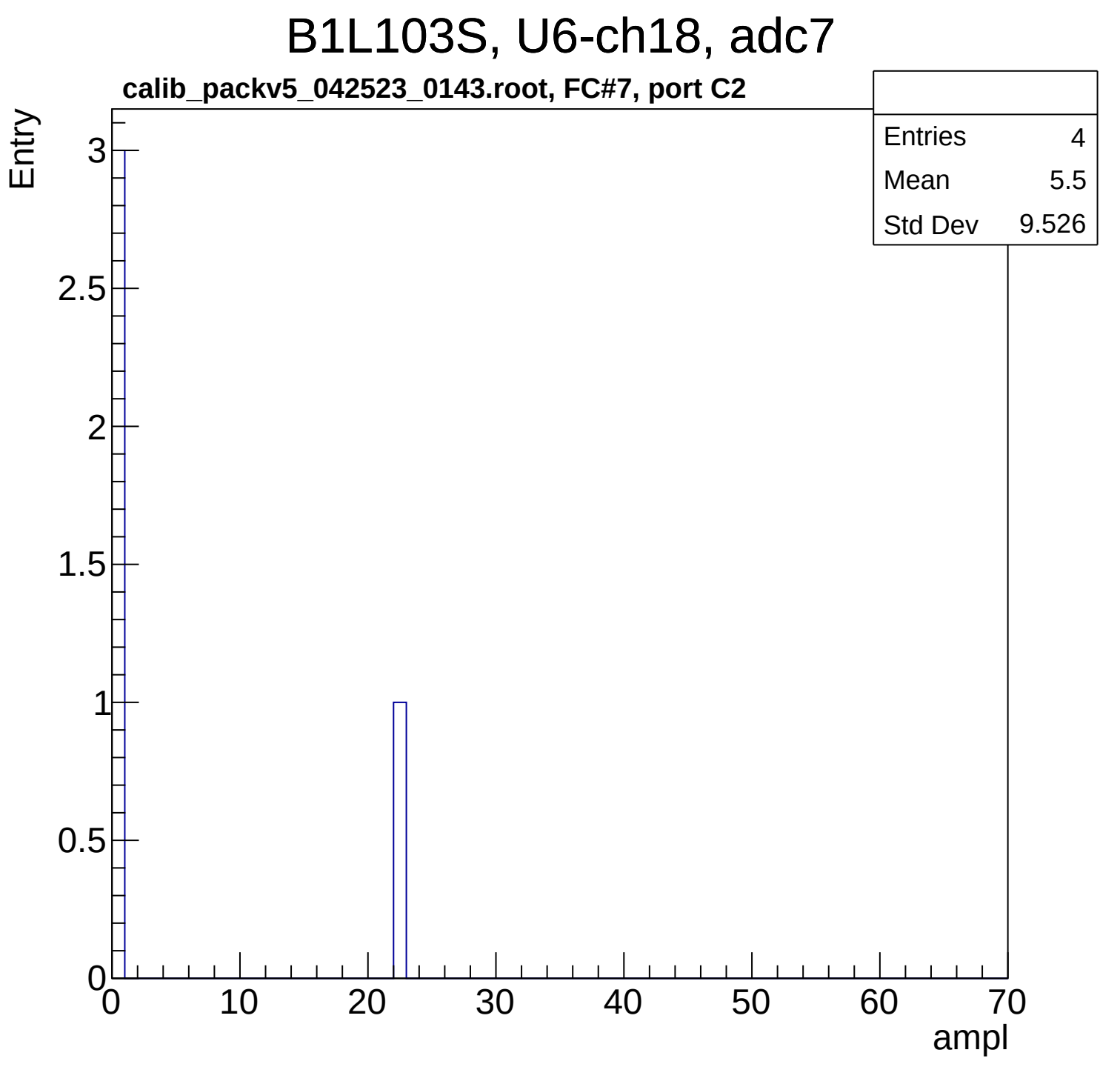

B1L103S, U6-ch18, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.23
Std Dev 9.712
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch18, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.77
Std Dev 1.367
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch18, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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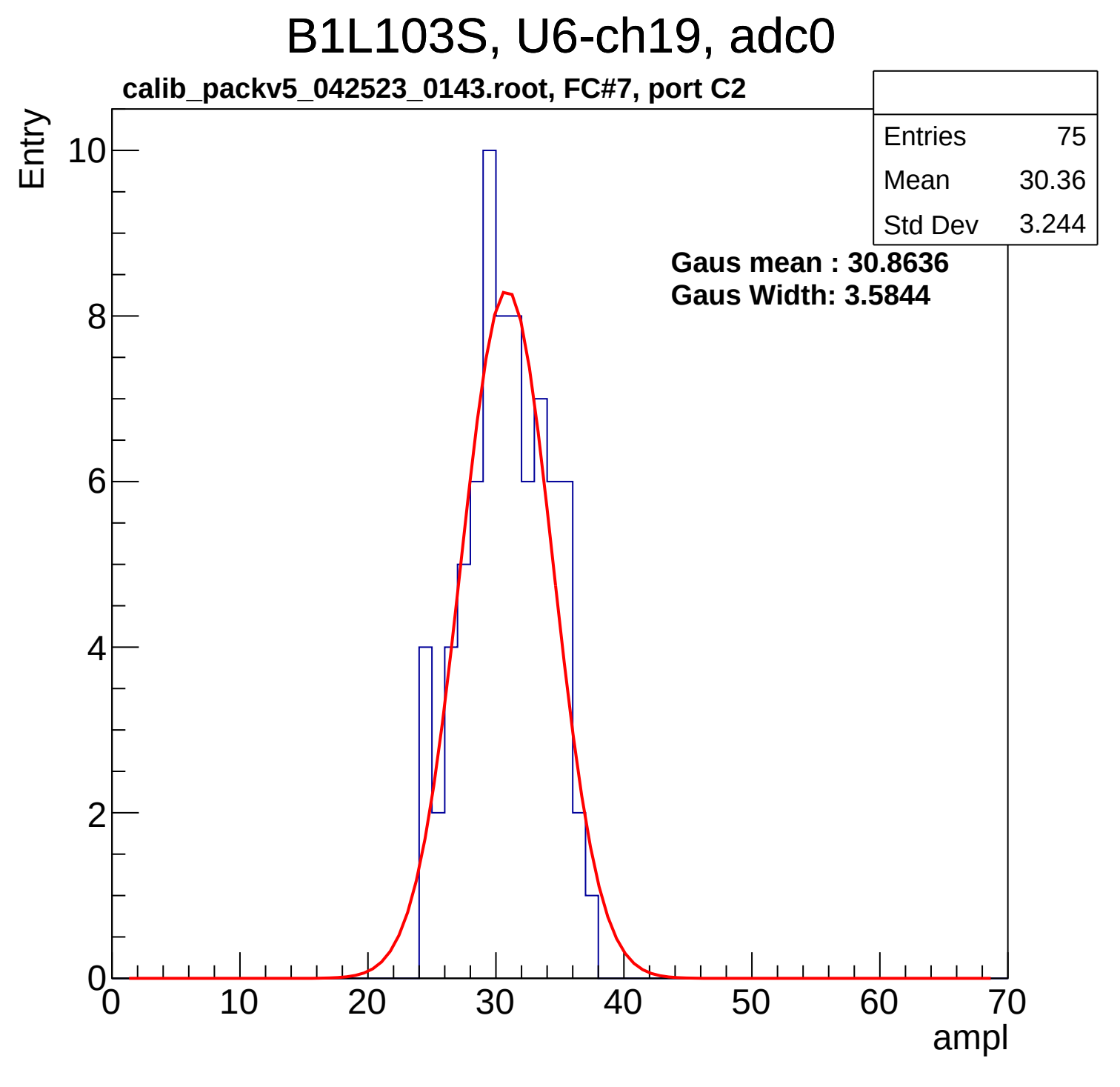
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch19, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.36
Std Dev 3.244
Gaus mean : 30.8636
Gaus Width: 3.5844
Entry
ampl
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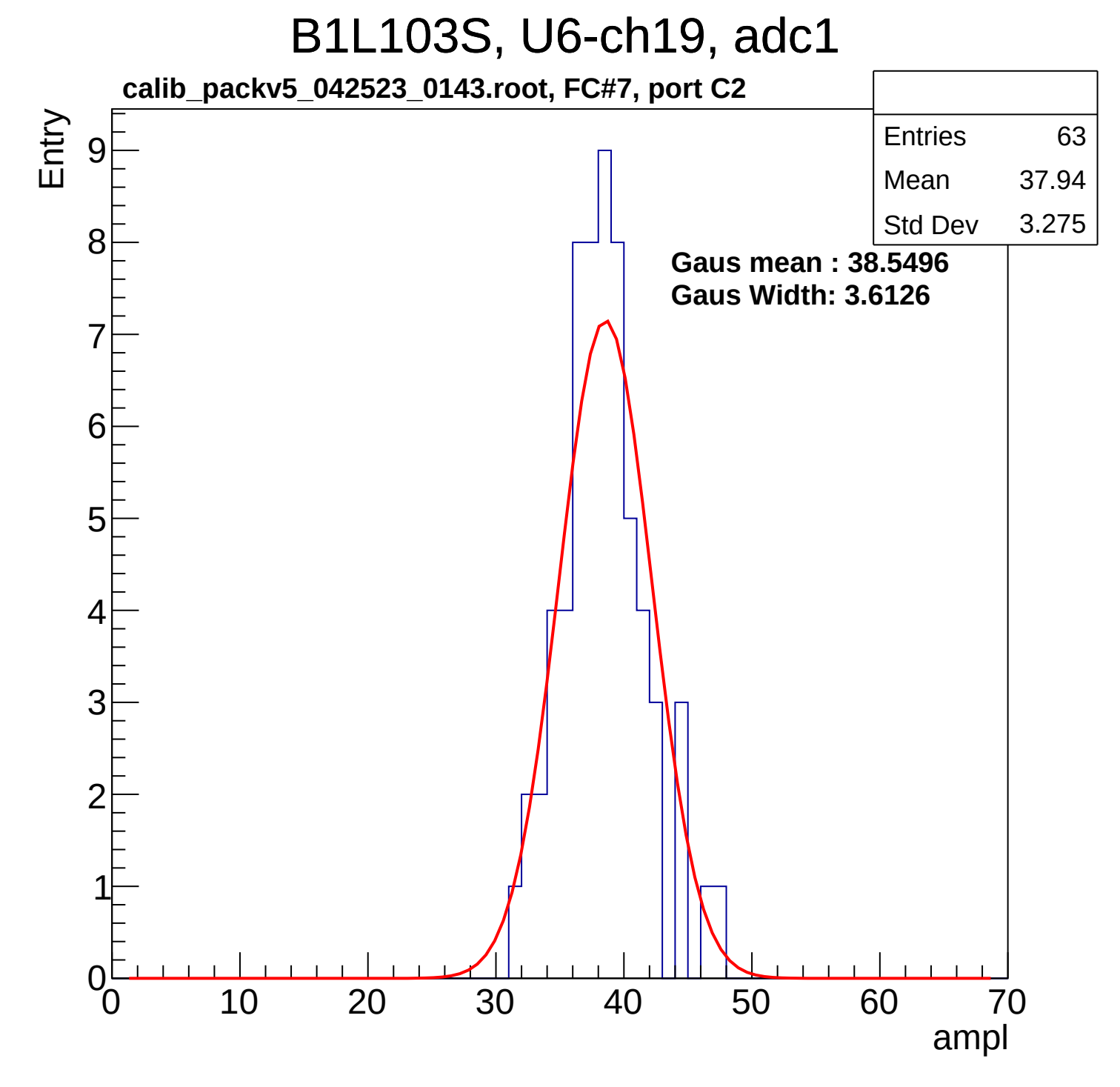
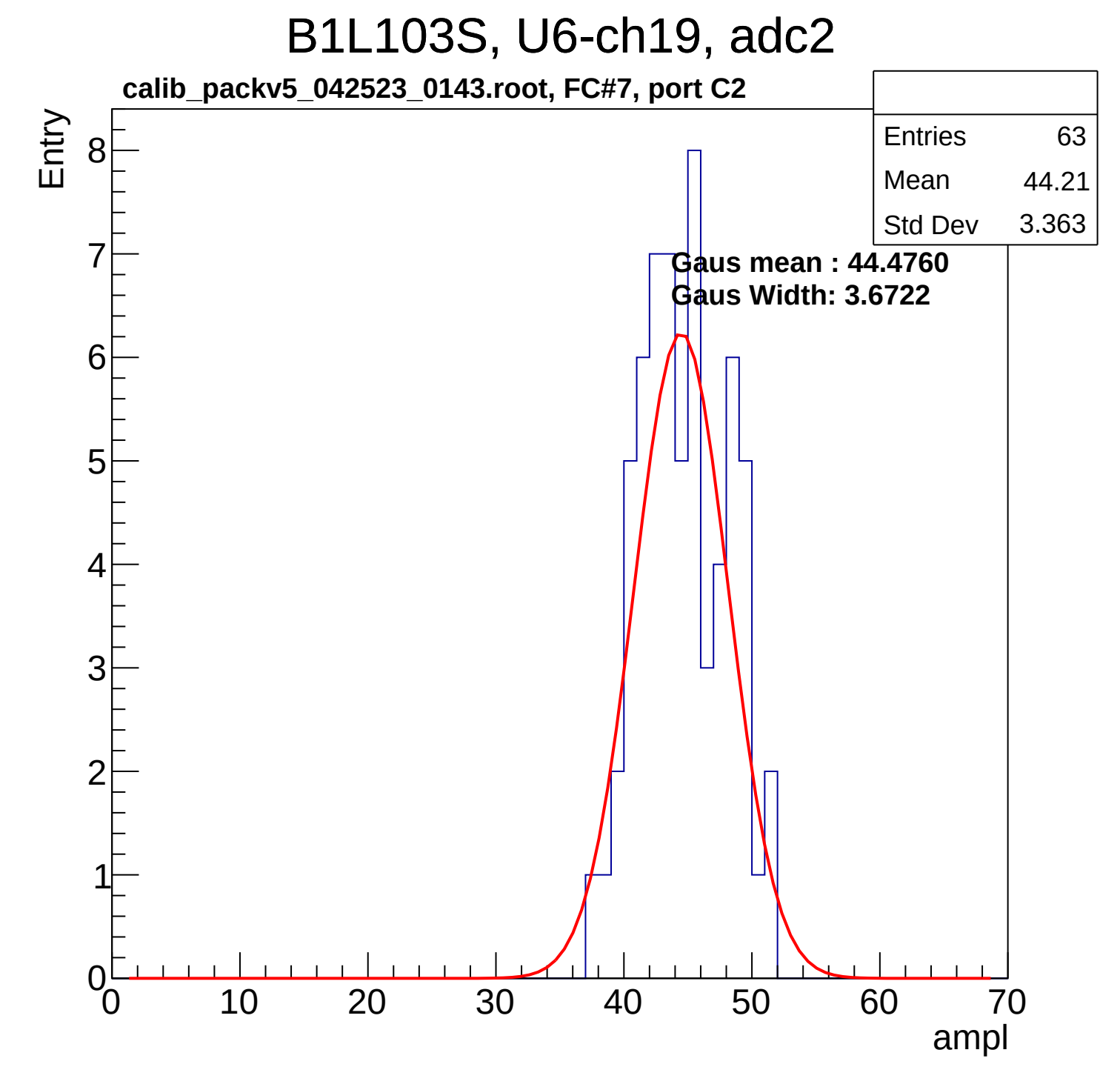
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch19, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 37.94
Std Dev 3.275
Gaus mean : 38.5496
Gaus Width: 3.6126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch19, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 44.21
Std Dev 3.363
Gaus mean : 44.4760
Gaus Width: 3.6722
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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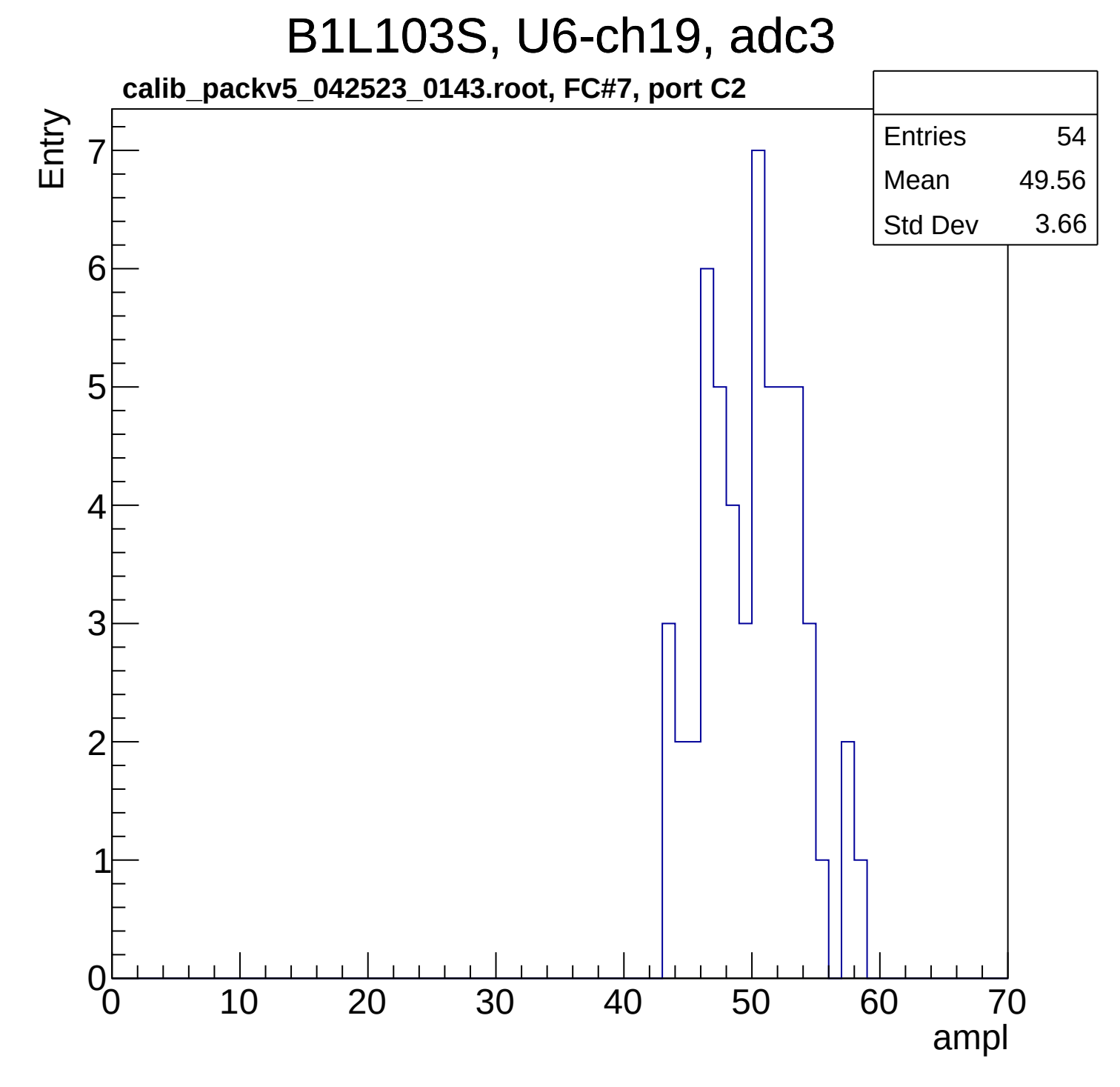
B1L103S, U6-ch19, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.56
Std Dev 3.66
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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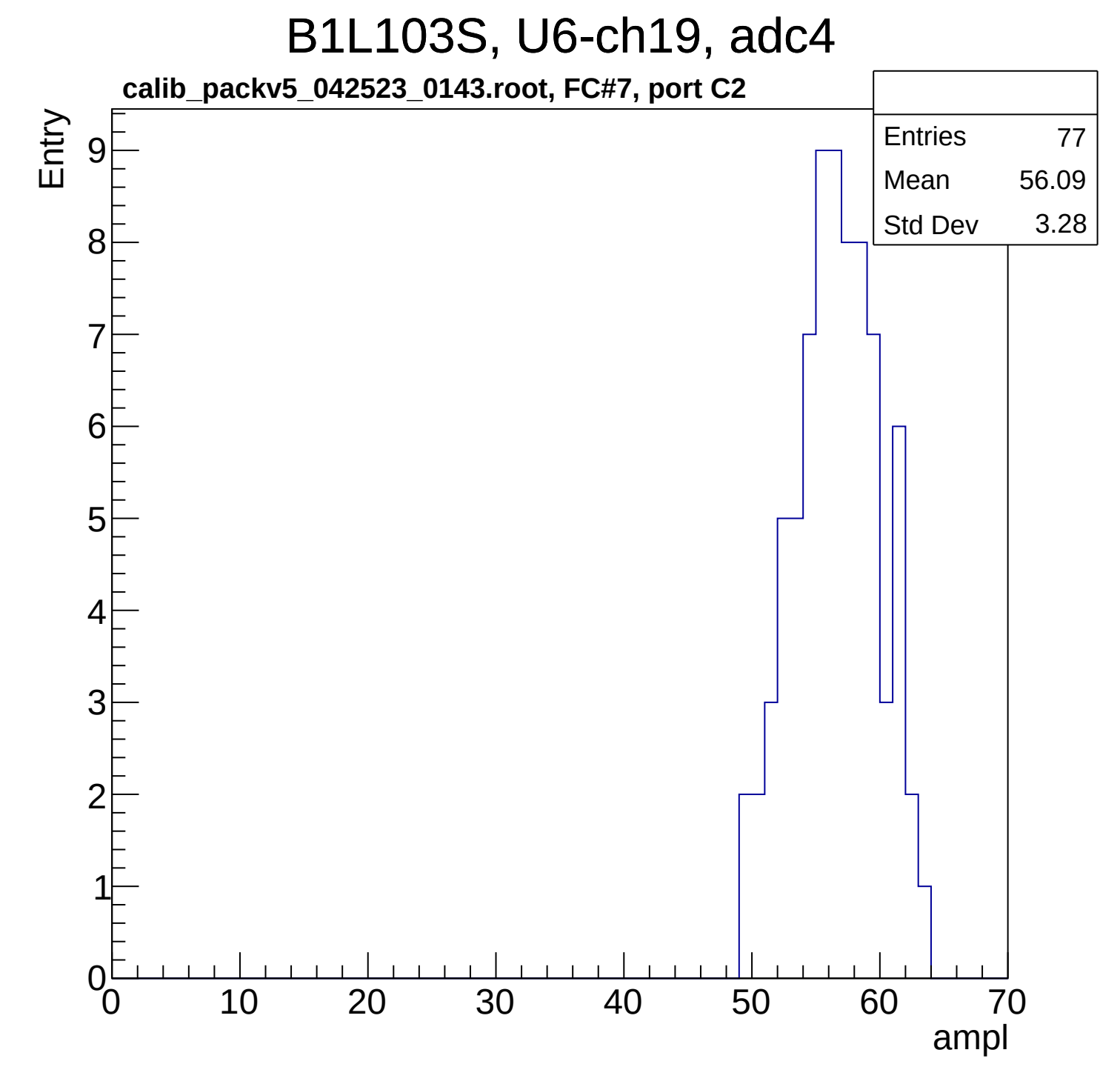
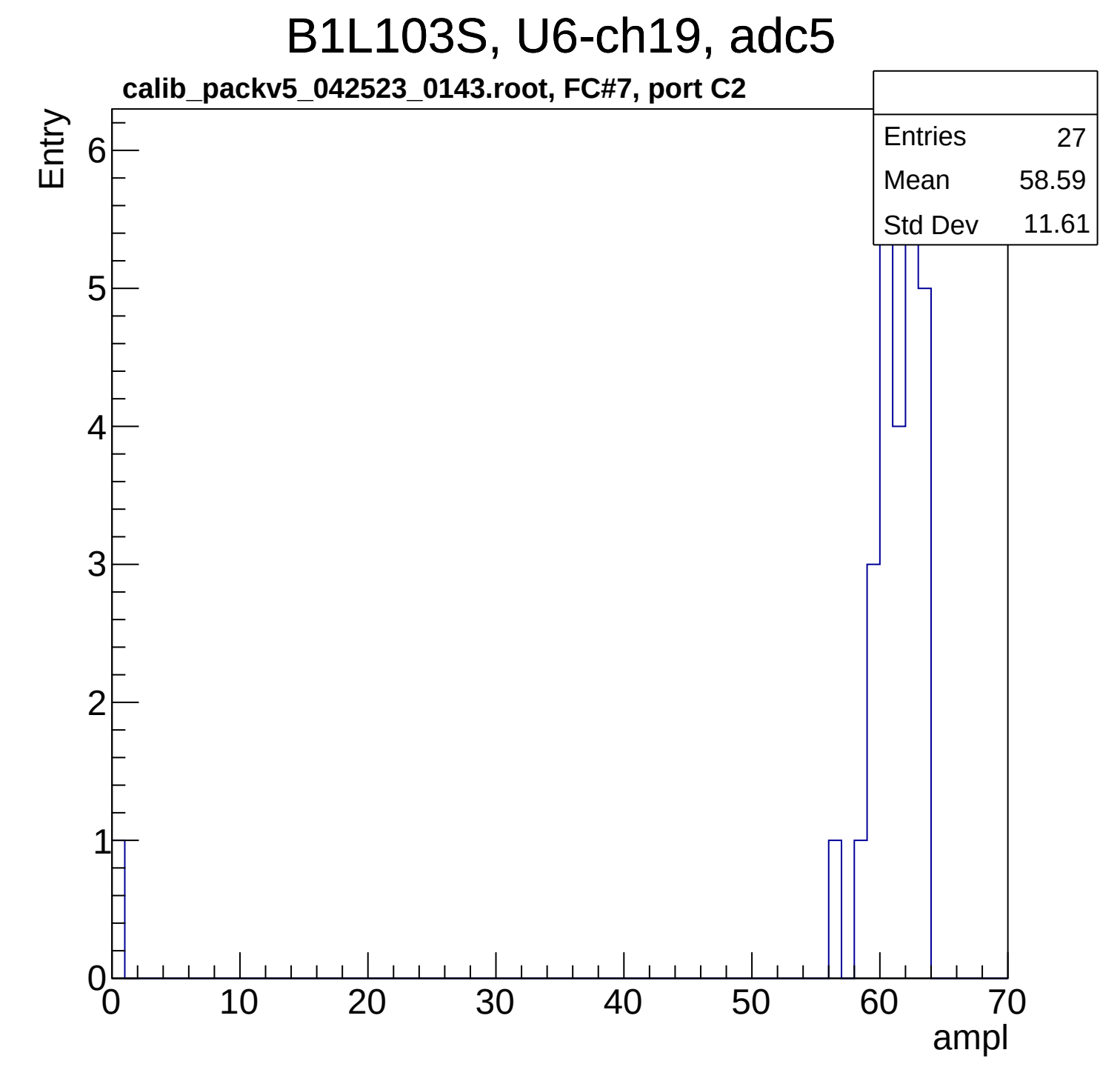
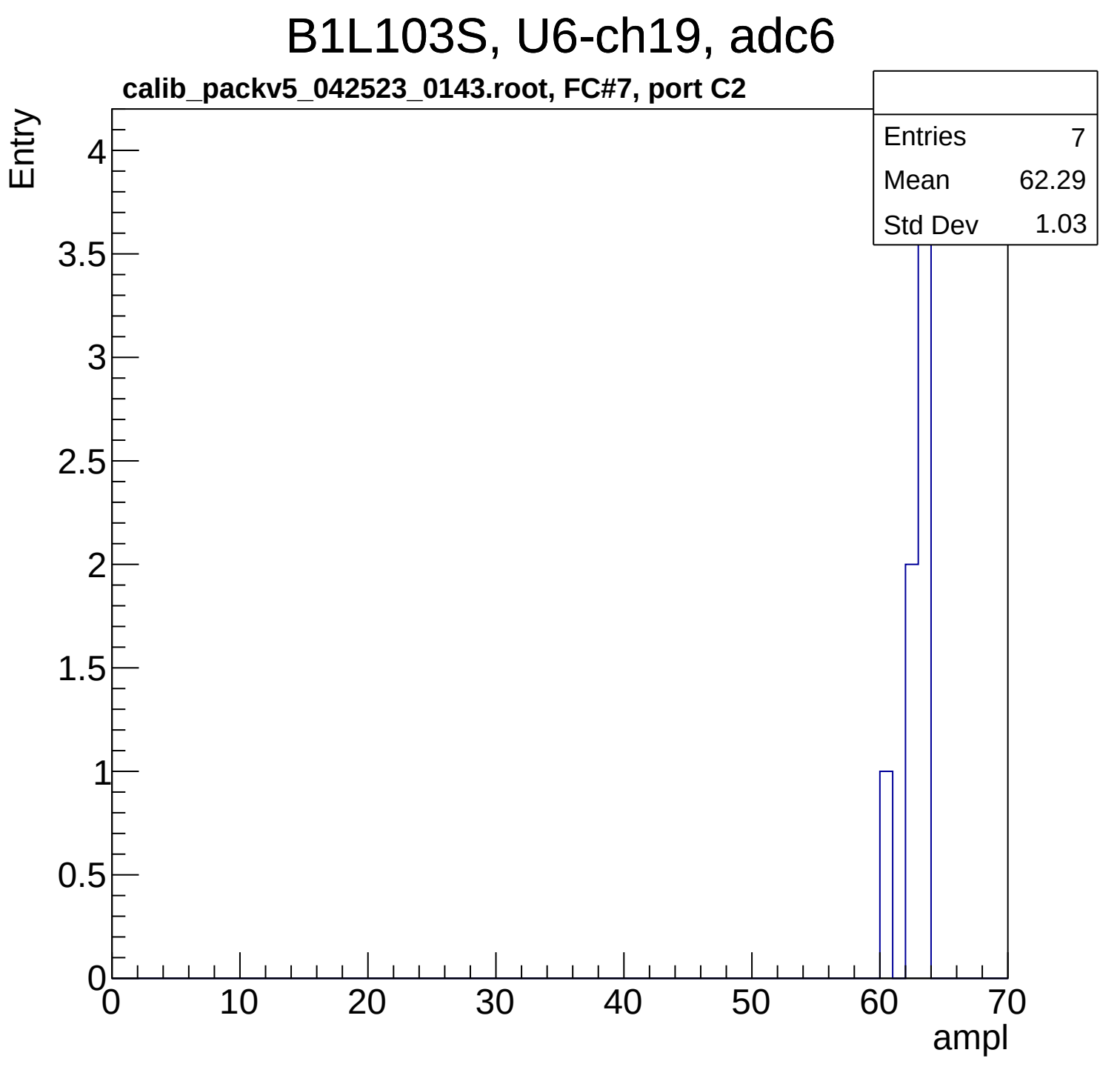
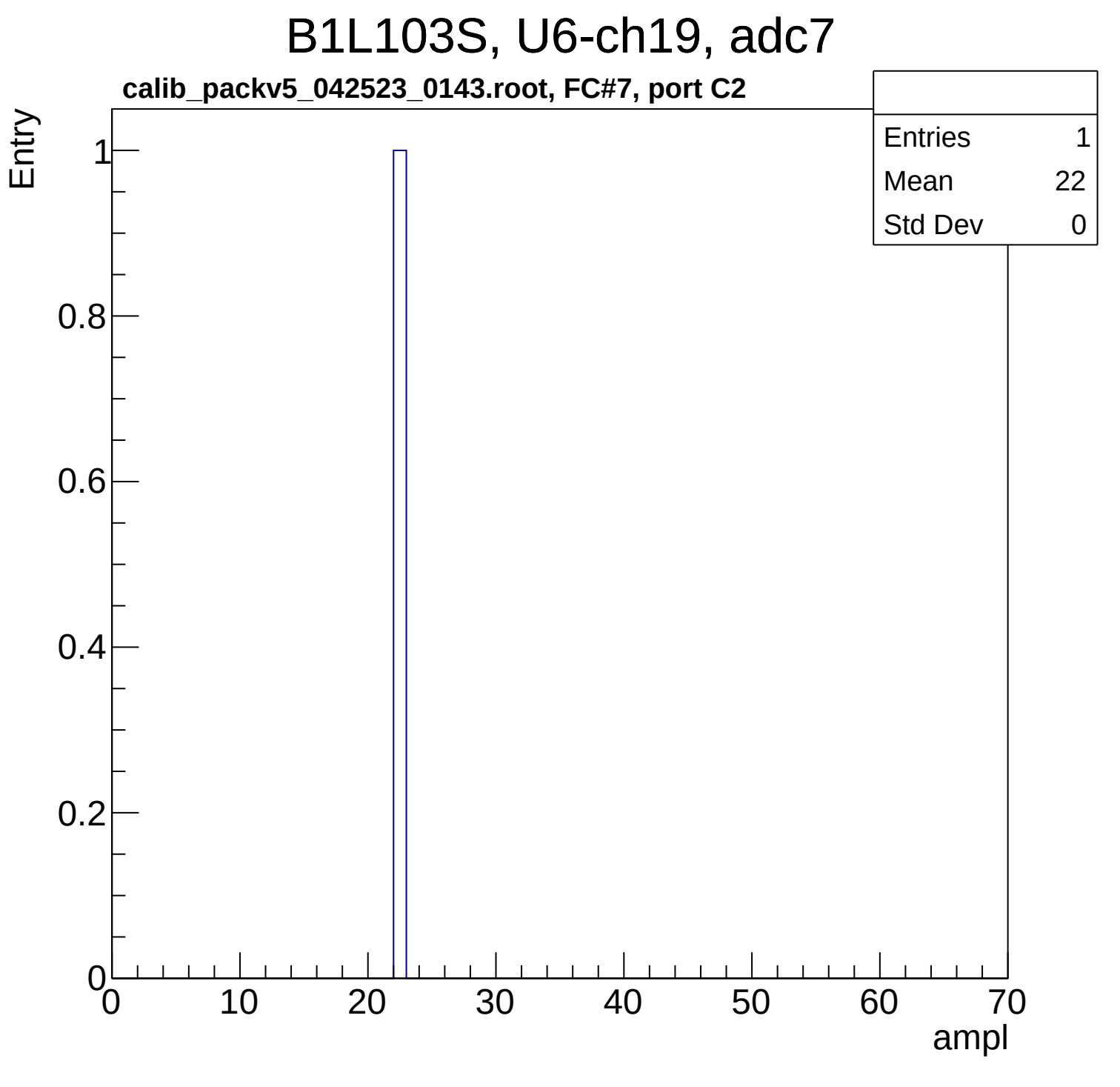
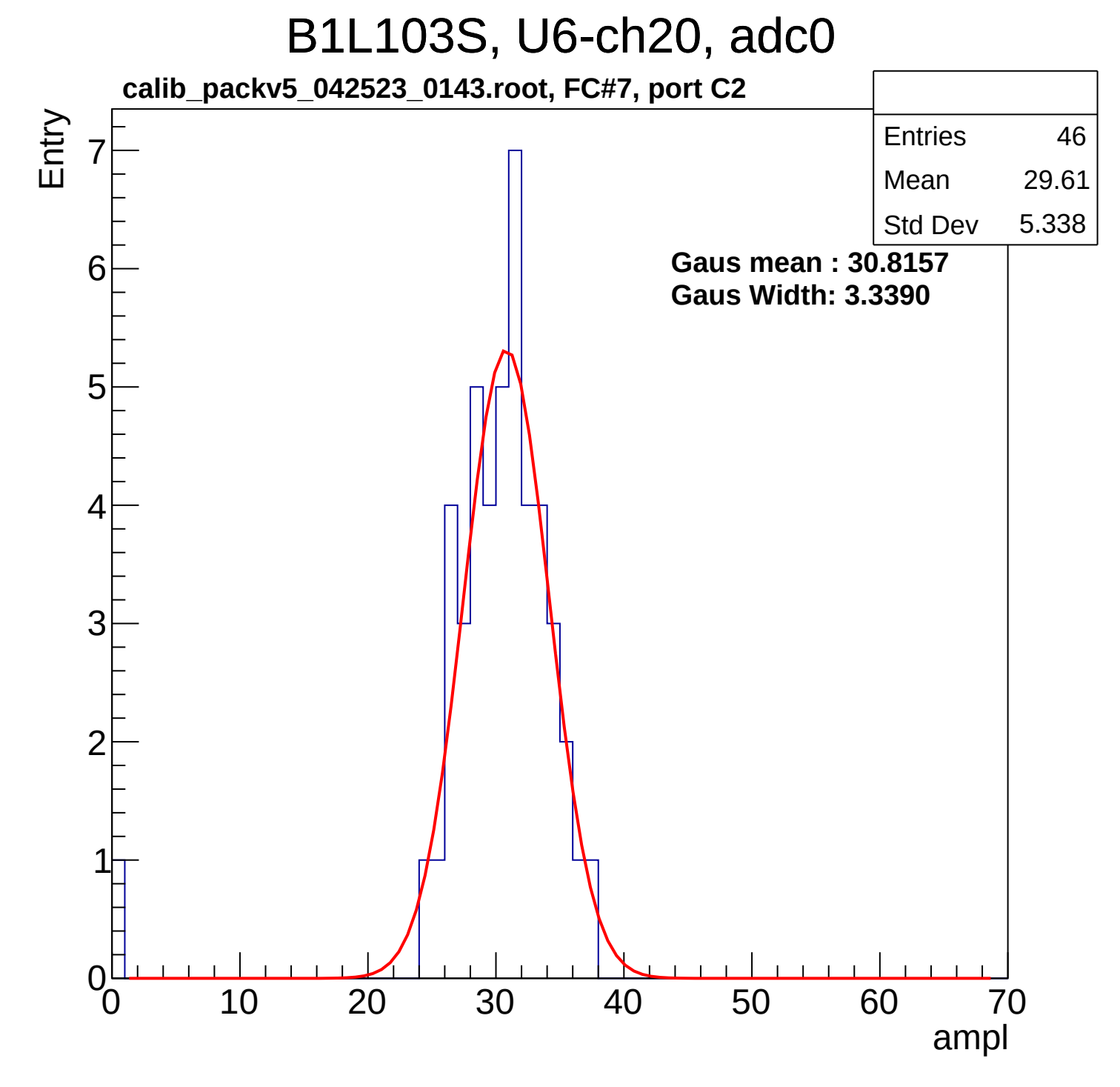
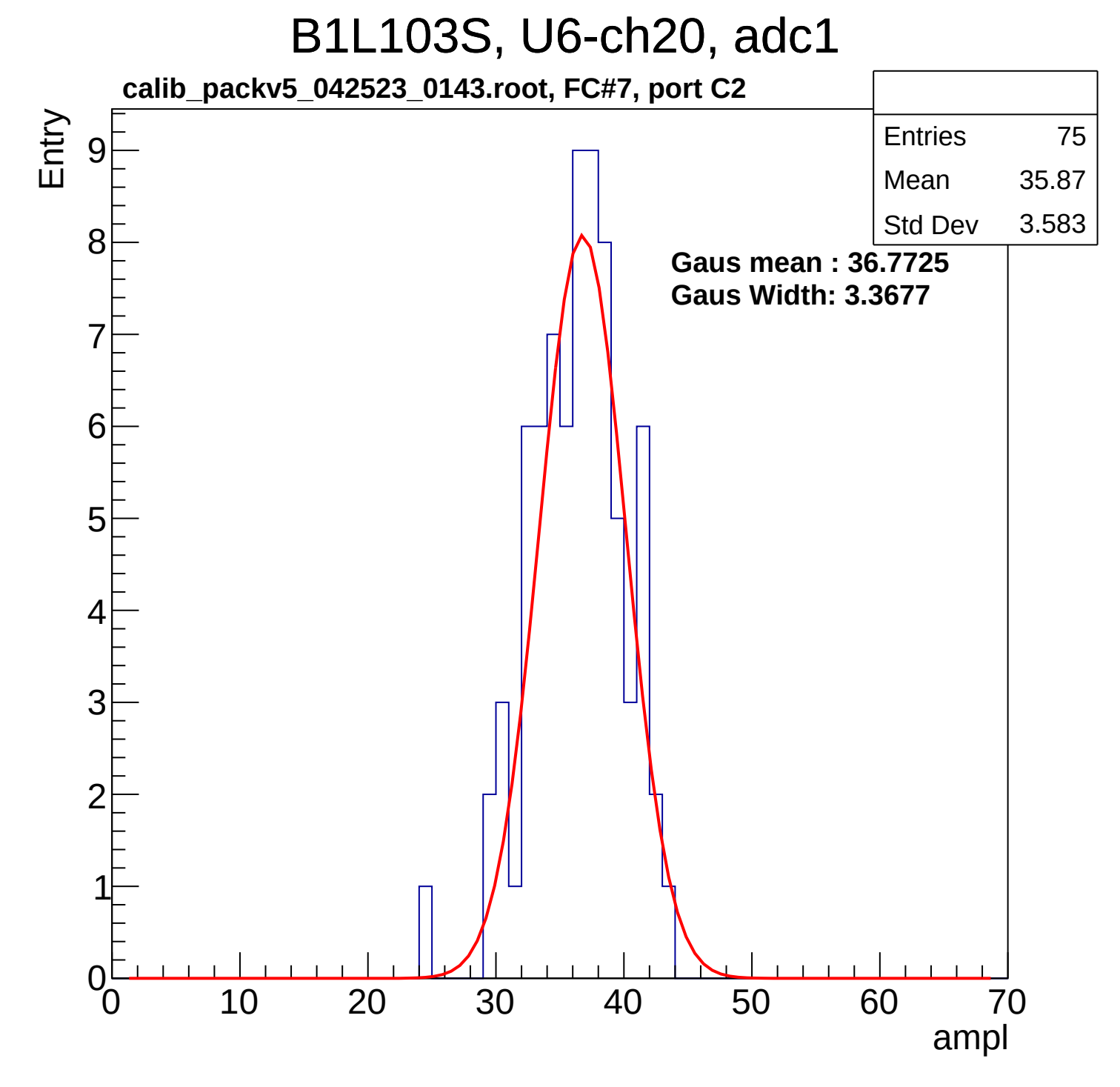

B1L103S, U6-ch19, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 56.09
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch19, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 27
Mean 58.59
Std Dev 11.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch19, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.29
Std Dev 1.03
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch19, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch20, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 29.61
Std Dev 5.338
Gaus mean : 30.8157
Gaus Width: 3.3390
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch20, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 35.87
Std Dev 3.583
Gaus mean : 36.7725
Gaus Width: 3.3677
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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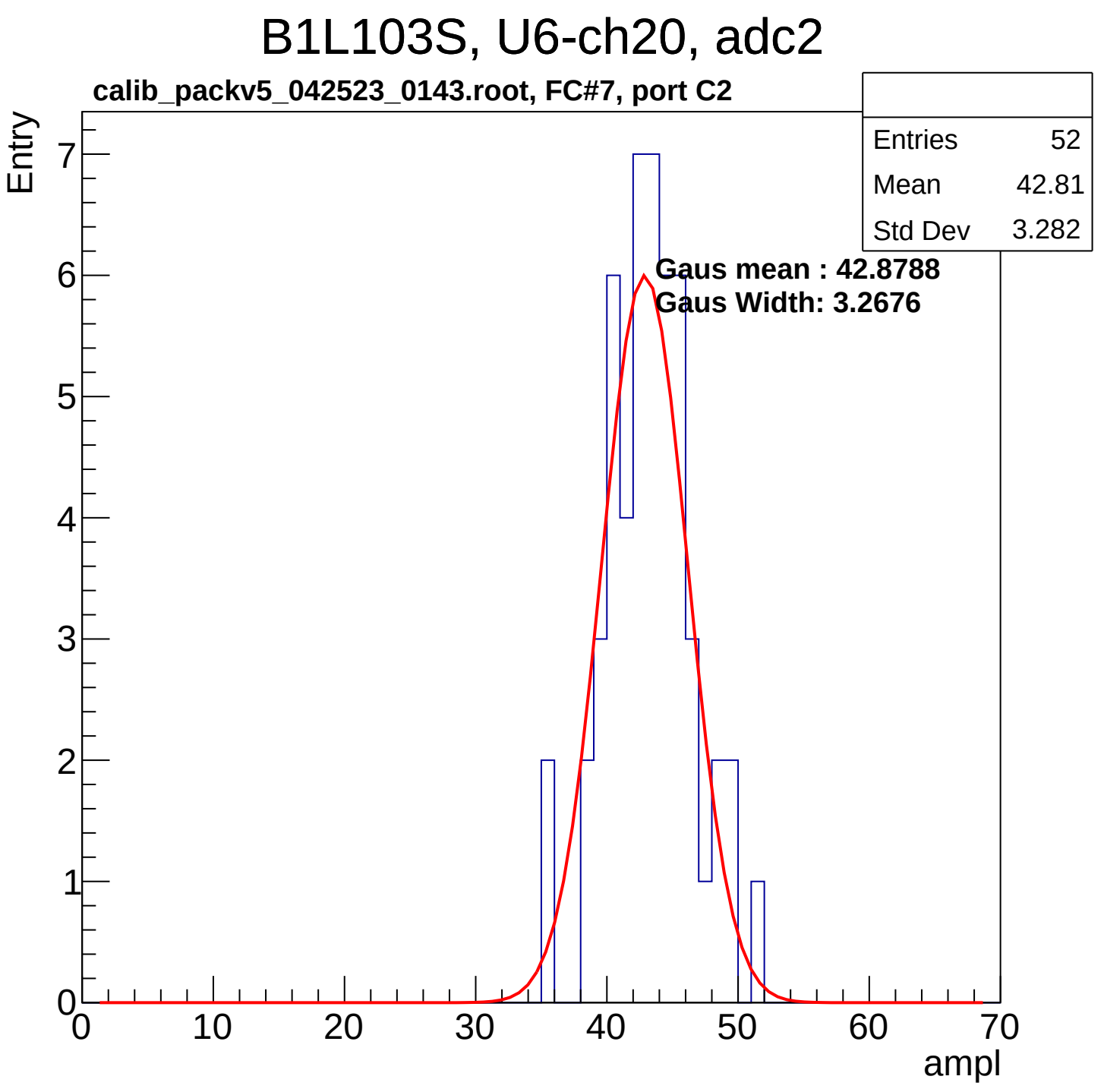
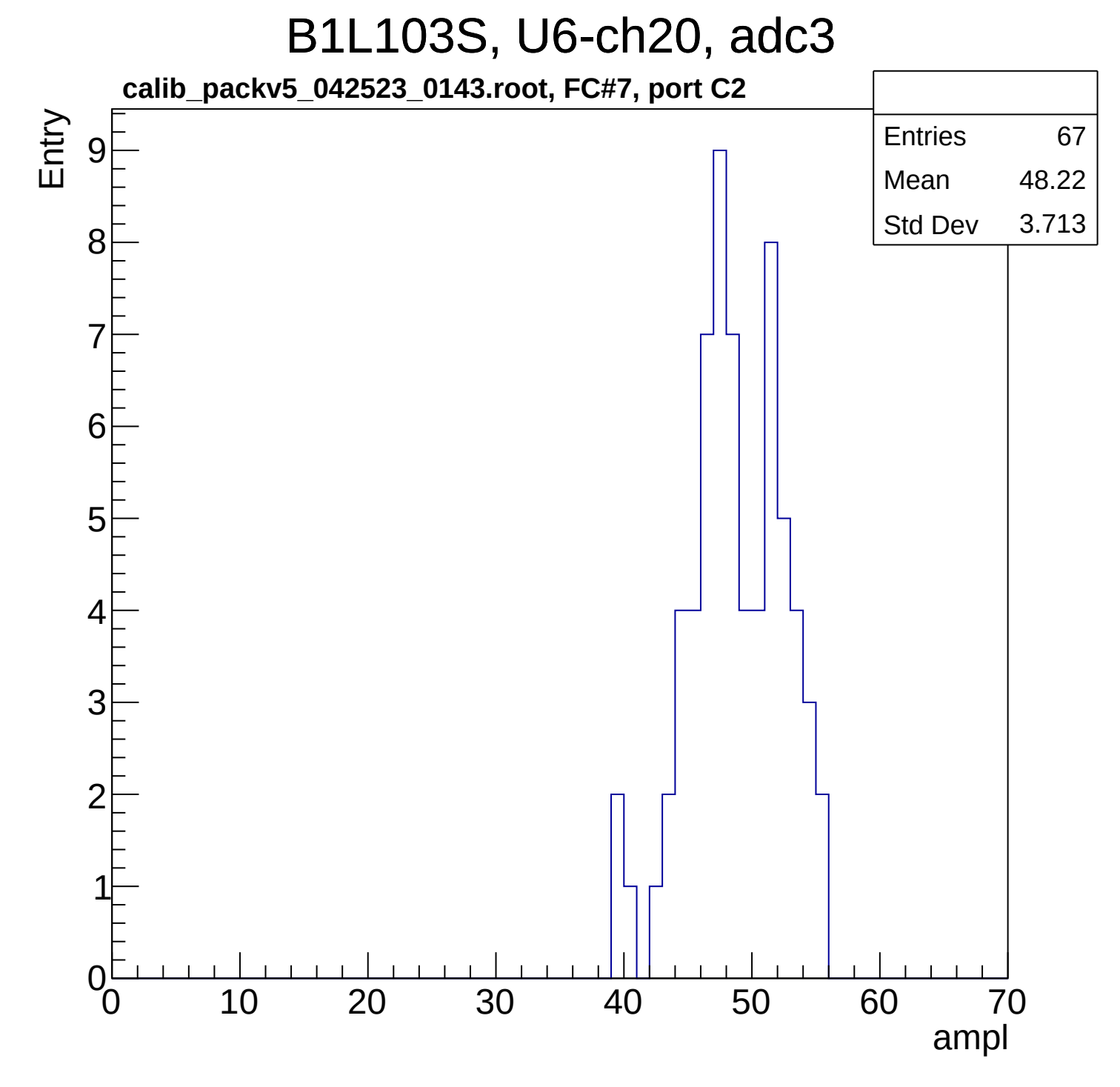

B1L103S, U6-ch20, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 42.81
Std Dev 3.282
Gaus mean : 42.8788
Gaus Width: 3.2676
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch20, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.22
Std Dev 3.713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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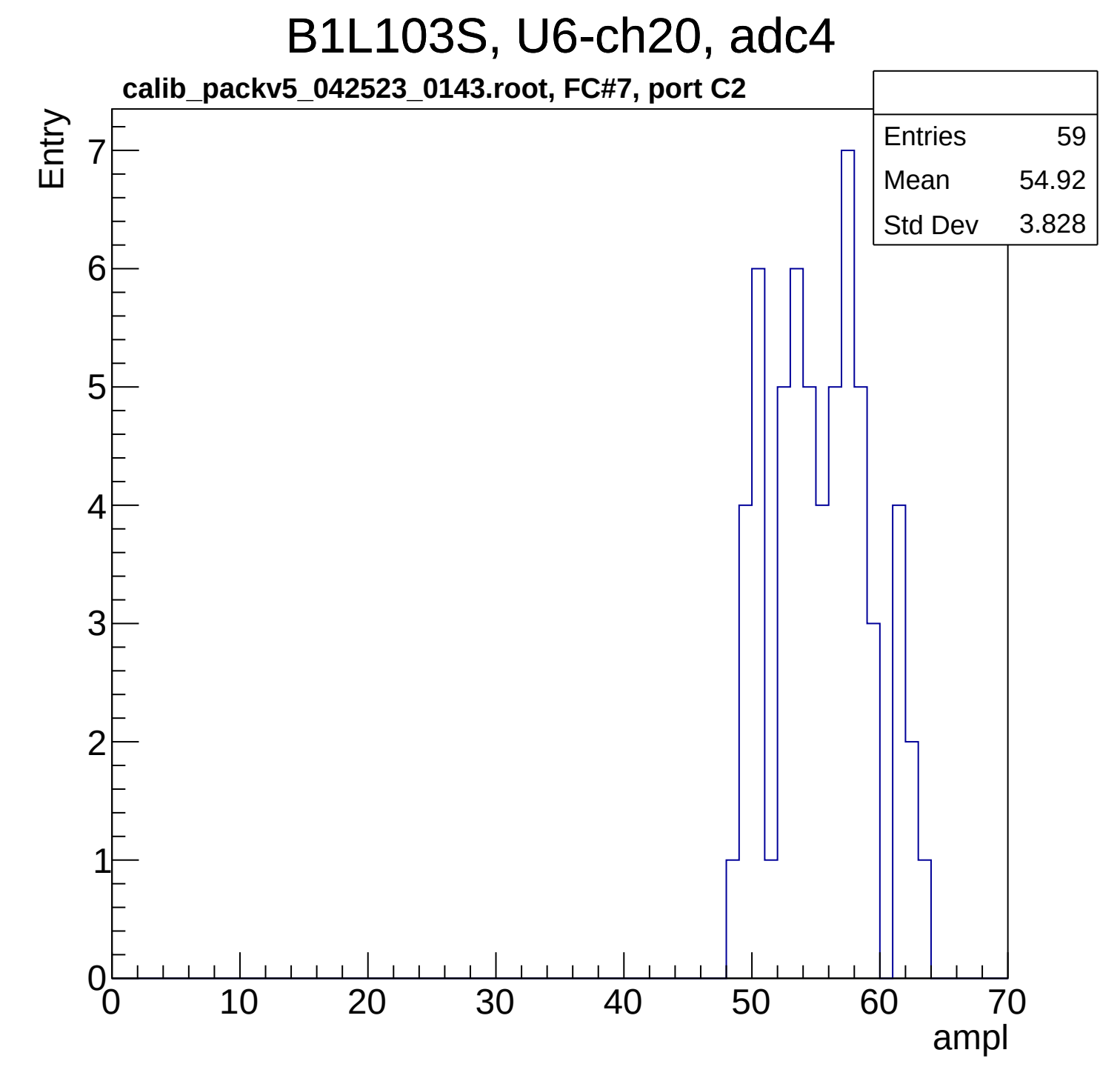
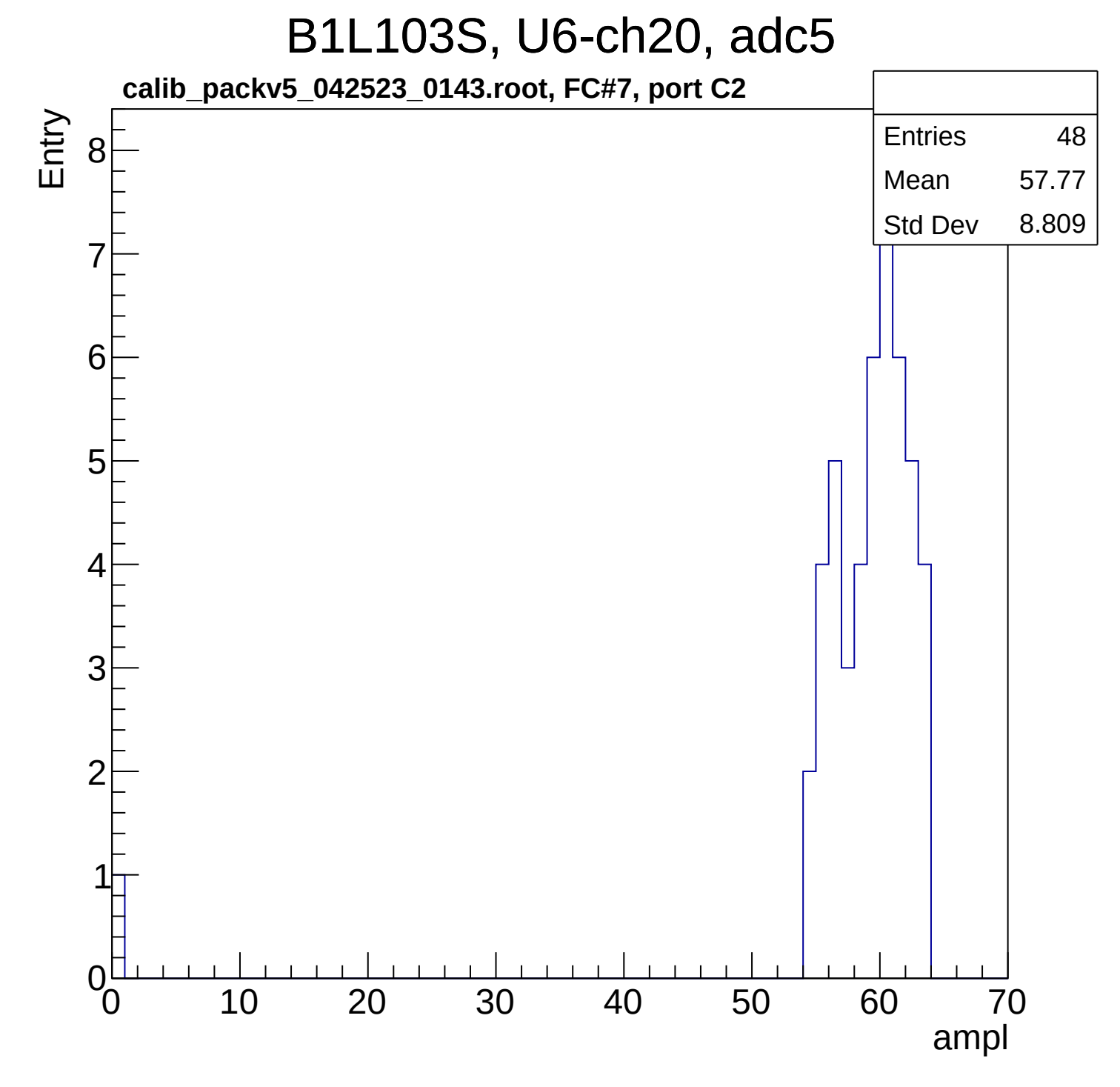
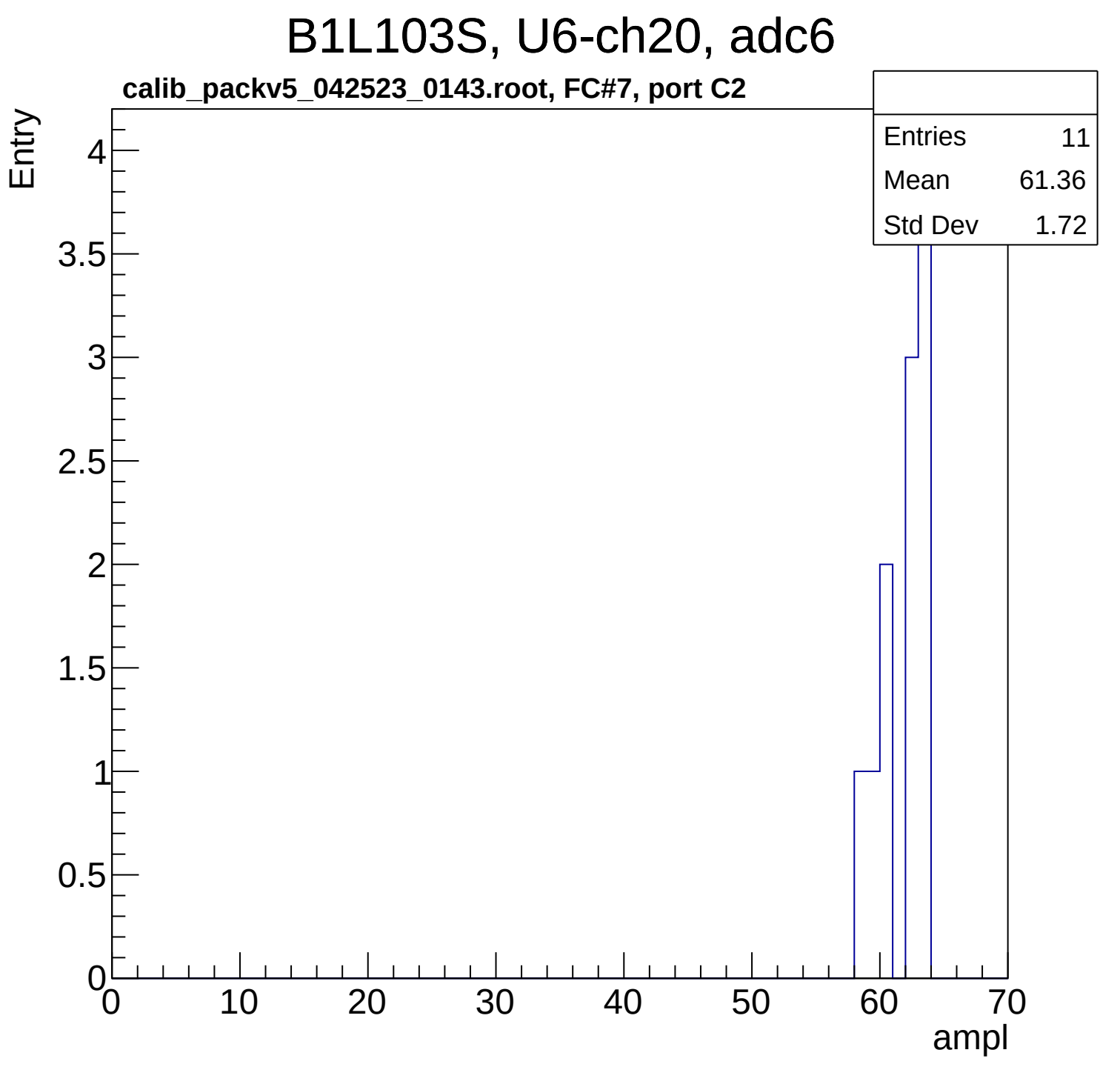
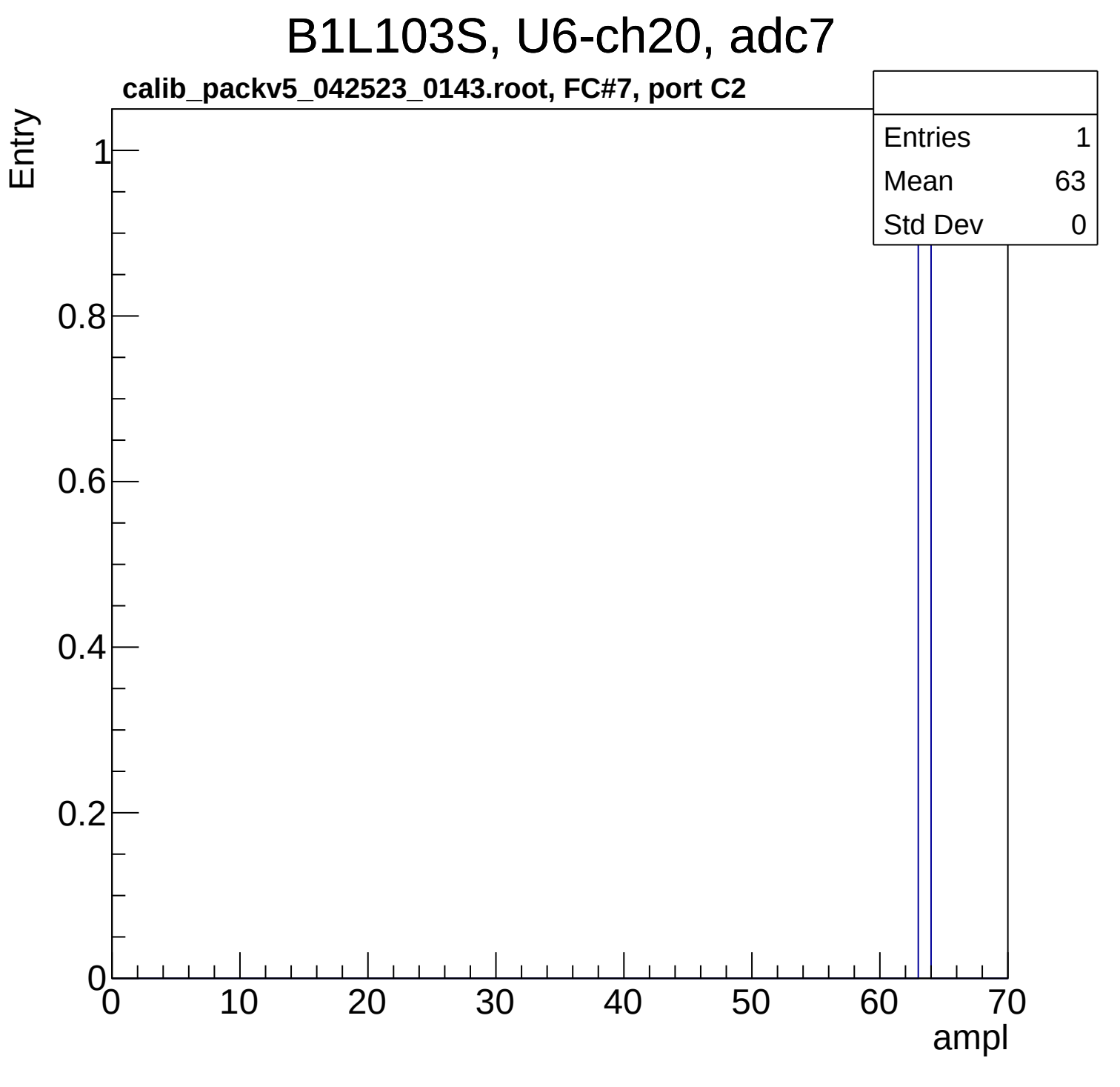
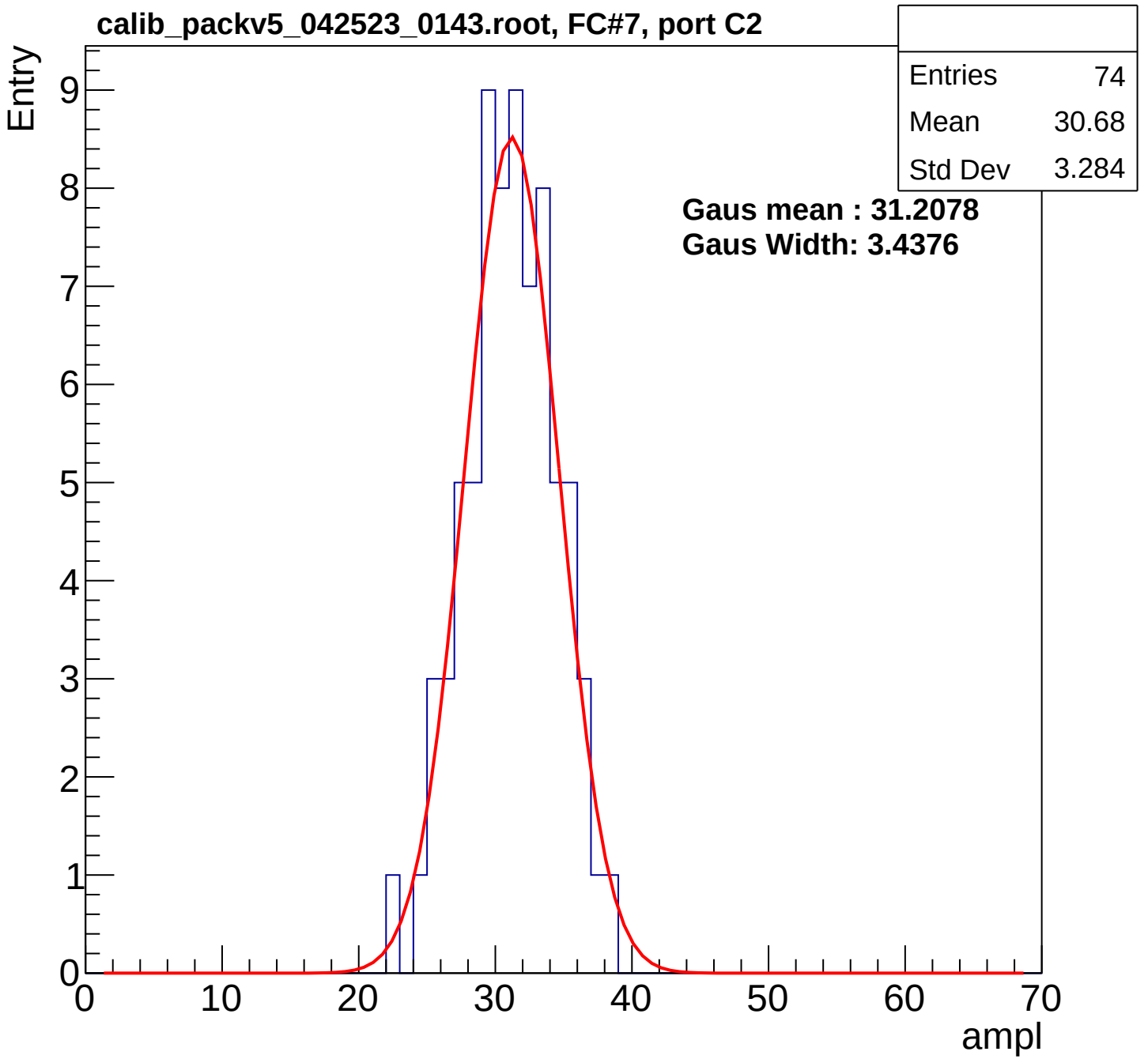
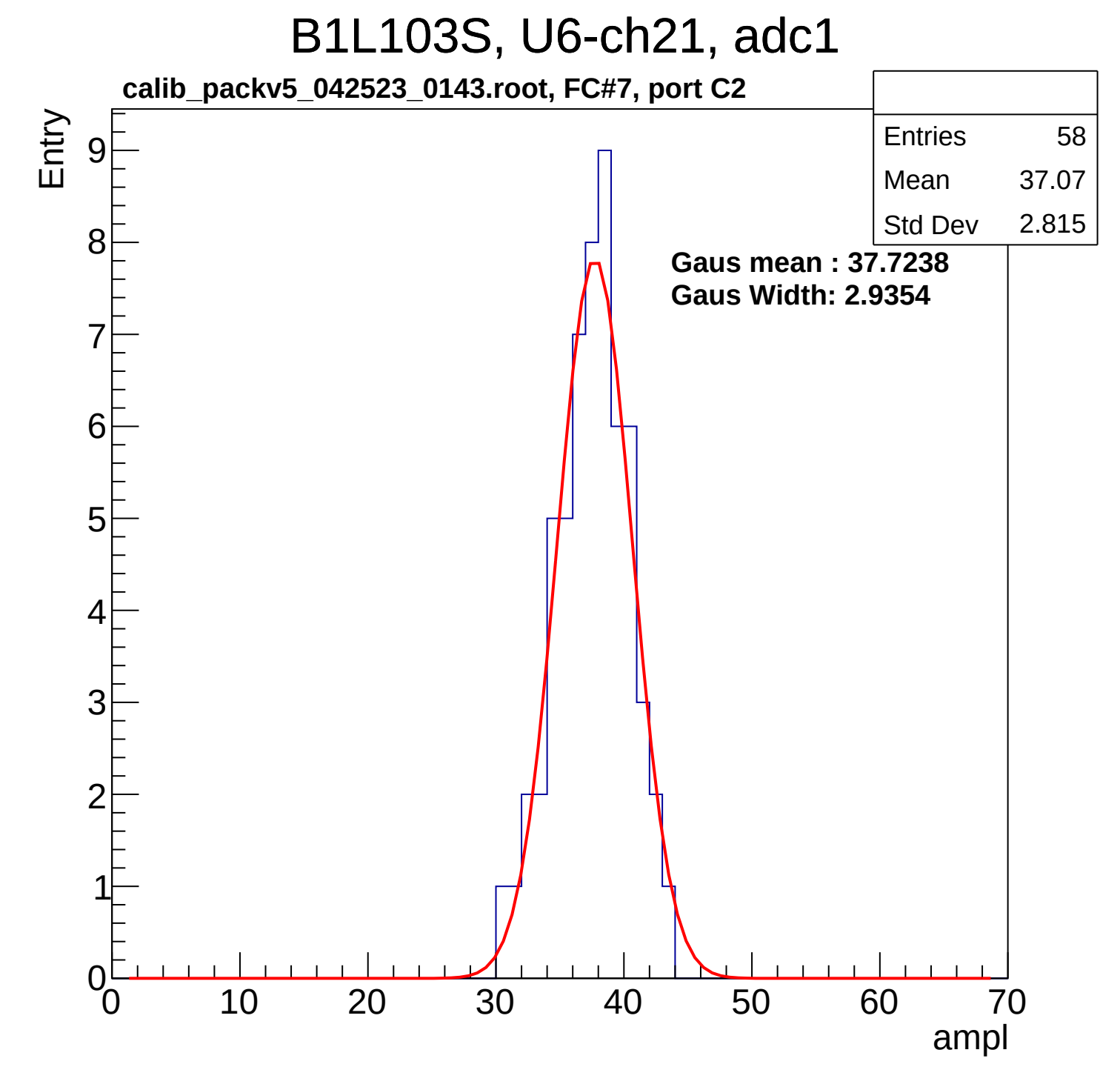

B1L103S, U6-ch20, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.92
Std Dev 3.828
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

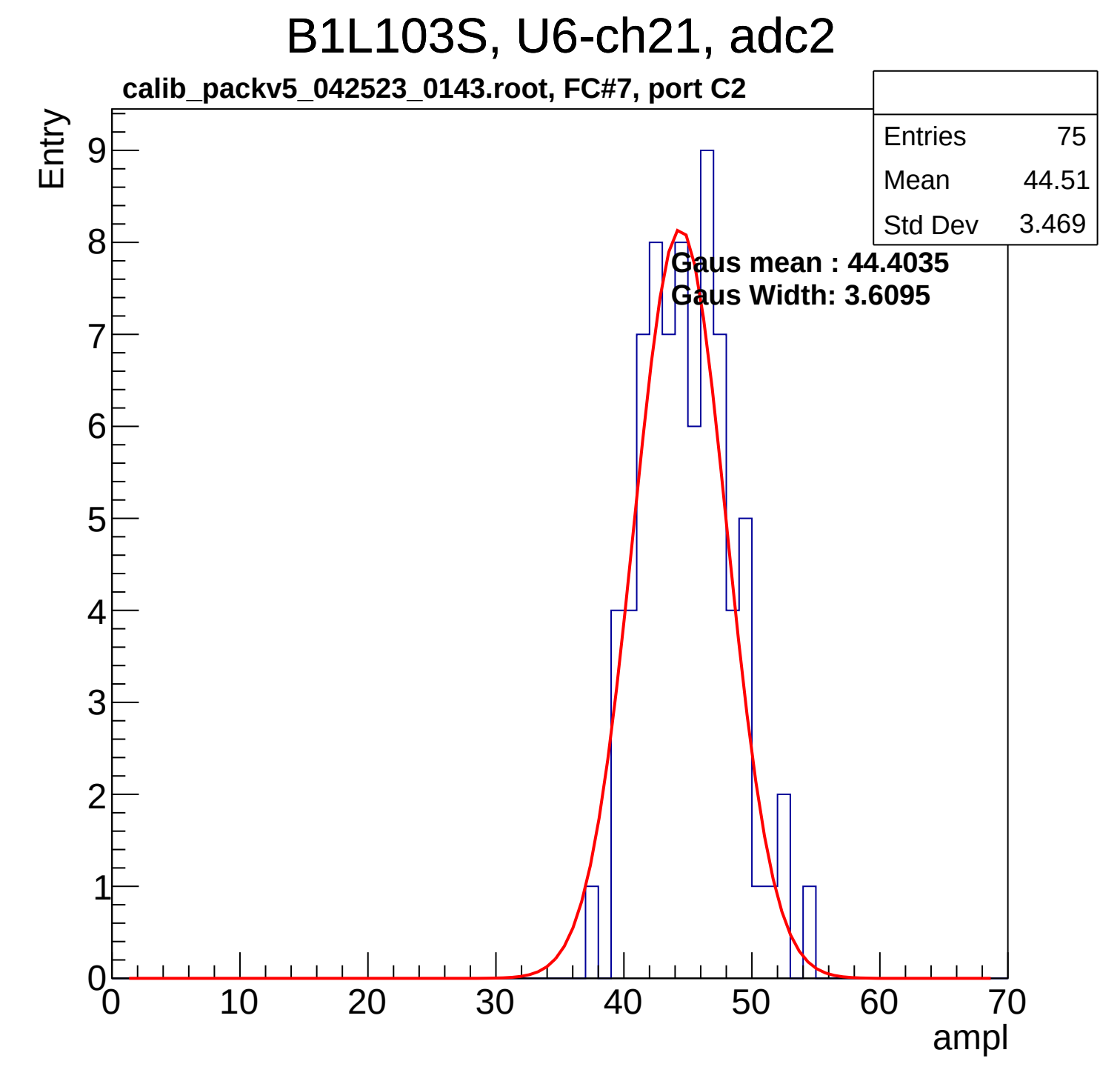

B1L103S, U6-ch20, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 57.77
Std Dev 8.809
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch20, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61.36
Std Dev 1.72
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch20, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch21, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.68
Std Dev 3.284
Gaus mean : 31.2078
Gaus Width: 3.4376
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U6-ch21, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 37.07
Std Dev 2.815
Gaus mean : 37.7238
Gaus Width: 2.9354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch21, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 44.51
Std Dev 3.469
Gaus mean : 44.4035
Gaus Width: 3.6095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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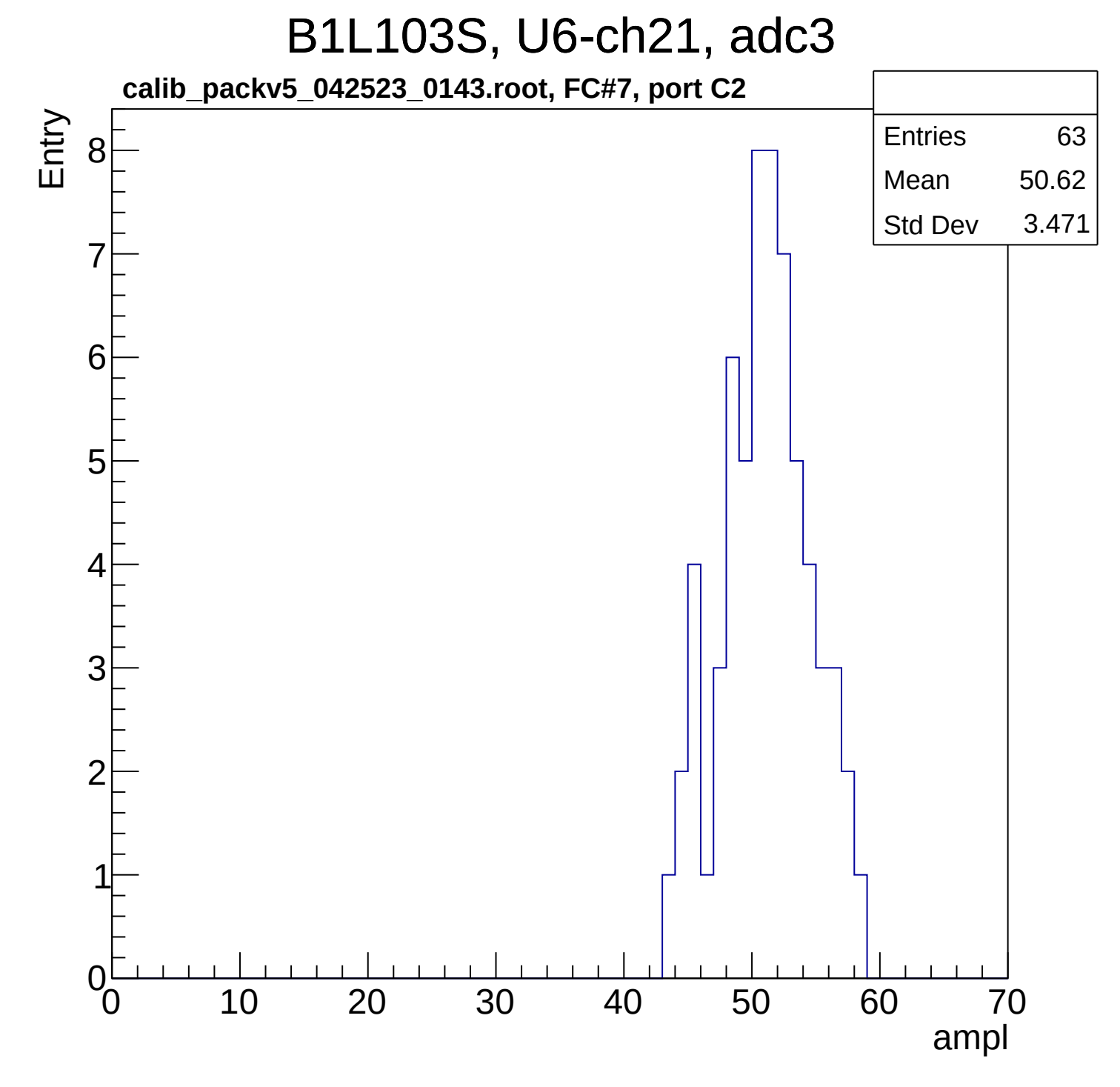
B1L103S, U6-ch21, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 50.62
Std Dev 3.471
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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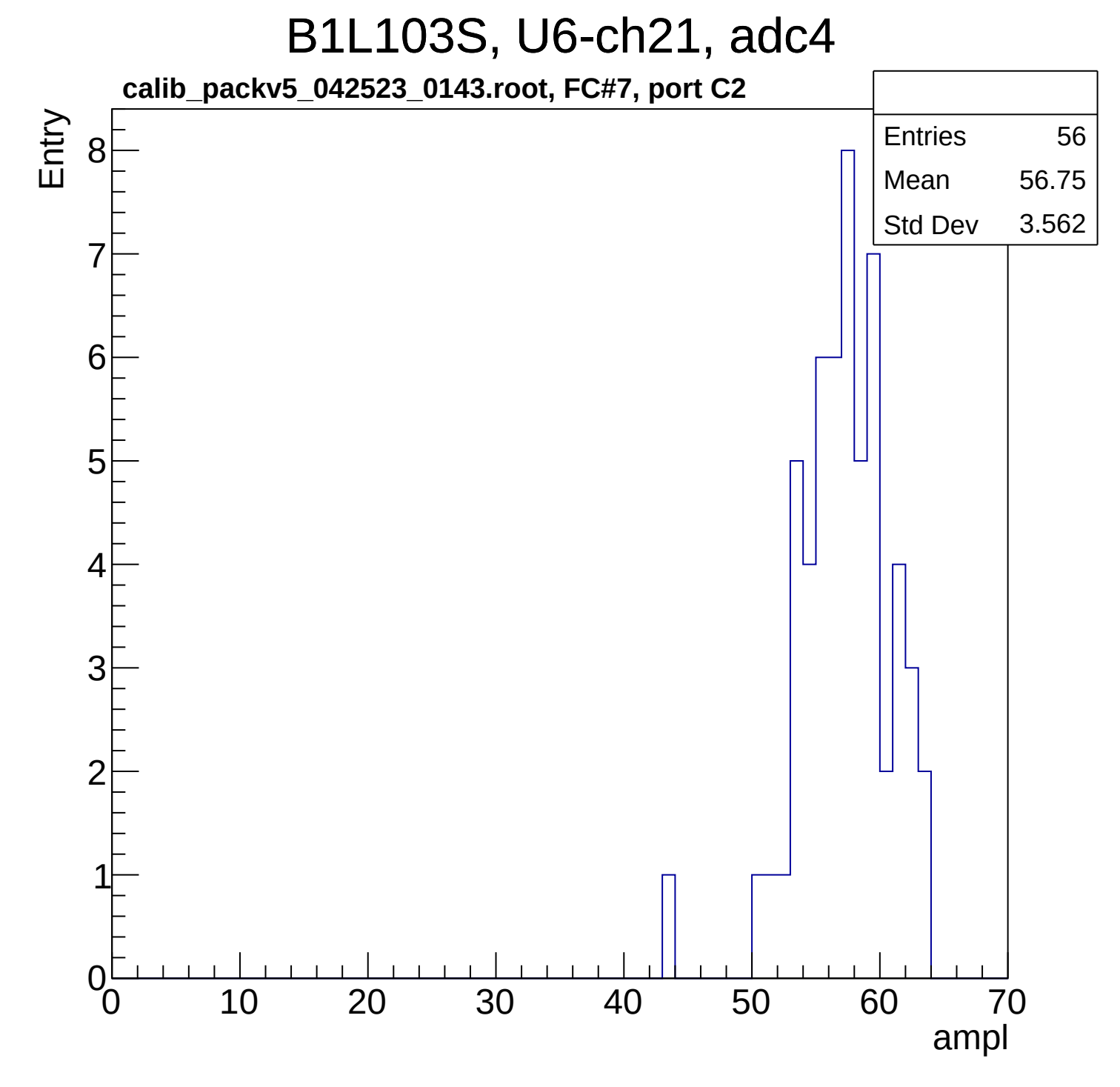
60
70


B1L103S, U6-ch21, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 56.75
Std Dev 3.562
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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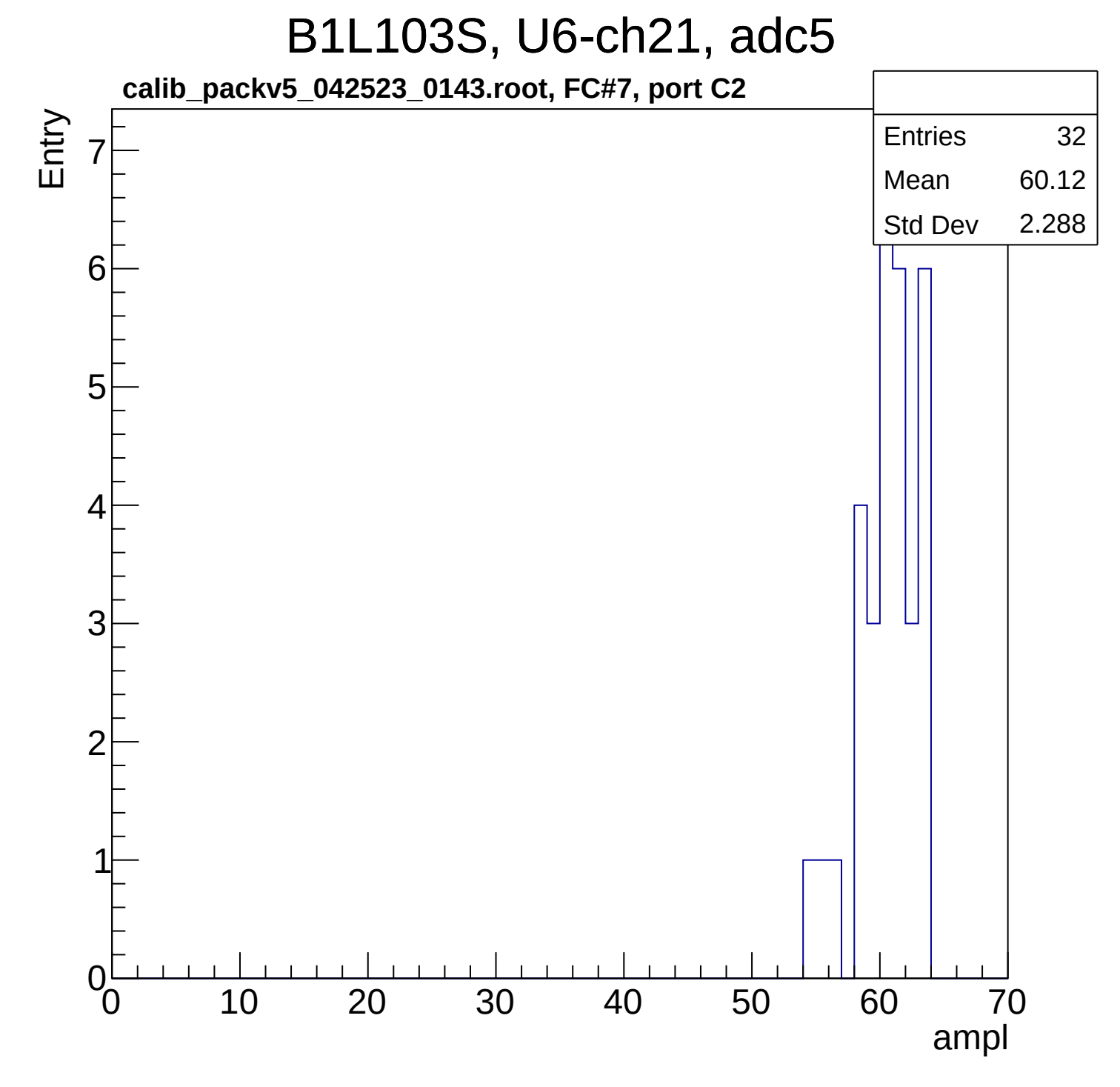
B1L103S, U6-ch21, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 60.12
Std Dev 2.288
Entry
ampl
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1
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3
4
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6
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0
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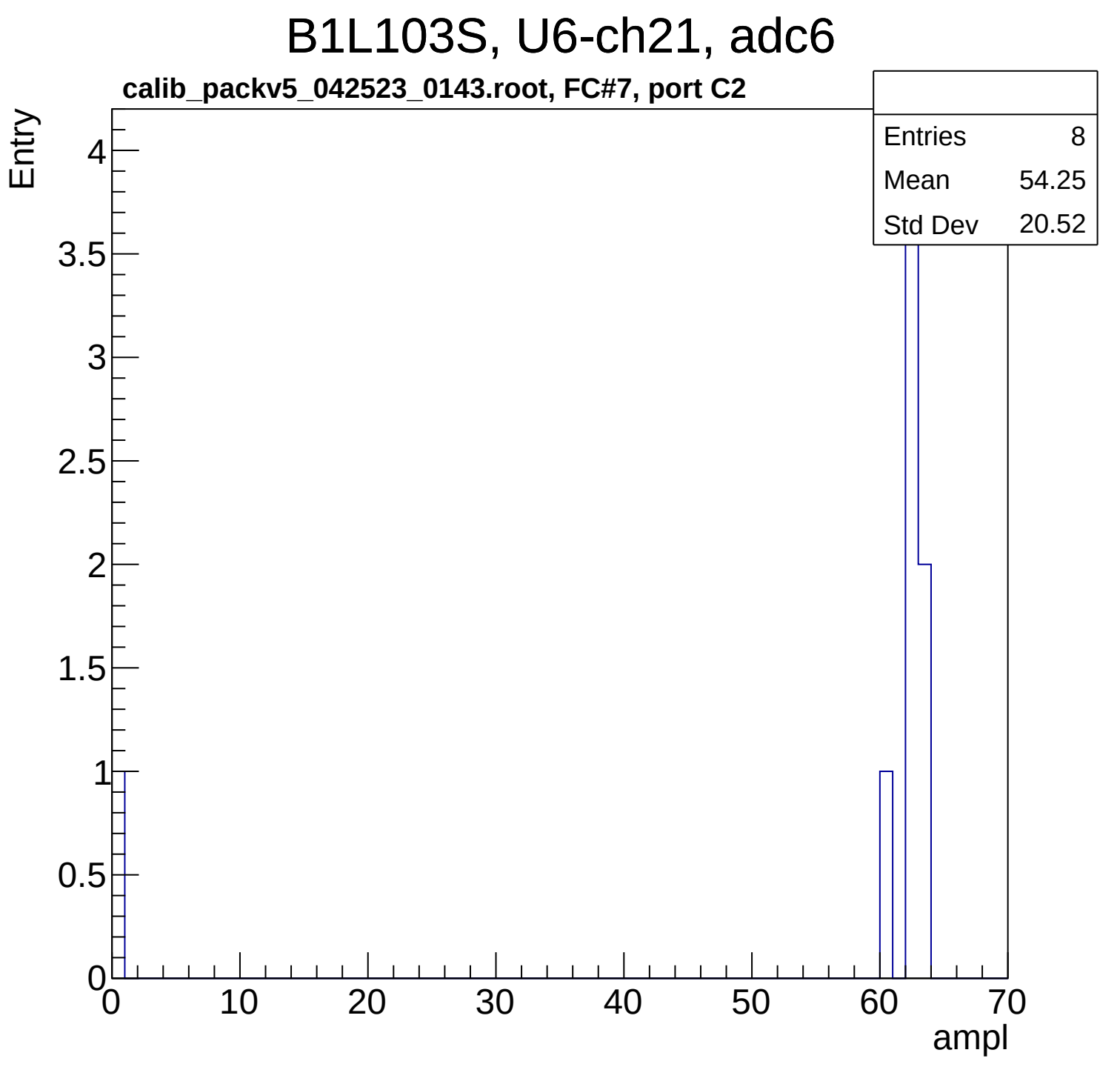
60
70


B1L103S, U6-ch21, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 54.25
Std Dev 20.52
Entry
ampl
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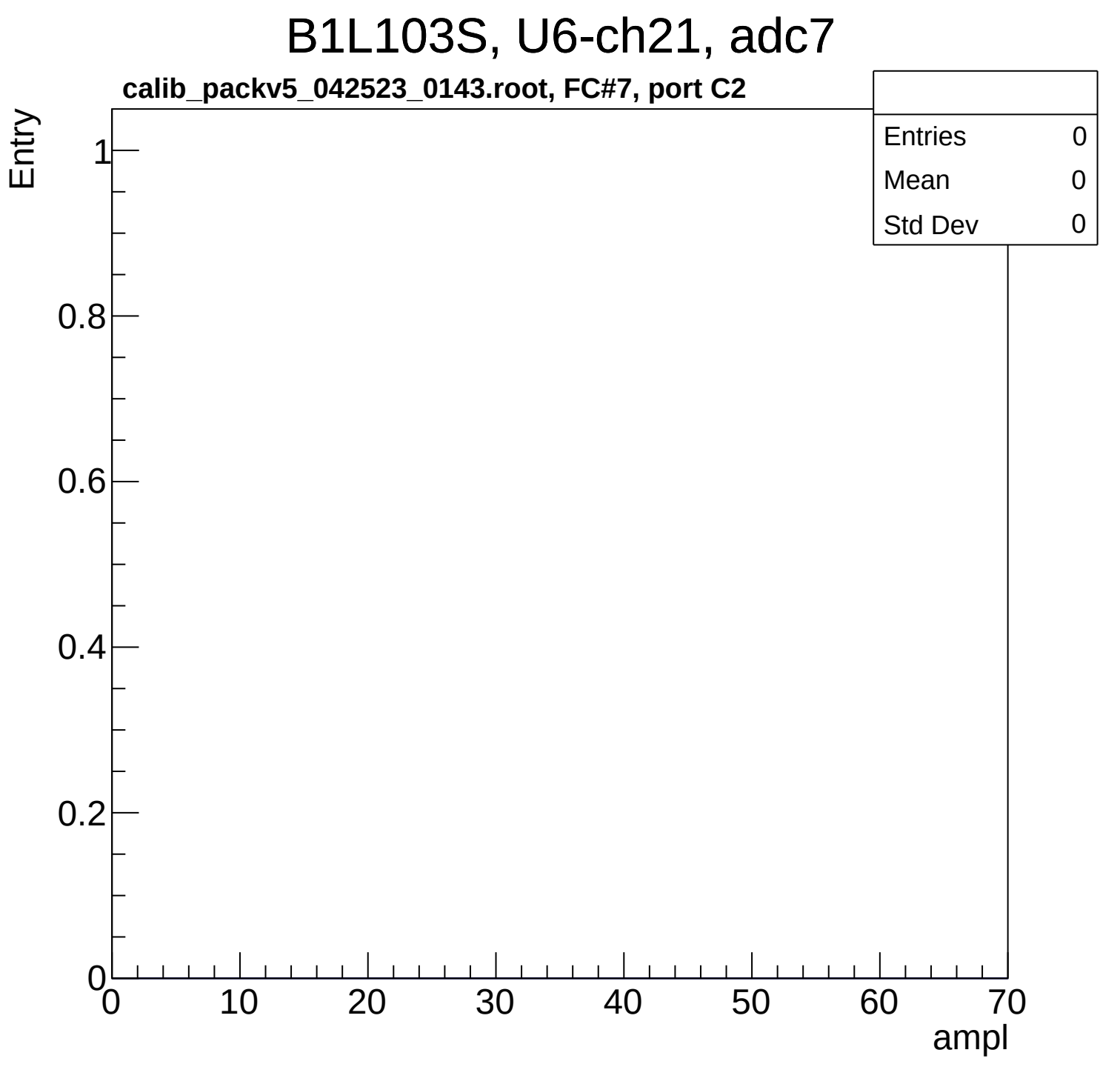
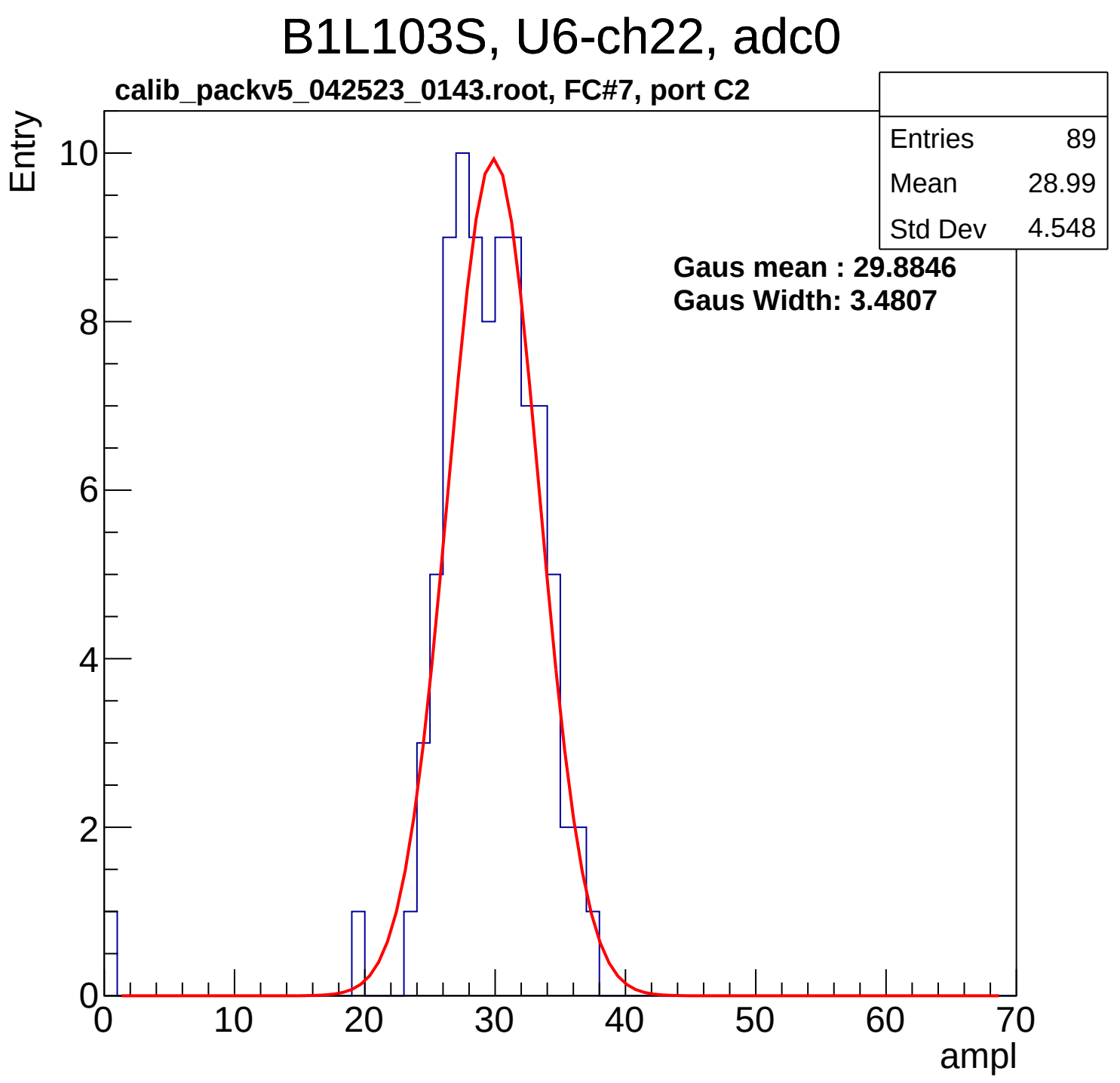
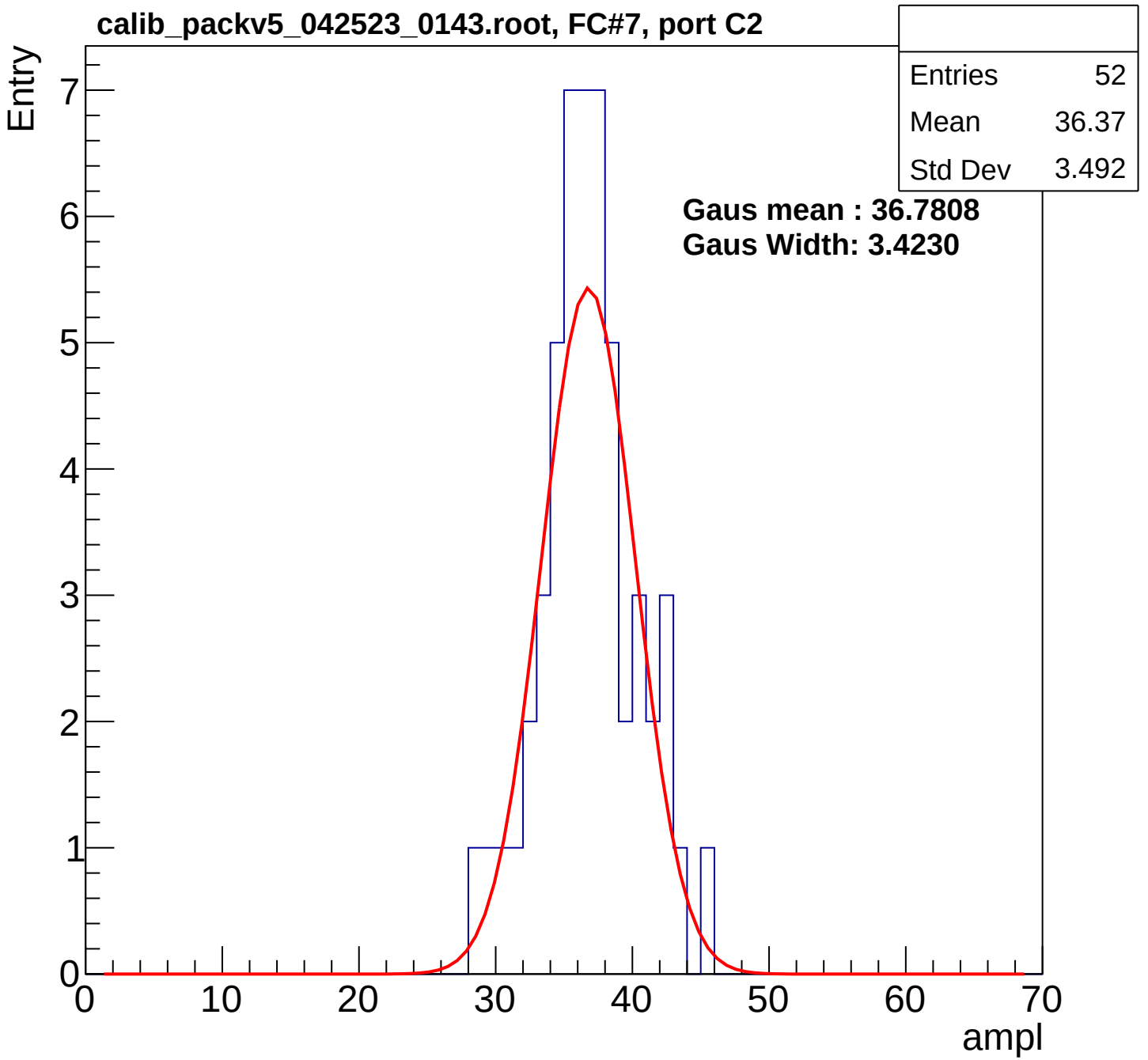
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch21, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch22, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 28.99
Std Dev 4.548
Gaus mean : 29.8846
Gaus Width: 3.4807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch22, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 36.37
Std Dev 3.492
Gaus mean : 36.7808
Gaus Width: 3.4230
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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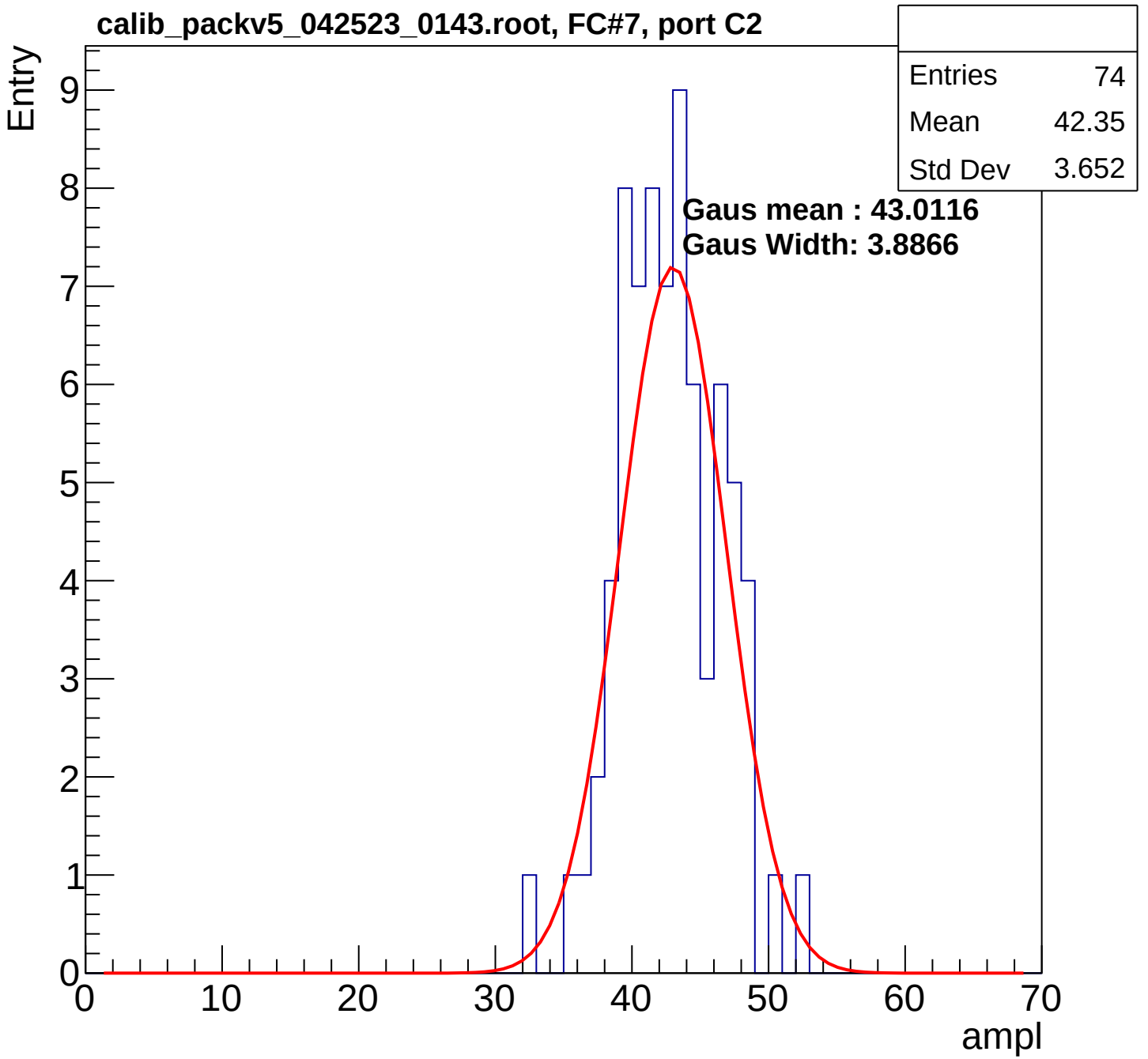
B1L103S, U6-ch22, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 42.35
Std Dev 3.652
Gaus mean : 43.0116
Gaus Width: 3.8866
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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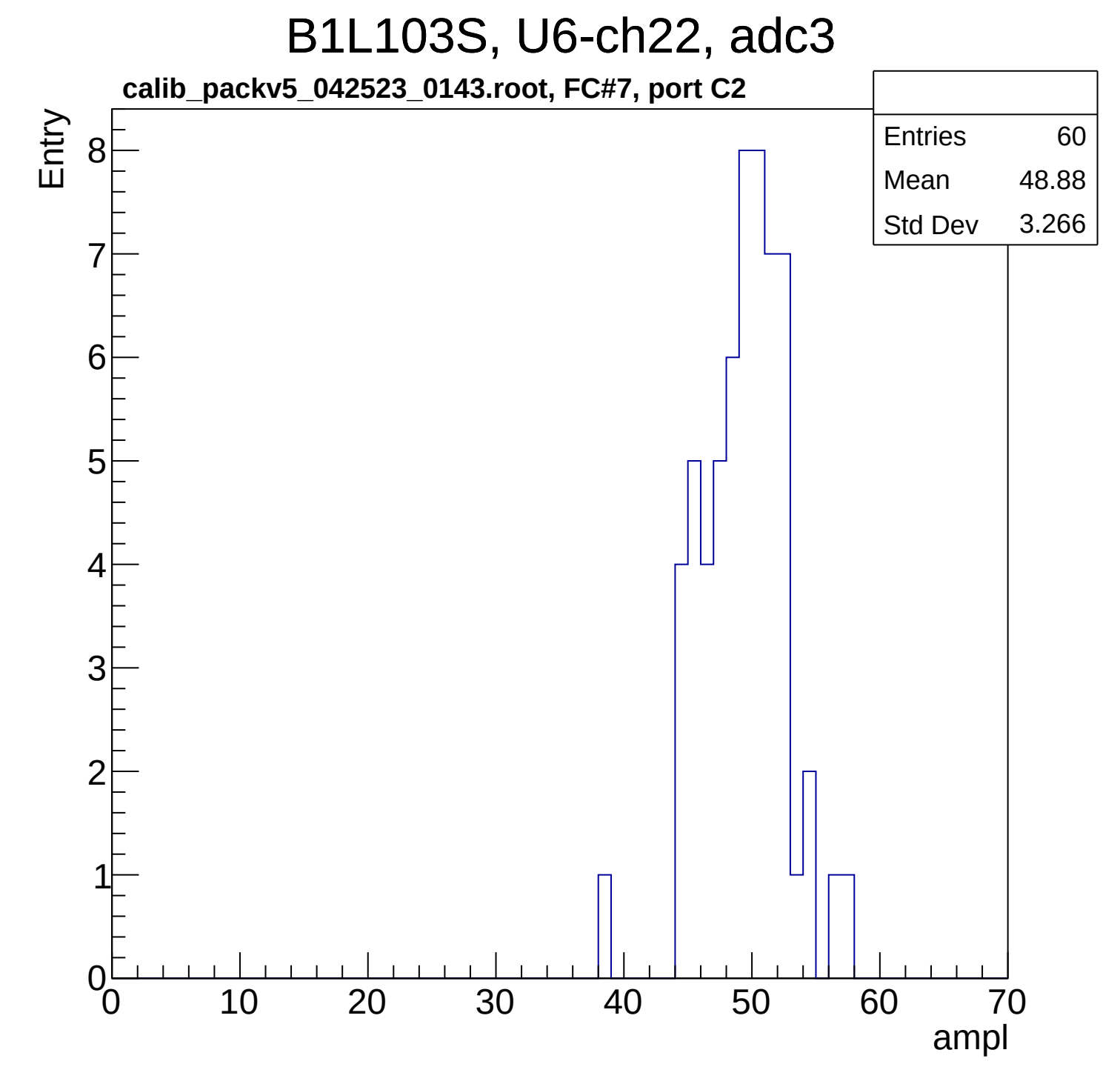
B1L103S, U6-ch22, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.88
Std Dev 3.266
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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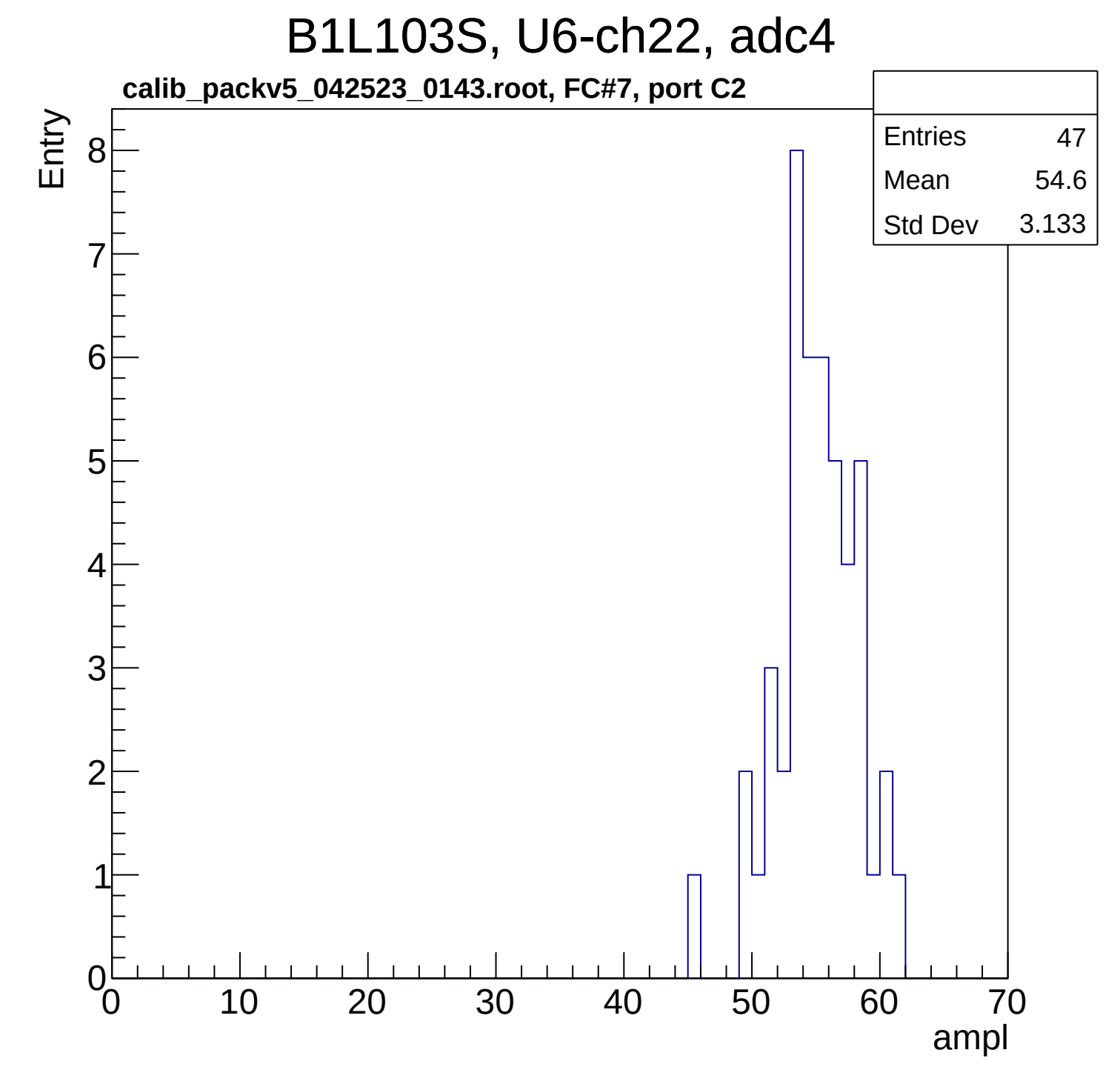
B1L103S, U6-ch22, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 54.6
Std Dev 3.133
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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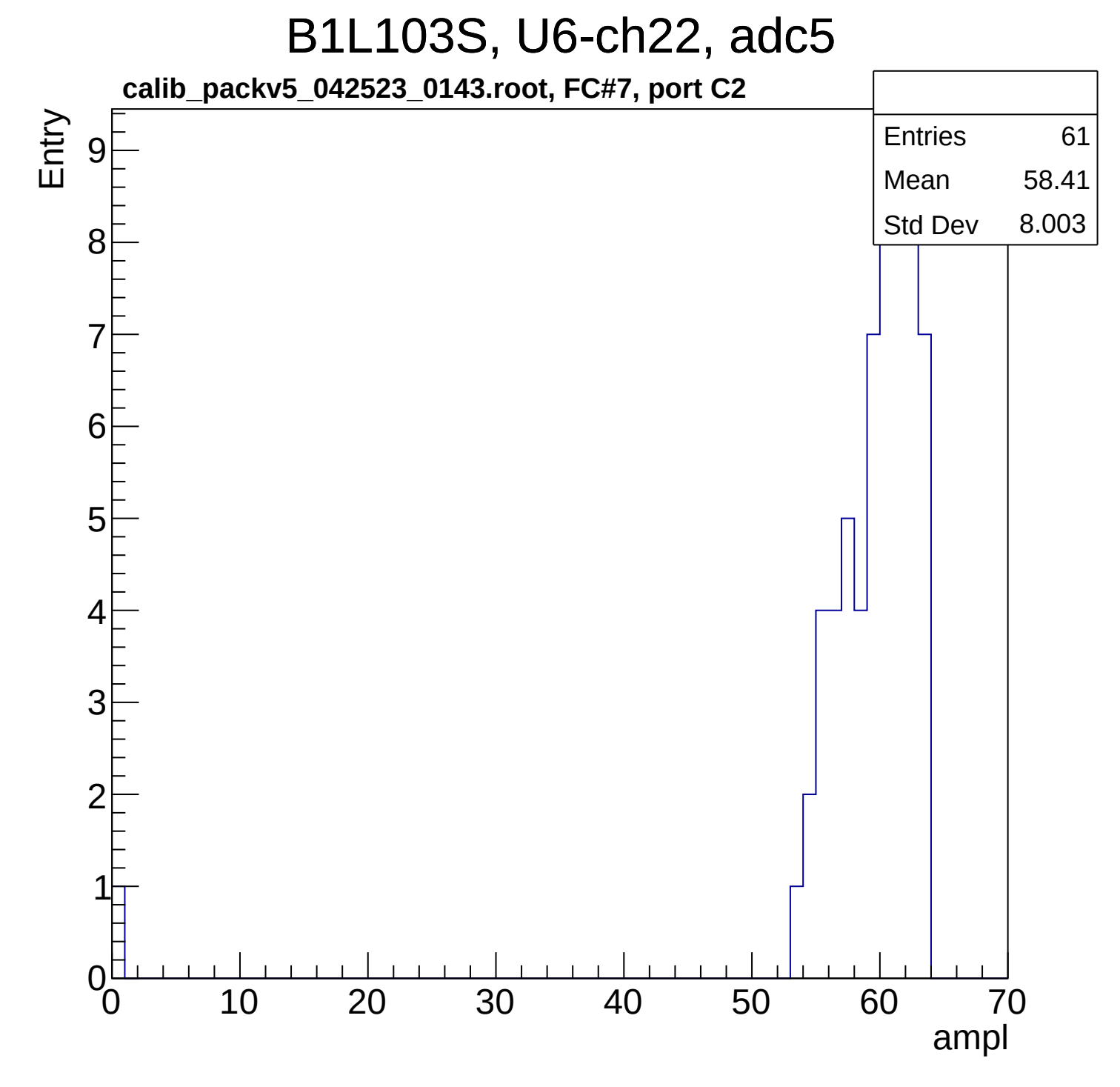
B1L103S, U6-ch22, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 58.41
Std Dev 8.003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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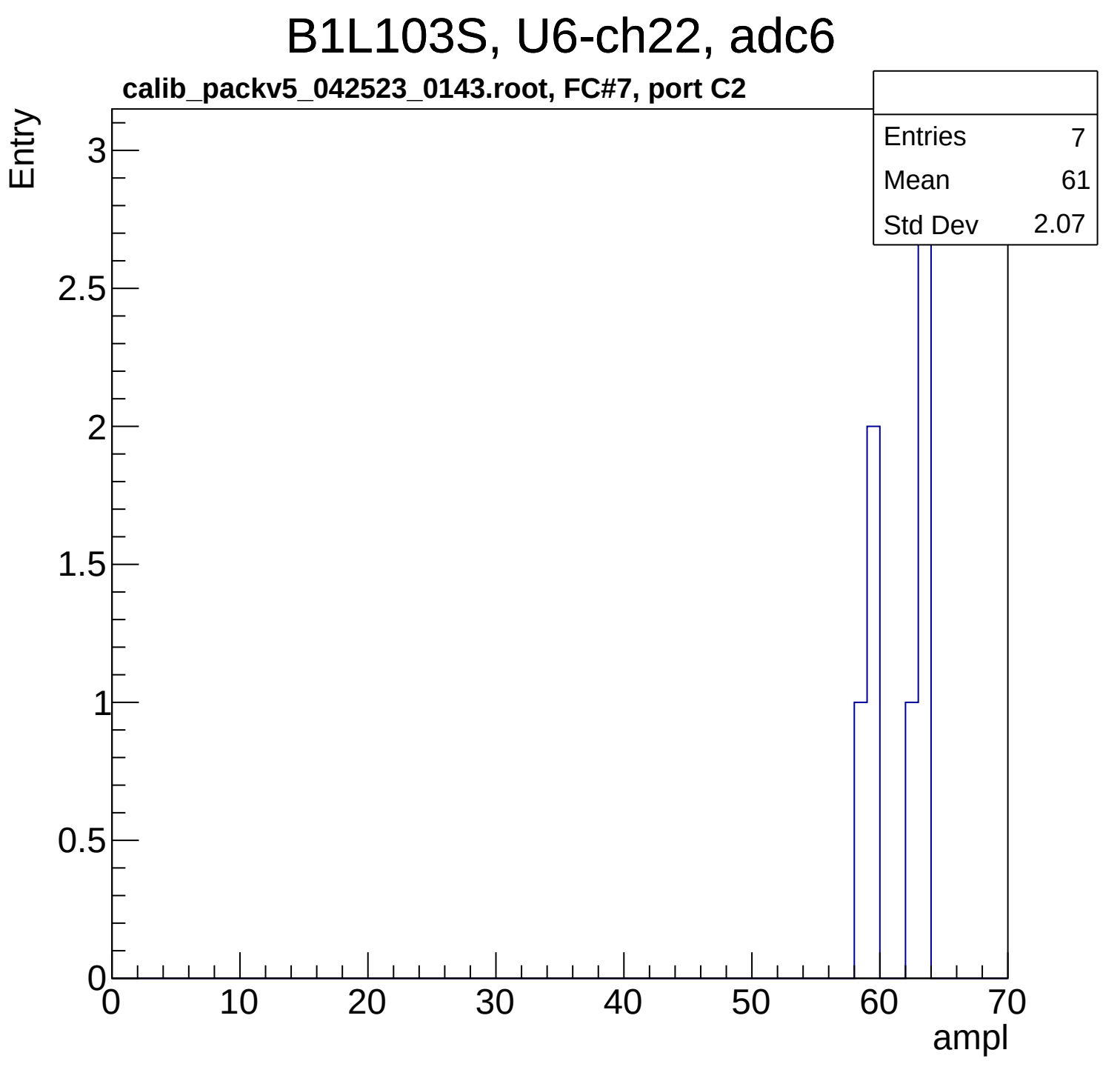
B1L103S, U6-ch22, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61
Std Dev 2.07
Entry
ampl
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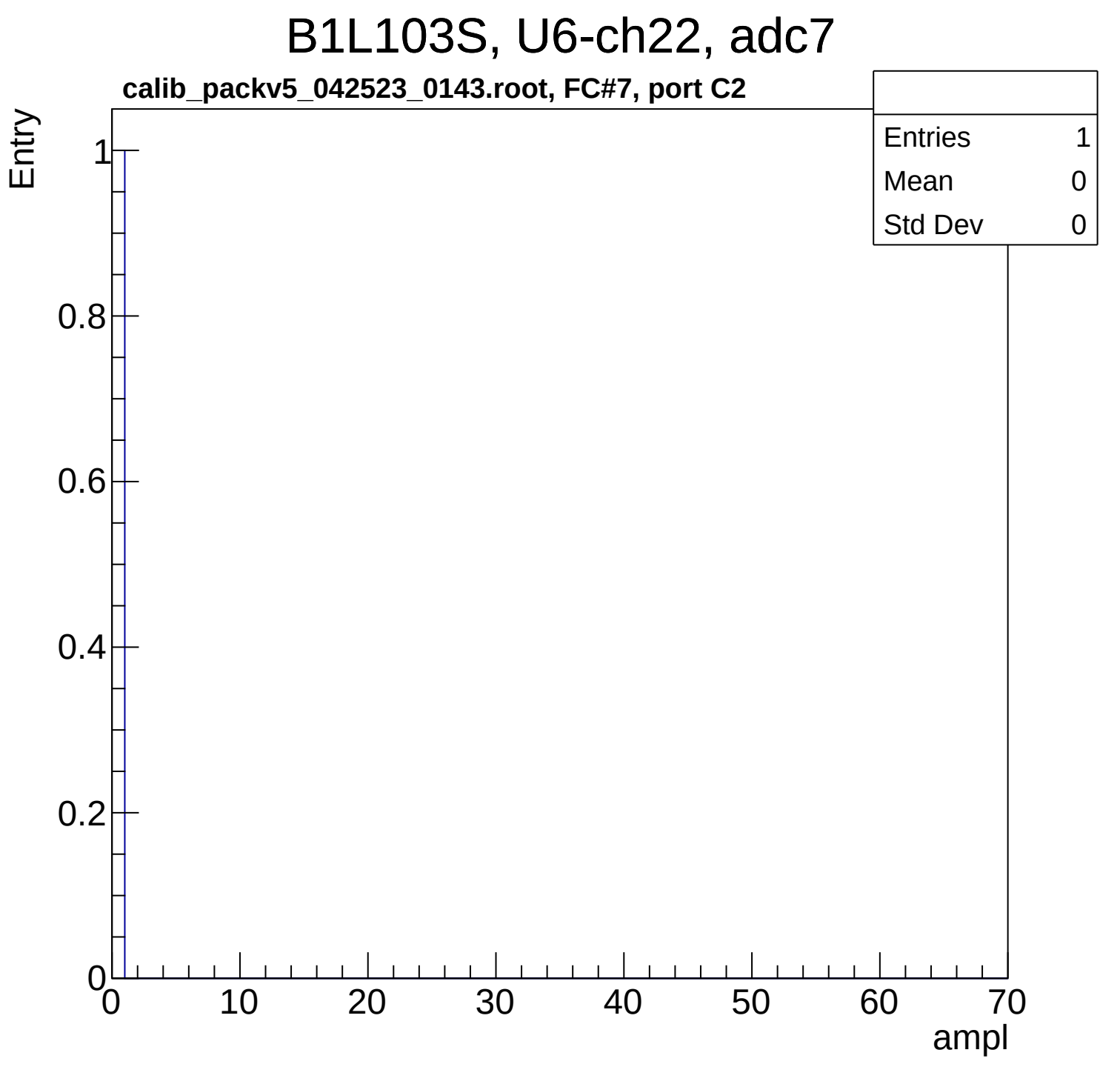
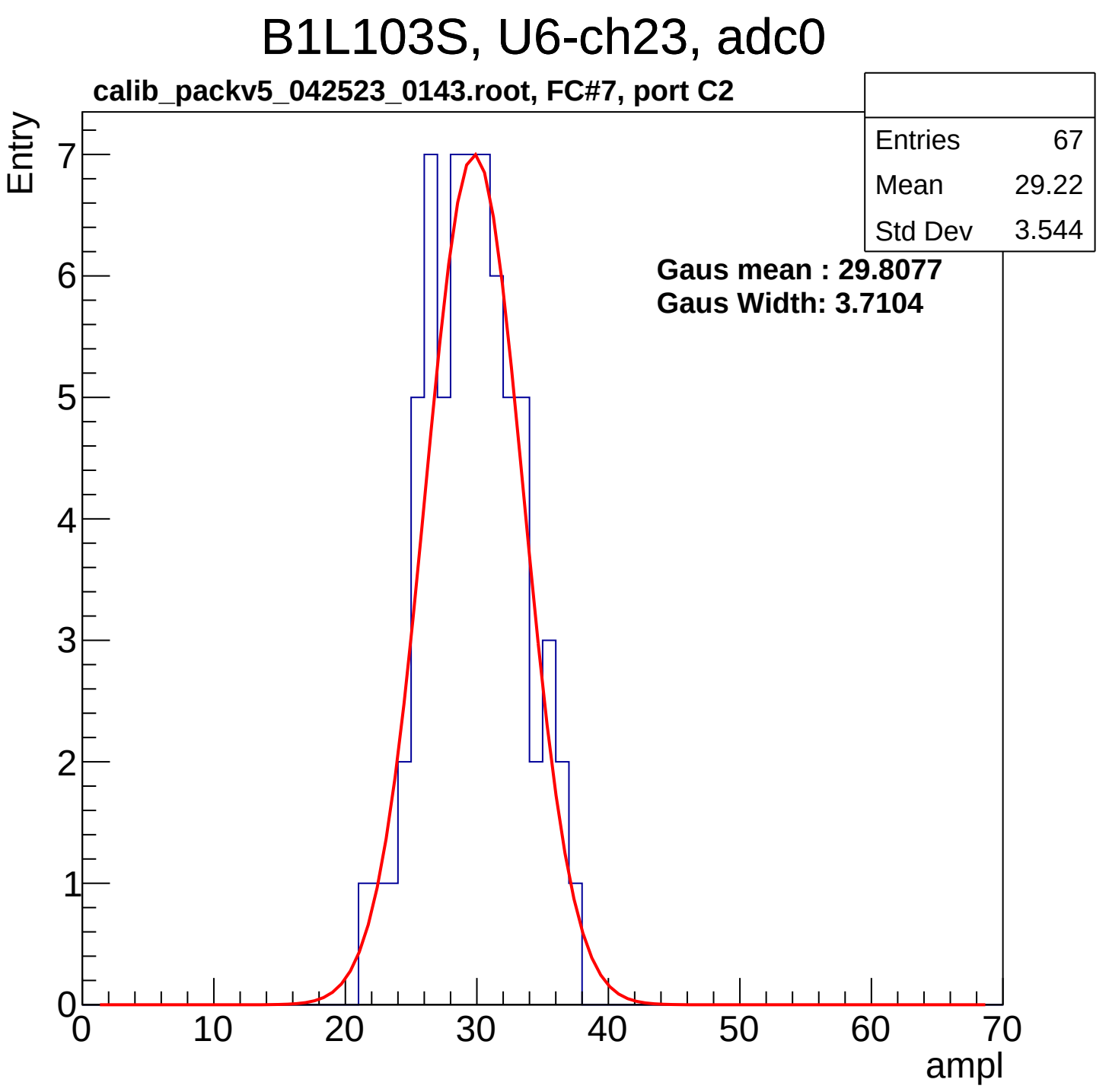
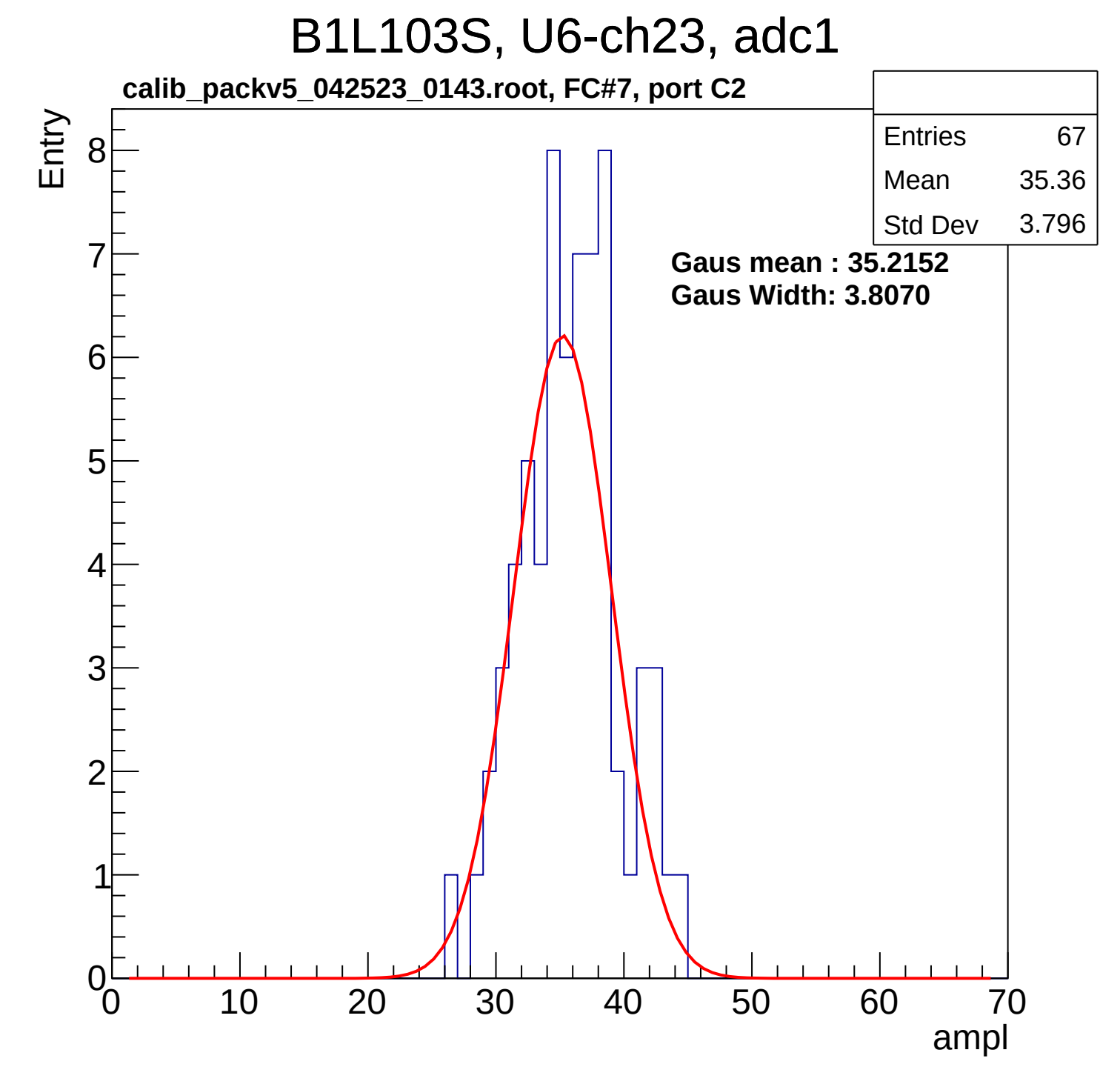
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch22, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch23, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.22
Std Dev 3.544
Gaus mean : 29.8077
Gaus Width: 3.7104
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch23, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.36
Std Dev 3.796
Gaus mean : 35.2152
Gaus Width: 3.8070
Entry
ampl
0
1
2
3
4
5
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7
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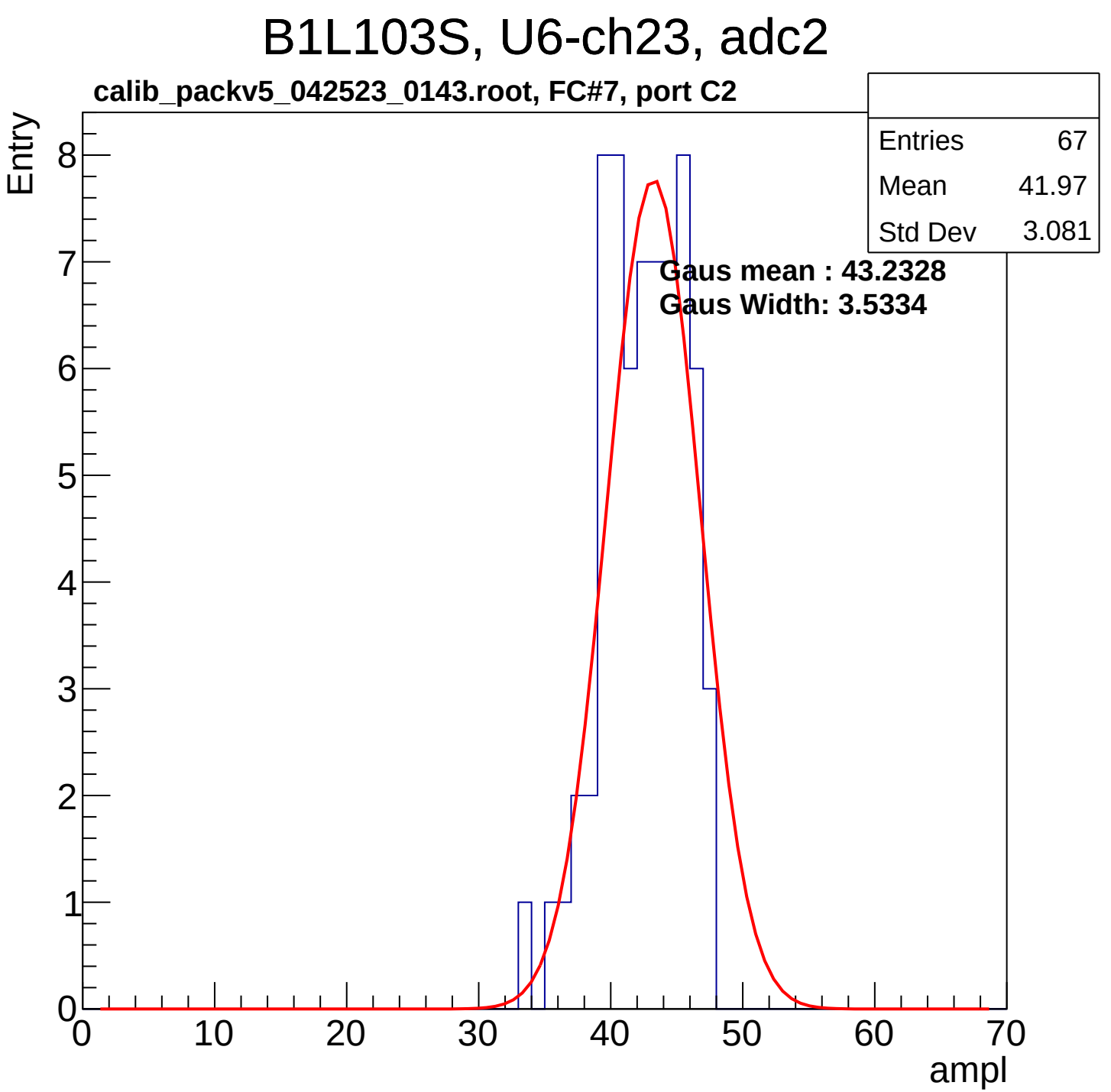
B1L103S, U6-ch23, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 41.97
Std Dev 3.081
Gaus mean : 43.2328
Gaus Width: 3.5334
Entry
ampl
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0
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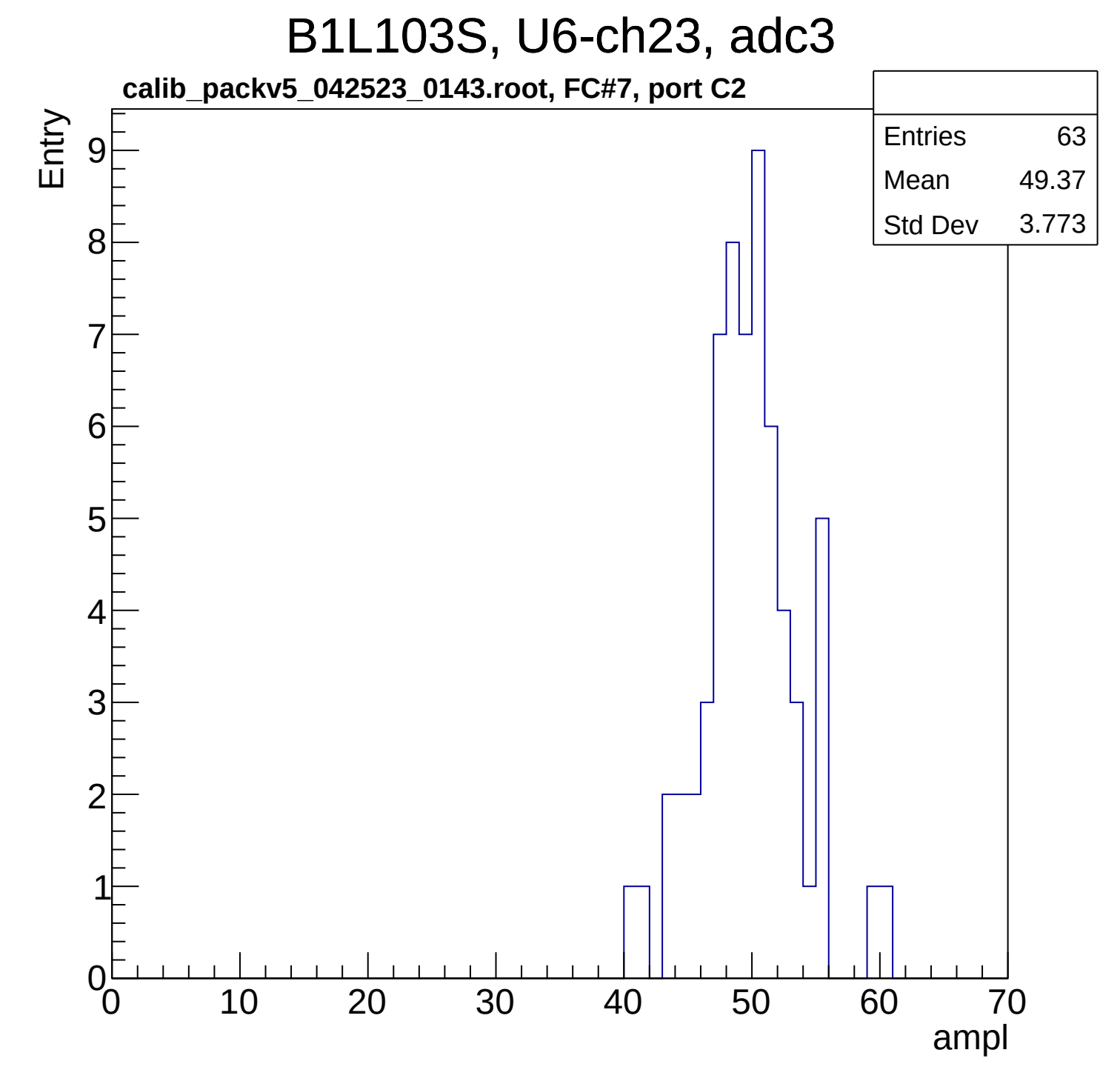
B1L103S, U6-ch23, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.37
Std Dev 3.773
Entry
ampl
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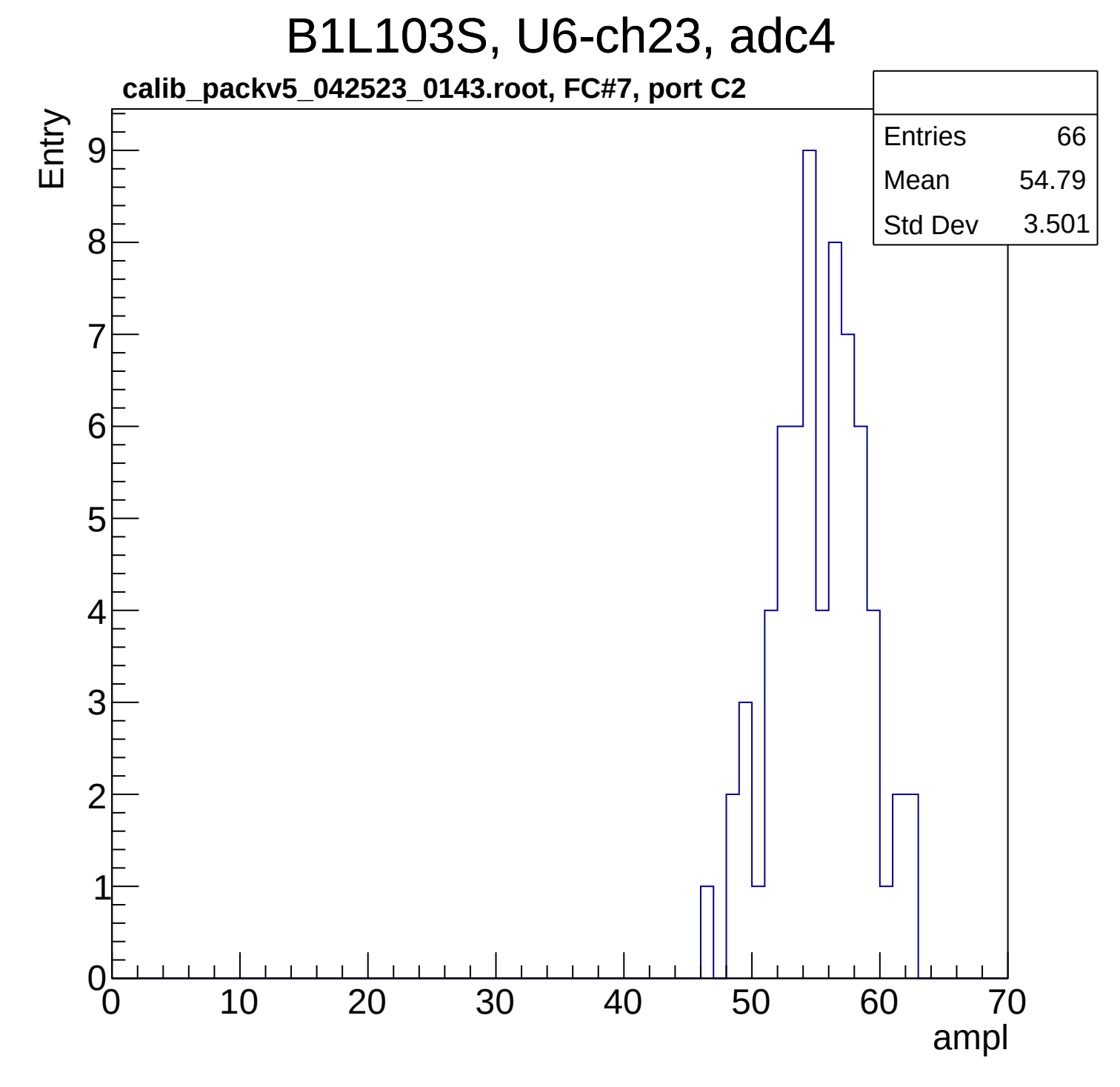
B1L103S, U6-ch23, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 54.79
Std Dev 3.501
Entry
ampl
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2
3
4
5
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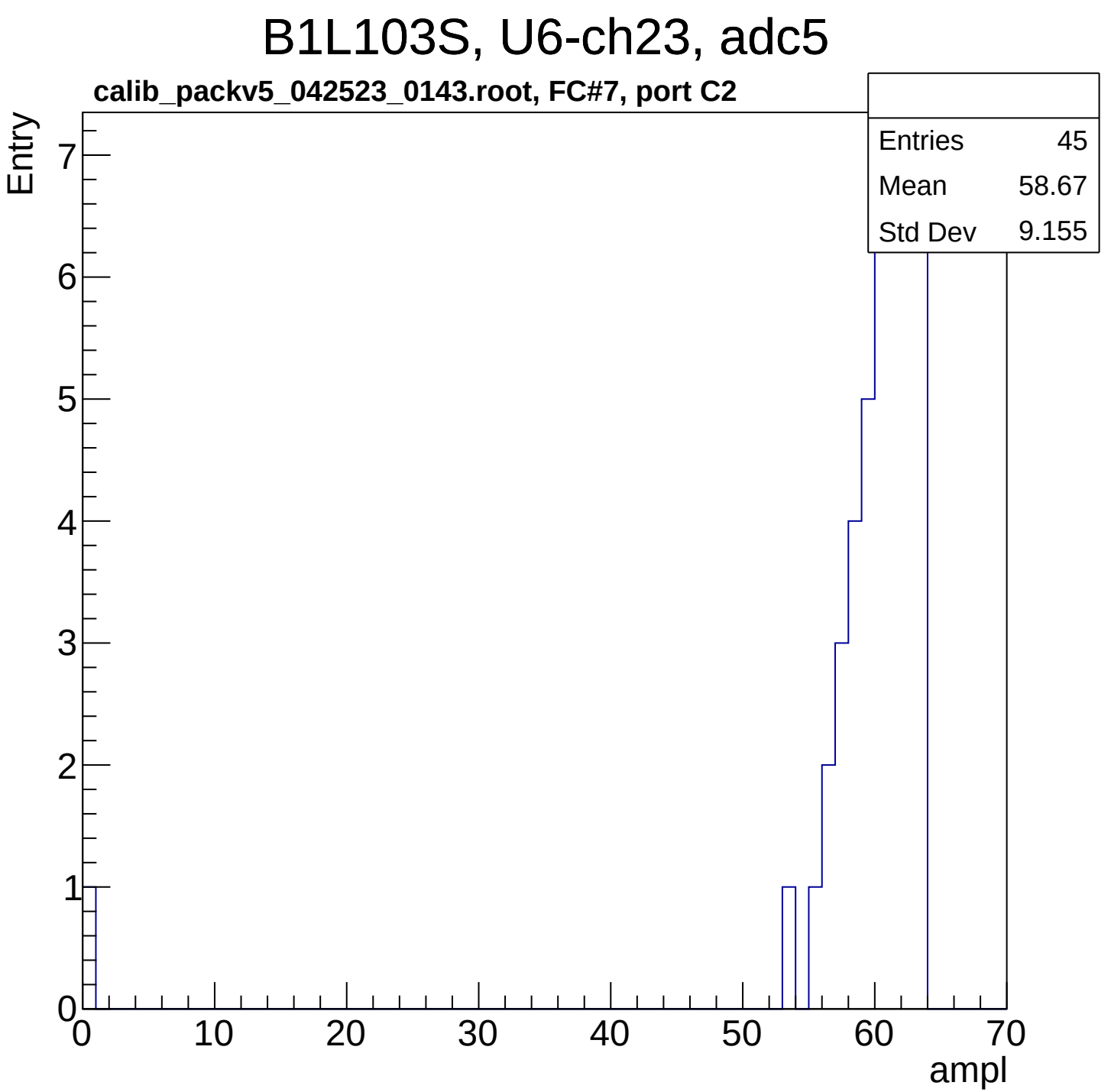
B1L103S, U6-ch23, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.67
Std Dev 9.155
Entry
ampl
0
1
2
3
4
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6
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0
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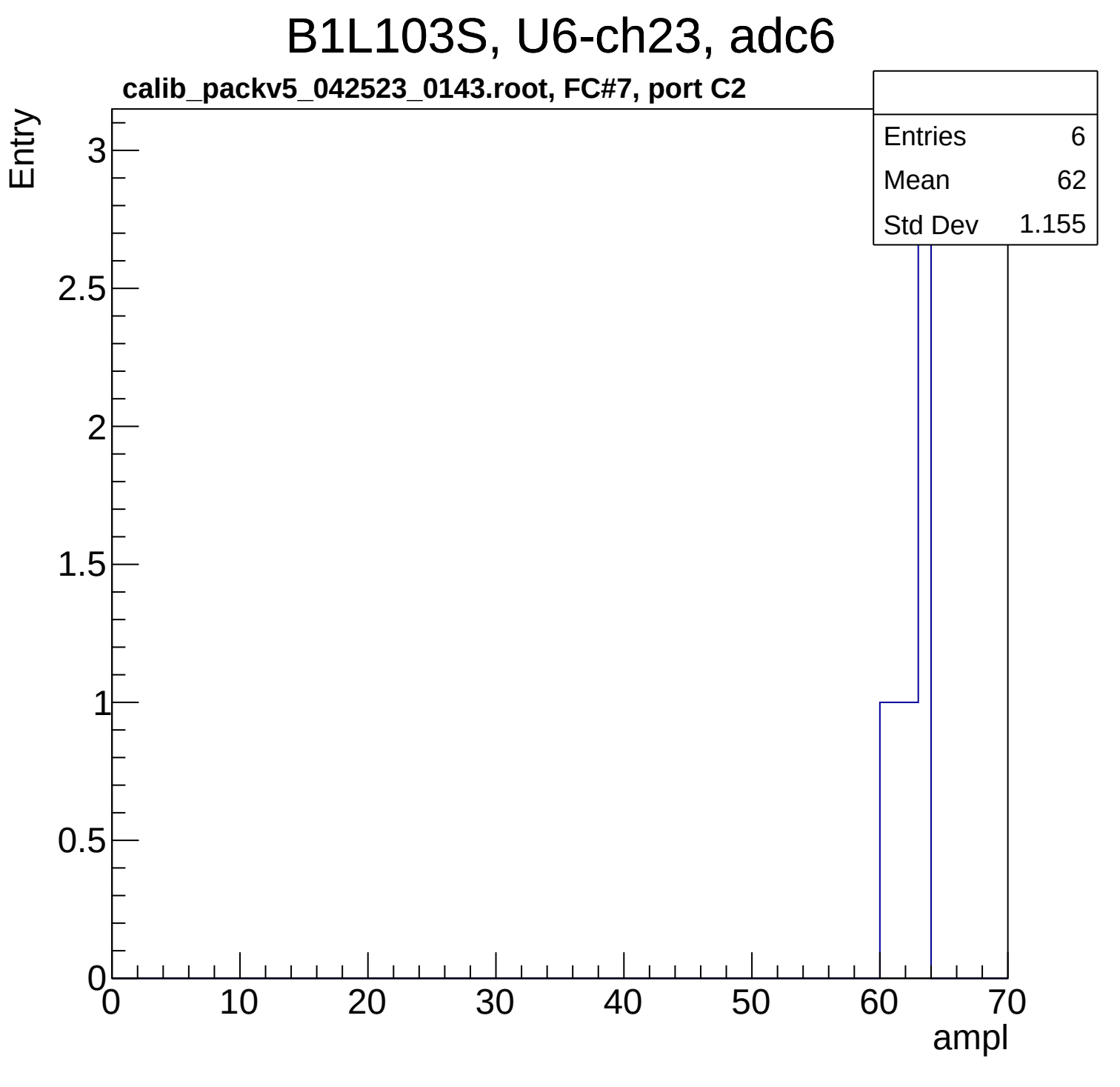
B1L103S, U6-ch23, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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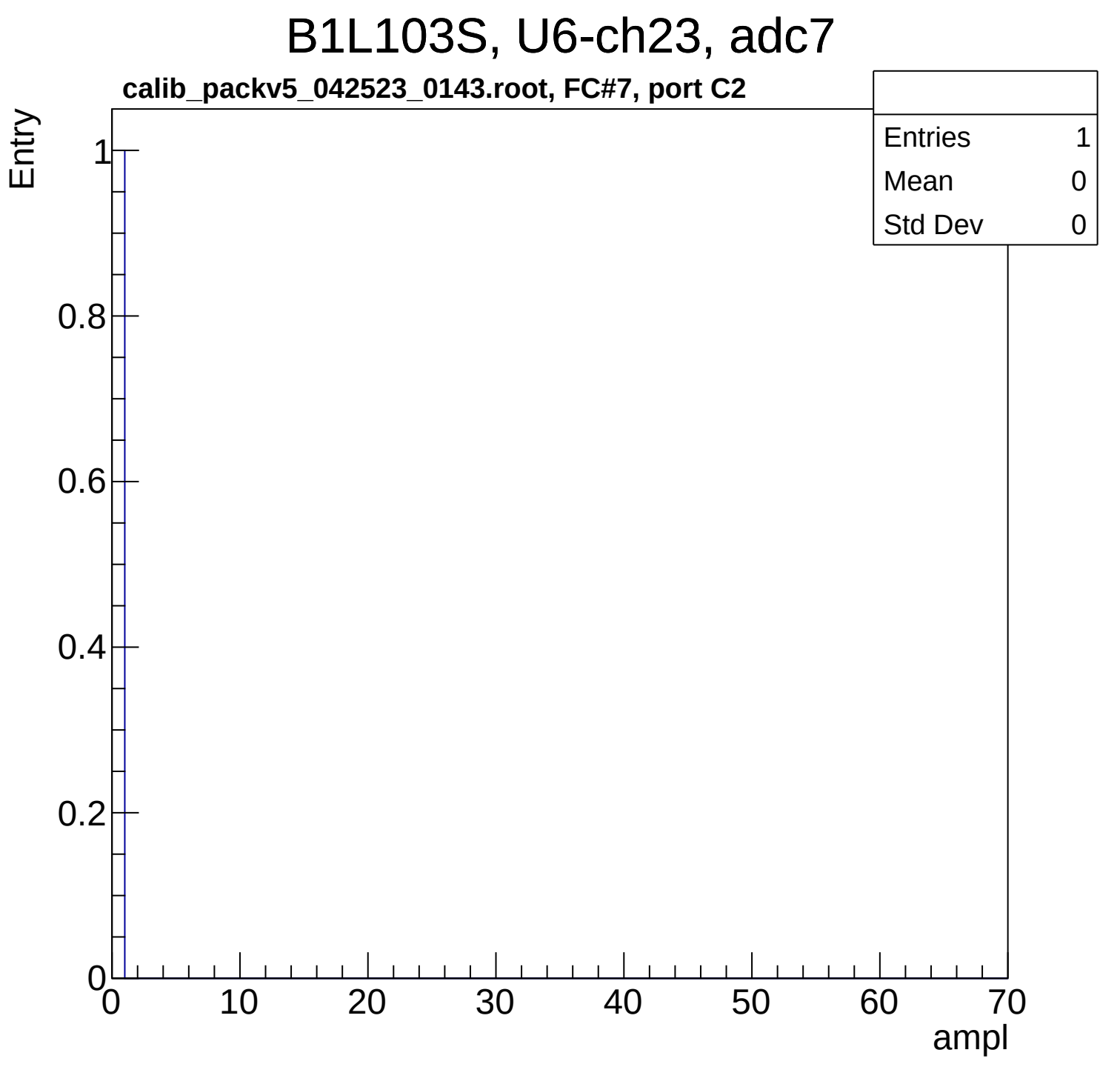
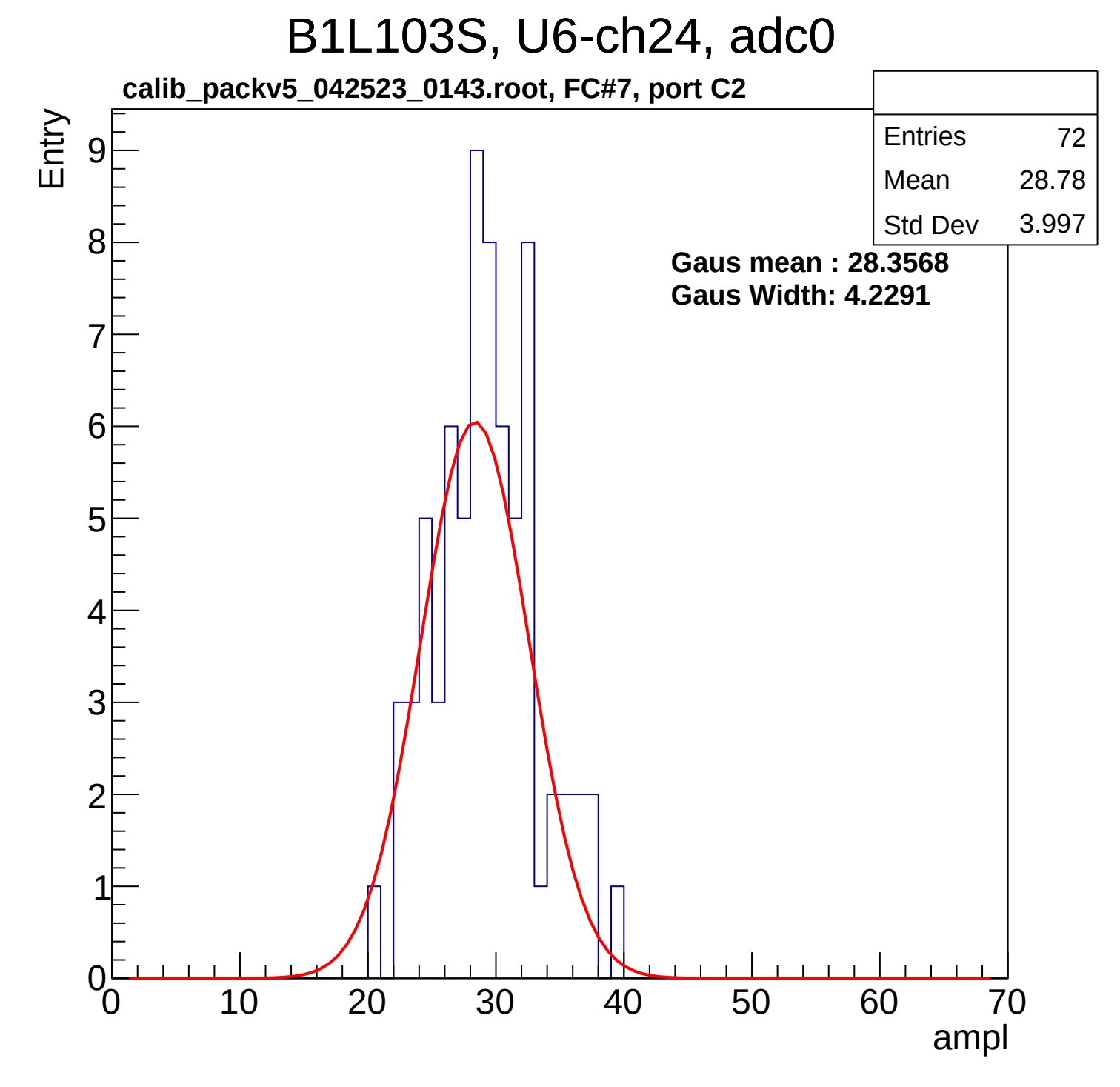

B1L103S, U6-ch23, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch24, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 28.78
Std Dev 3.997
Gaus mean : 28.3568
Gaus Width: 4.2291
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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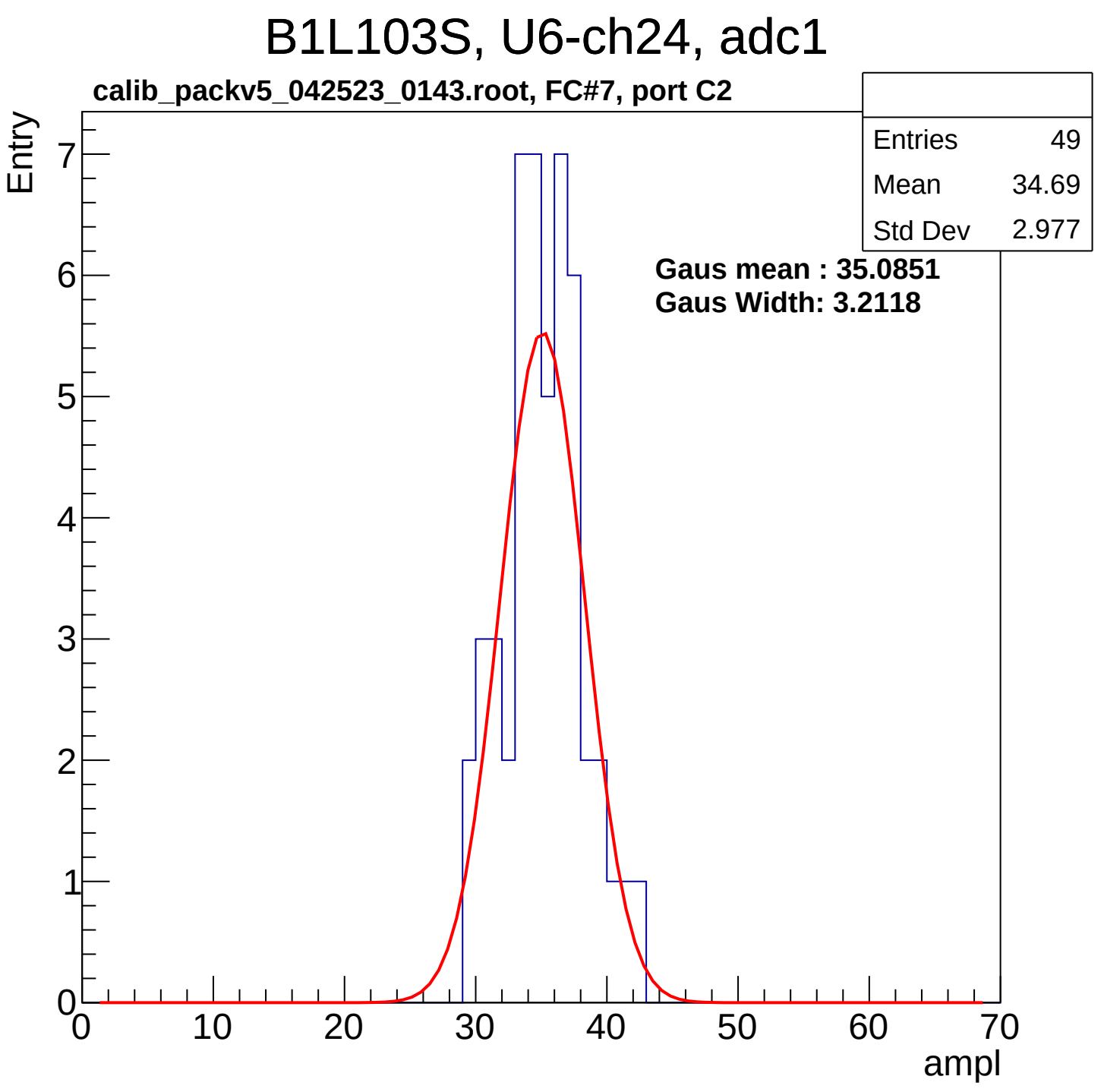
B1L103S, U6-ch24, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 34.69
Std Dev 2.977
Gaus mean : 35.0851
Gaus Width: 3.2118
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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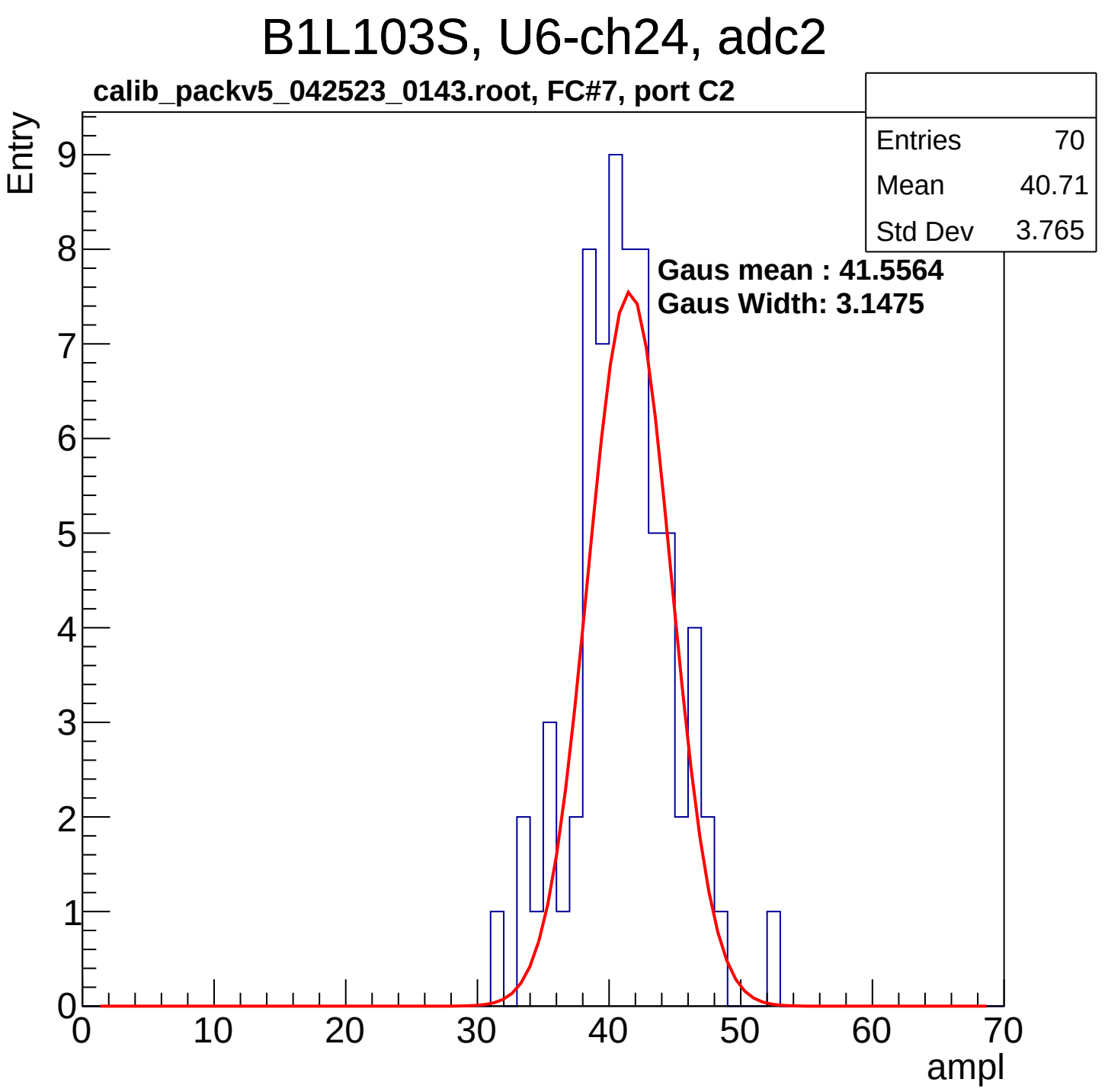
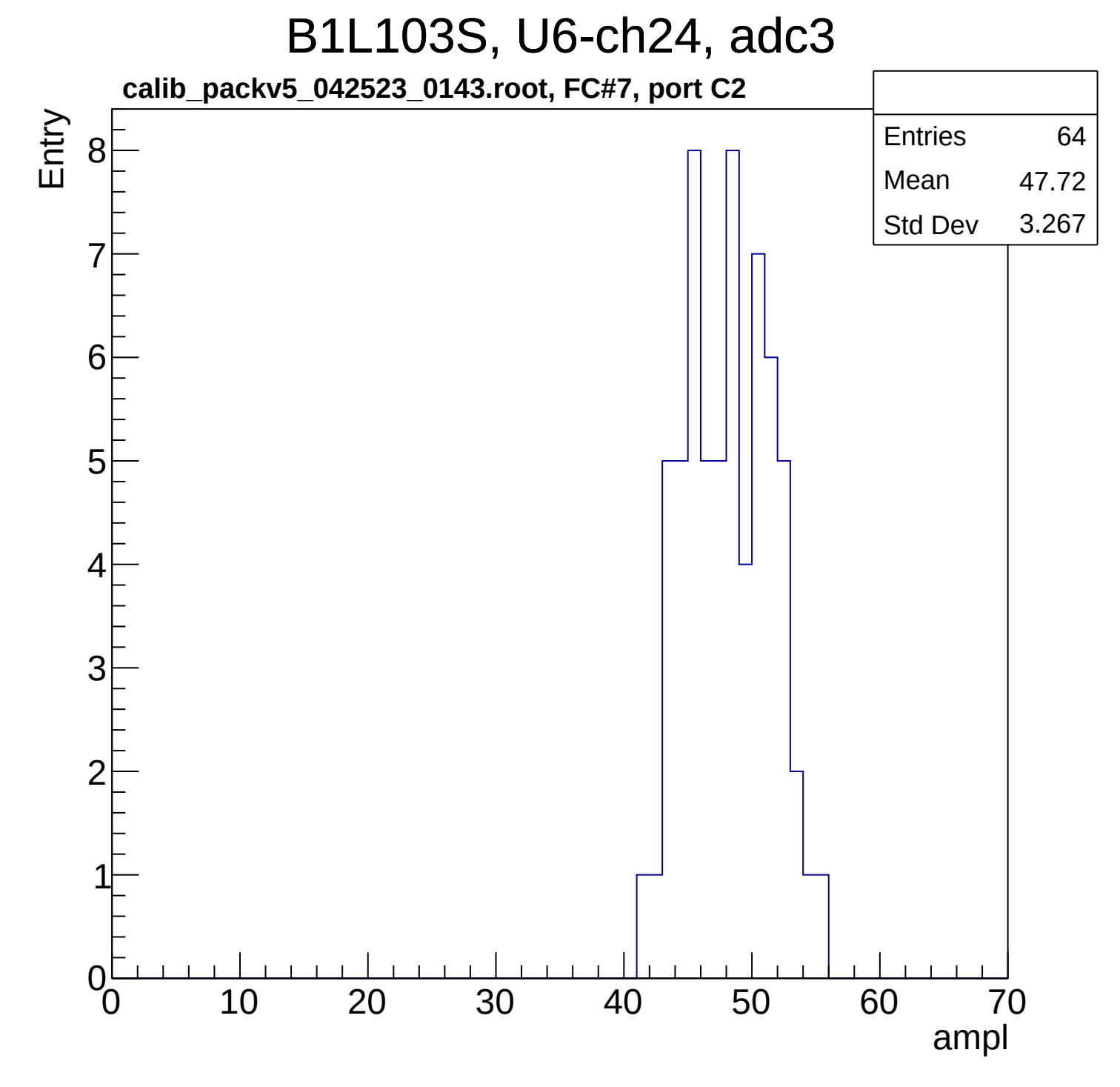

B1L103S, U6-ch24, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 40.71
Std Dev 3.765
Gaus mean : 41.5564
Gaus Width: 3.1475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch24, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.72
Std Dev 3.267
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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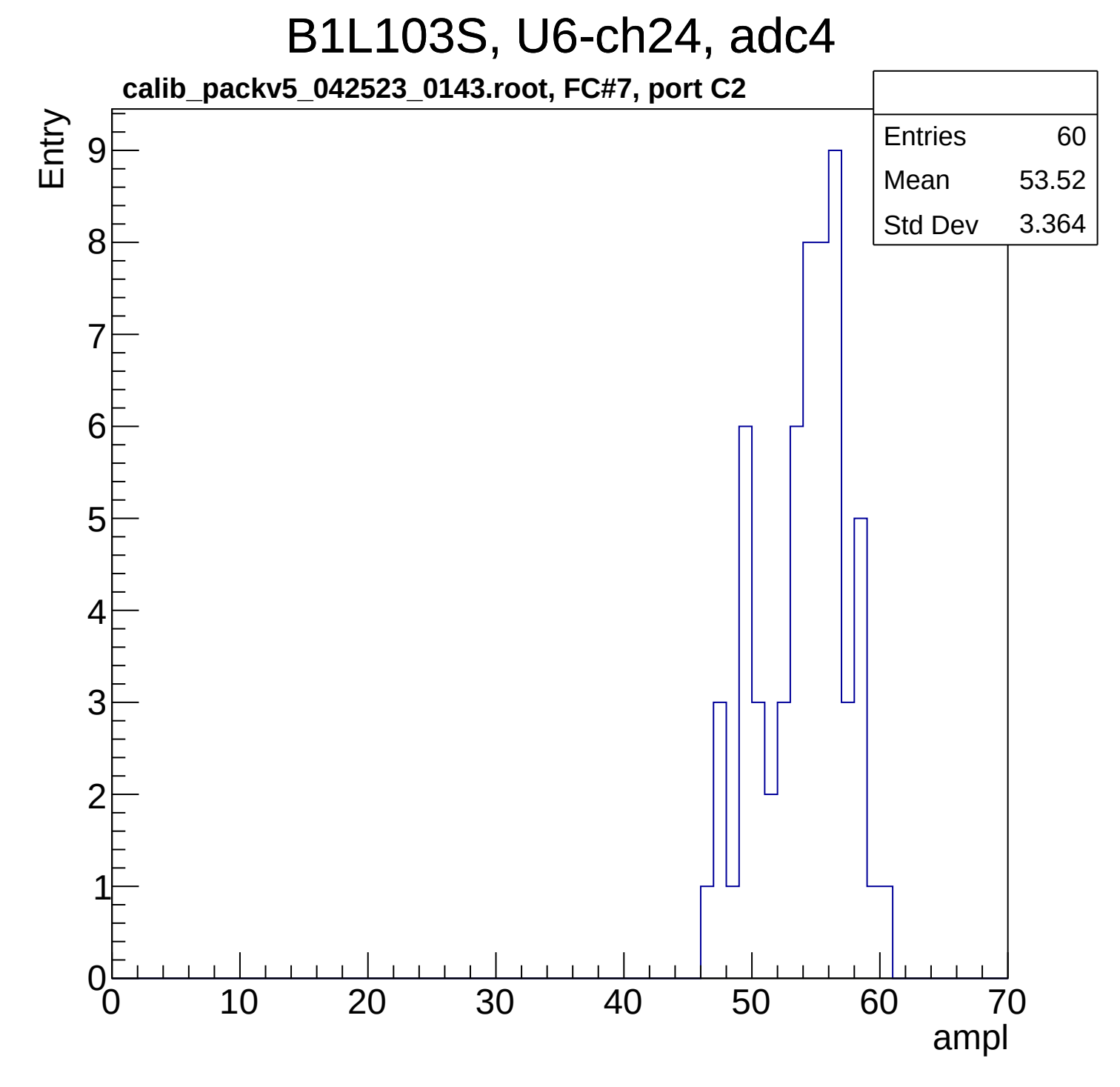
B1L103S, U6-ch24, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 53.52
Std Dev 3.364
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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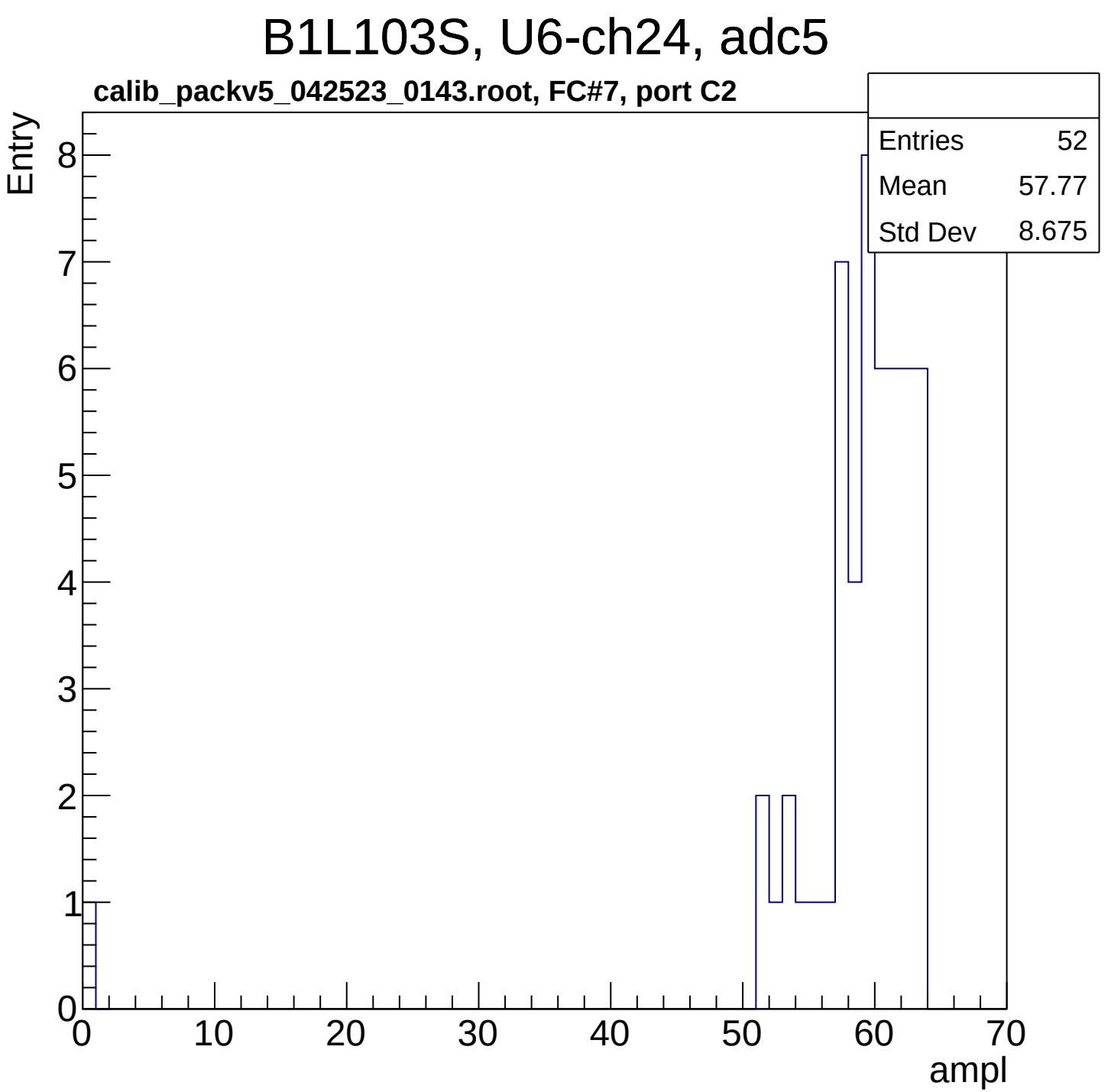
B1L103S, U6-ch24, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.77
Std Dev 8.675
Entry
ampl
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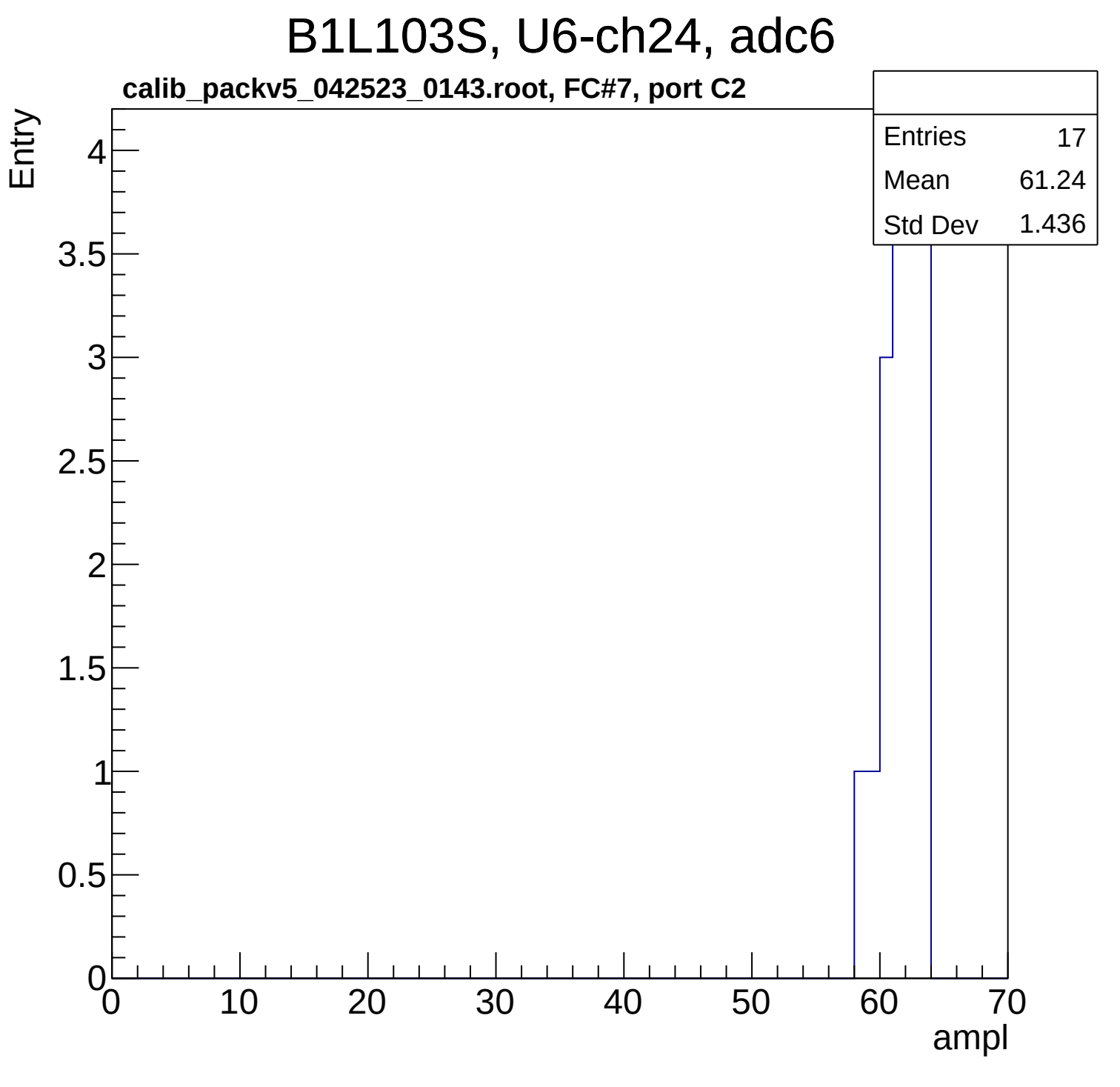
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch24, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 61.24
Std Dev 1.436
Entry
ampl
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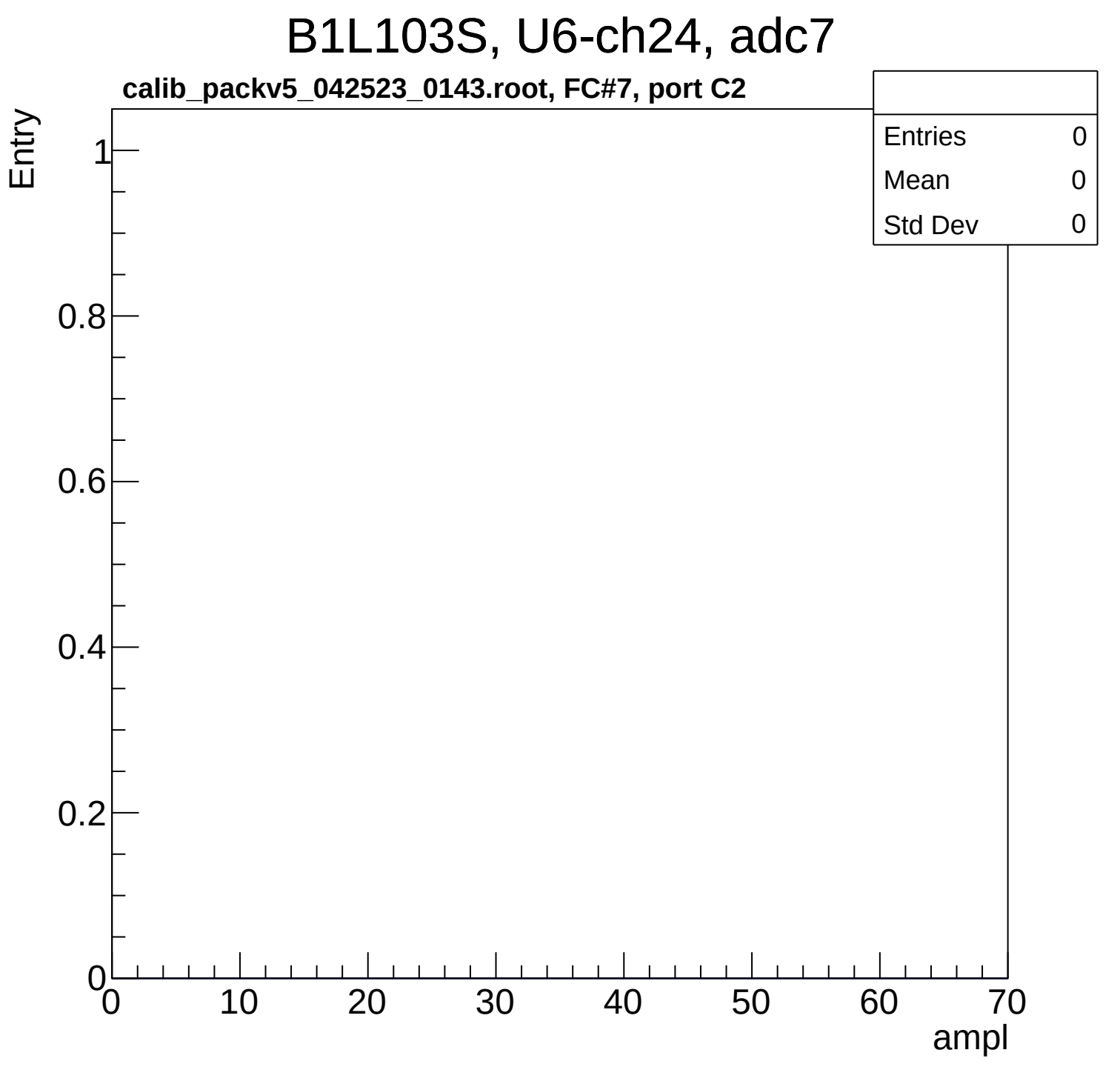
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch24, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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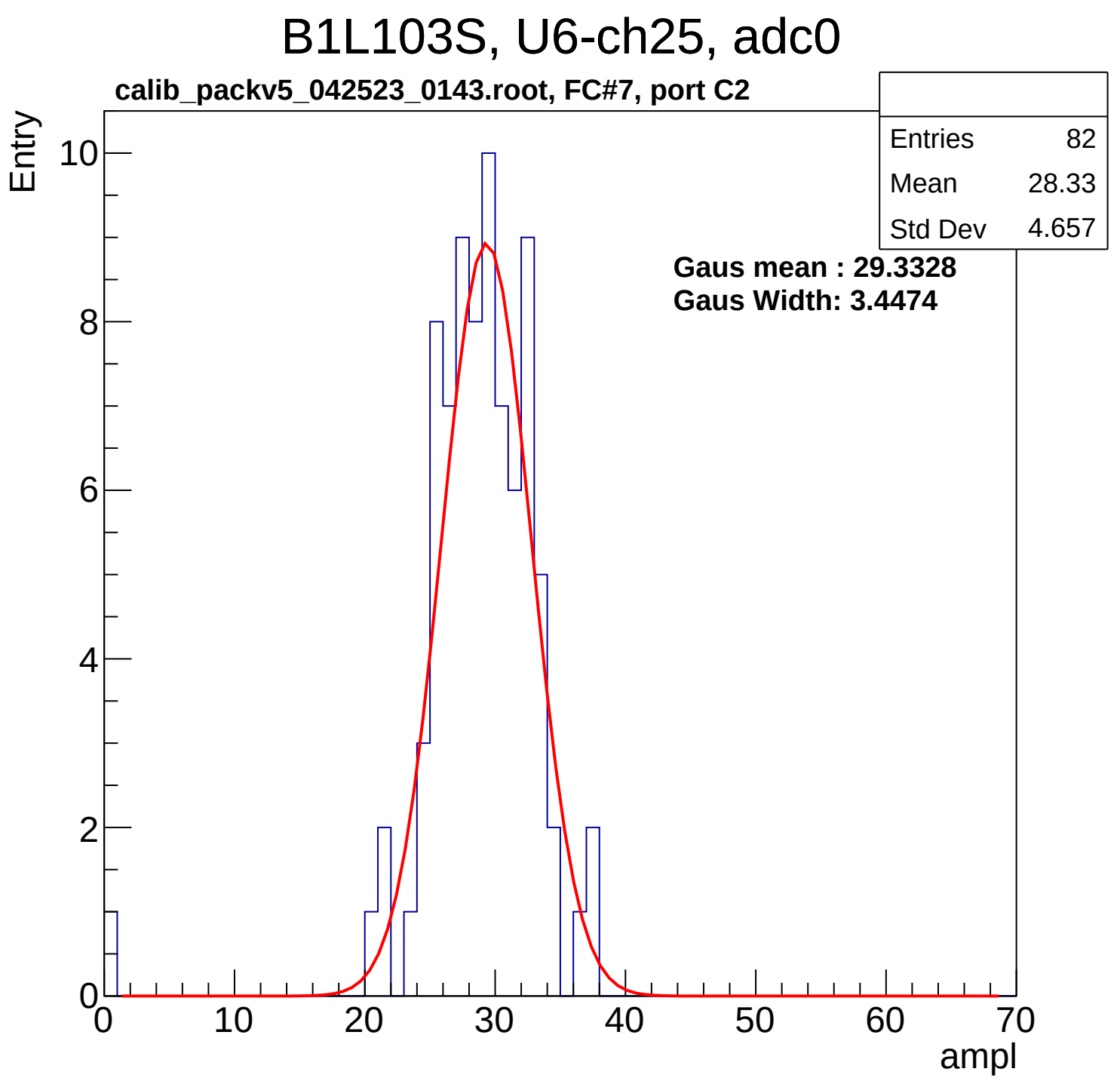
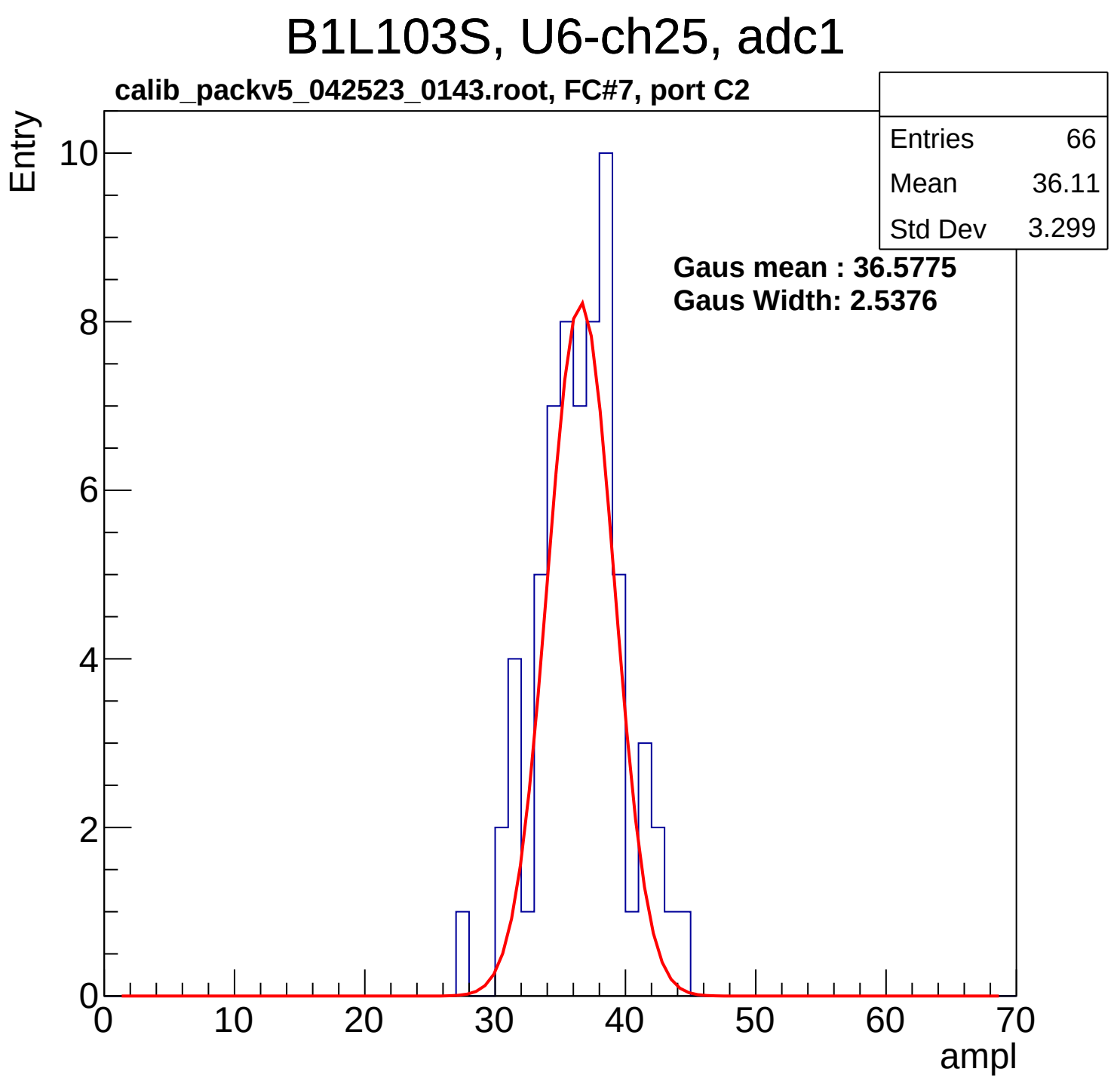
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch25, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 28.33
Std Dev 4.657
Gaus mean : 29.3328
Gaus Width: 3.4474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch25, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 36.11
Std Dev 3.299
Gaus mean : 36.5775
Gaus Width: 2.5376
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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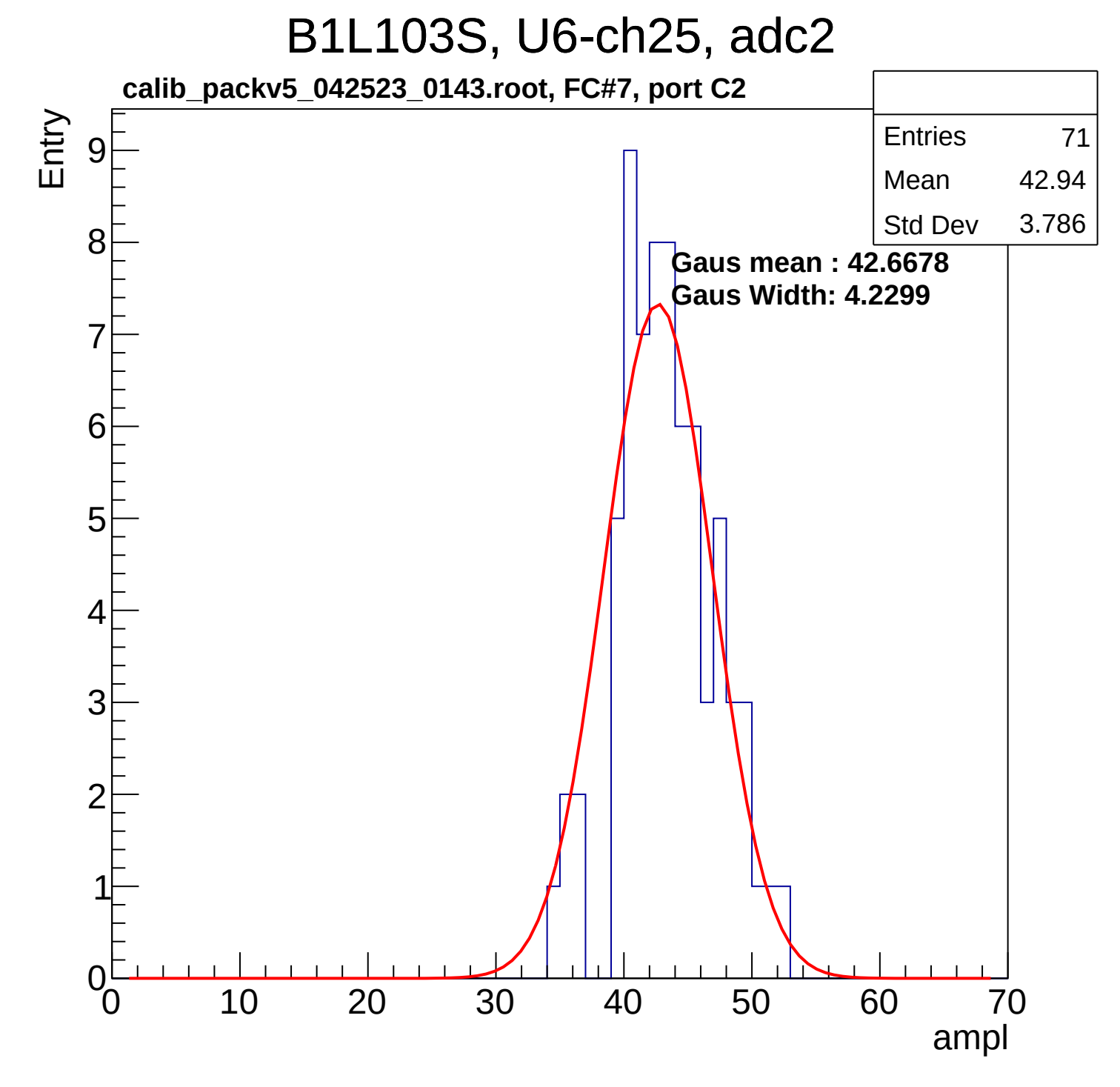
B1L103S, U6-ch25, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.94
Std Dev 3.786
Gaus mean : 42.6678
Gaus Width: 4.2299
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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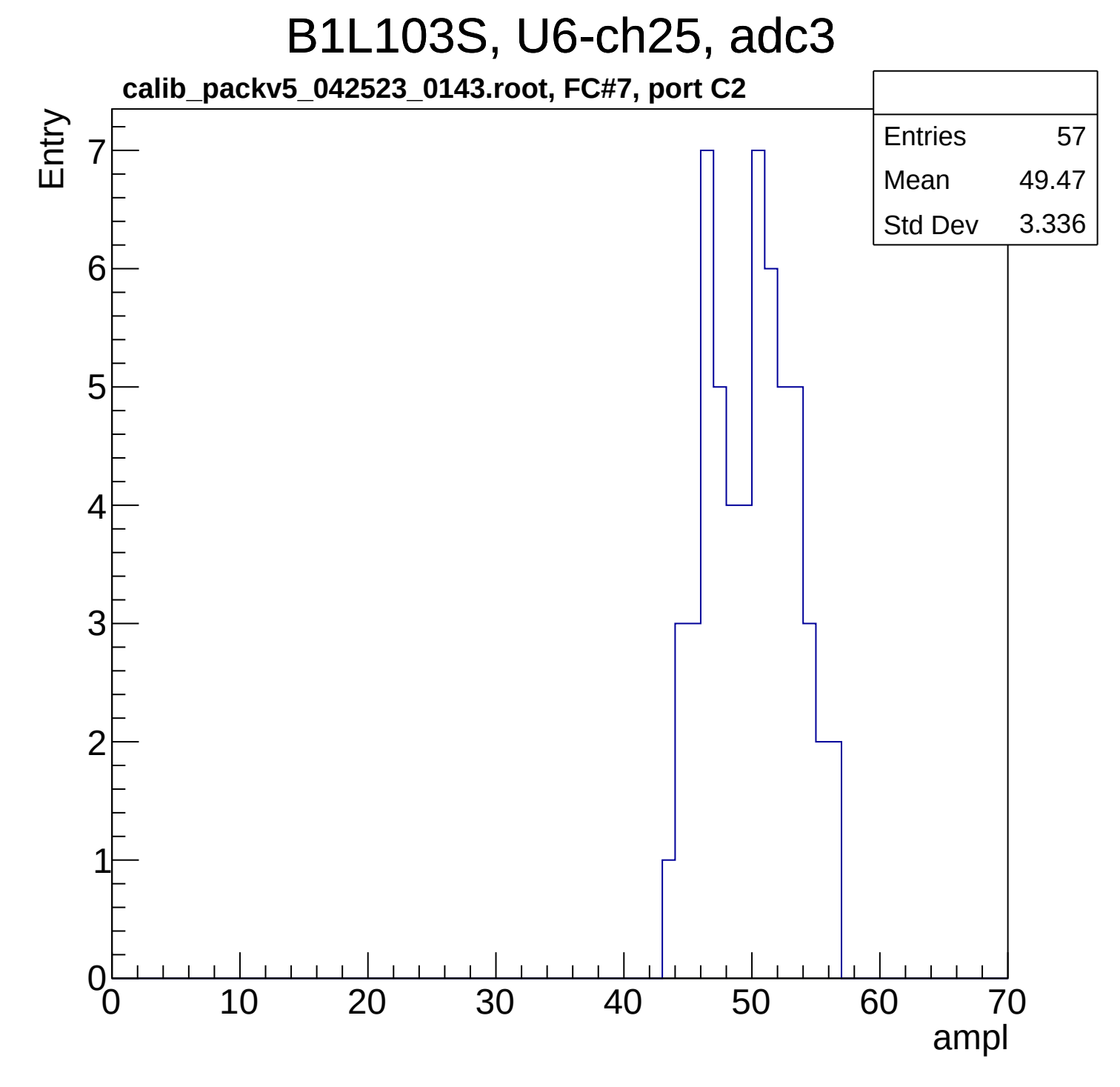
B1L103S, U6-ch25, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 49.47
Std Dev 3.336
Entry
ampl
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1
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3
4
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6
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0
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20
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40
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60
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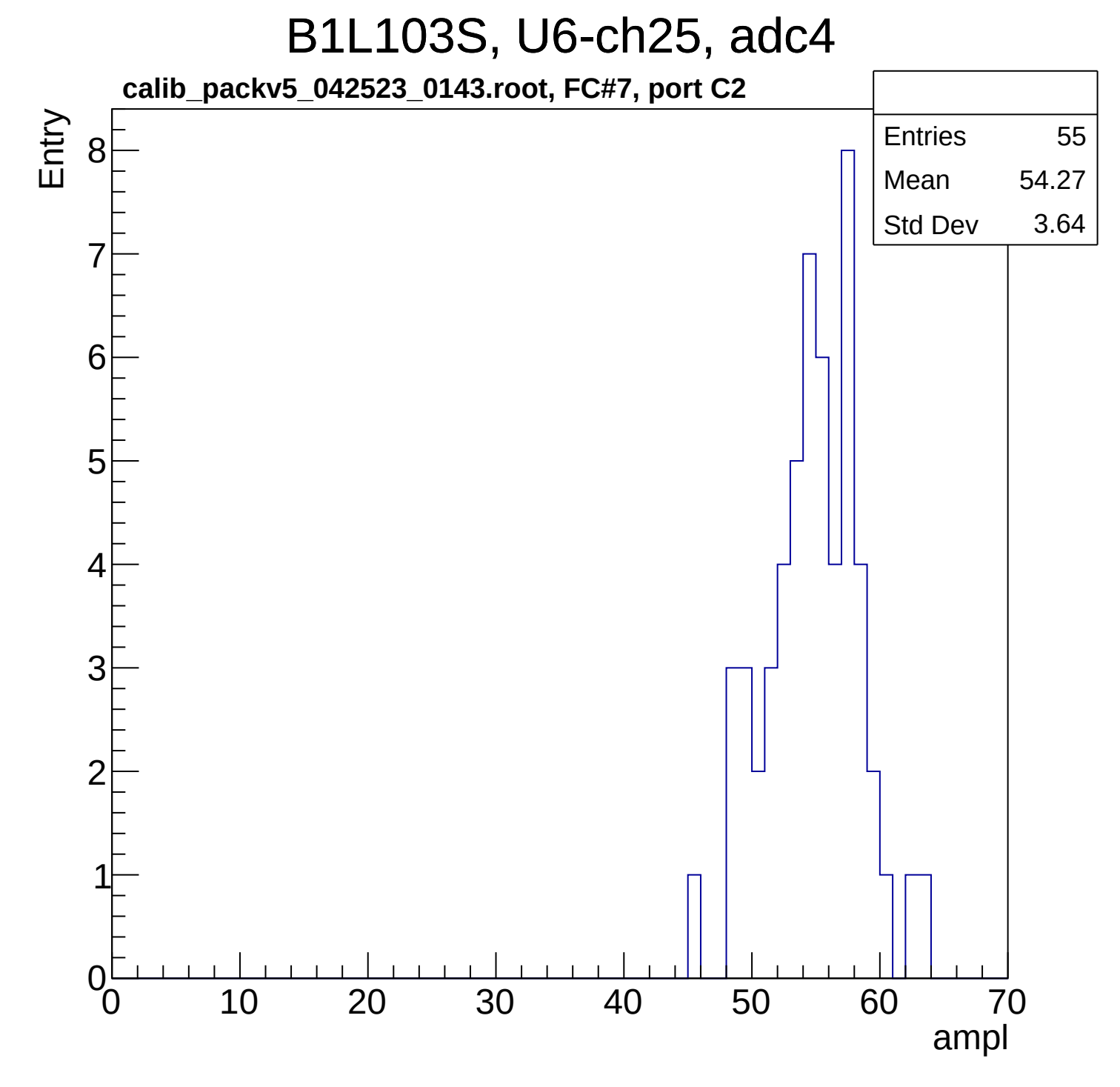
B1L103S, U6-ch25, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.27
Std Dev 3.64
Entry
ampl
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2
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4
5
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7
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0
10
20
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40
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60
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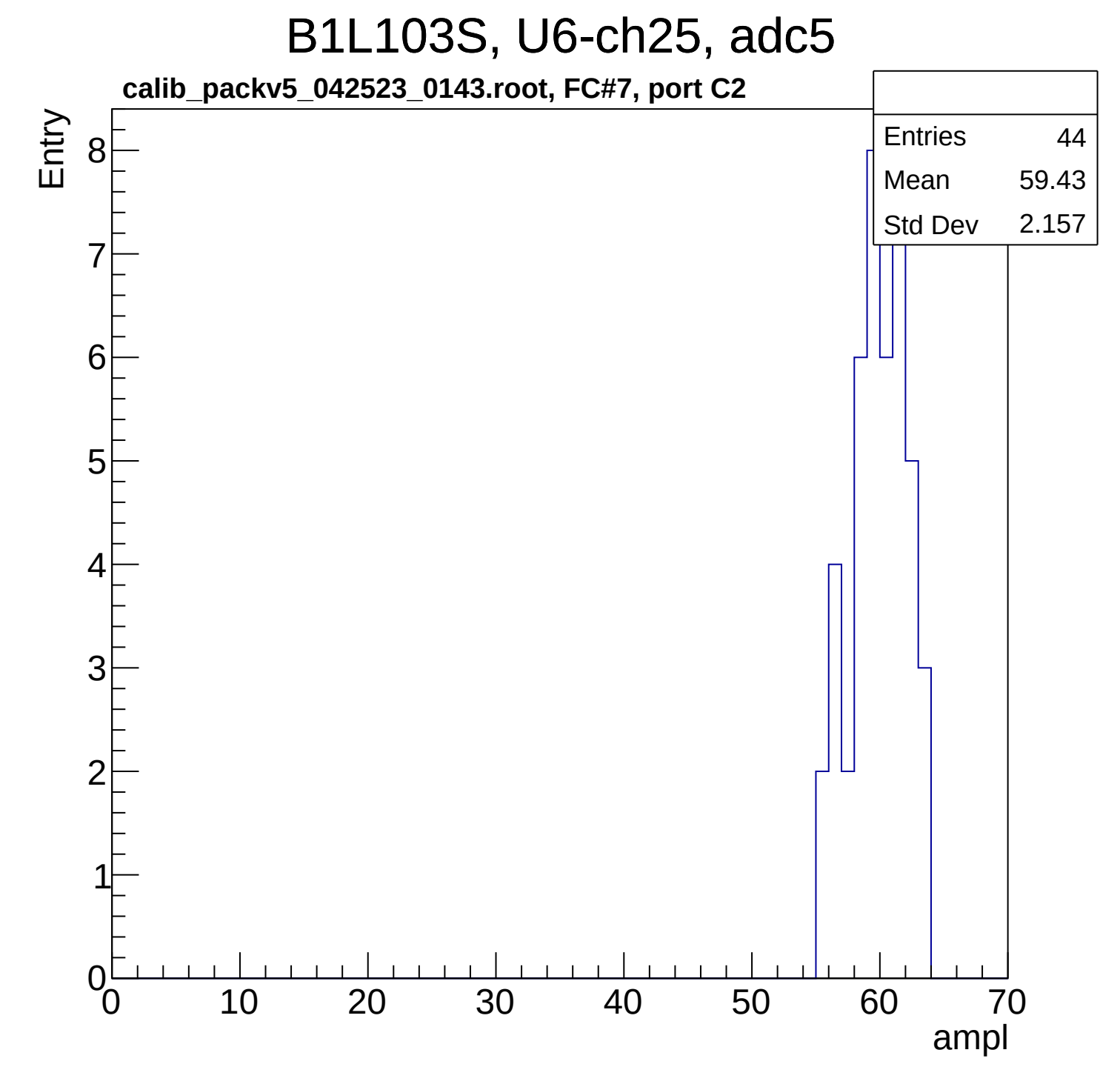
B1L103S, U6-ch25, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.43
Std Dev 2.157
Entry
ampl
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4
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6
7
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0
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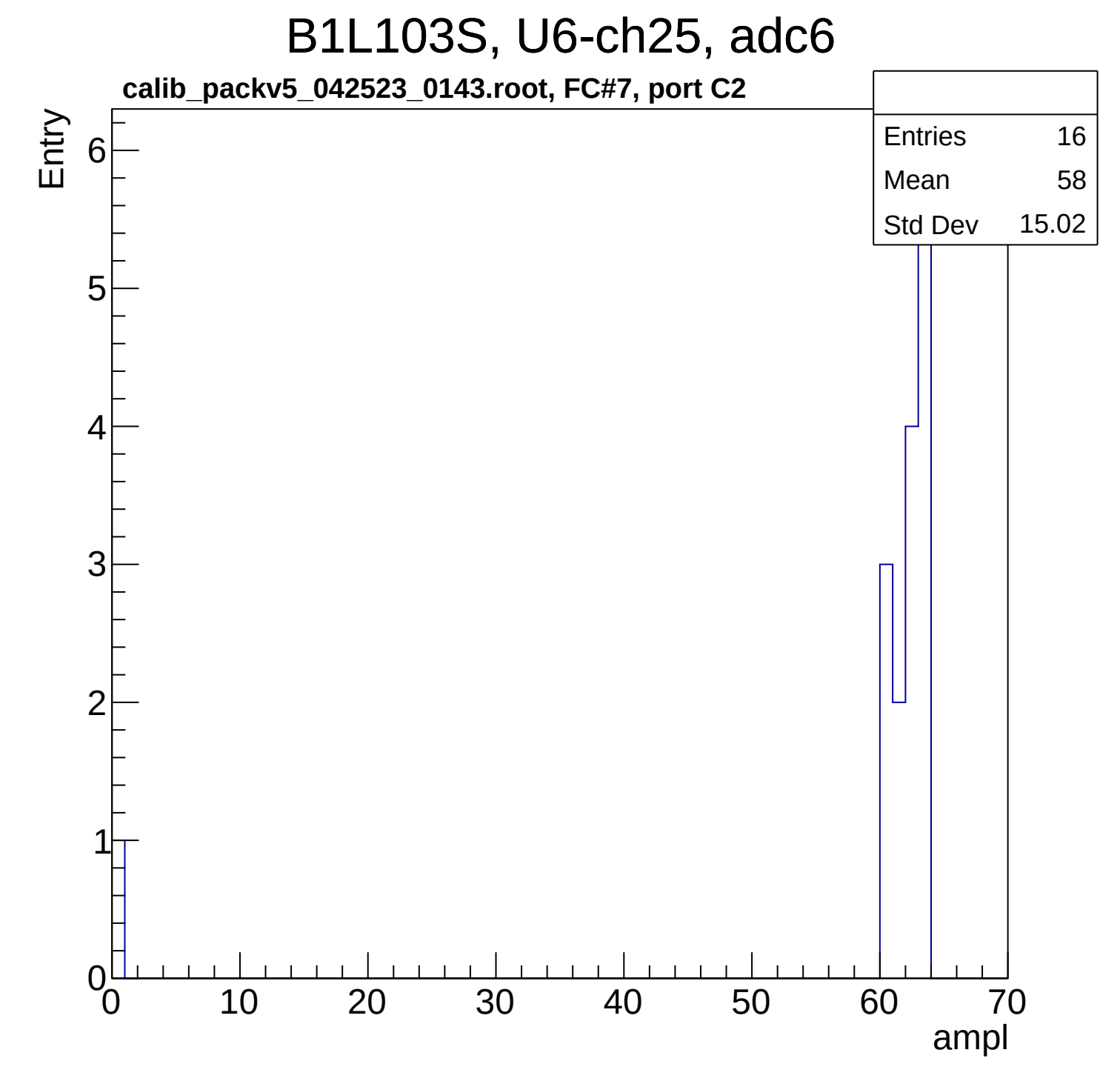
B1L103S, U6-ch25, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 58
Std Dev 15.02
Entry
ampl
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0
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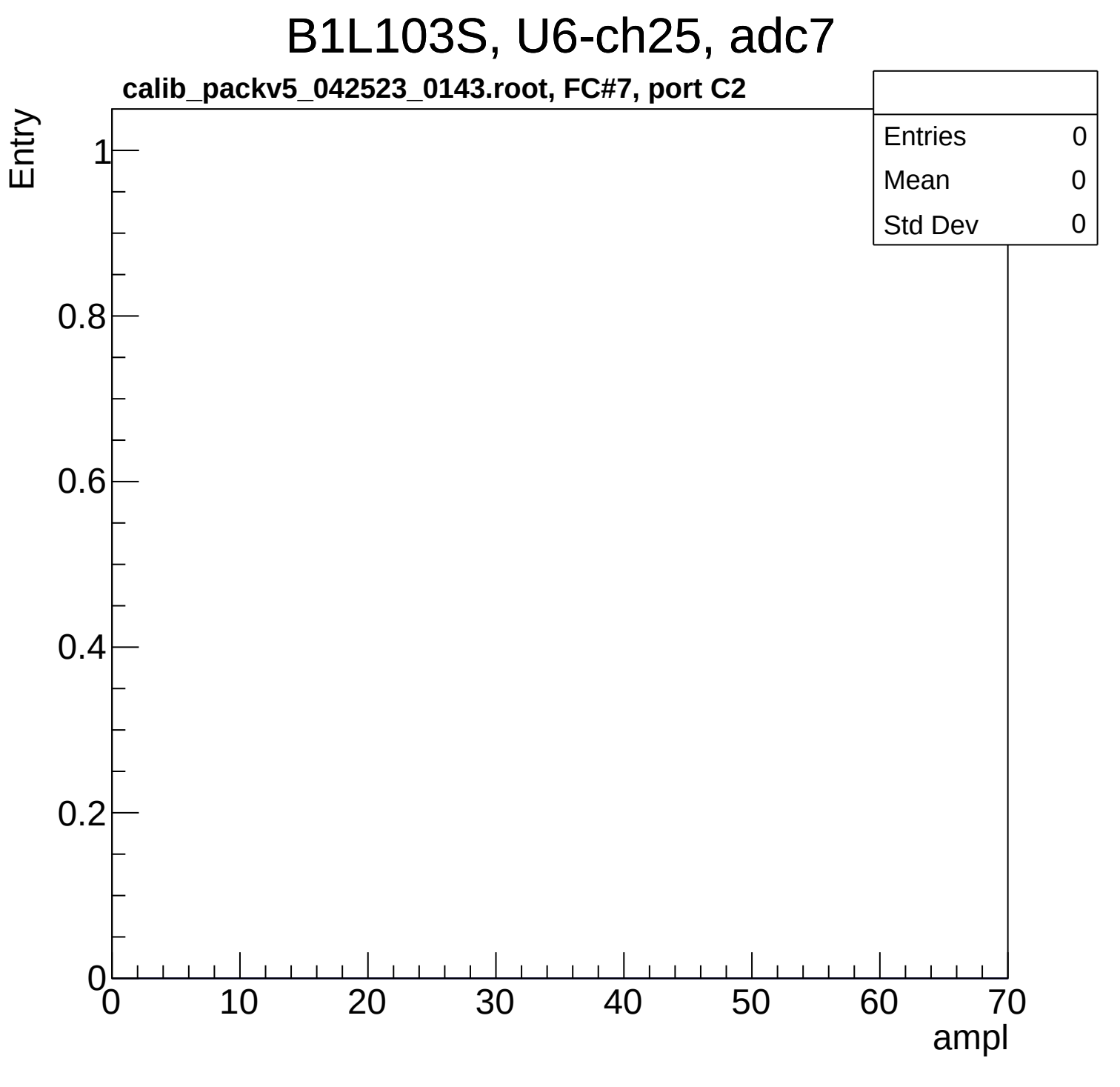
60
70


B1L103S, U6-ch25, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
20
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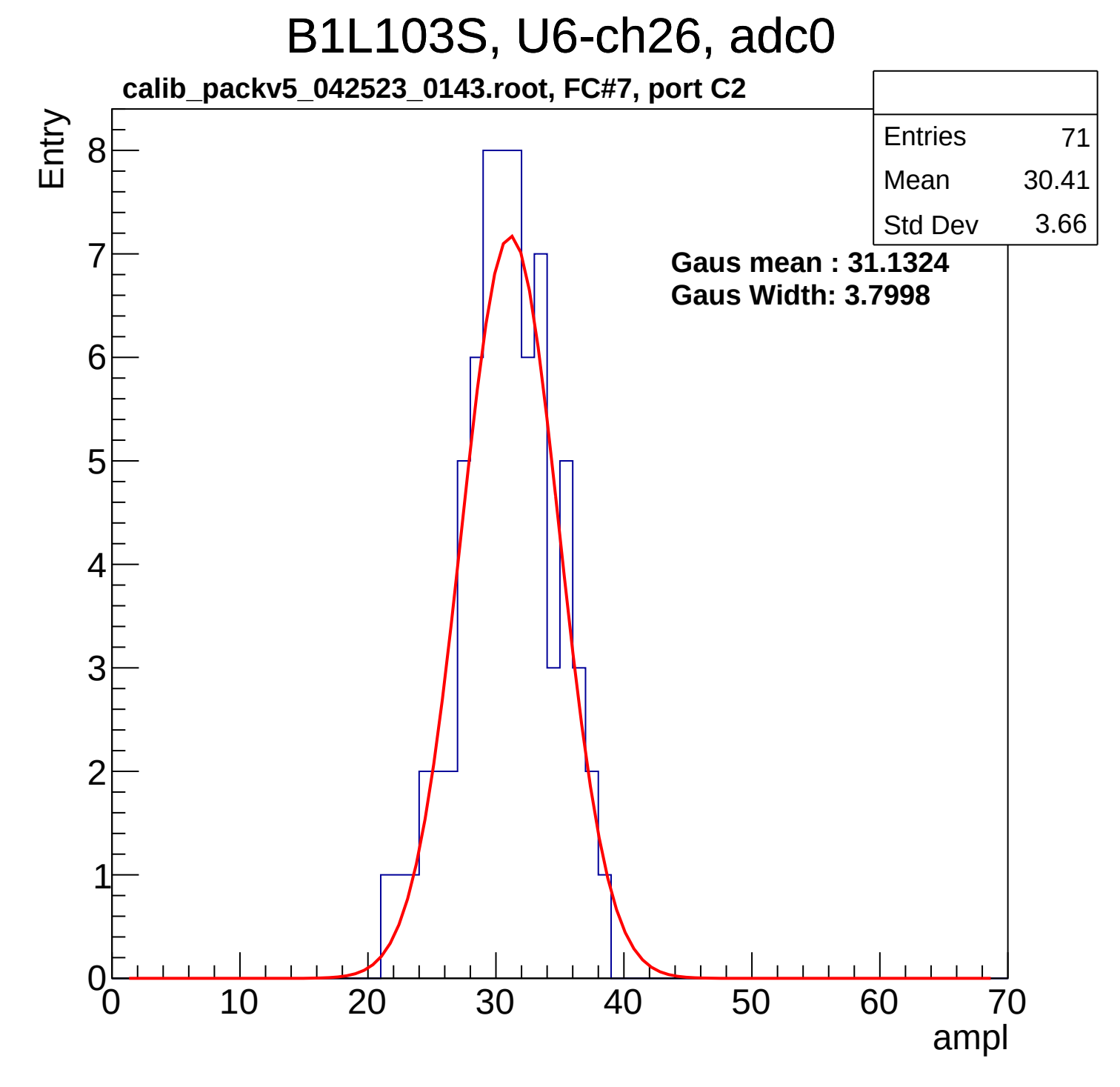
40
50
60
70
ampl


B1L103S, U6-ch26, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 30.41
Std Dev 3.66
Gaus mean : 31.1324
Gaus Width: 3.7998
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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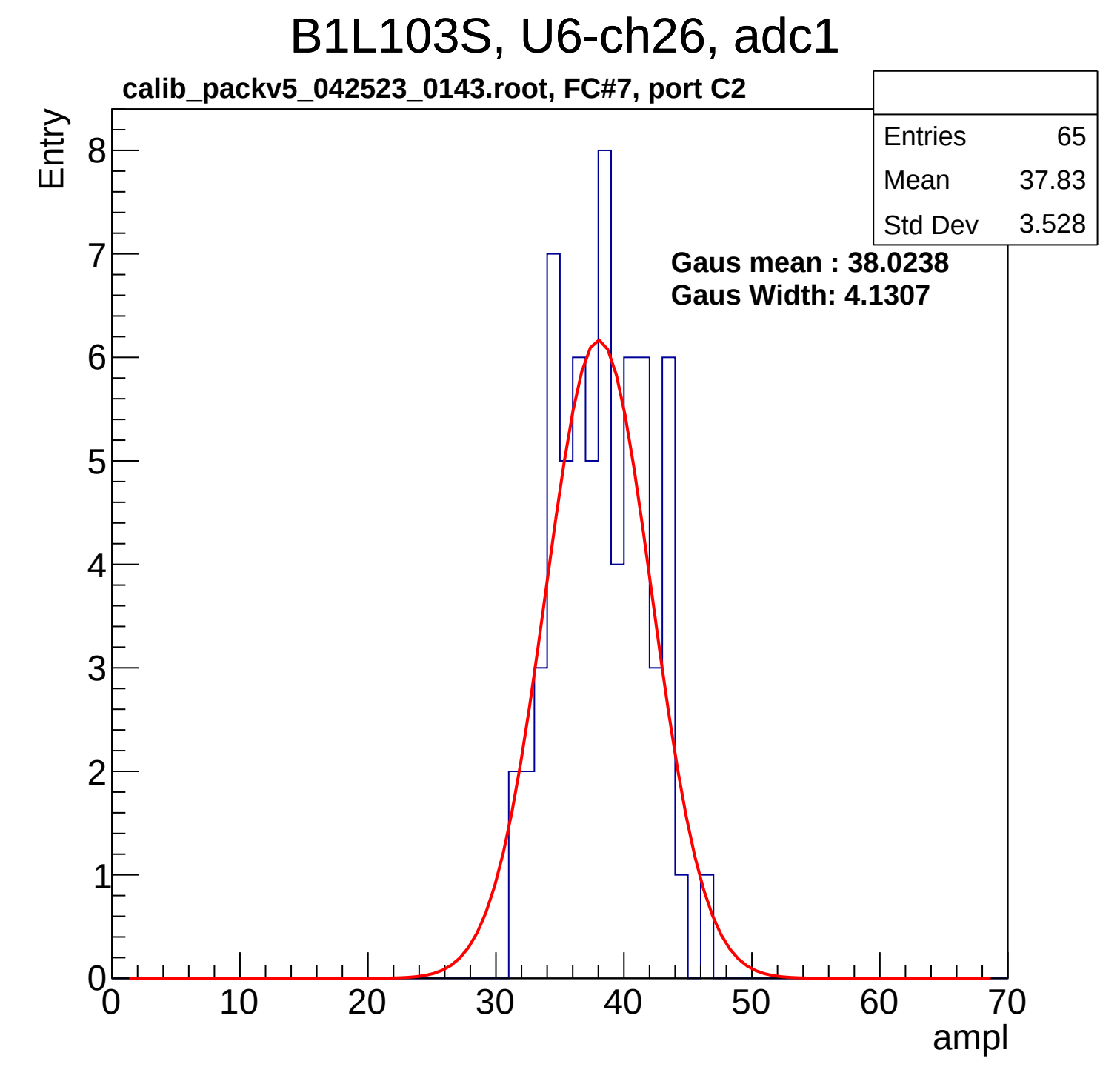
B1L103S, U6-ch26, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 37.83
Std Dev 3.528
Gaus mean : 38.0238
Gaus Width: 4.1307
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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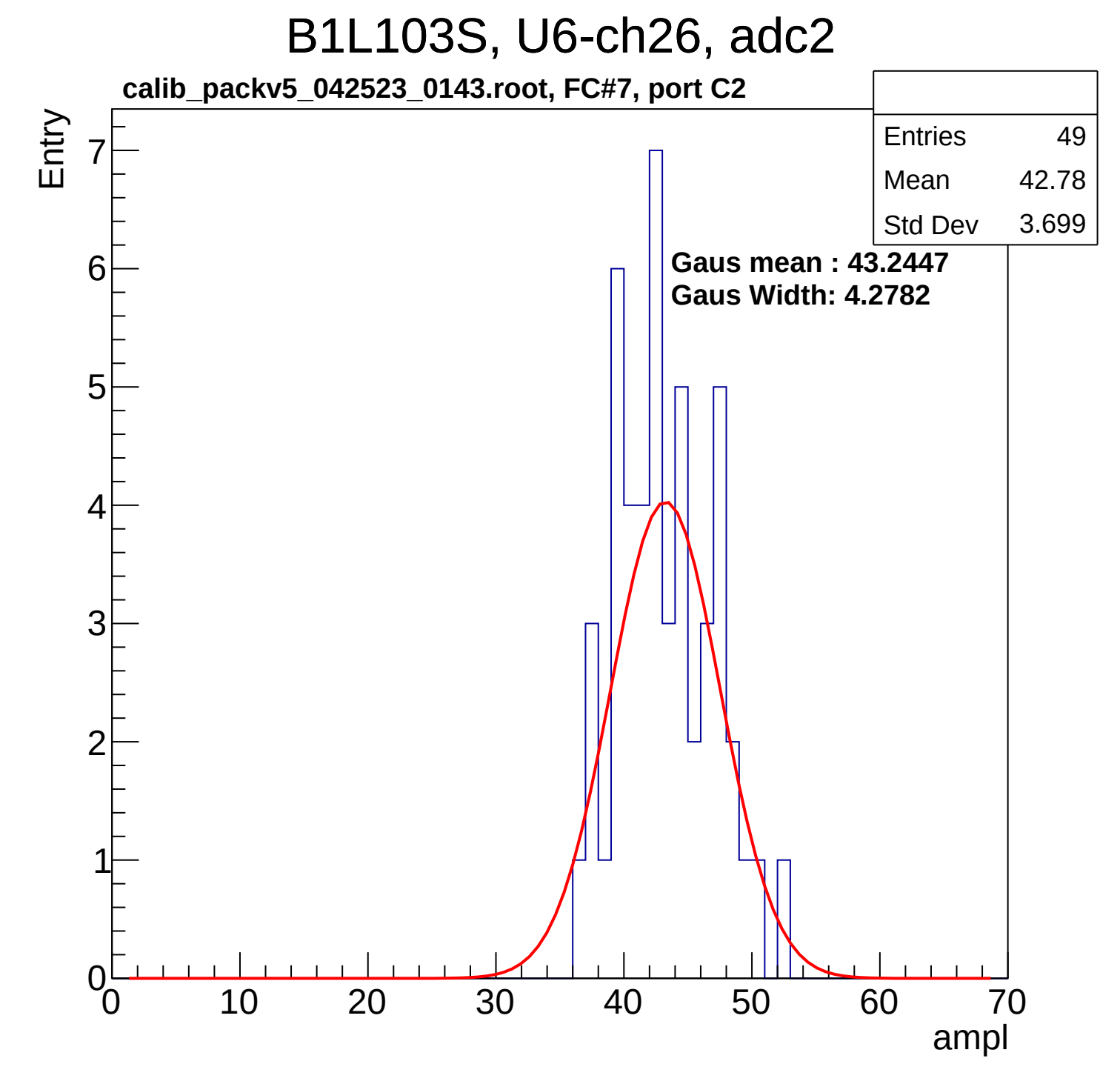
B1L103S, U6-ch26, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 42.78
Std Dev 3.699
Gaus mean : 43.2447
Gaus Width: 4.2782
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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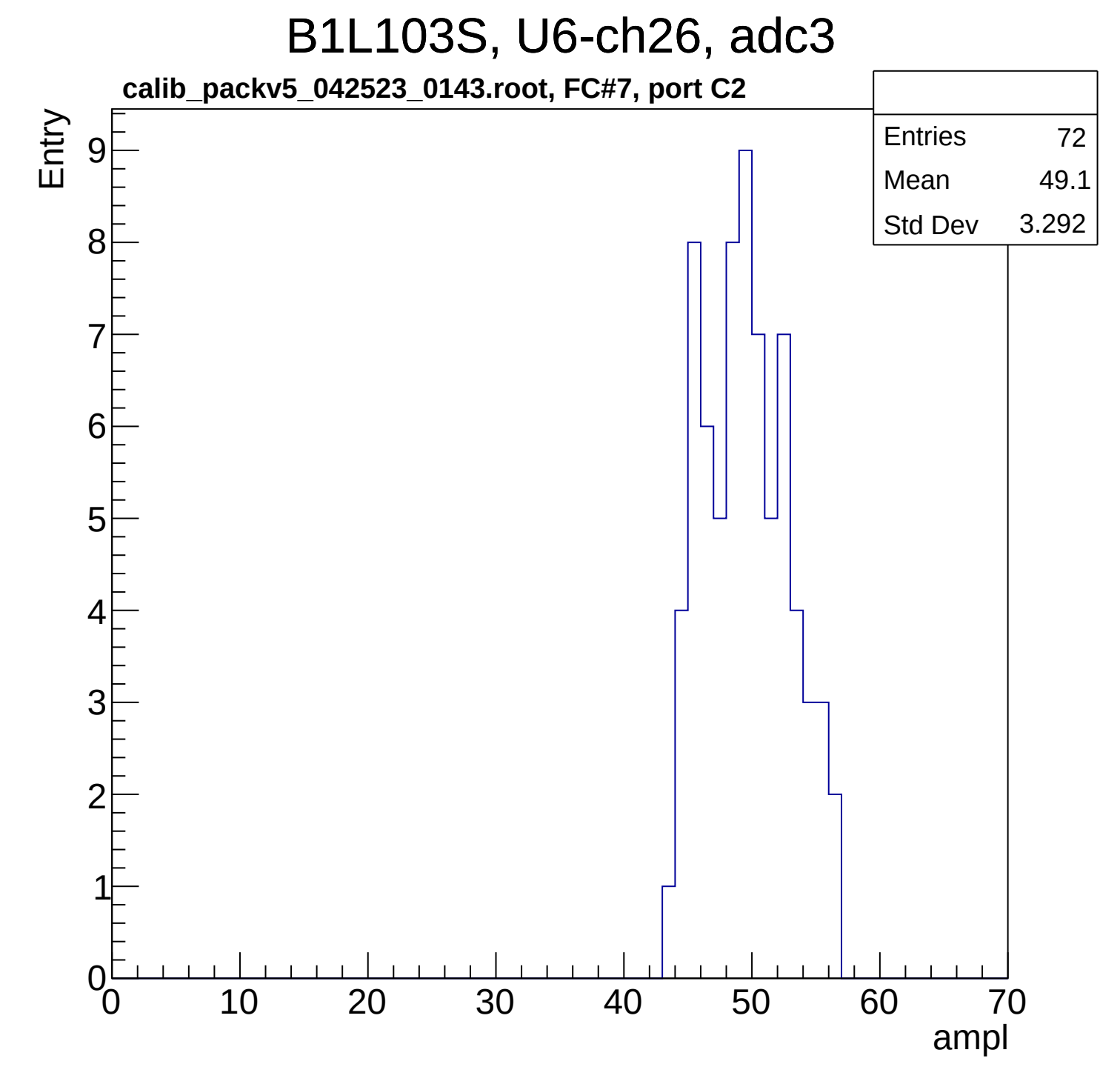
B1L103S, U6-ch26, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.1
Std Dev 3.292
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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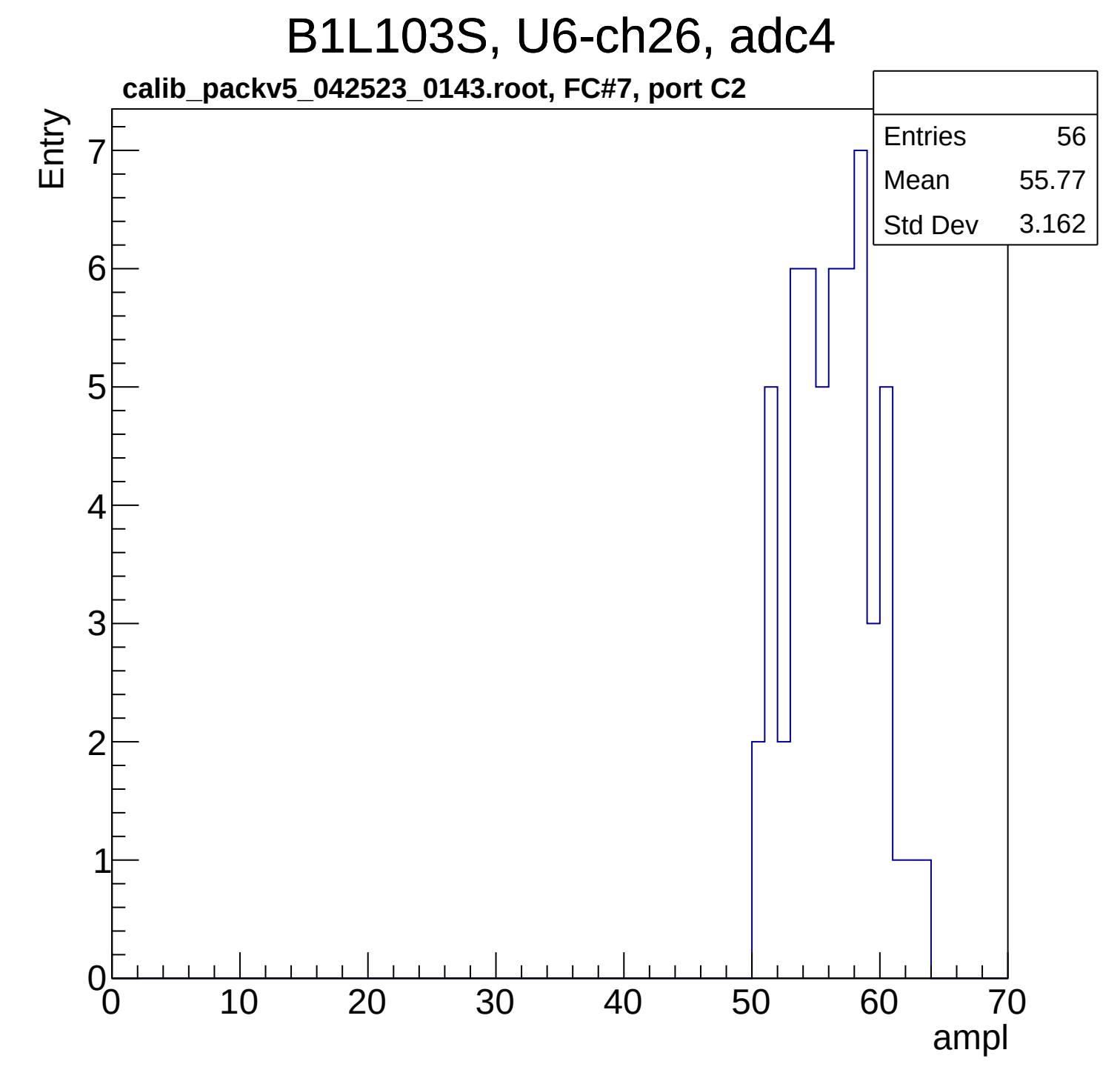
B1L103S, U6-ch26, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55.77
Std Dev 3.162
Entry
ampl
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6
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20
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40
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60
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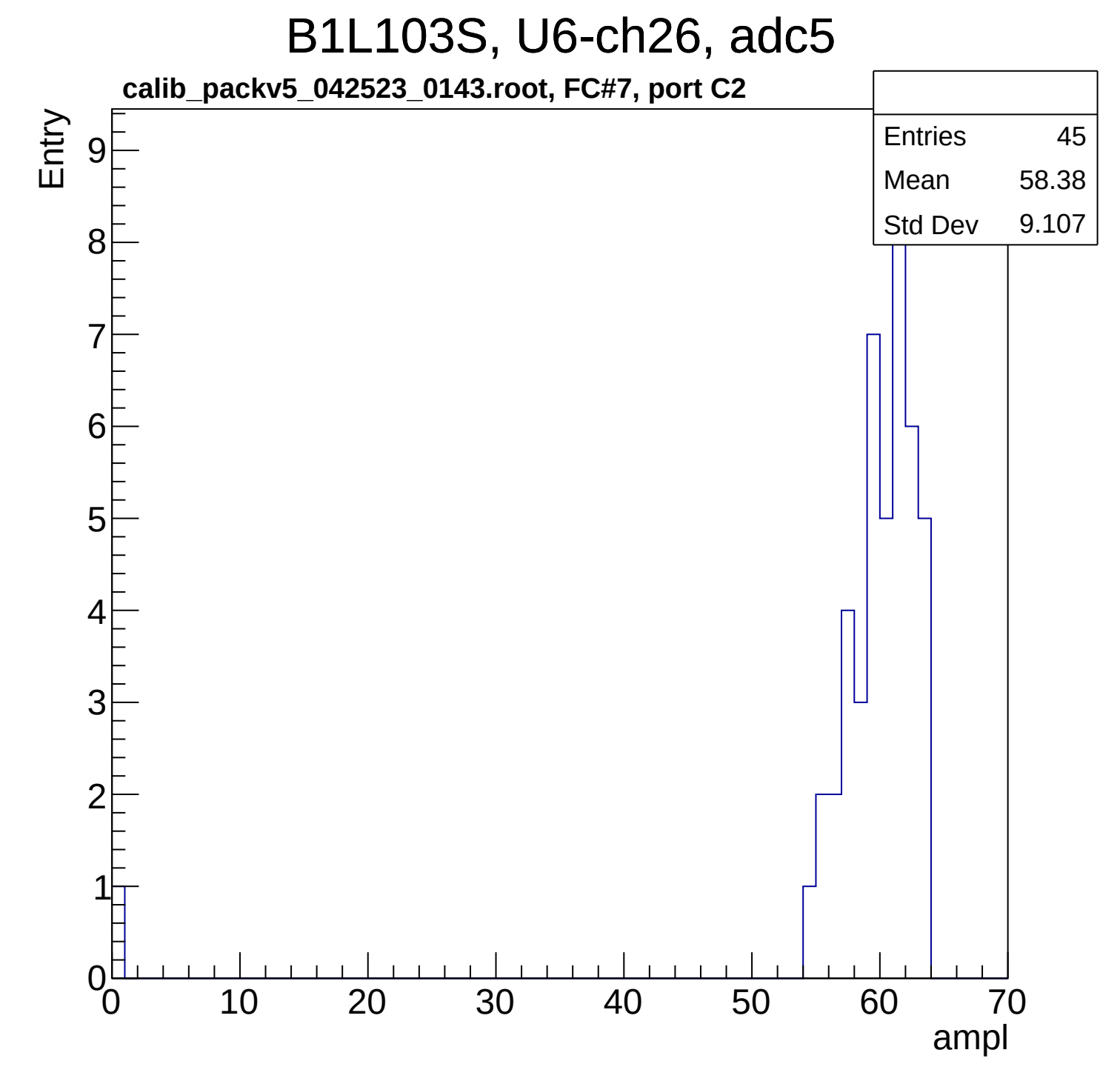
B1L103S, U6-ch26, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.38
Std Dev 9.107
Entry
ampl
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3
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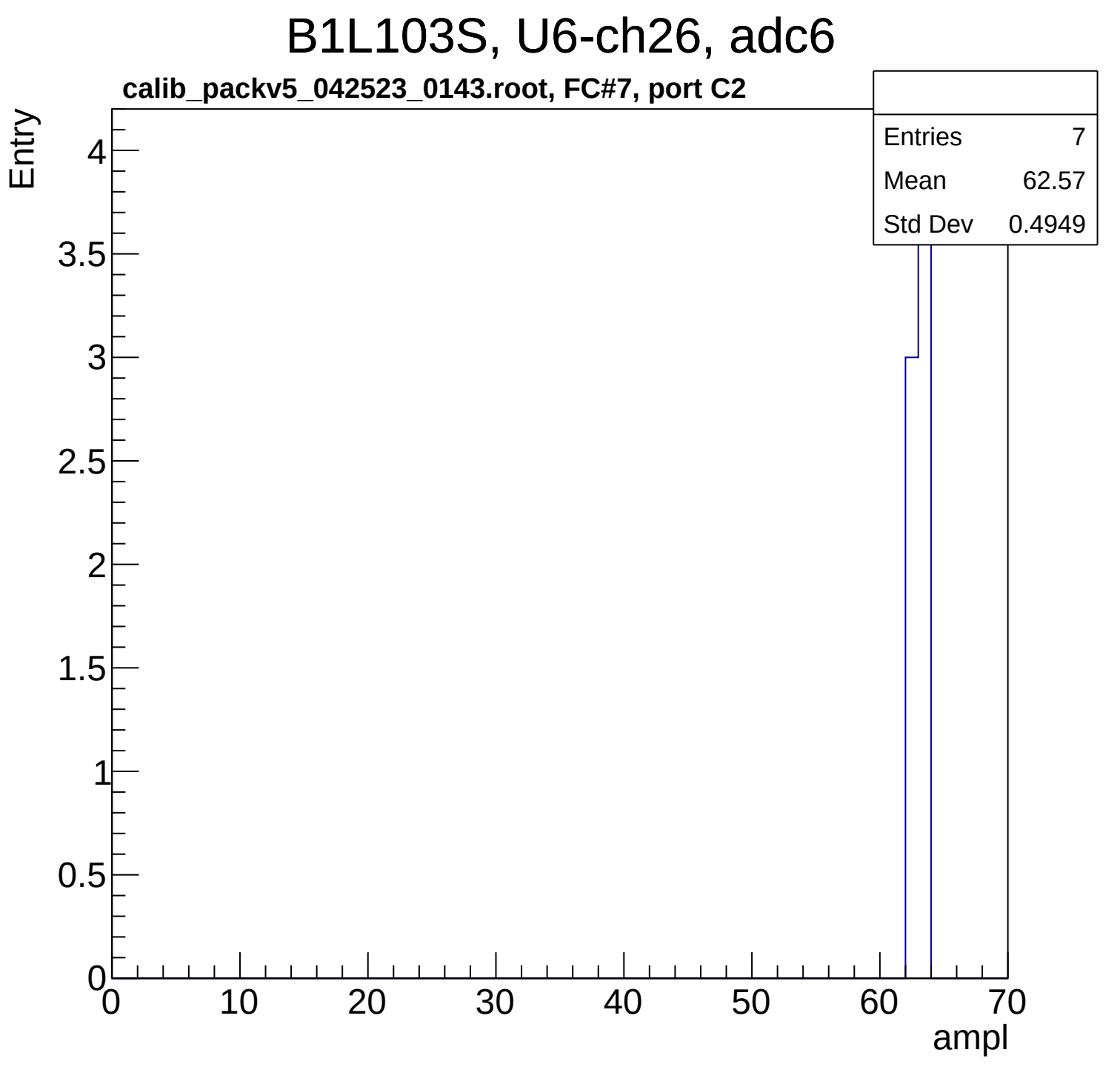
B1L103S, U6-ch26, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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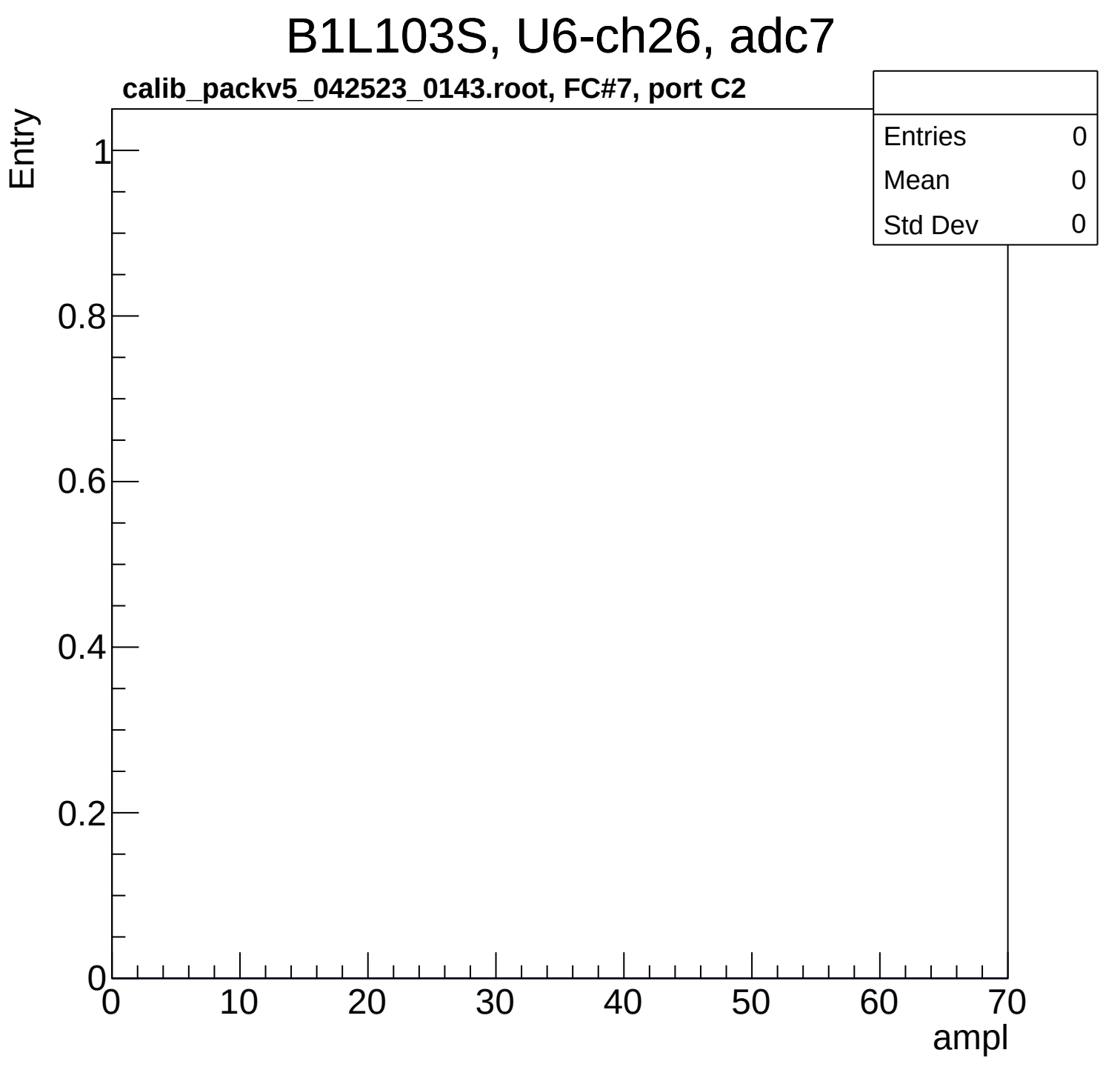
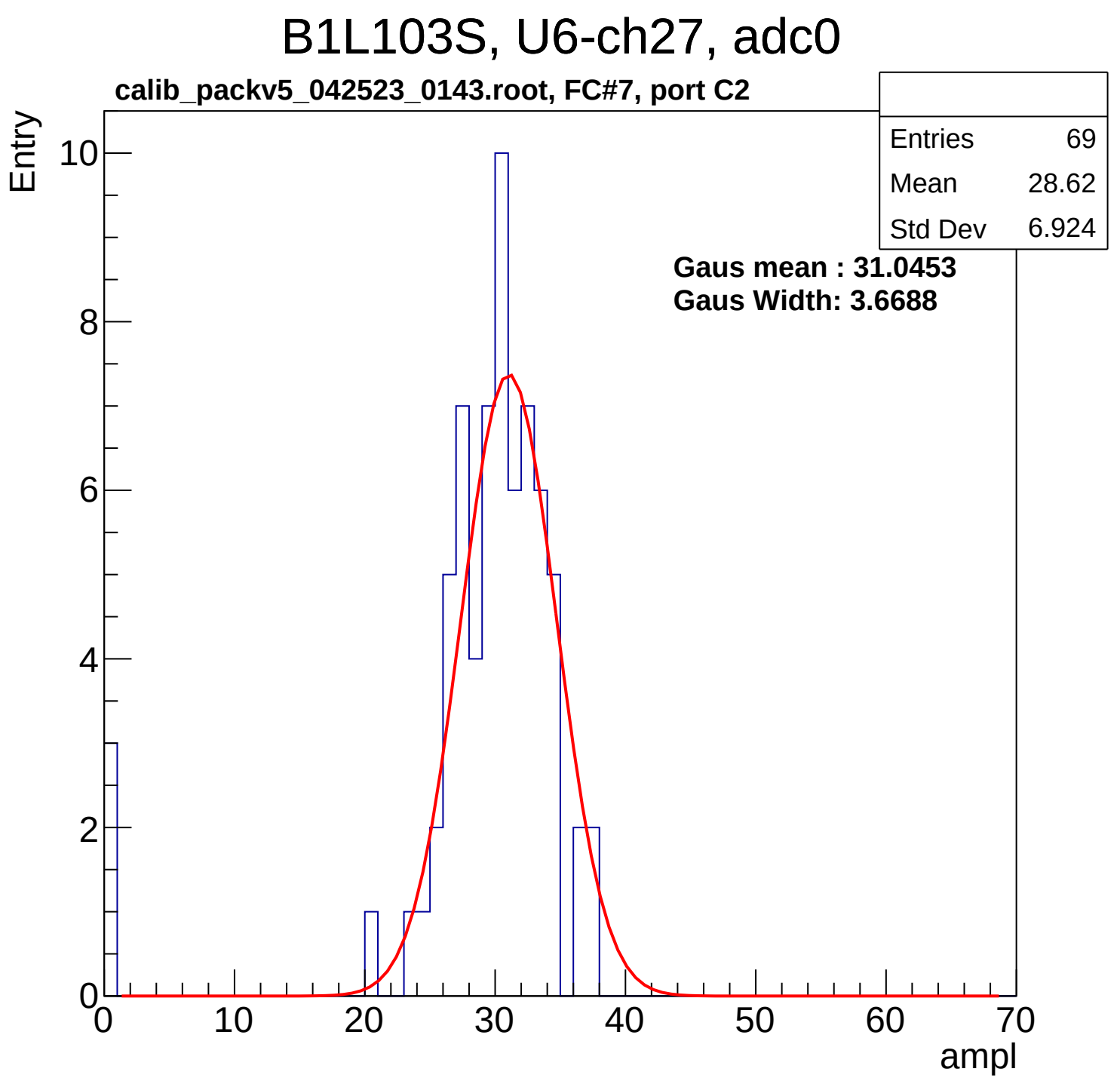
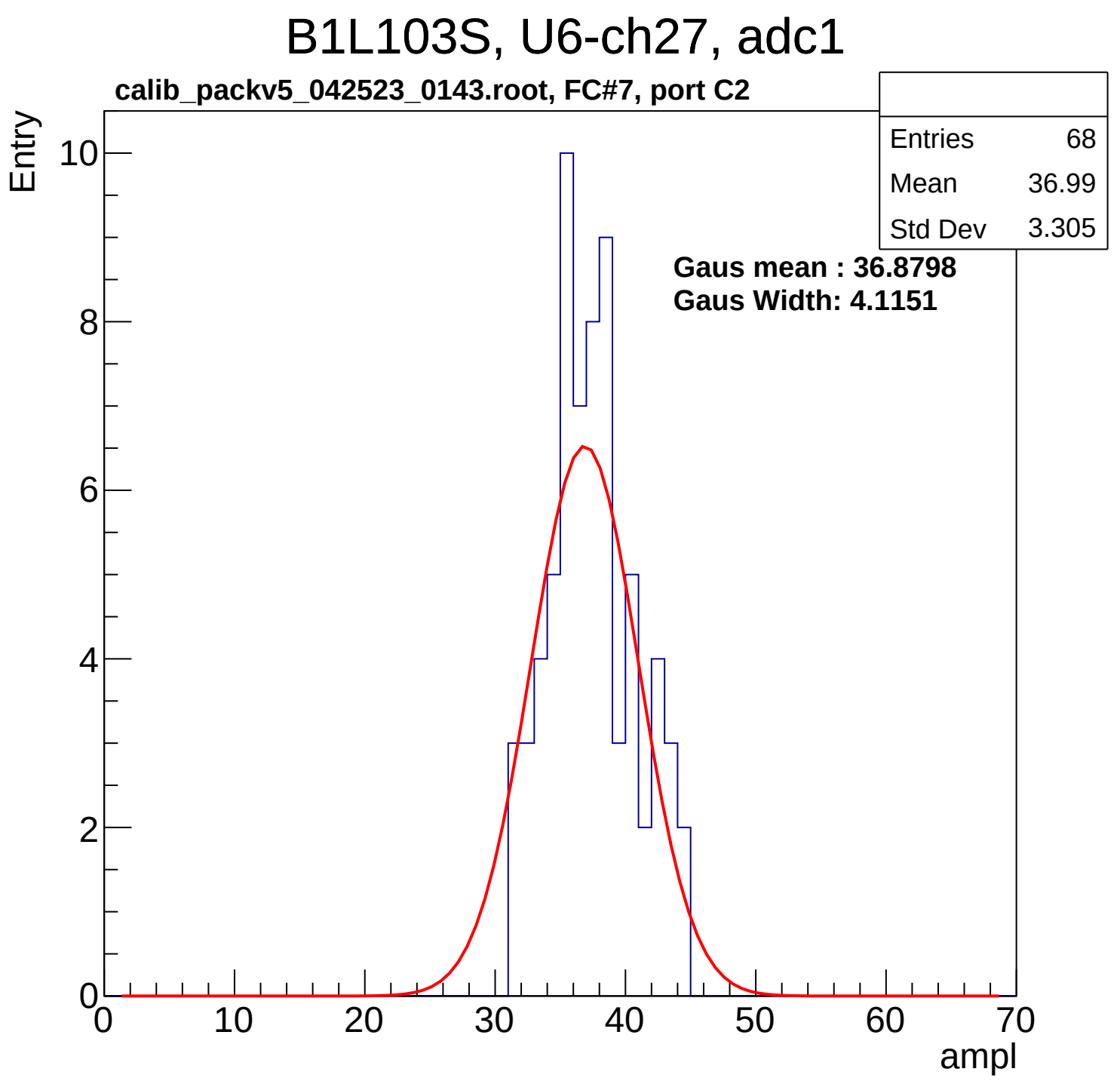
30
40
50
60
70


B1L103S, U6-ch26, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch27, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.62
Std Dev 6.924
Gaus mean : 31.0453
Gaus Width: 3.6688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch27, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.99
Std Dev 3.305
Gaus mean : 36.8798
Gaus Width: 4.1151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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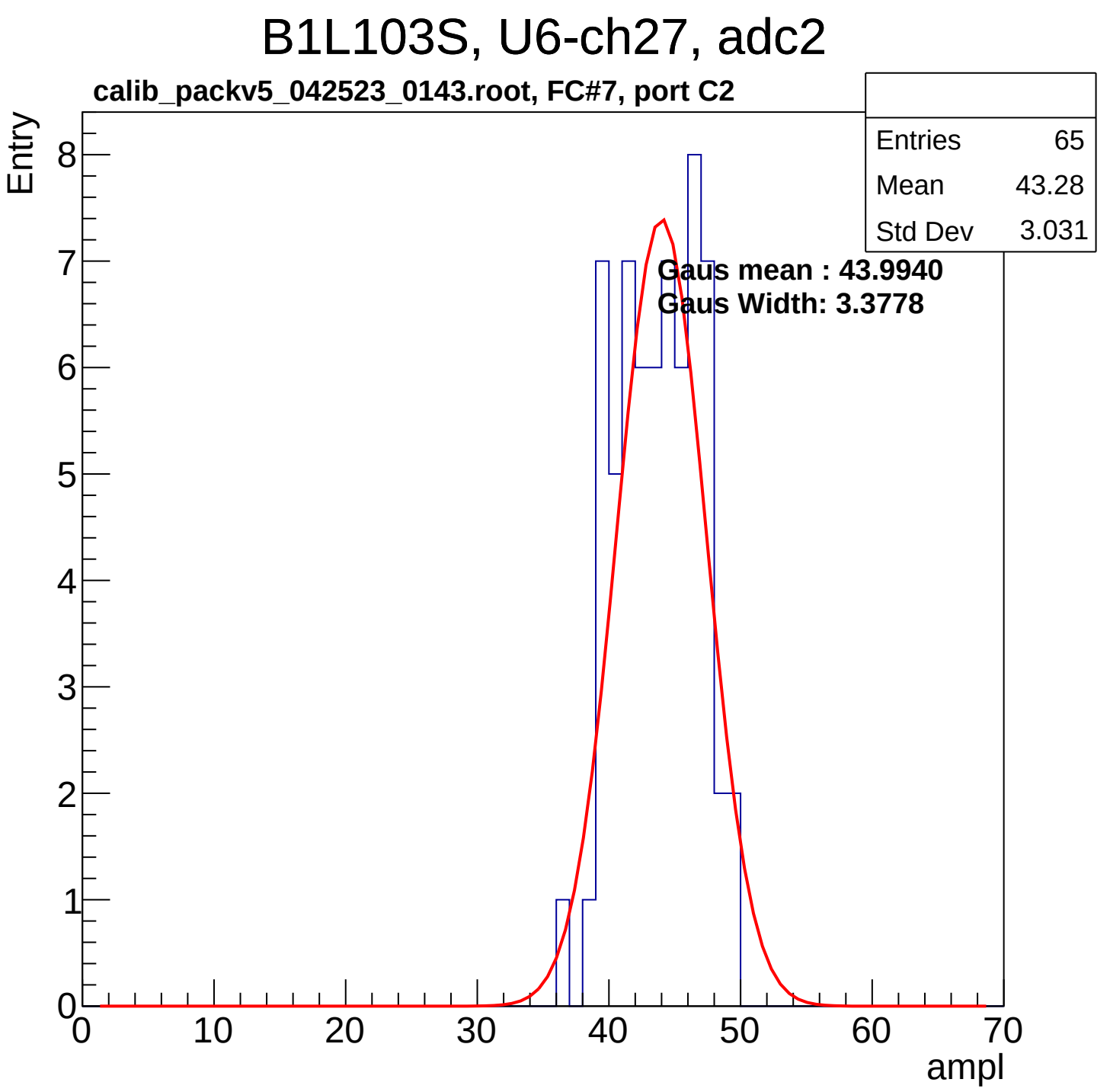
B1L103S, U6-ch27, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.28
Std Dev 3.031
Gaus mean : 43.9940
Gaus Width: 3.3778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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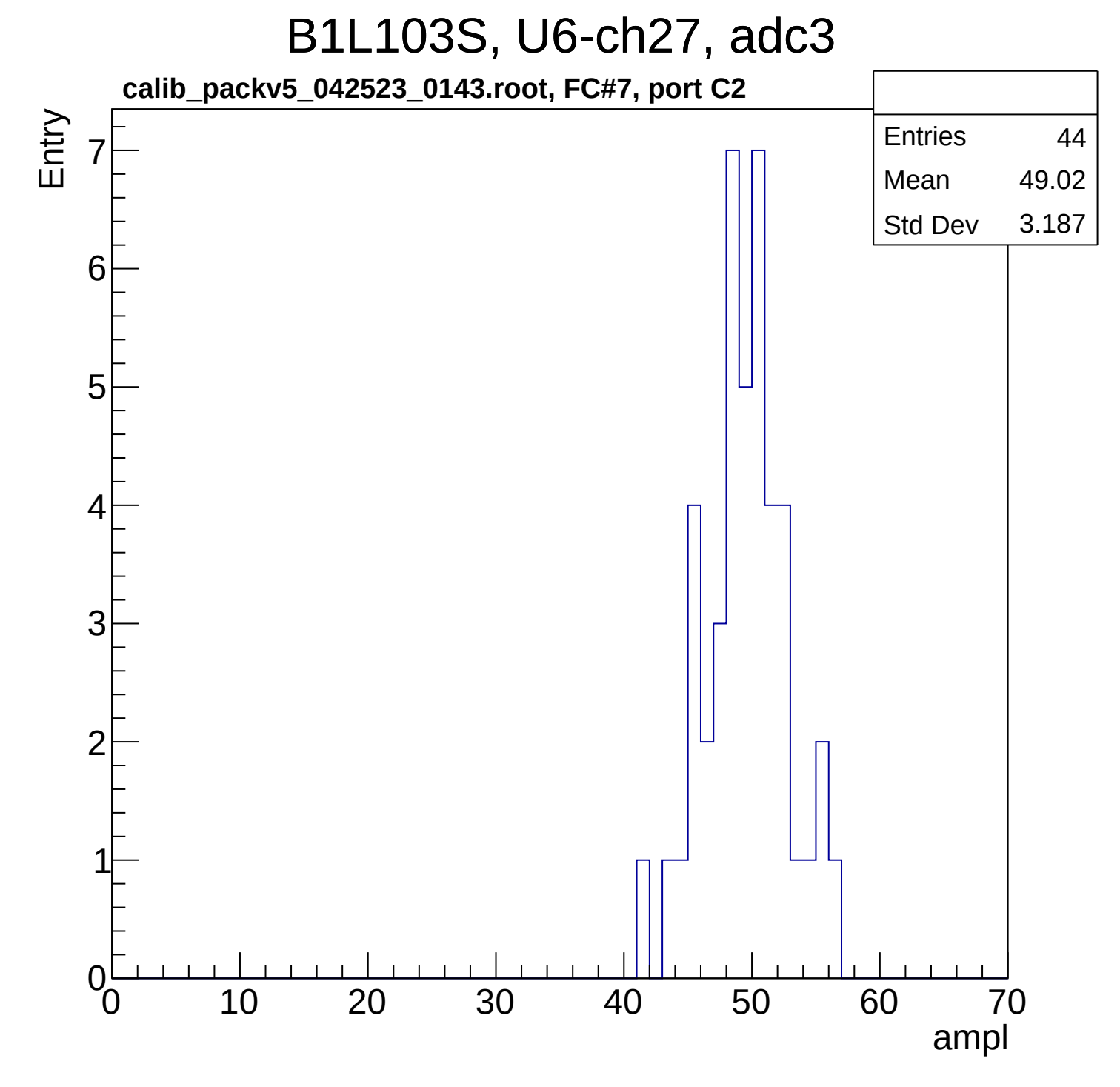
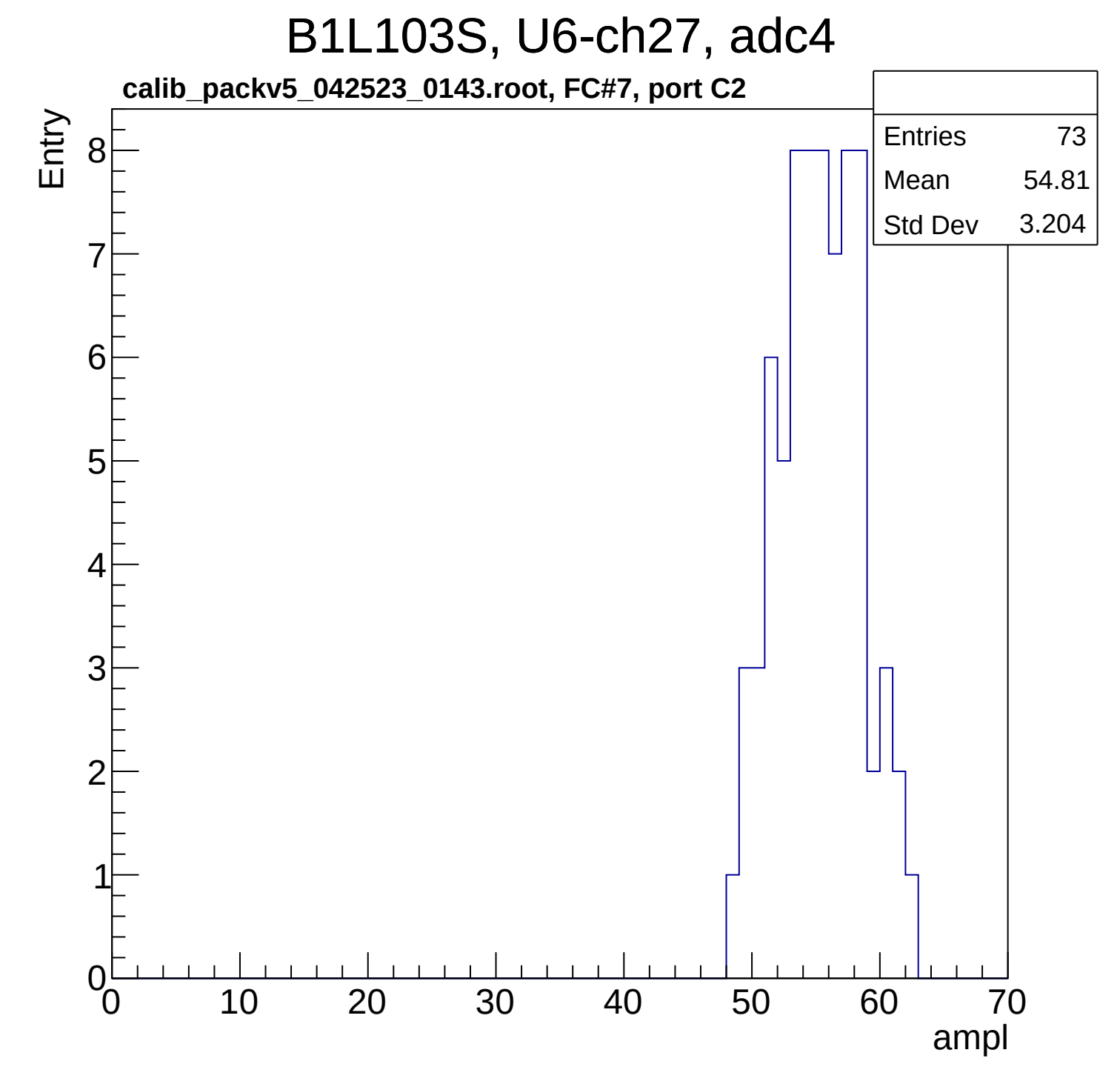

B1L103S, U6-ch27, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 49.02
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch27, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 54.81
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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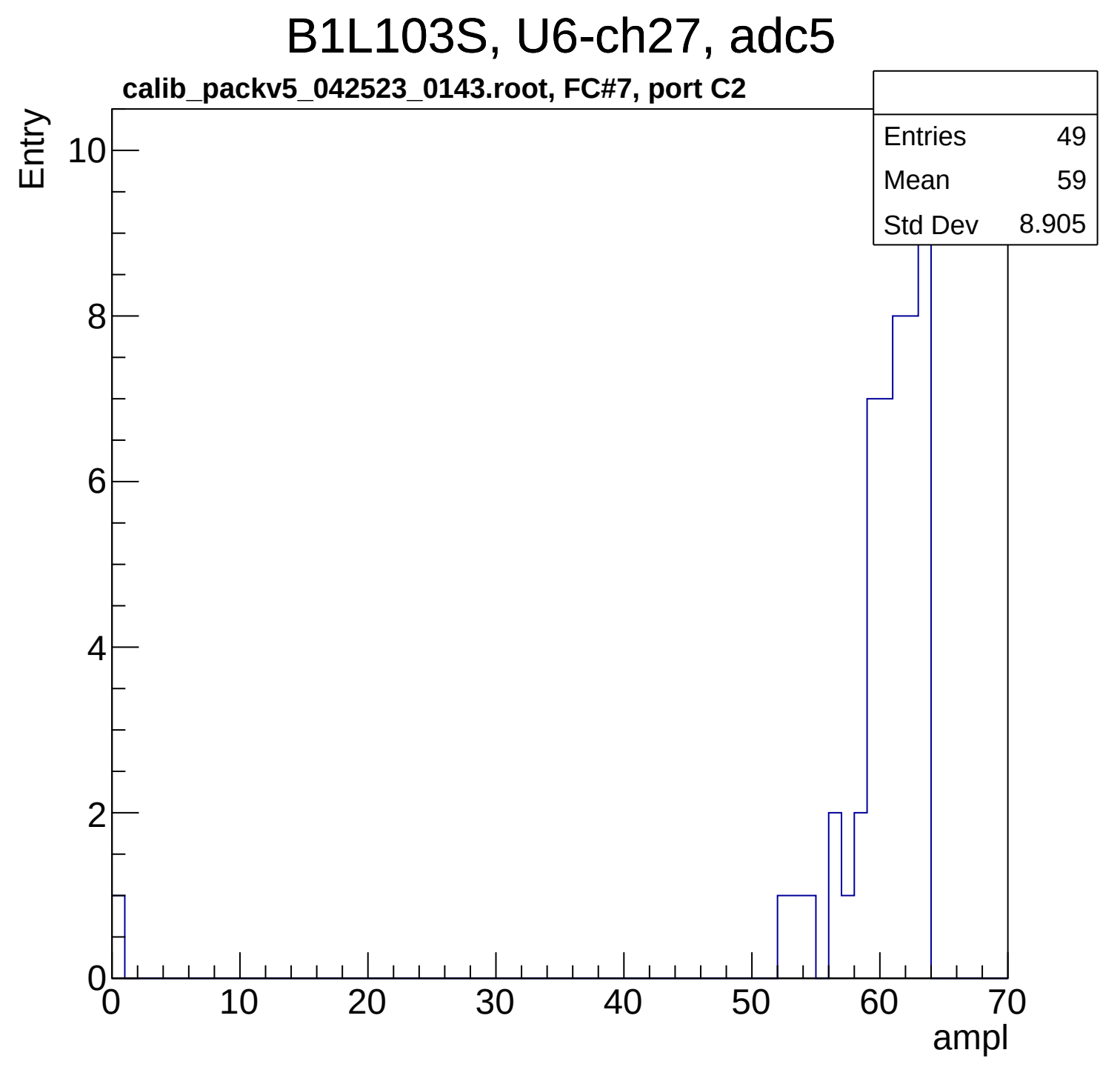
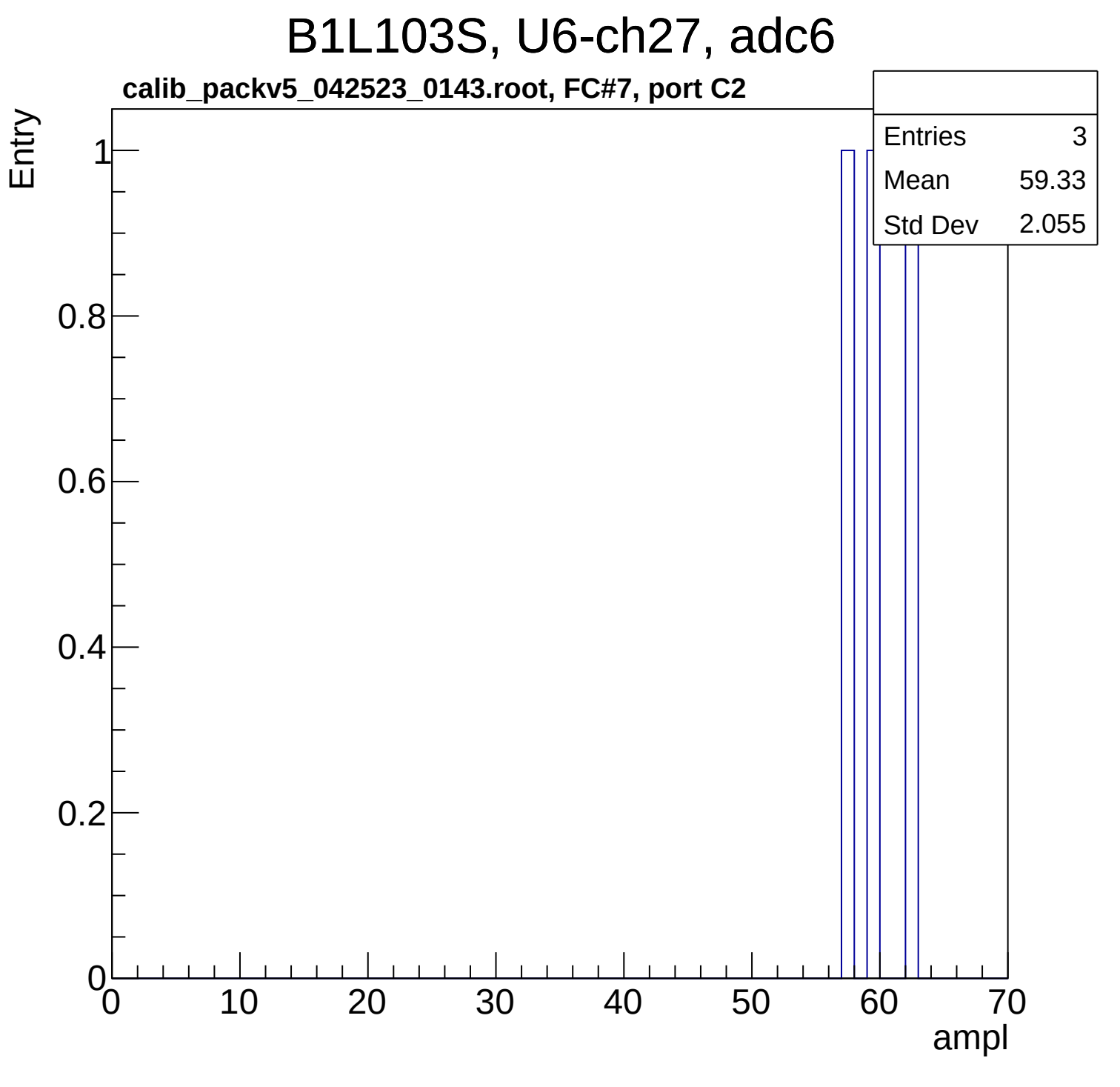
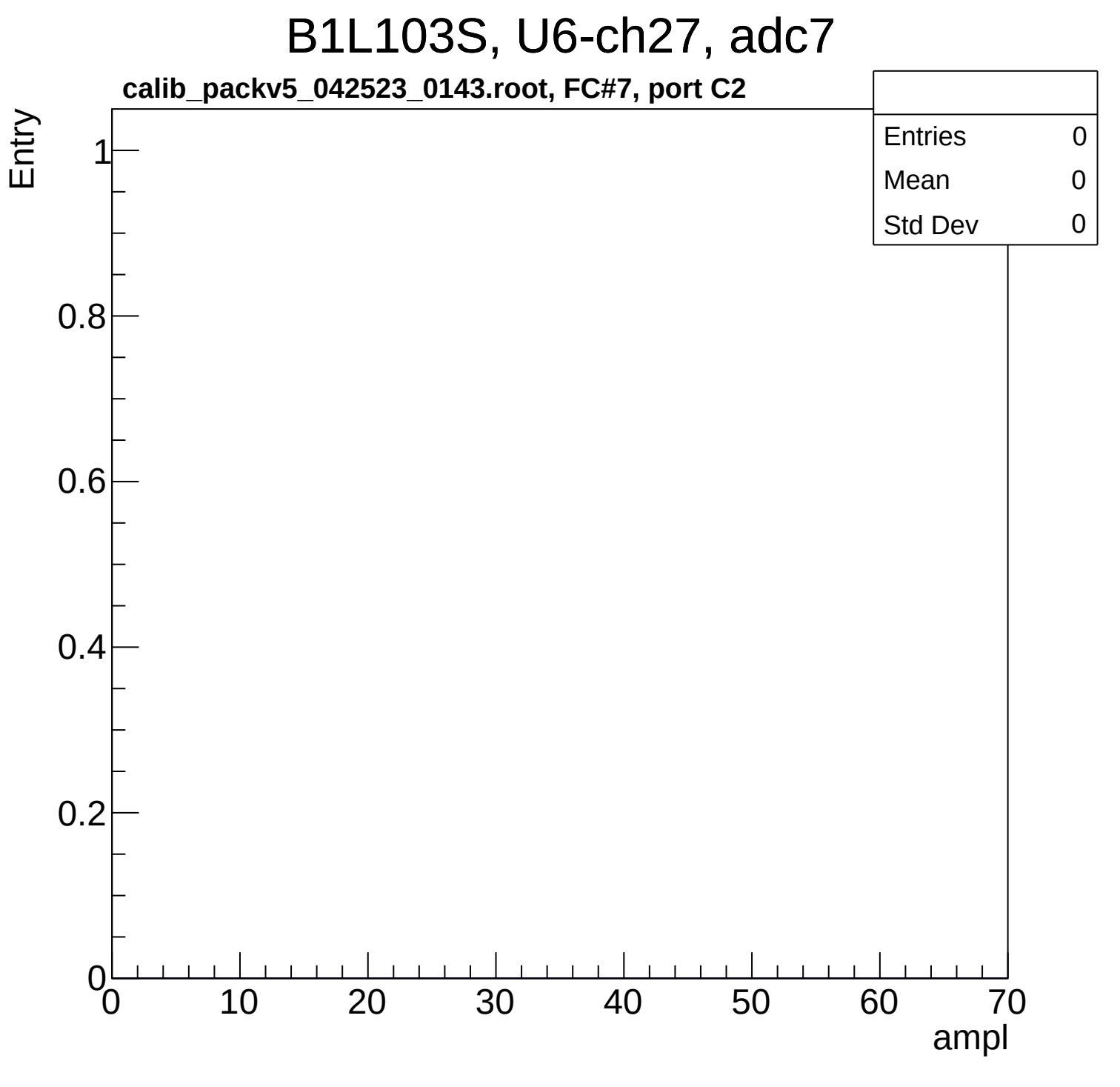
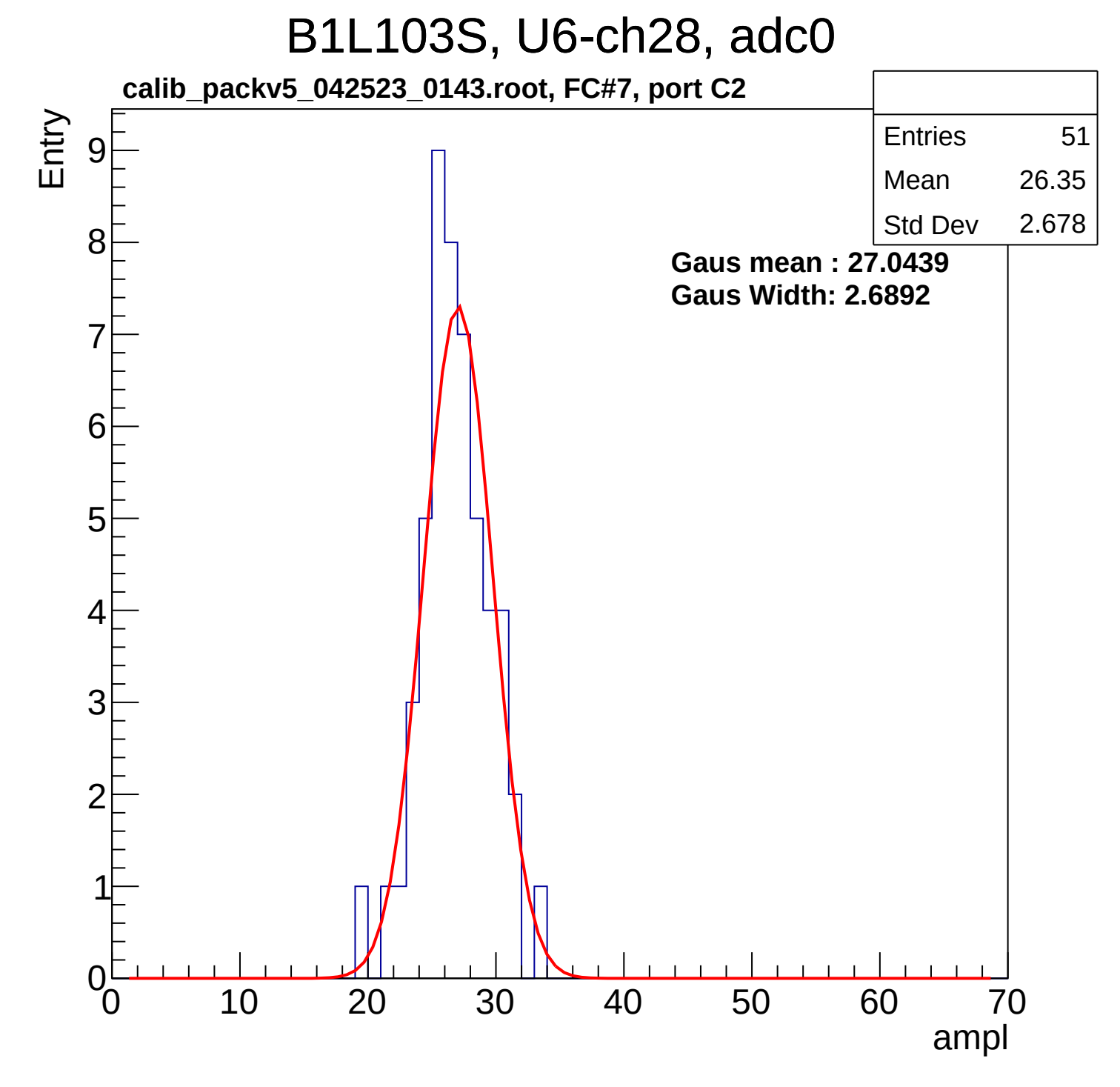
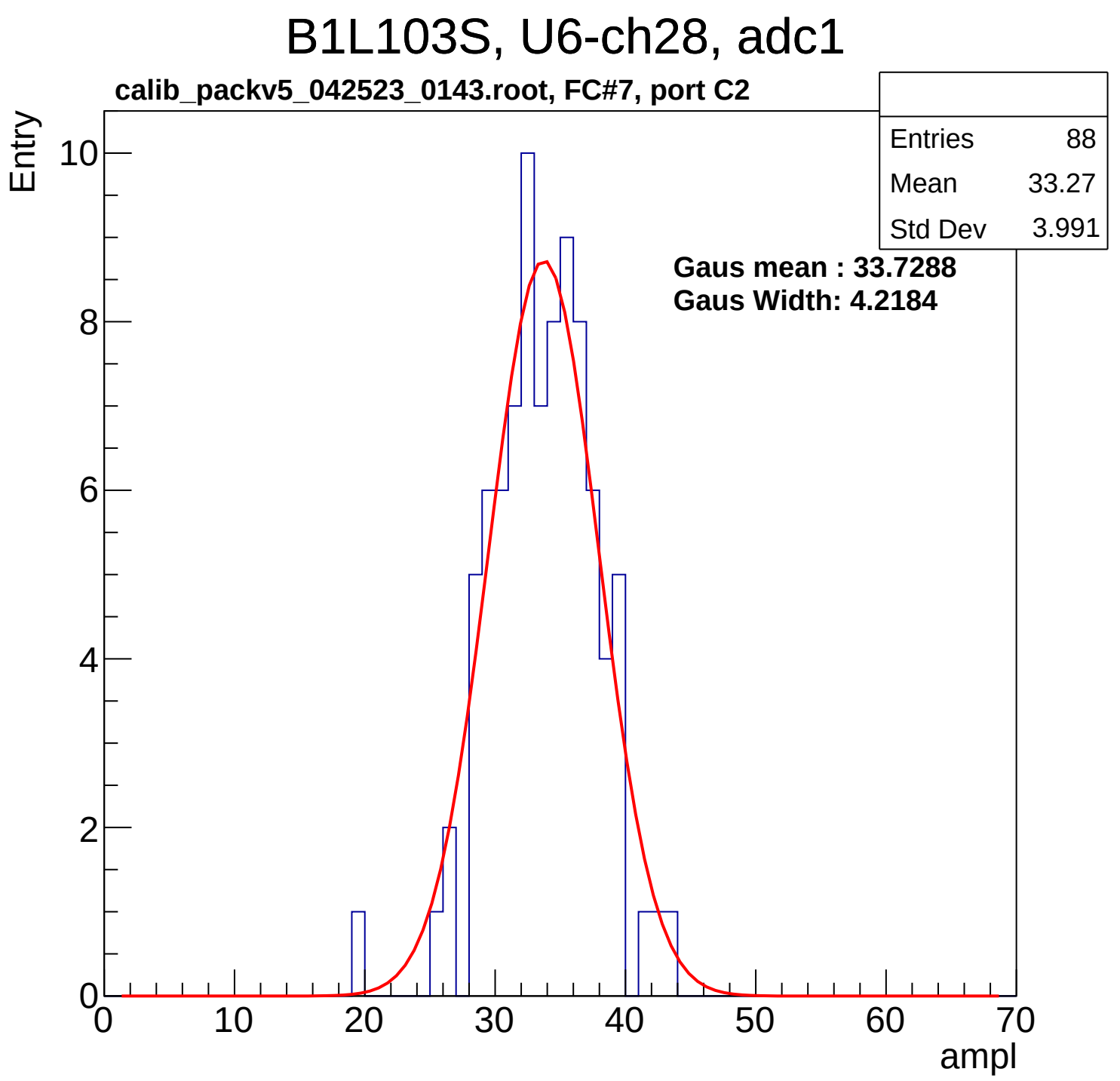
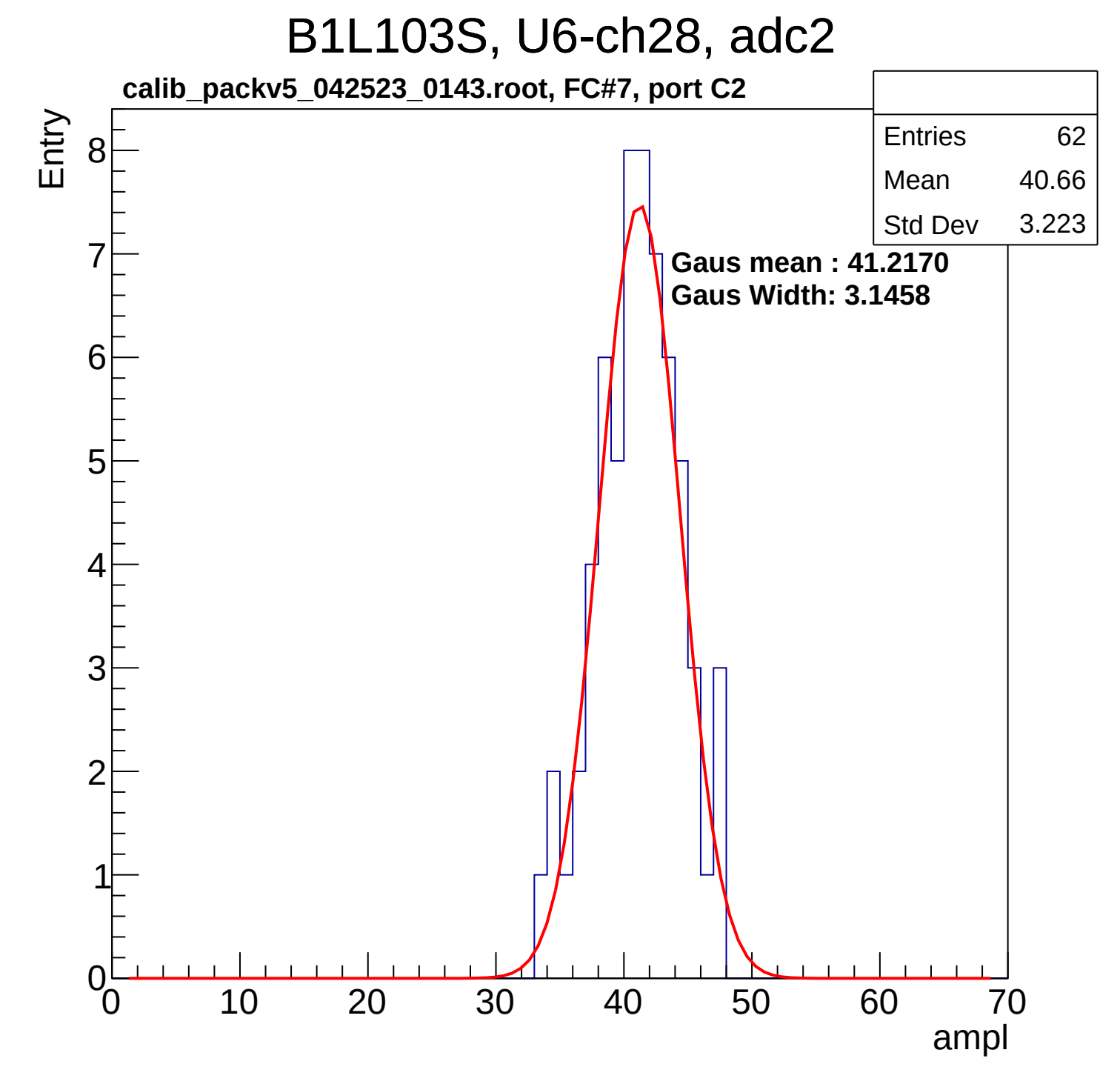

B1L103S, U6-ch27, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59
Std Dev 8.905
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

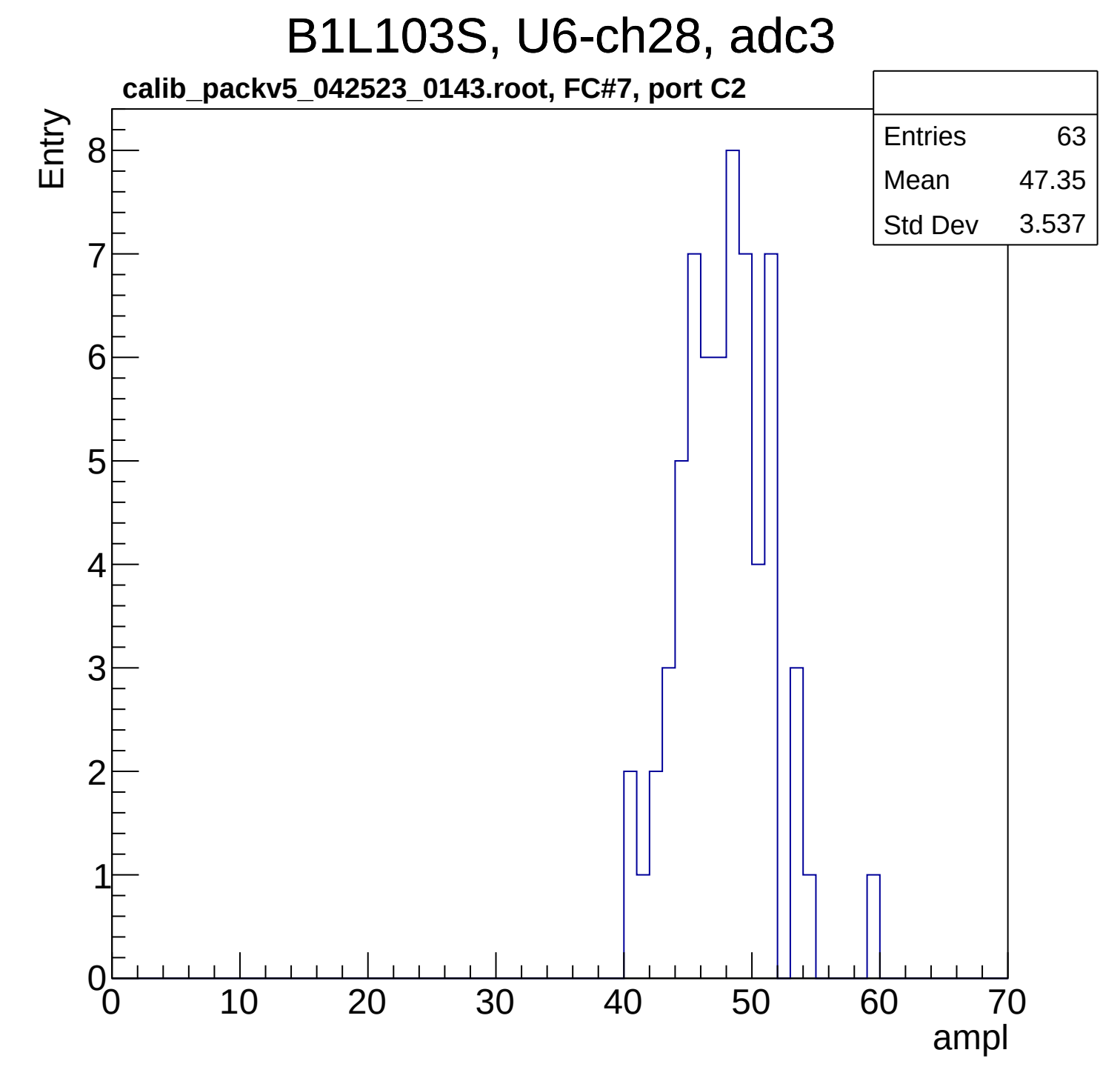

B1L103S, U6-ch27, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 59.33
Std Dev 2.055
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

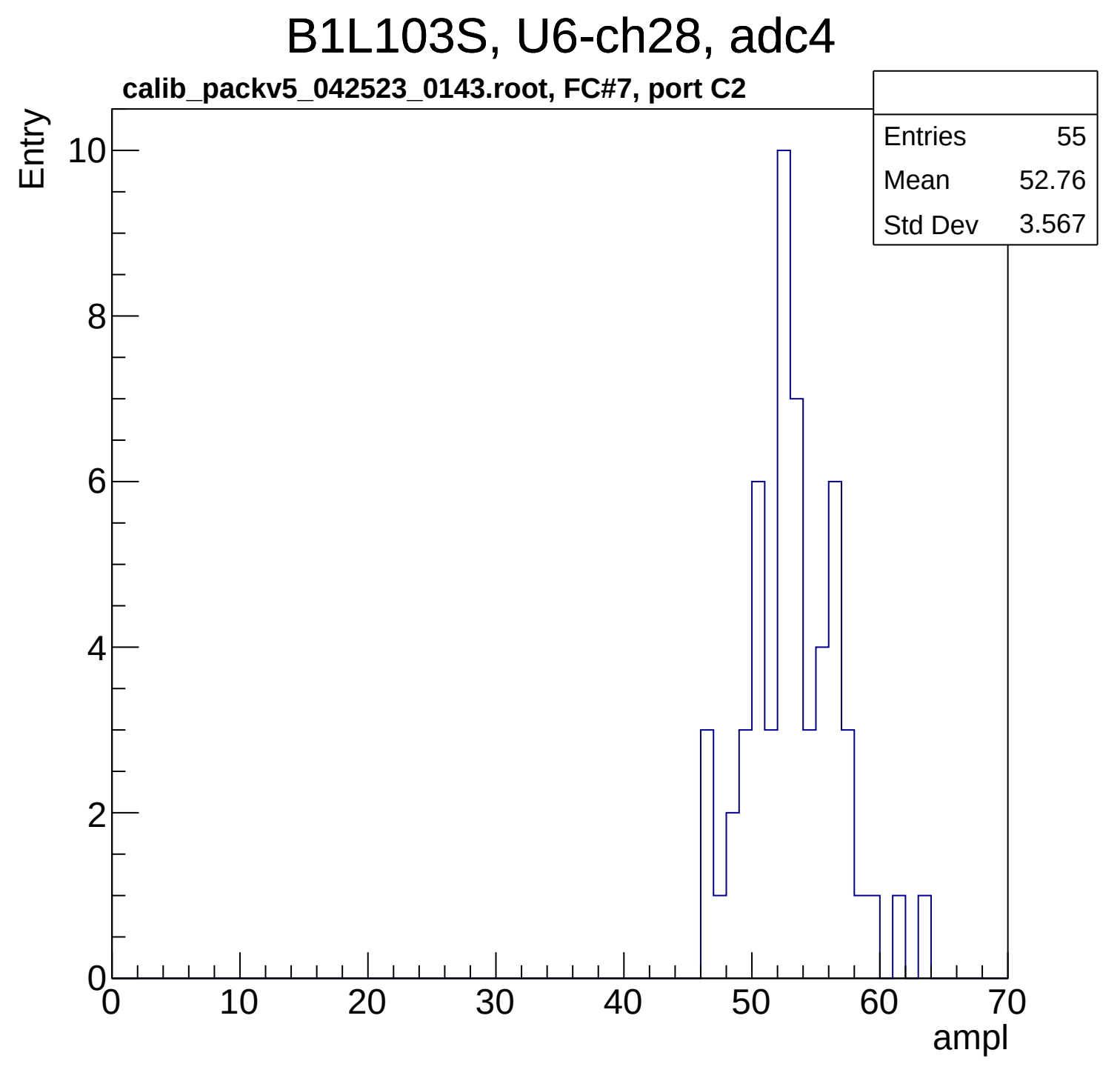

B1L103S, U6-ch27, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

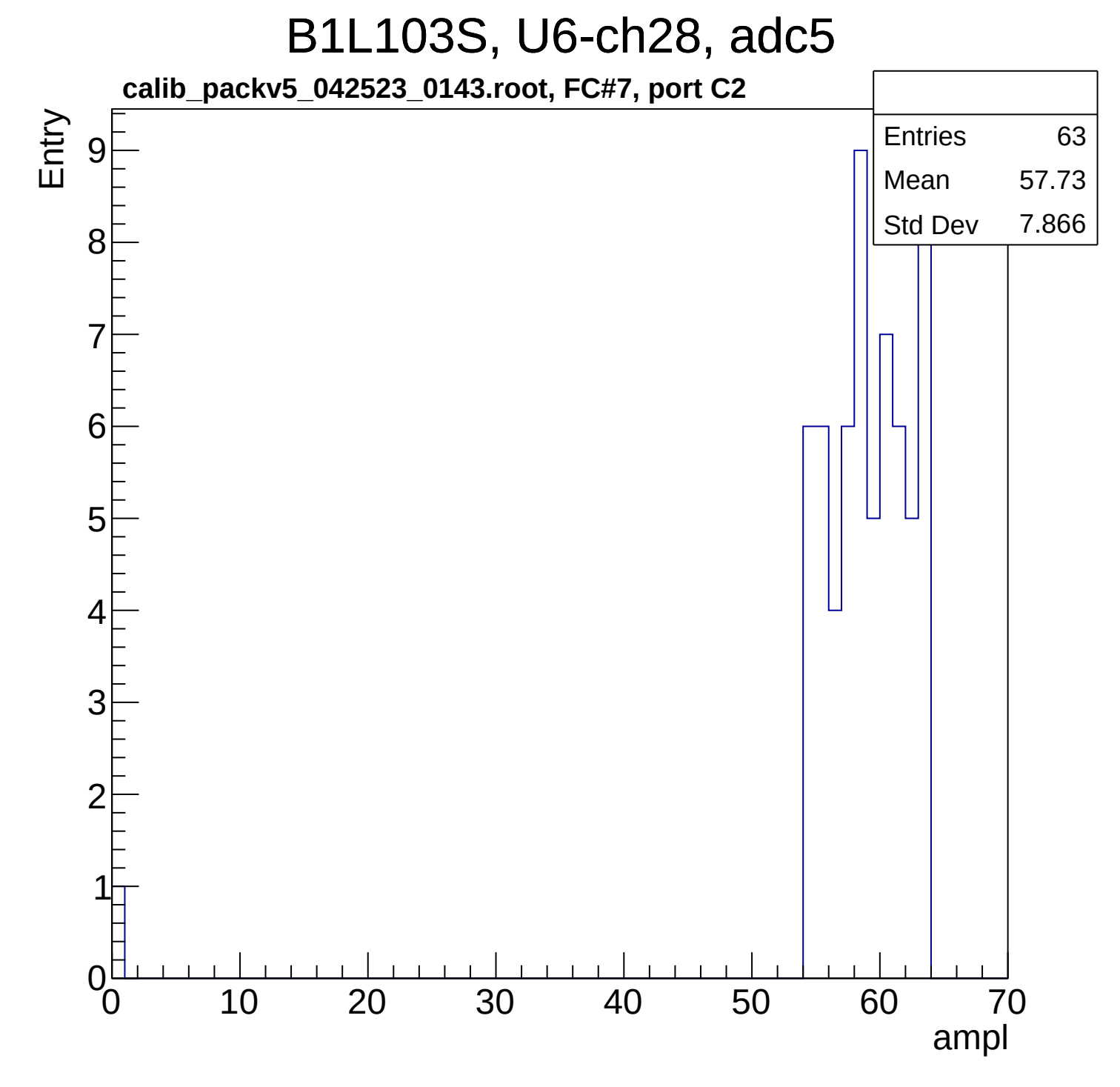

B1L103S, U6-ch28, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 26.35
Std Dev 2.678
Gaus mean : 27.0439
Gaus Width: 2.6892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 33.27
Std Dev 3.991
Gaus mean : 33.7288
Gaus Width: 4.2184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 40.66
Std Dev 3.223
Gaus mean : 41.2170
Gaus Width: 3.1458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 47.35
Std Dev 3.537
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 52.76
Std Dev 3.567
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 57.73
Std Dev 7.866
Entry
ampl
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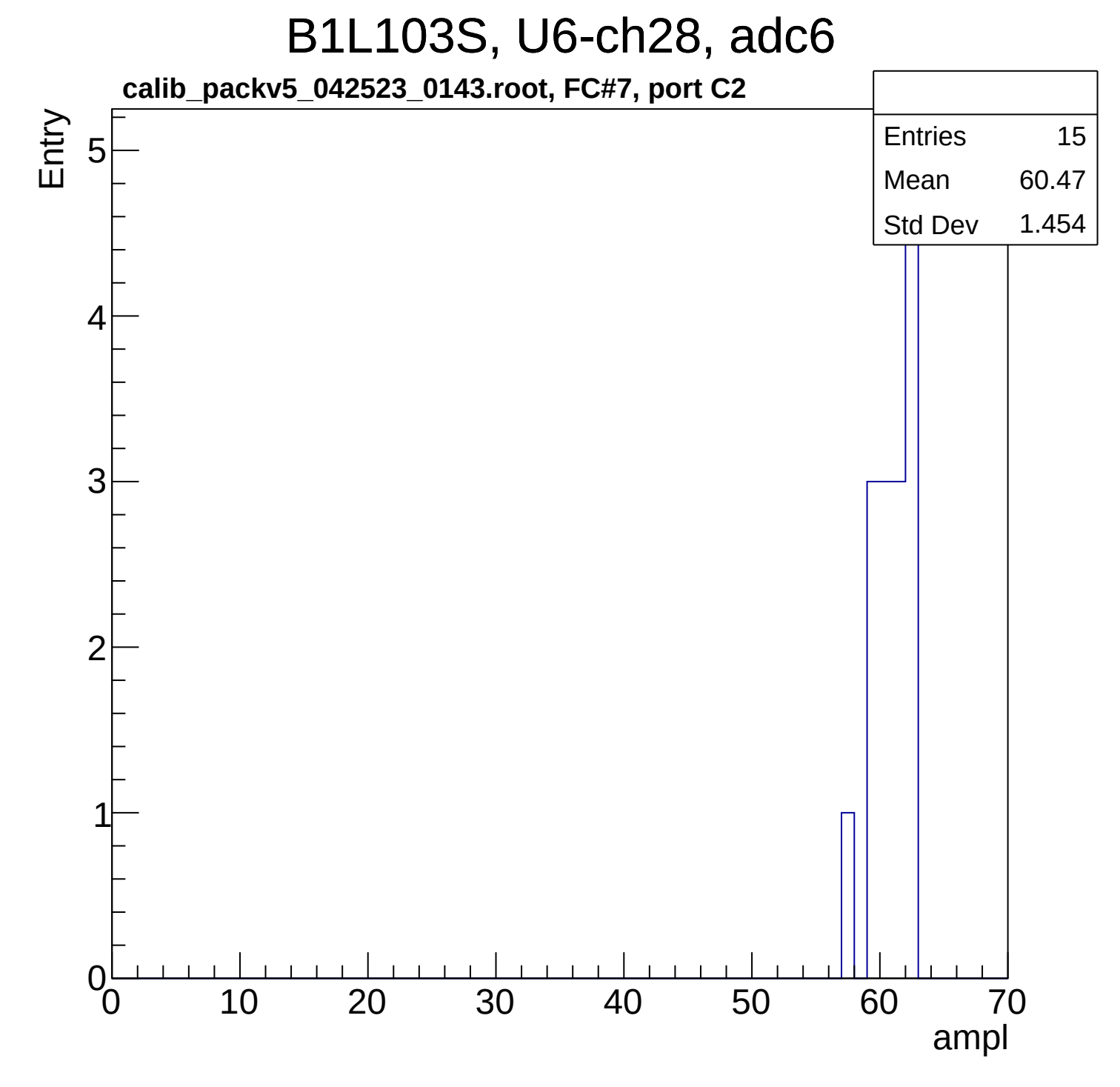
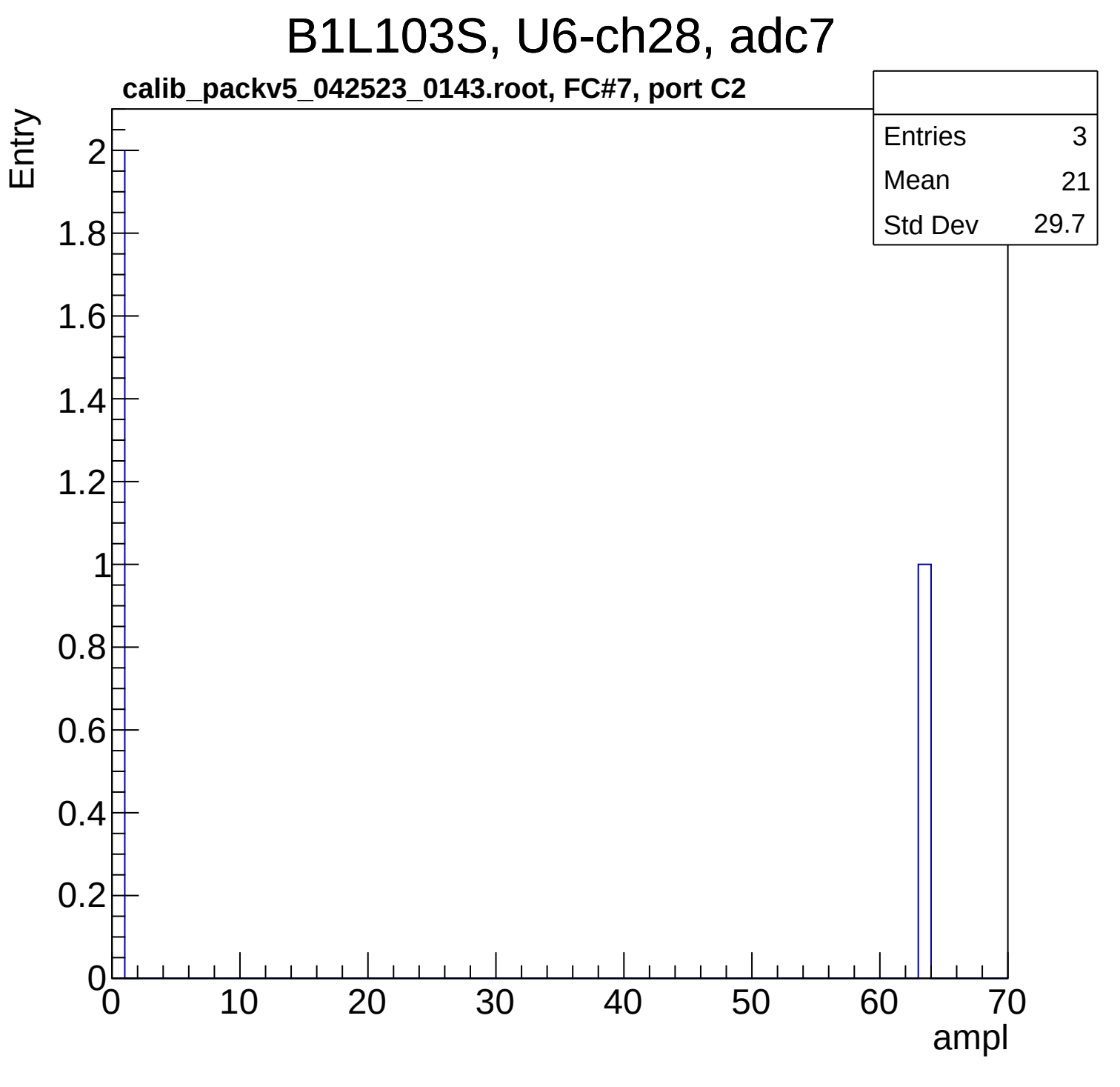
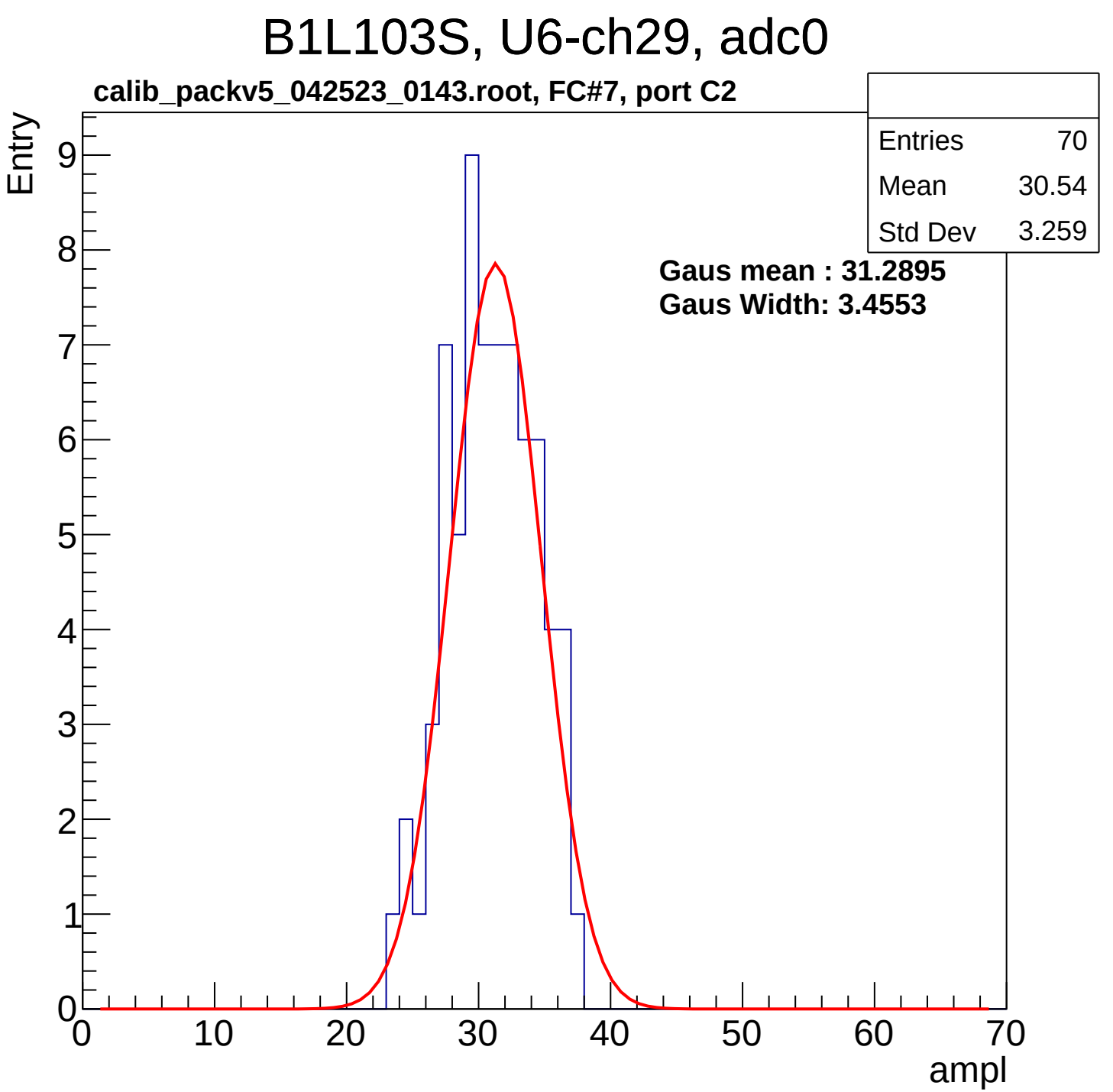
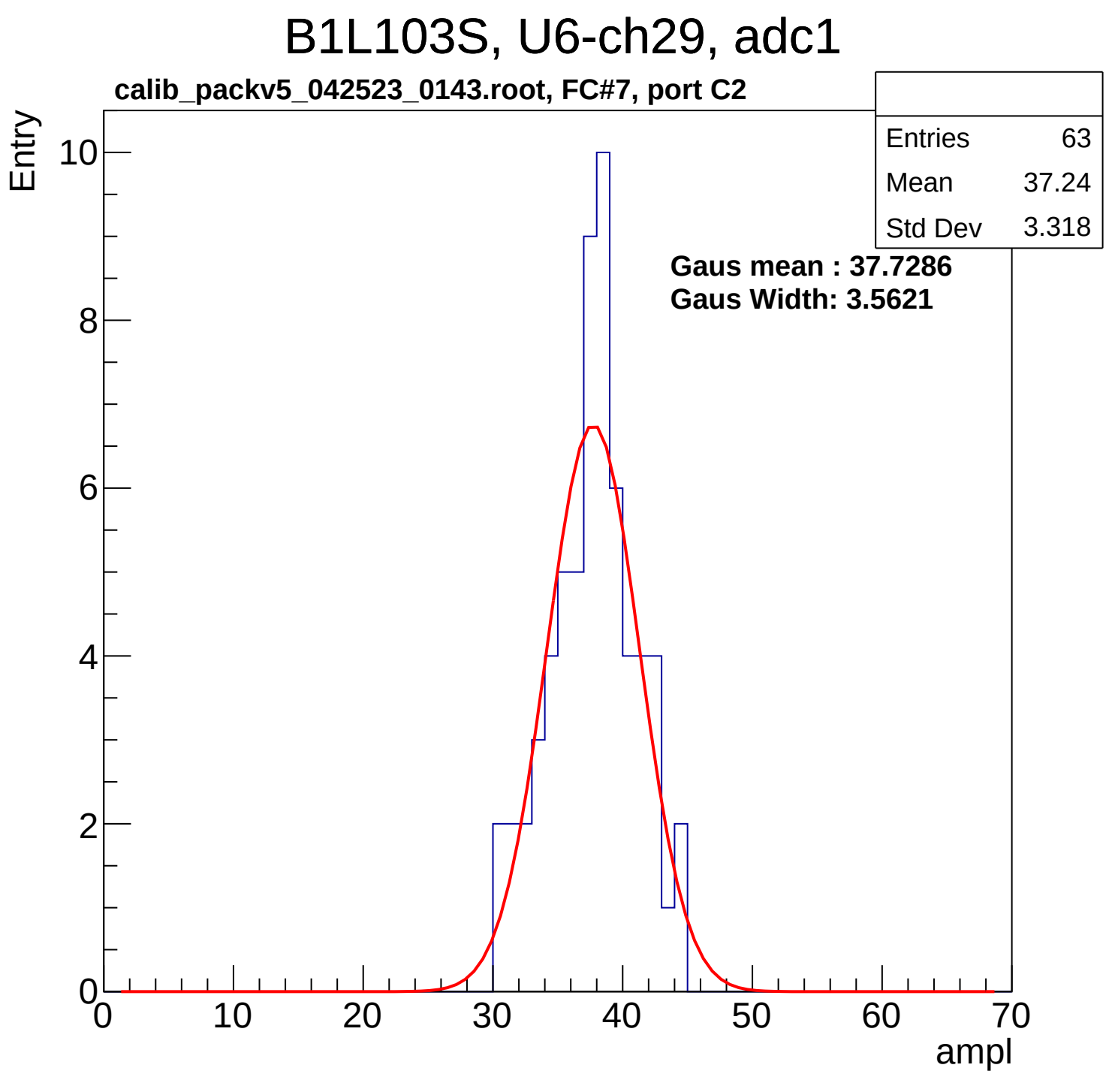
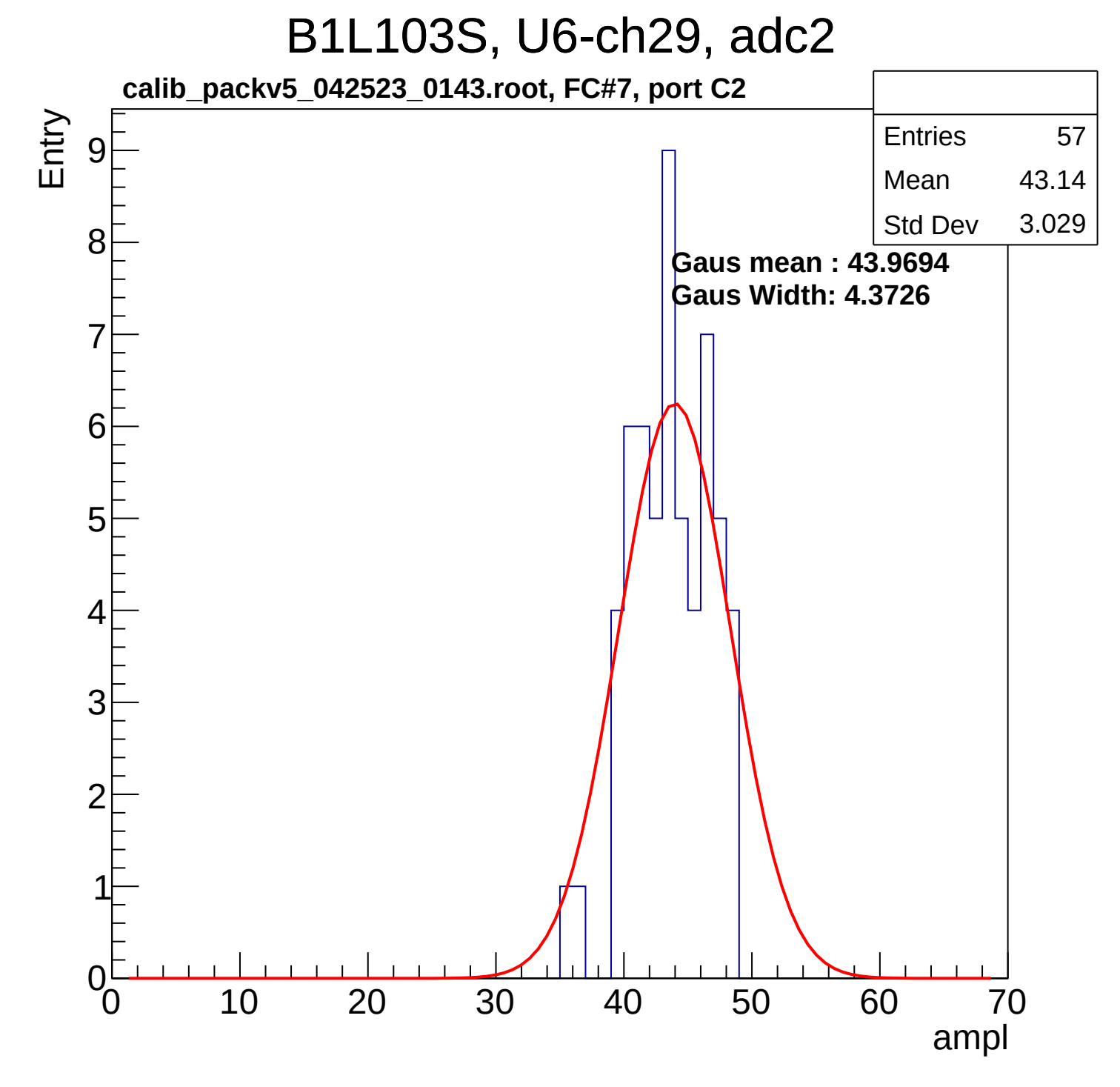
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch28, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 60.47
Std Dev 1.454
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch28, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch29, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 30.54
Std Dev 3.259
Gaus mean : 31.2895
Gaus Width: 3.4553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch29, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 37.24
Std Dev 3.318
Gaus mean : 37.7286
Gaus Width: 3.5621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch29, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.14
Std Dev 3.029
Gaus mean : 43.9694
Gaus Width: 4.3726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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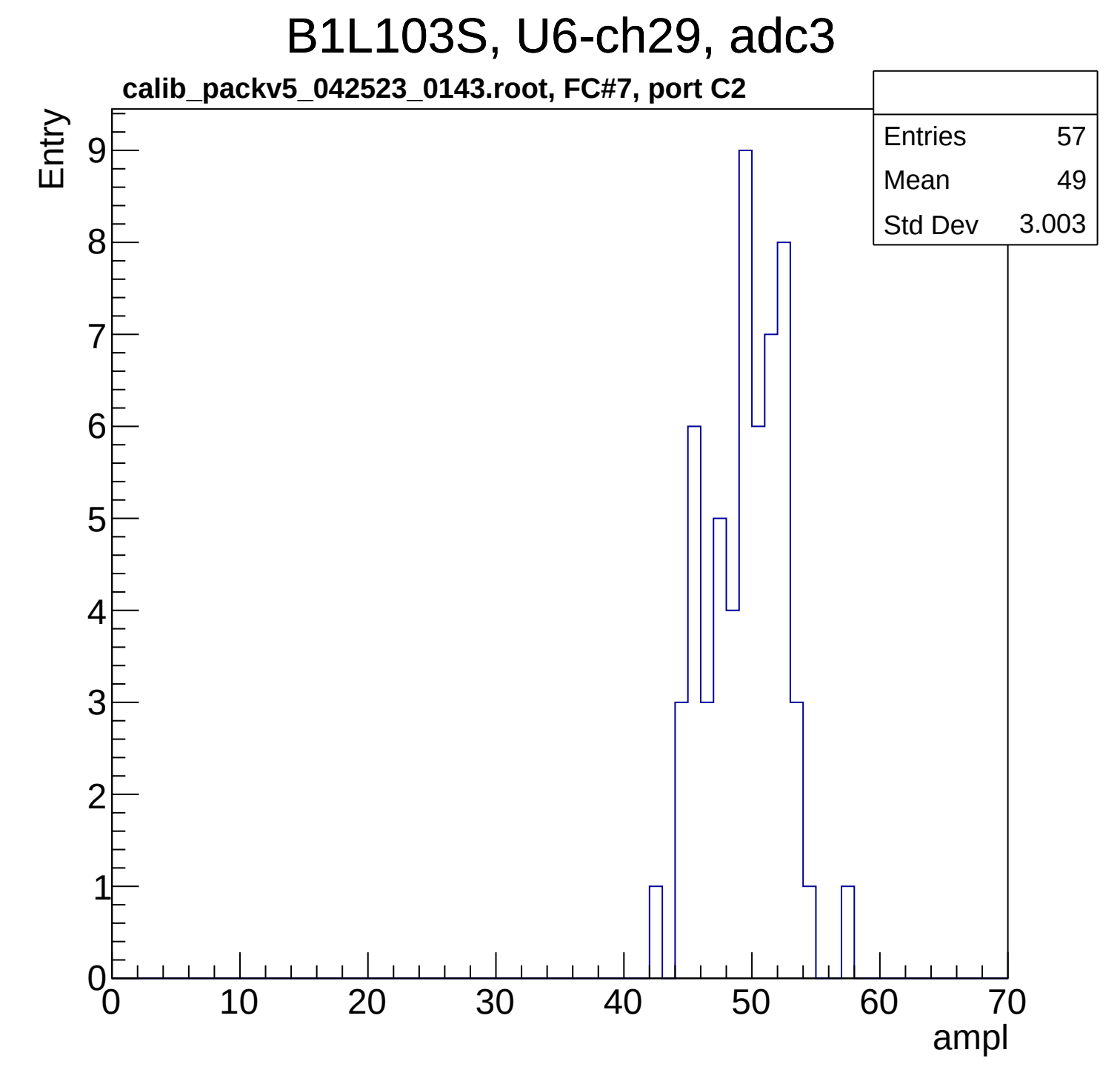
B1L103S, U6-ch29, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 49
Std Dev 3.003
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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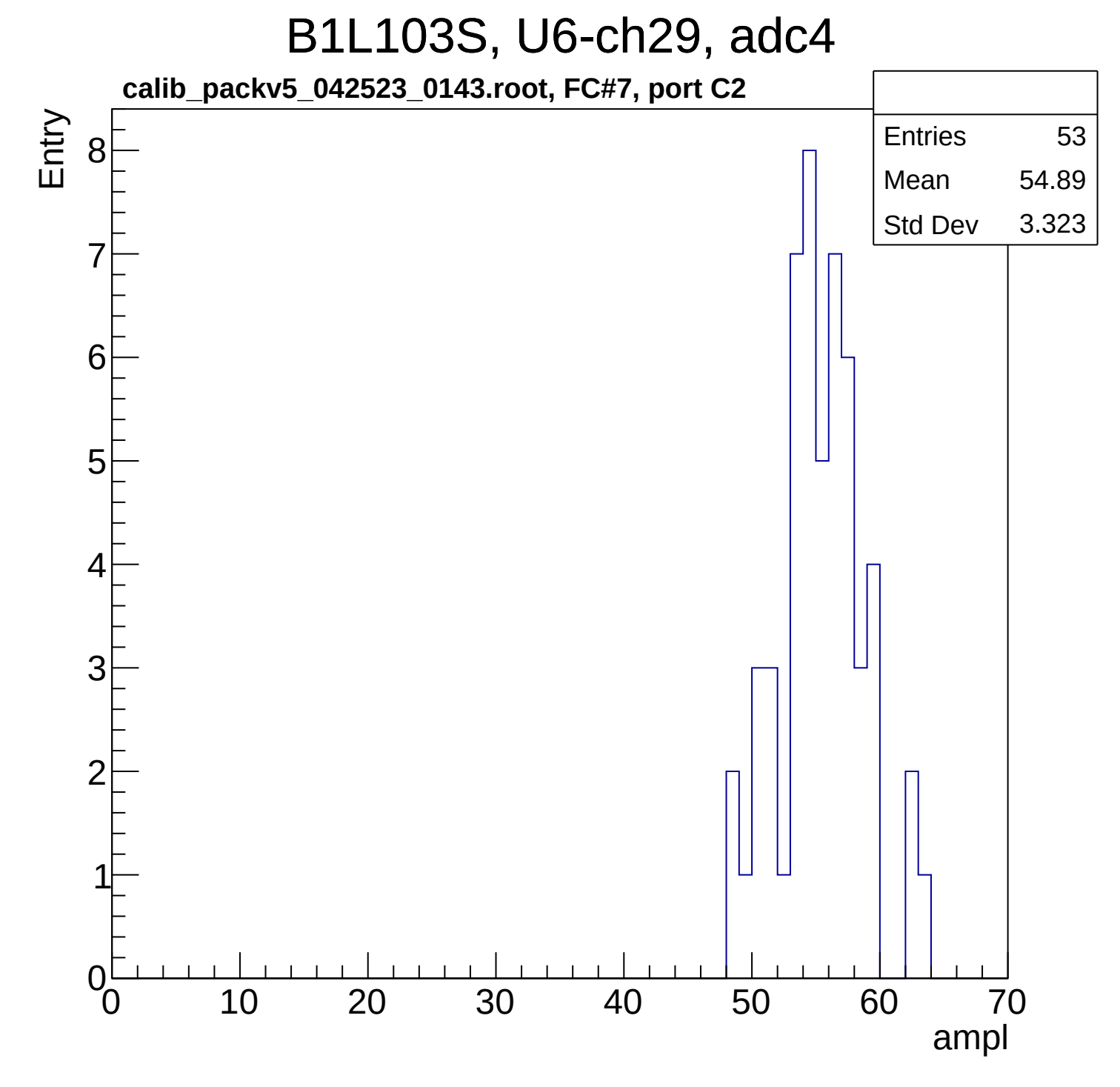
60
70


B1L103S, U6-ch29, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 54.89
Std Dev 3.323
Entry
ampl
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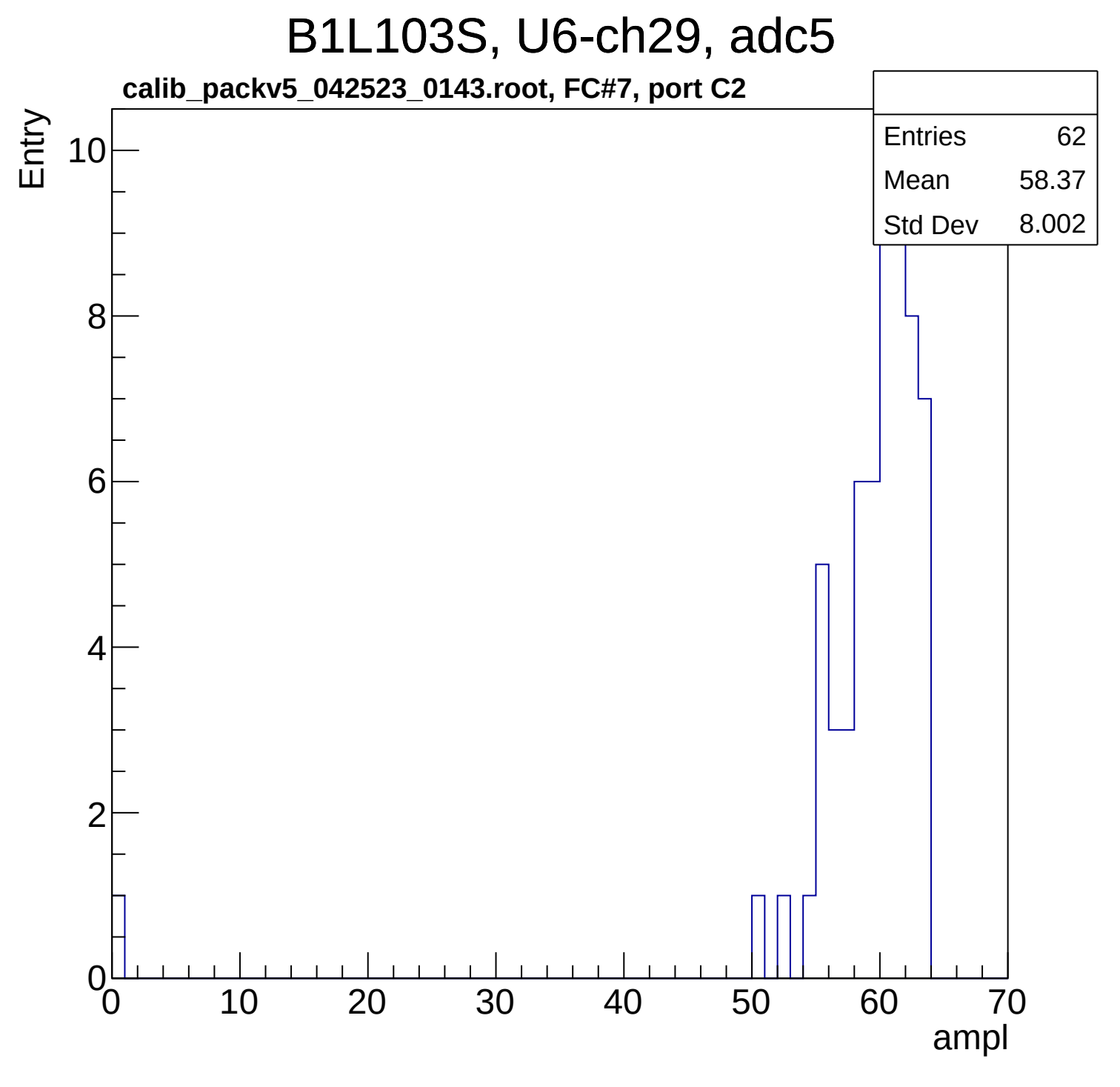
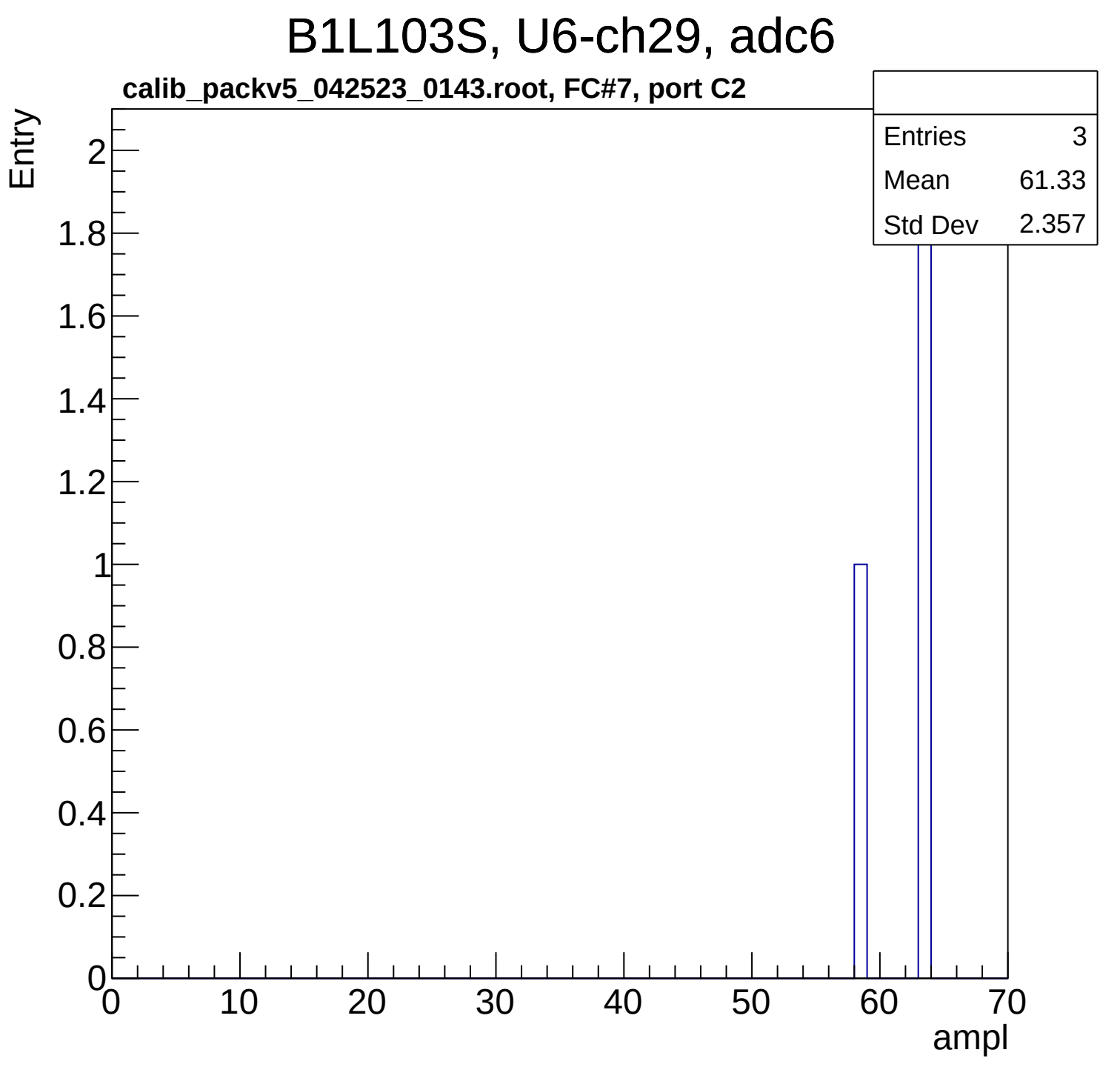
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch29, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 58.37
Std Dev 8.002
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch29, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.33
Std Dev 2.357
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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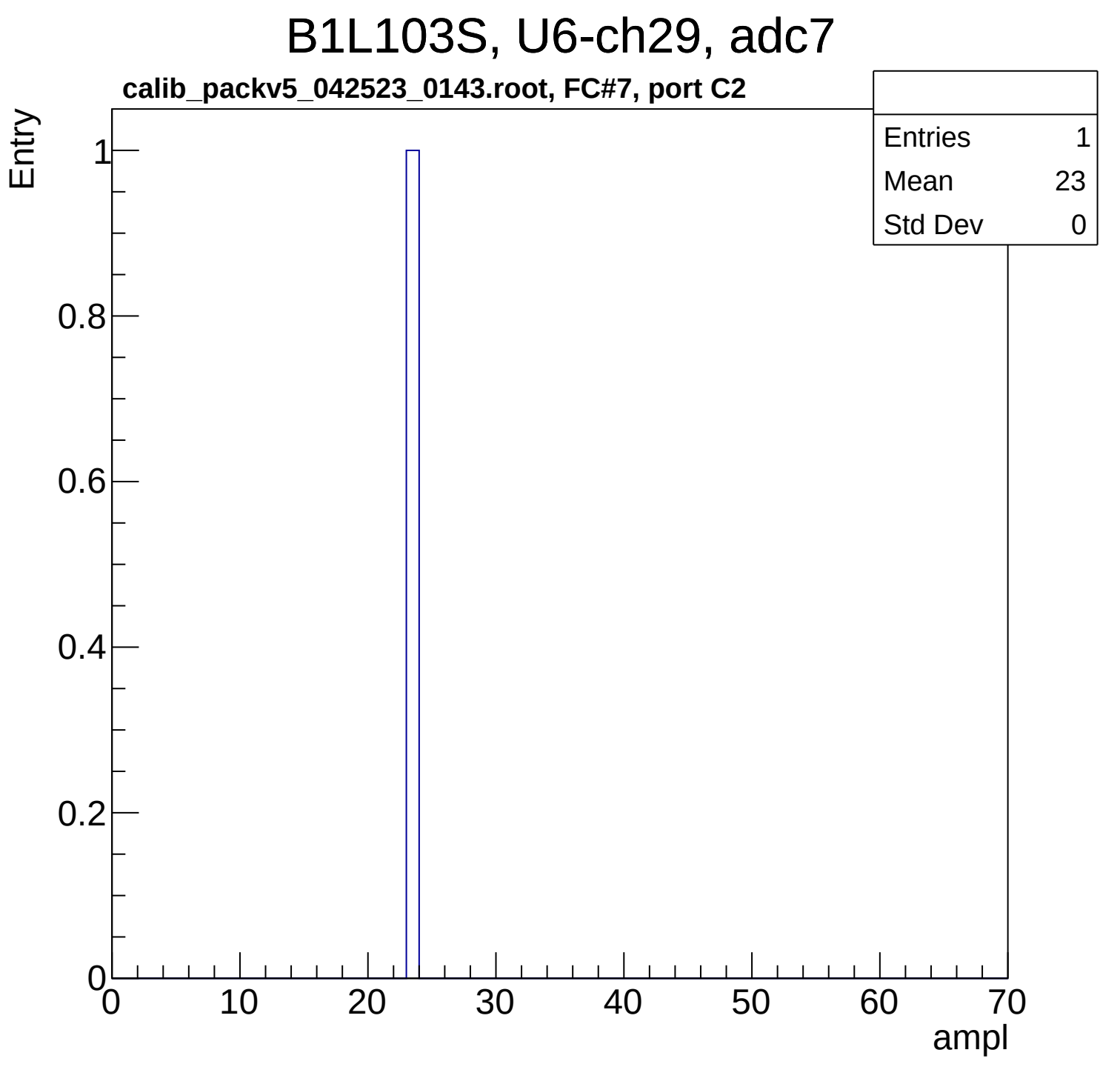
30
40
50
60
70


B1L103S, U6-ch29, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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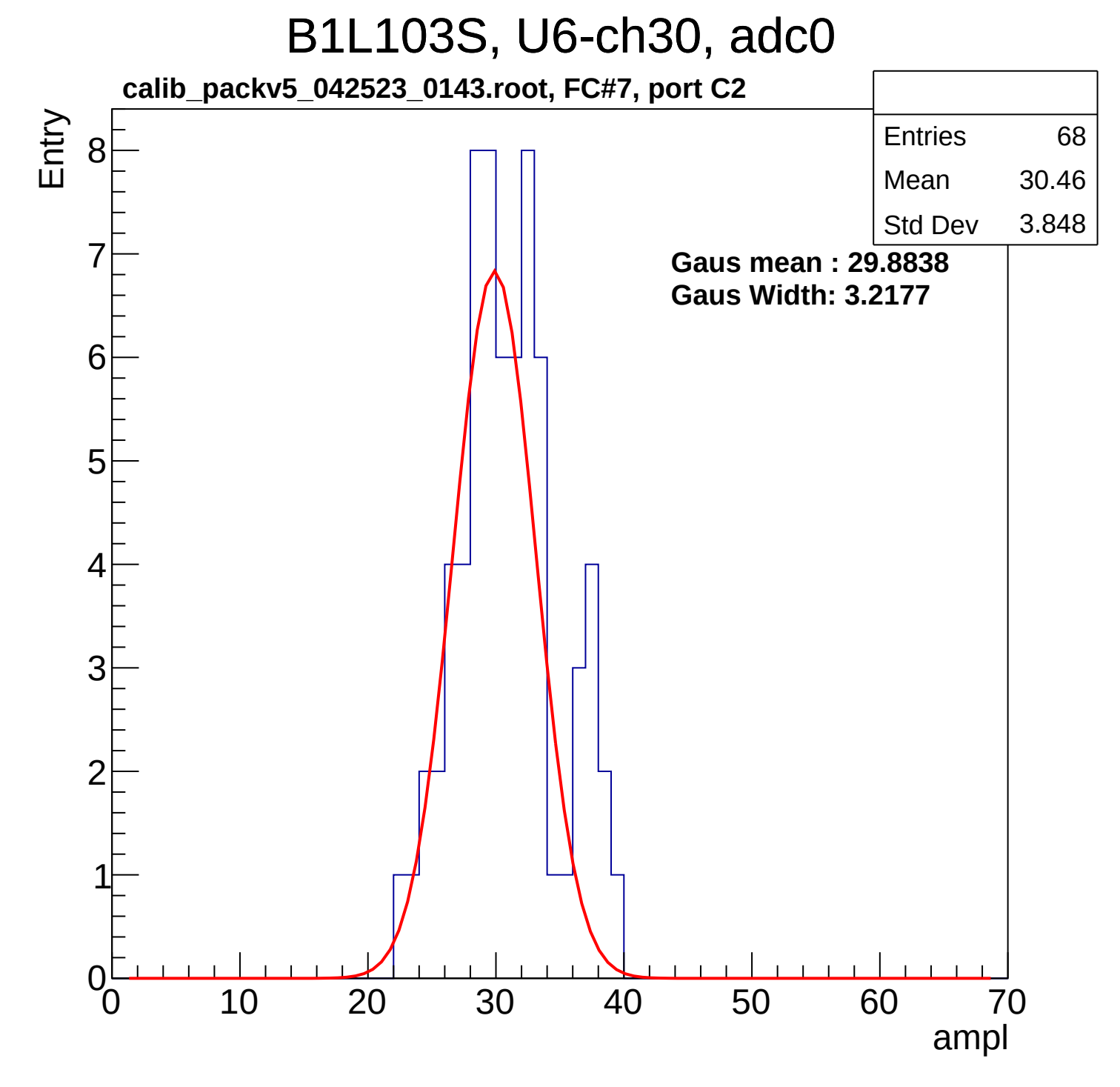
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch30, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.46
Std Dev 3.848
Gaus mean : 29.8838
Gaus Width: 3.2177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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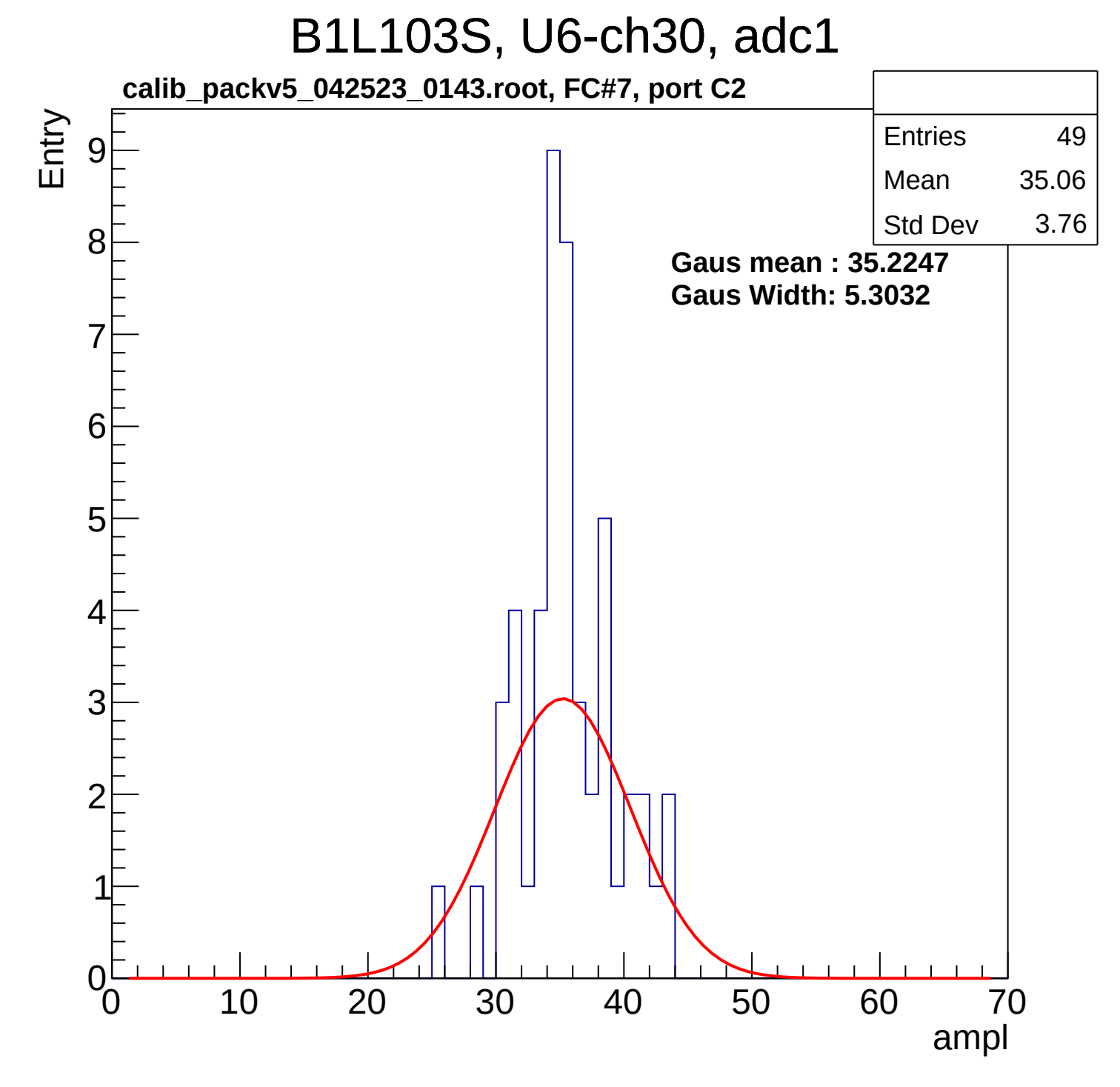
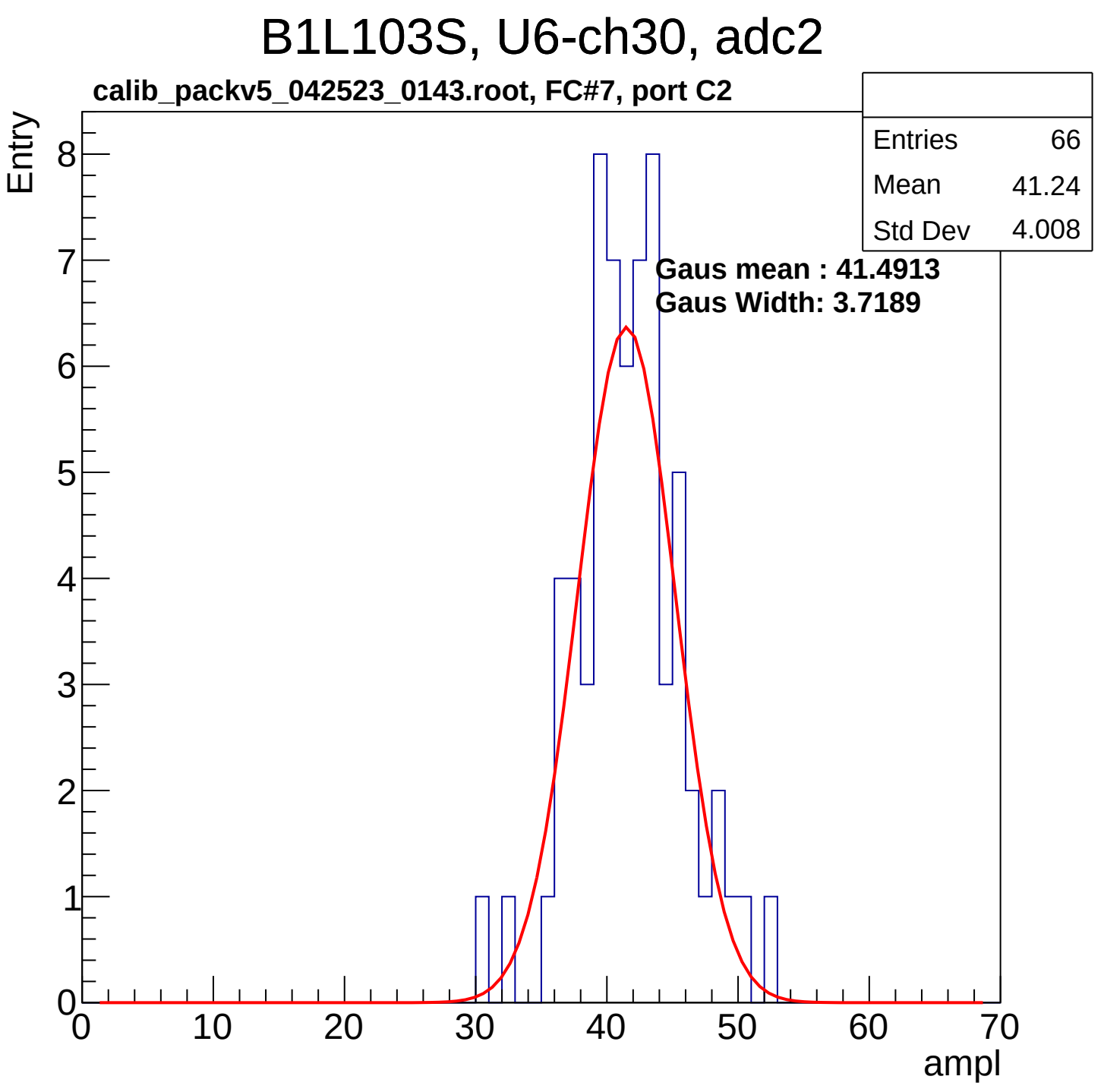

B1L103S, U6-ch30, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 35.06
Std Dev 3.76
Gaus mean : 35.2247
Gaus Width: 5.3032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch30, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 41.24
Std Dev 4.008
Gaus mean : 41.4913
Gaus Width: 3.7189
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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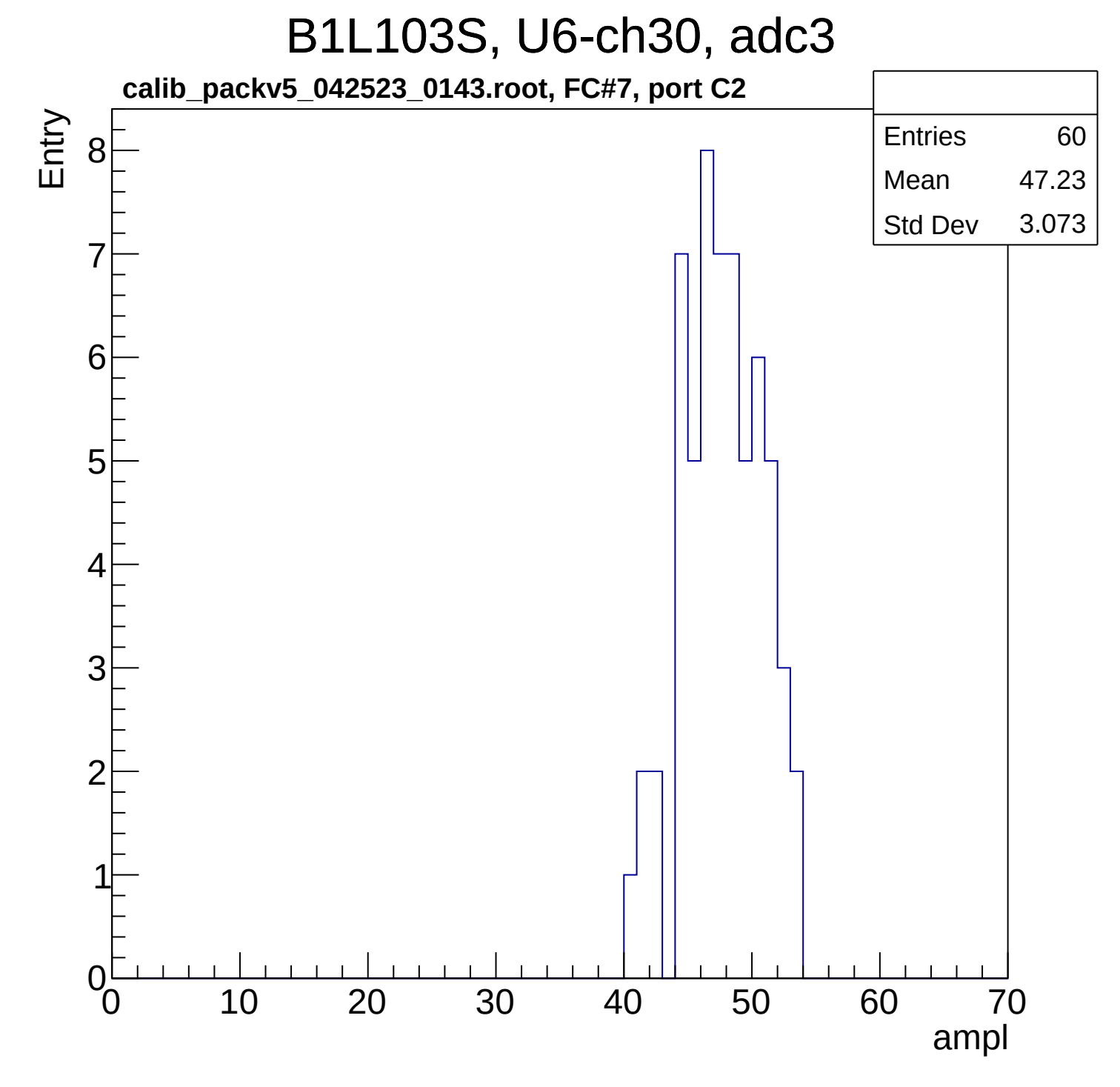
B1L103S, U6-ch30, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 47.23
Std Dev 3.073
Entry
ampl
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3
4
5
6
7
8
0
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20
30
40
50
60
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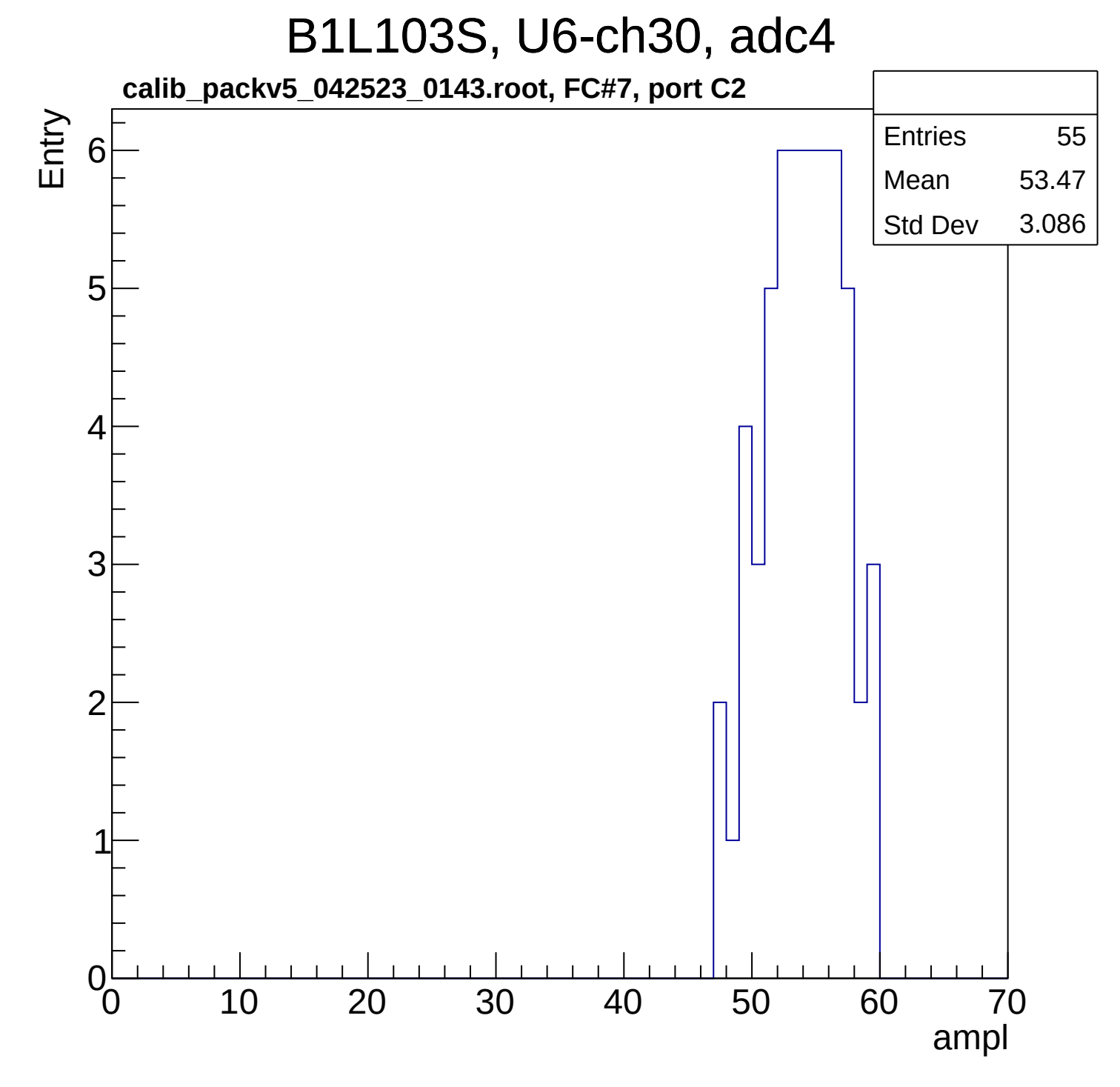
B1L103S, U6-ch30, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 53.47
Std Dev 3.086
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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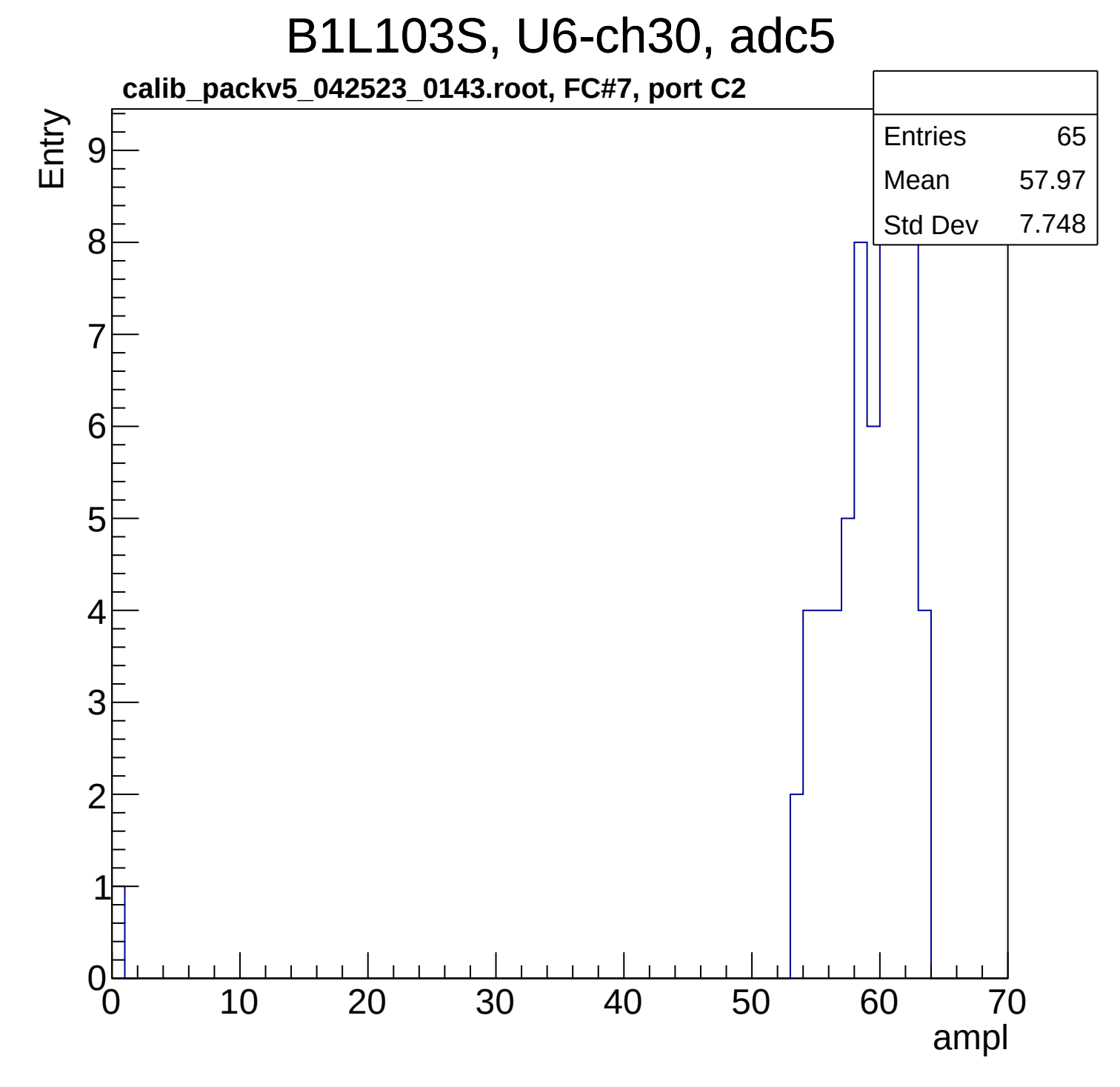
B1L103S, U6-ch30, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 57.97
Std Dev 7.748
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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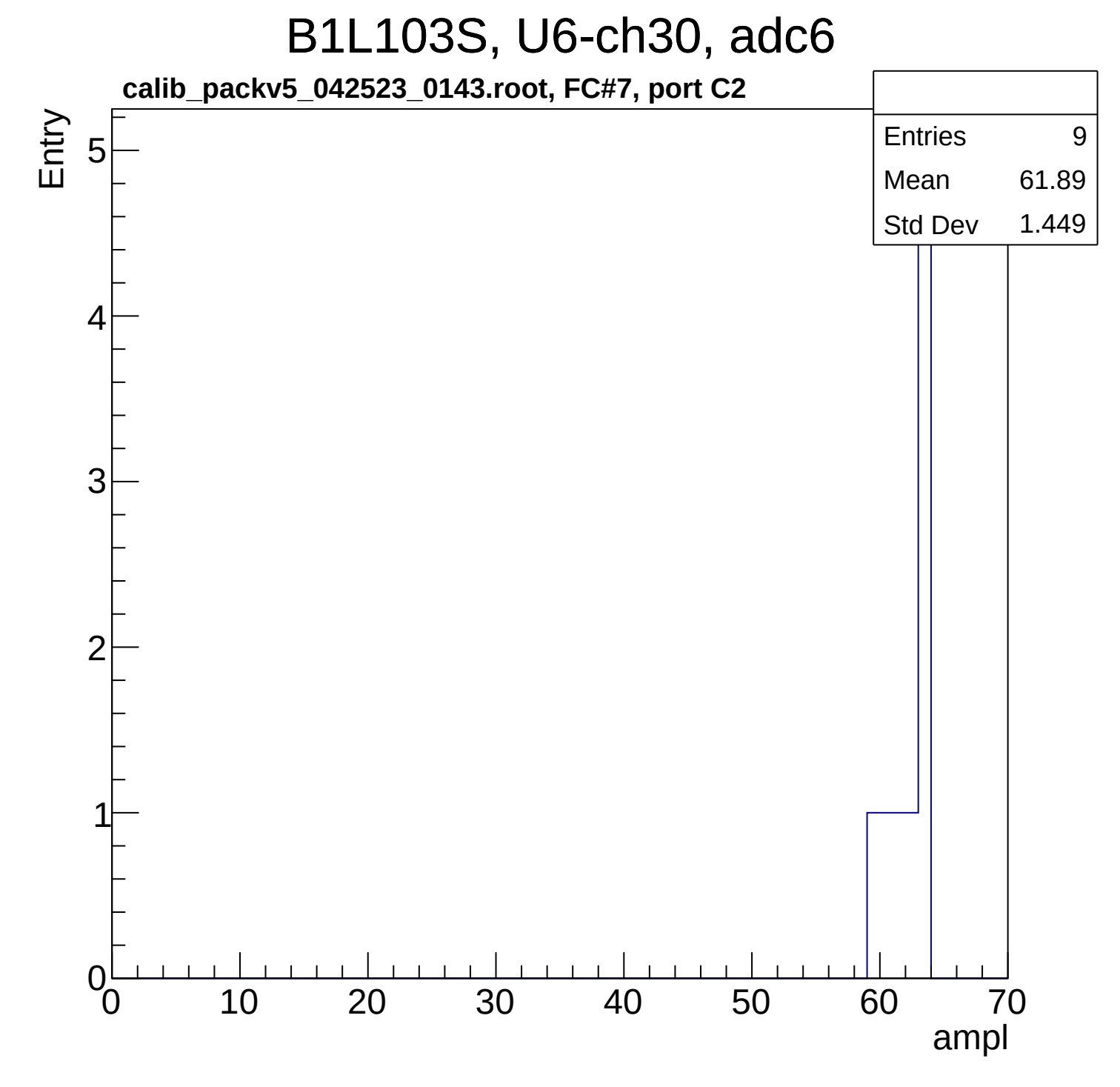
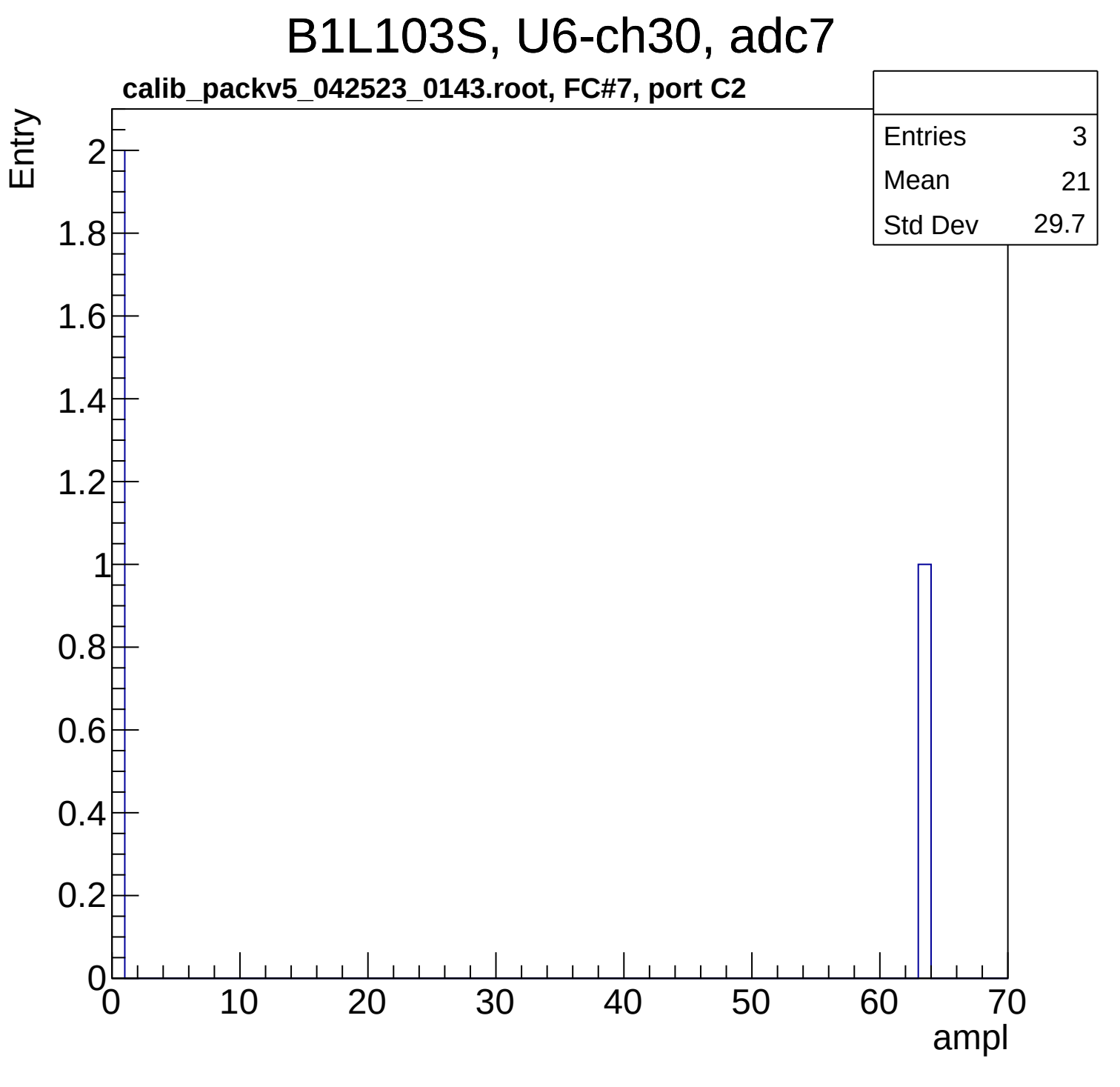

B1L103S, U6-ch30, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.89
Std Dev 1.449
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch30, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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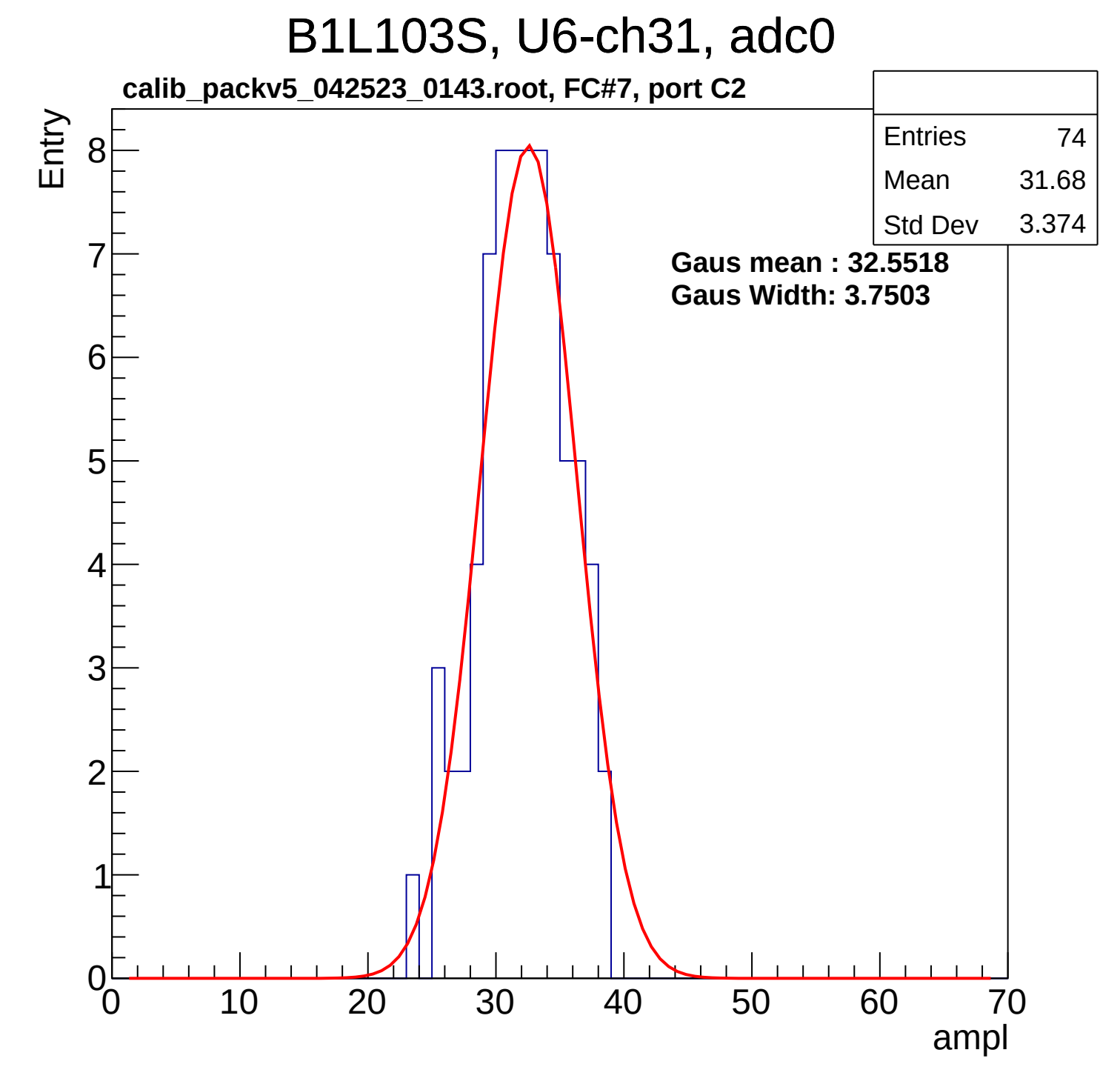
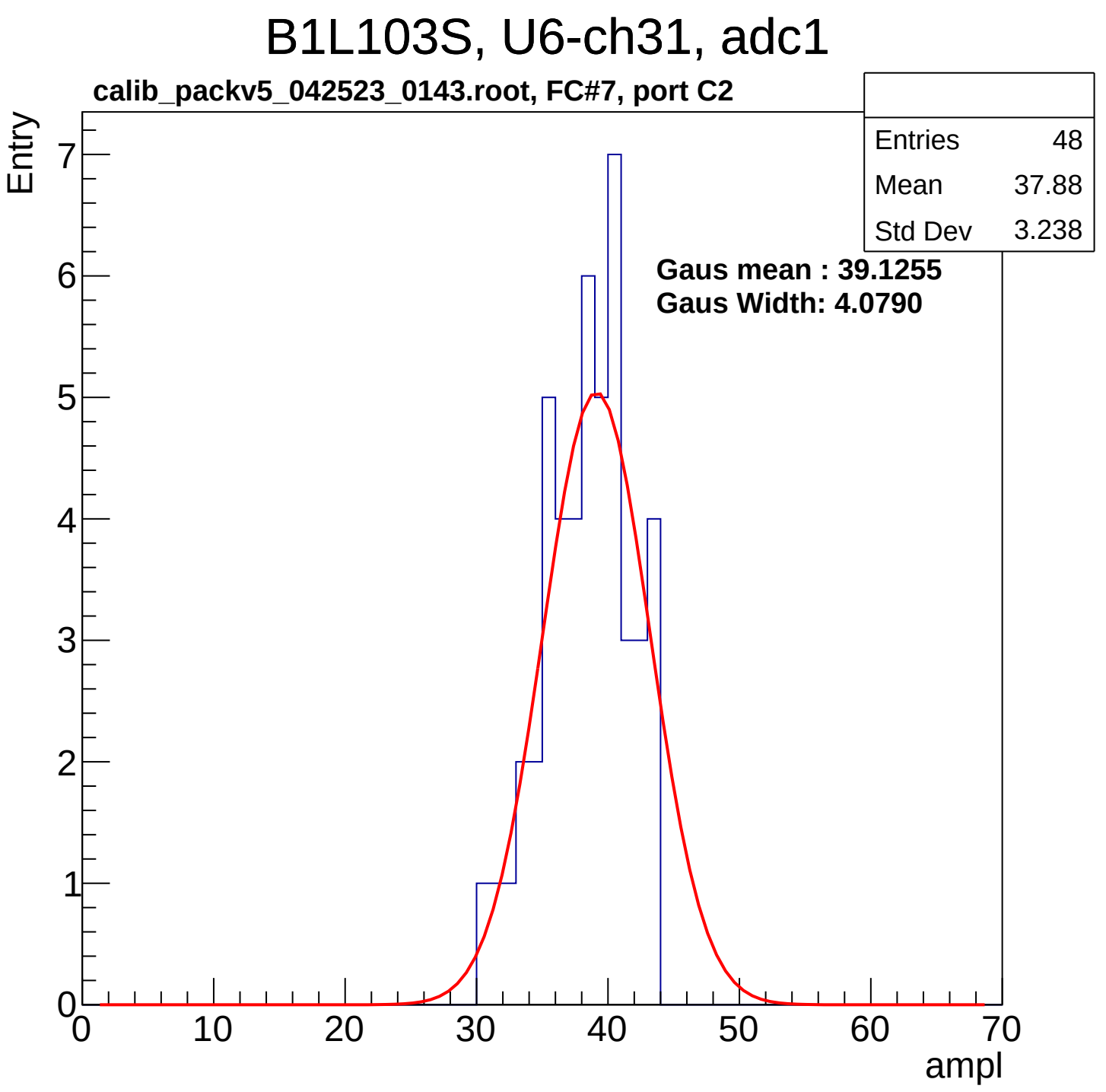

B1L103S, U6-ch31, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 31.68
Std Dev 3.374
Gaus mean : 32.5518
Gaus Width: 3.7503
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch31, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 37.88
Std Dev 3.238
Gaus mean : 39.1255
Gaus Width: 4.0790
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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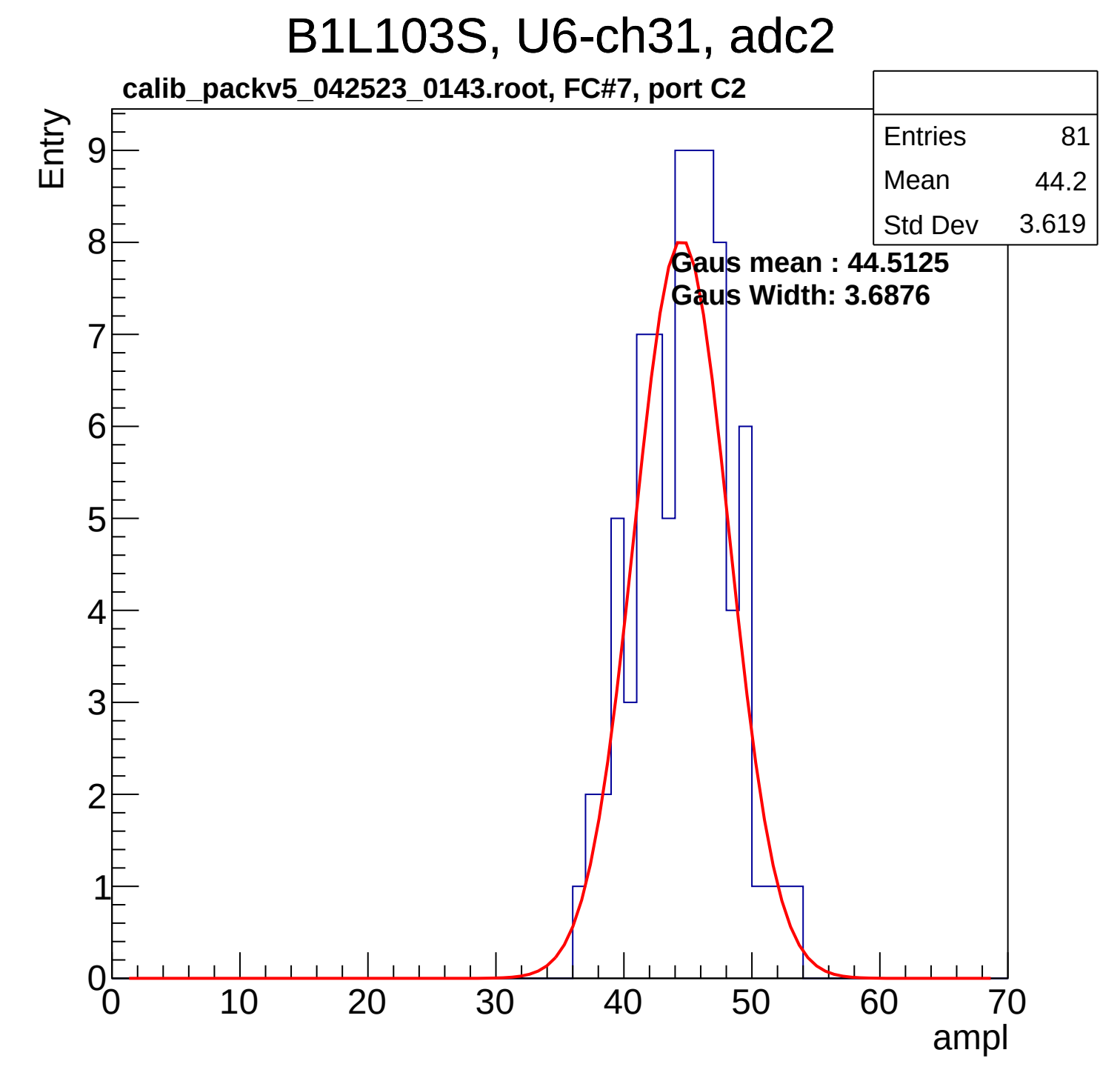
B1L103S, U6-ch31, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 44.2
Std Dev 3.619
Gaus mean : 44.5125
Gaus Width: 3.6876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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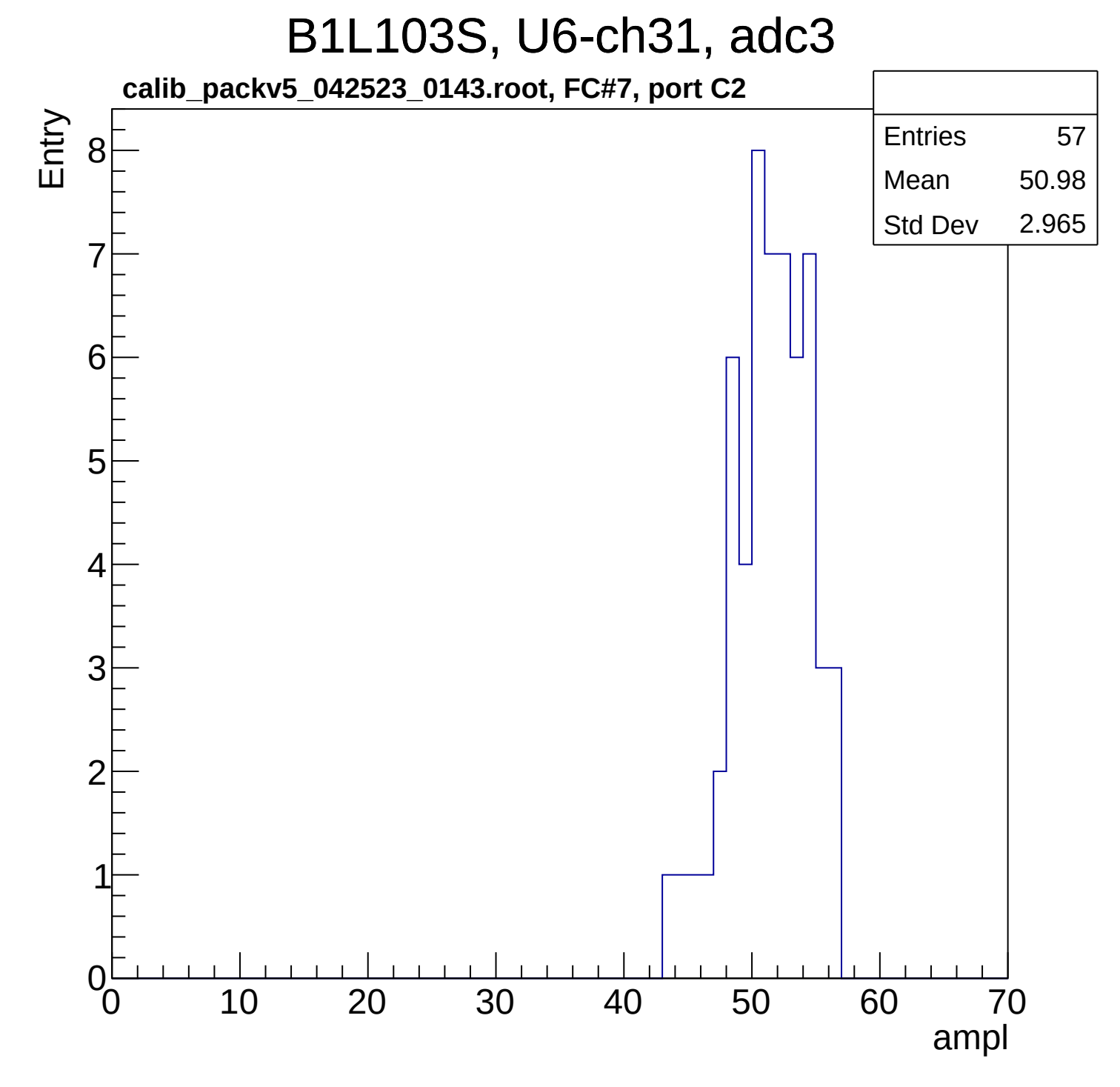
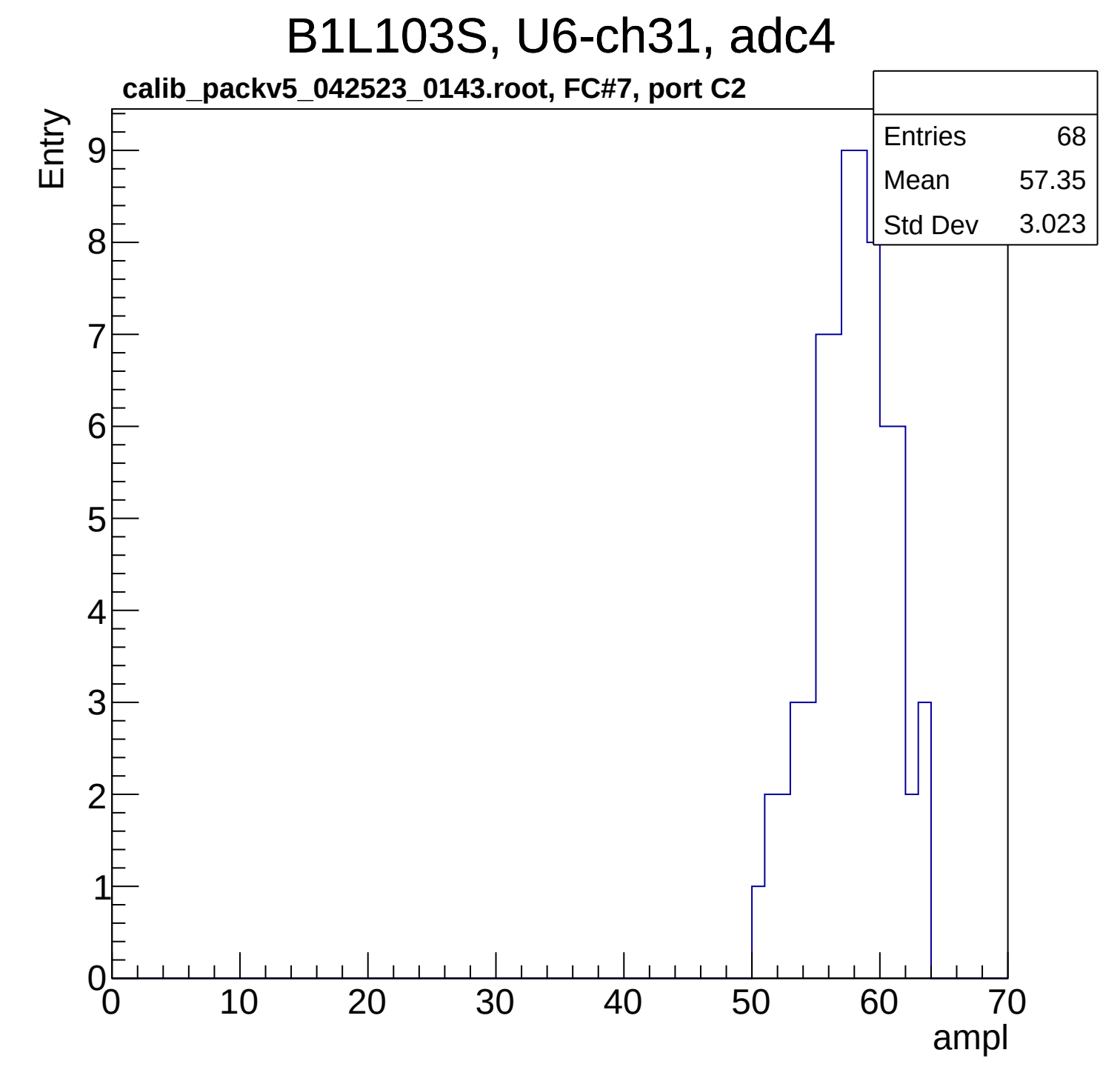

B1L103S, U6-ch31, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 50.98
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch31, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 57.35
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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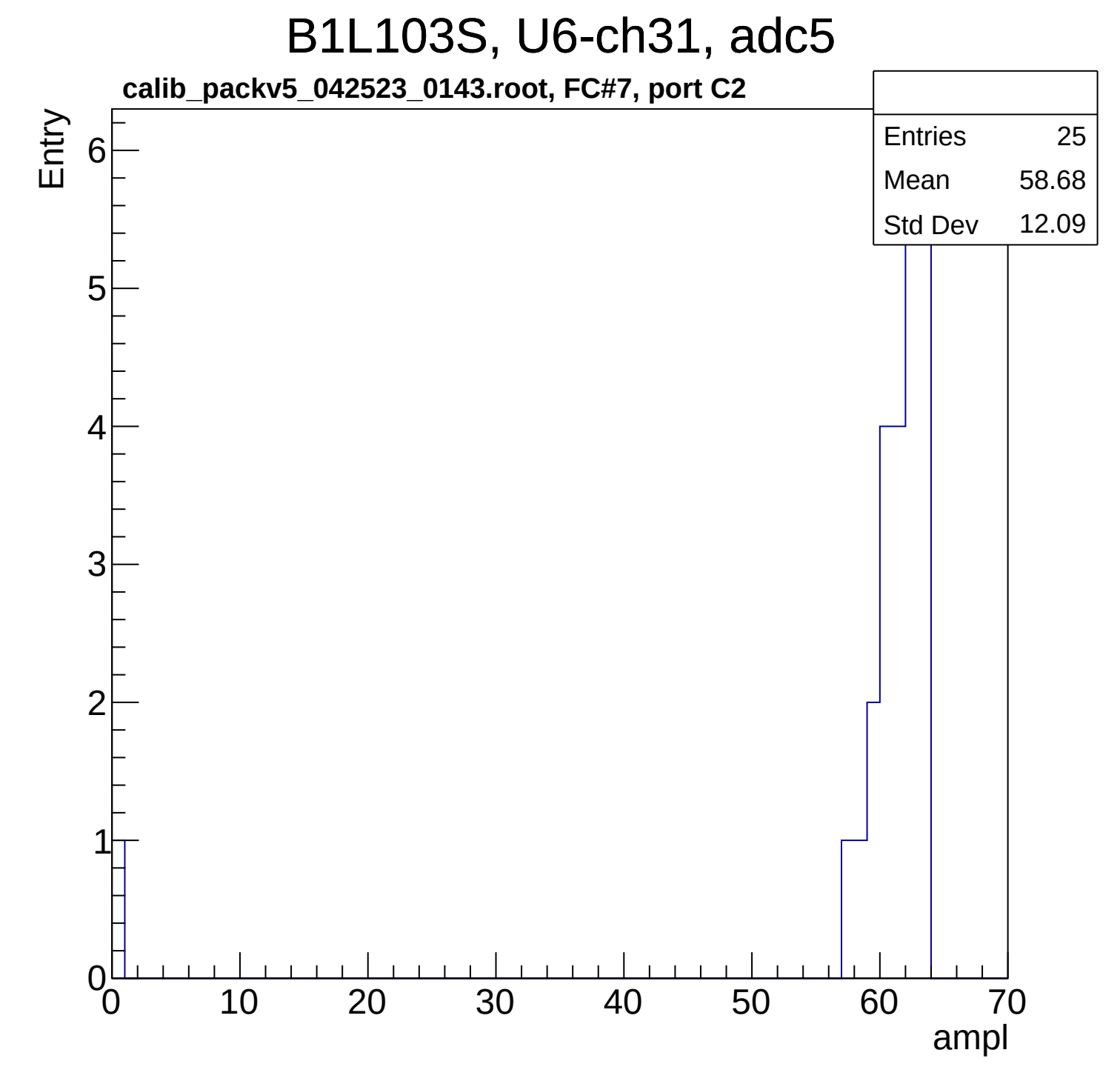
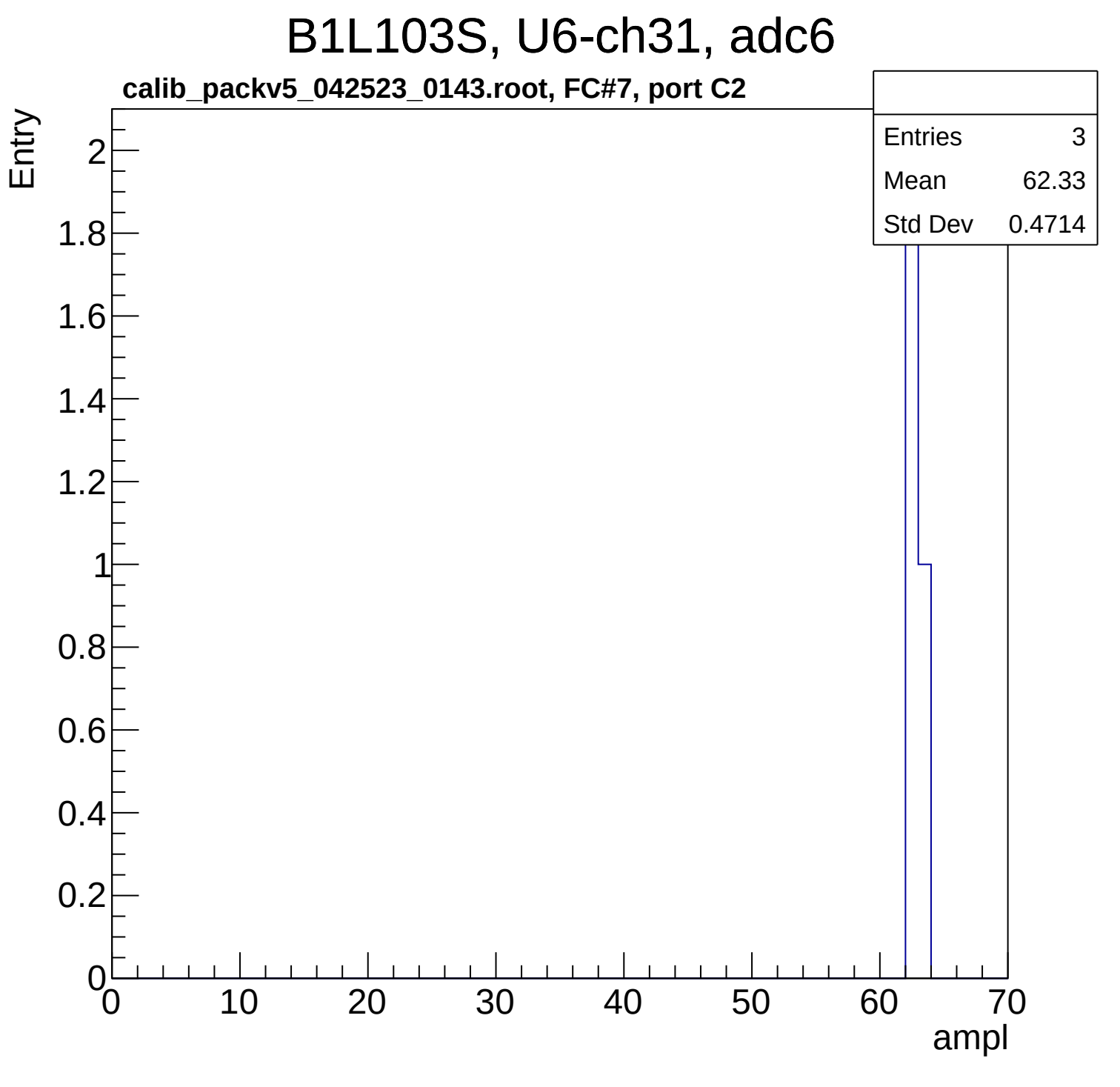
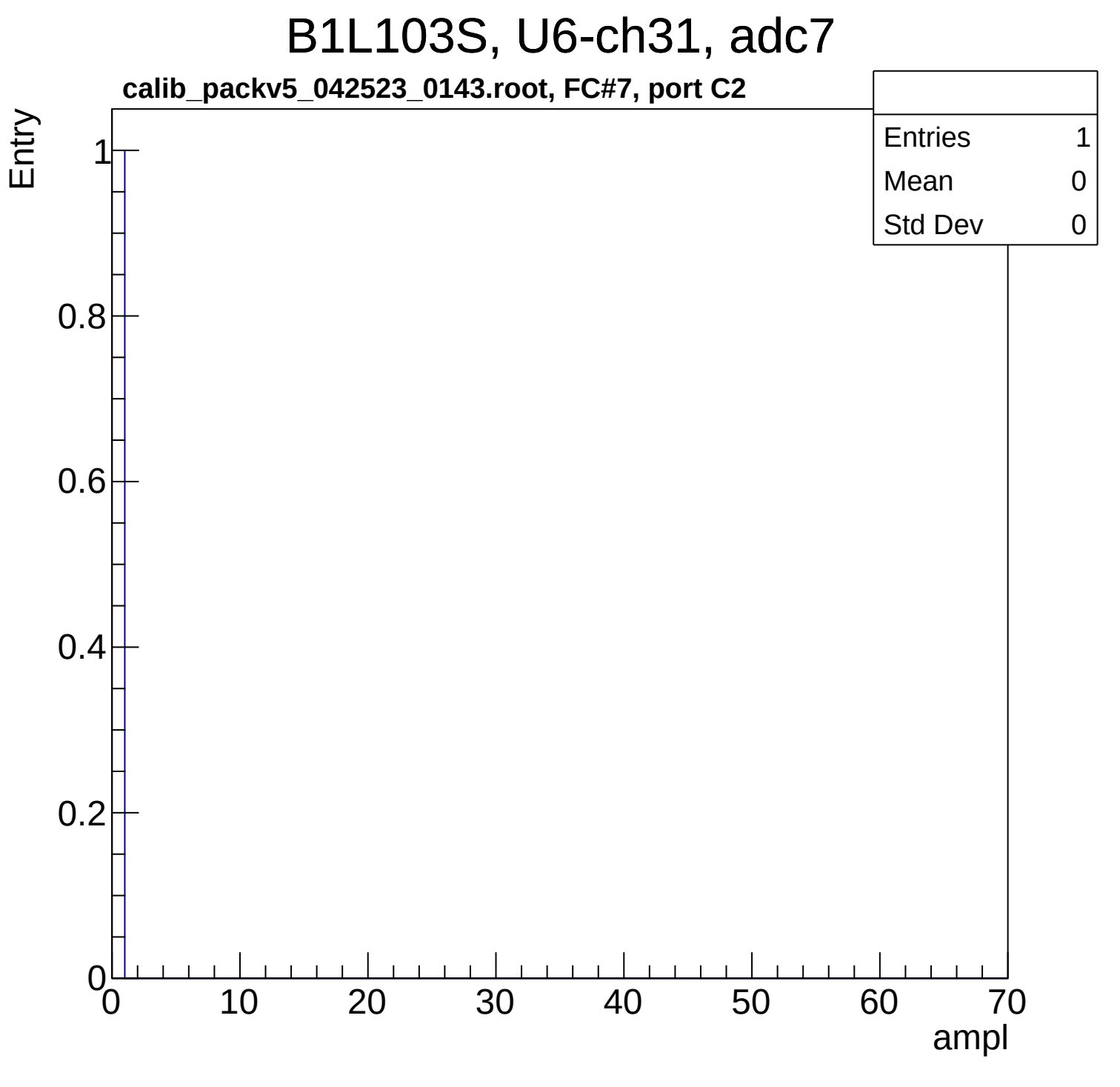
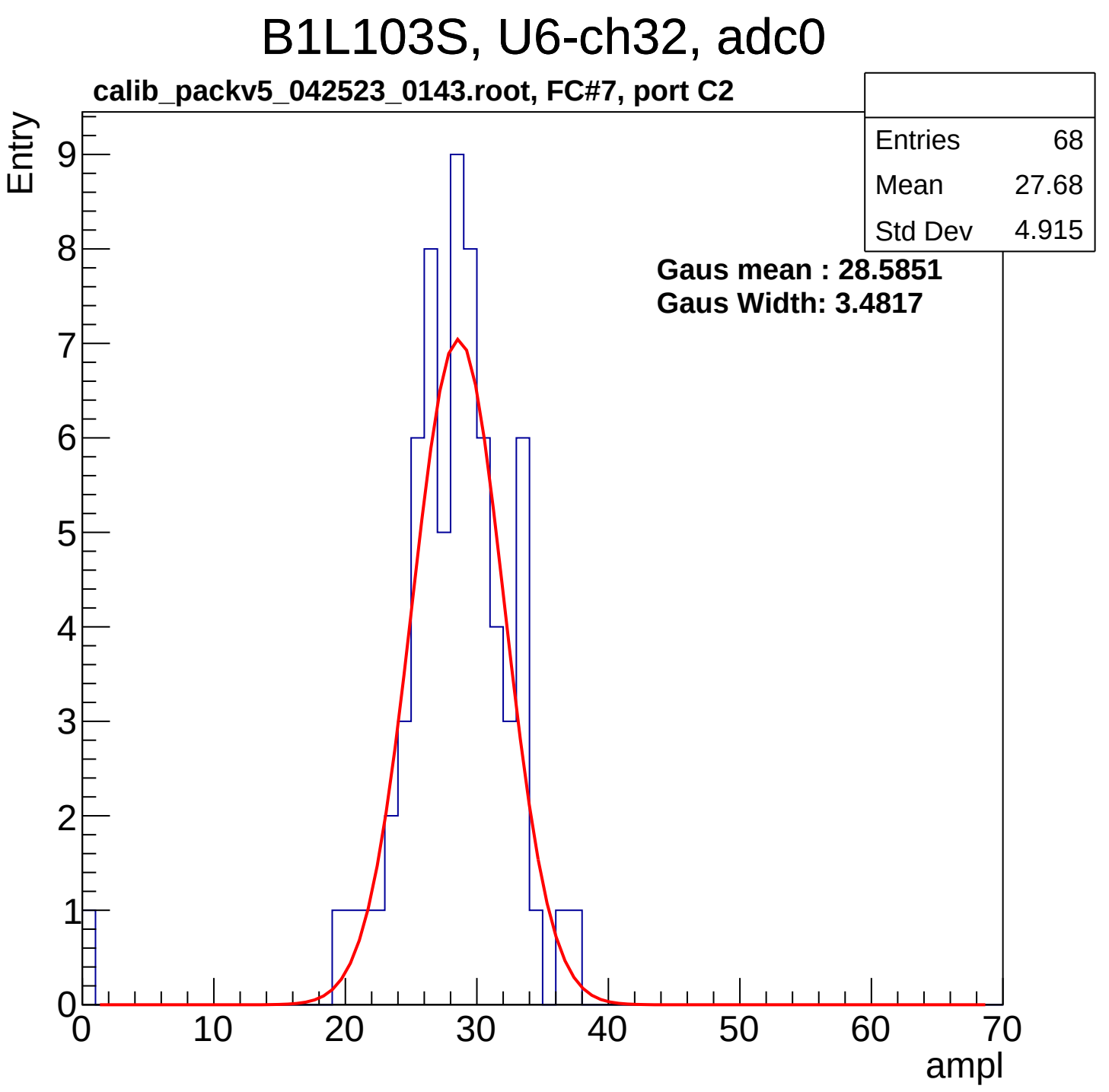
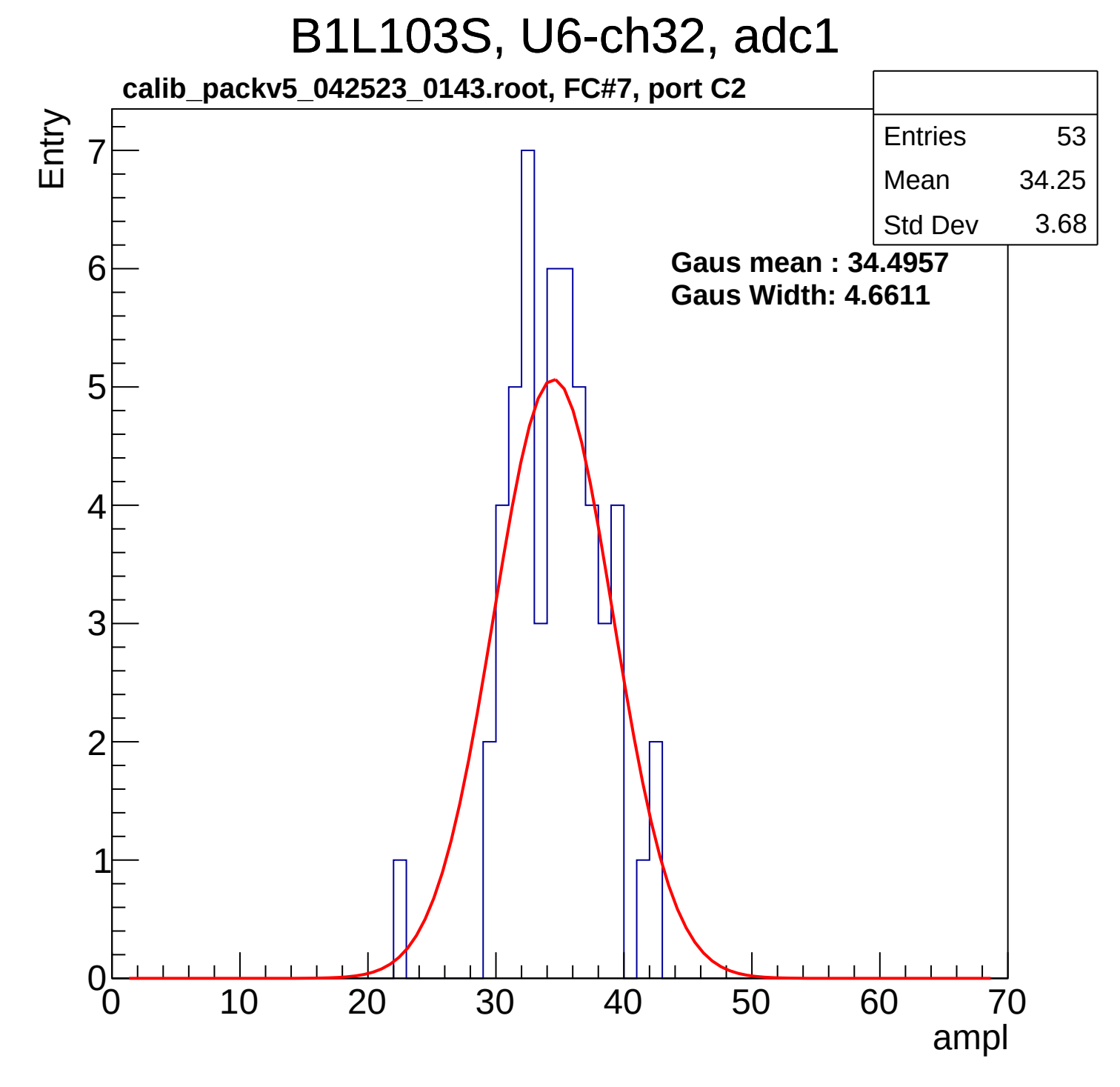

B1L103S, U6-ch31, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 25
Mean 58.68
Std Dev 12.09
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch31, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch31, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch32, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 27.68
Std Dev 4.915
Gaus mean : 28.5851
Gaus Width: 3.4817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch32, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 34.25
Std Dev 3.68
Gaus mean : 34.4957
Gaus Width: 4.6611
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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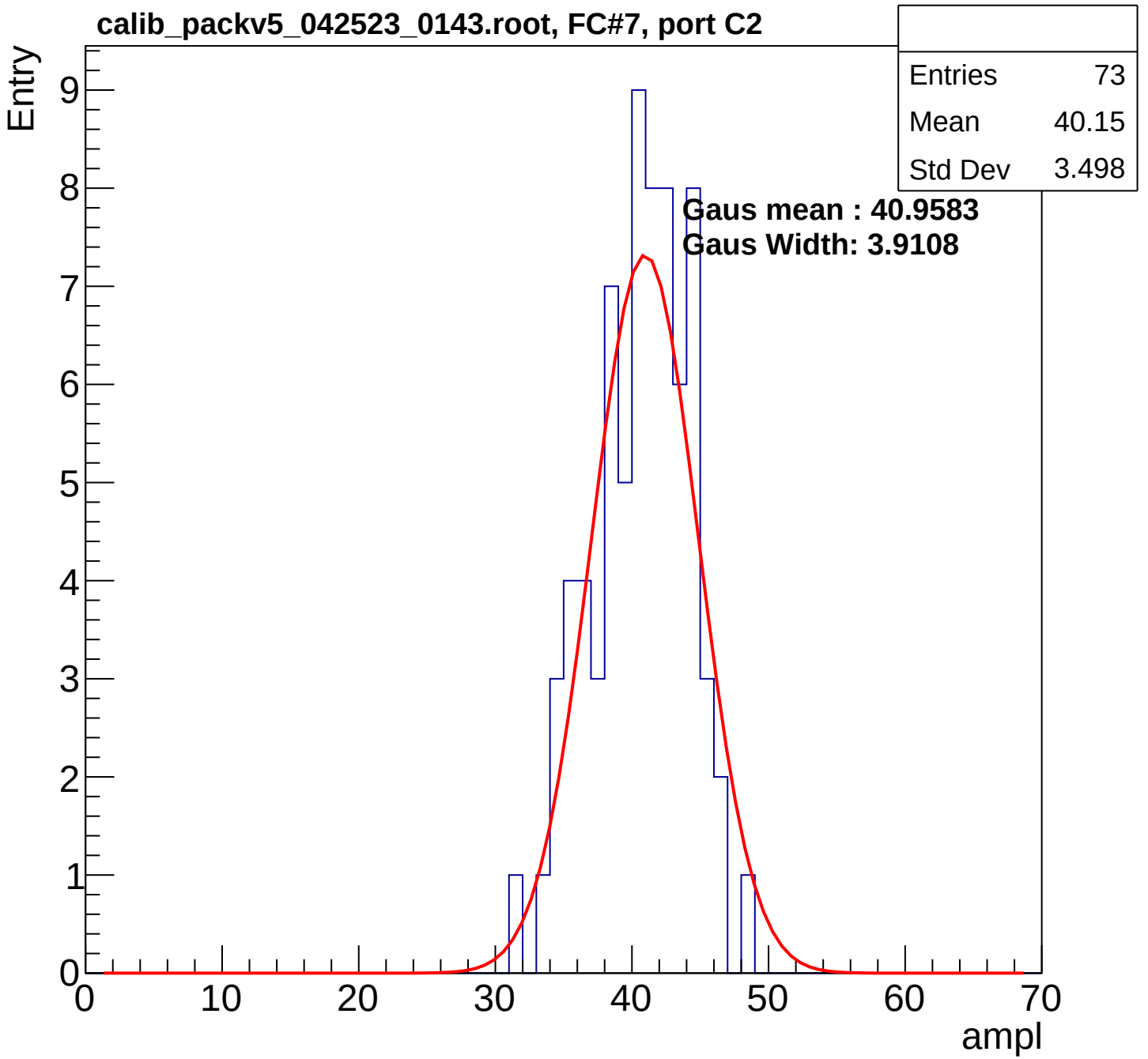
B1L103S, U6-ch32, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 40.15
Std Dev 3.498
Gaus mean : 40.9583
Gaus Width: 3.9108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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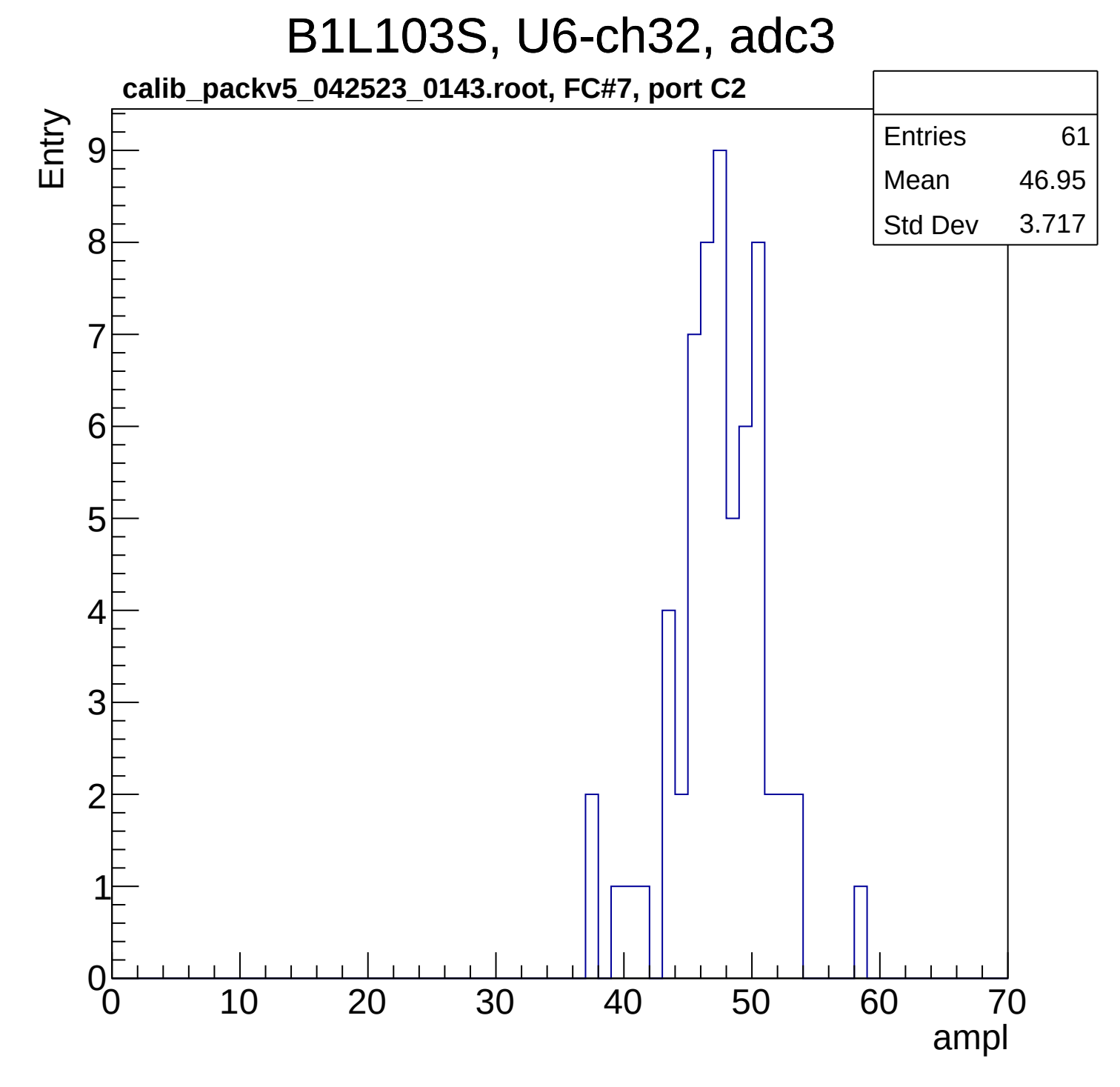
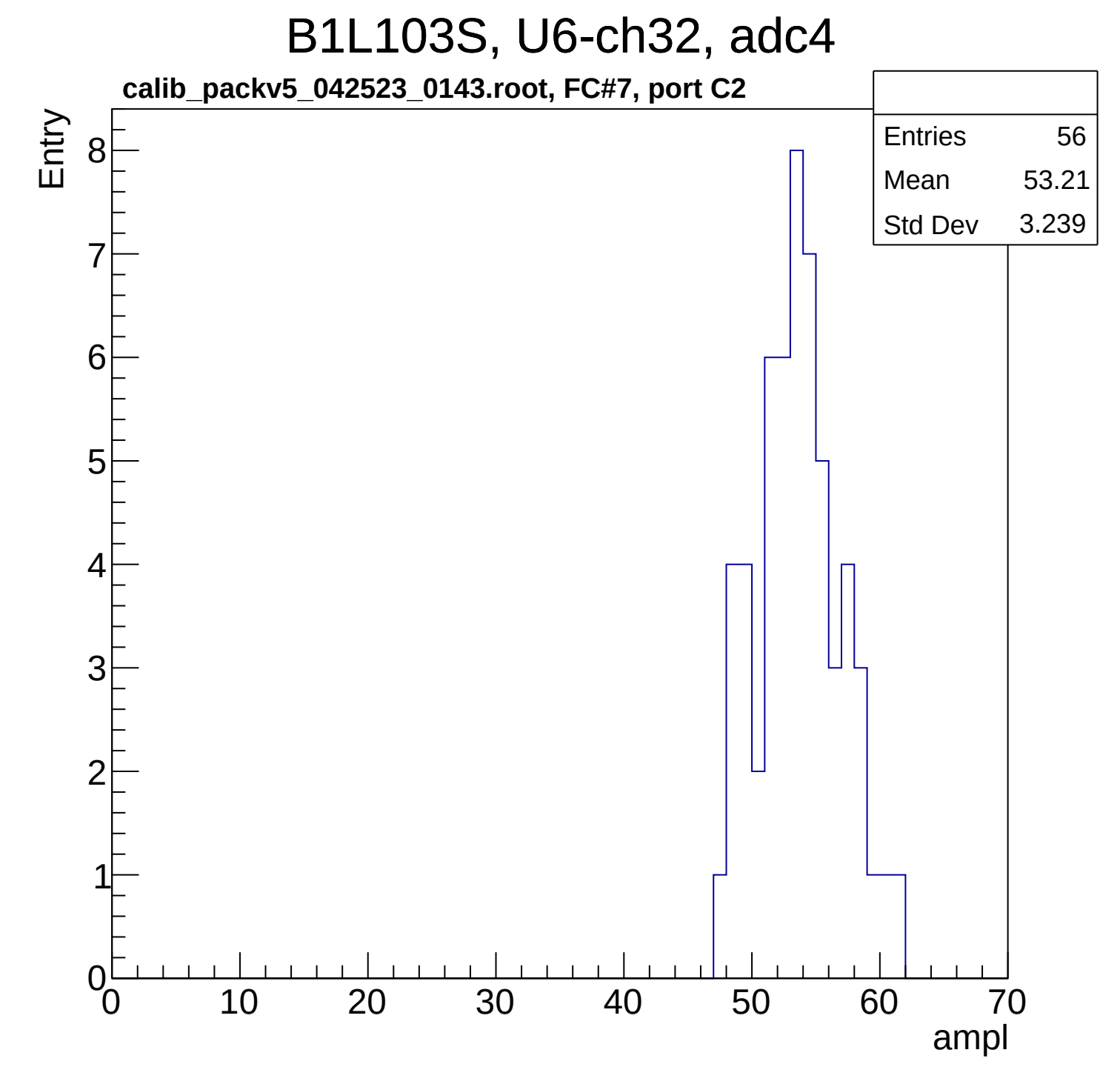

B1L103S, U6-ch32, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 46.95
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch32, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.21
Std Dev 3.239
Entry
ampl
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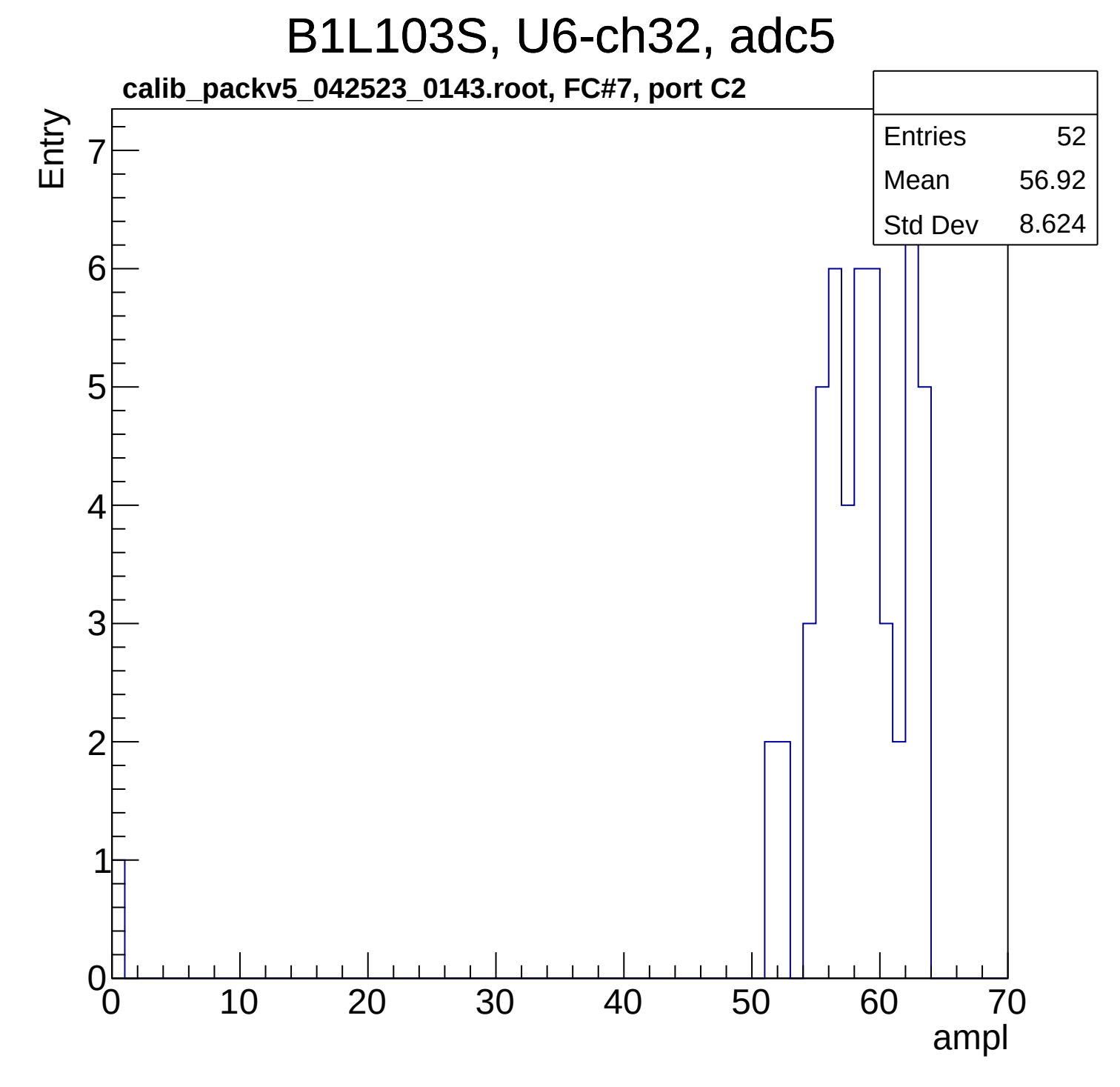
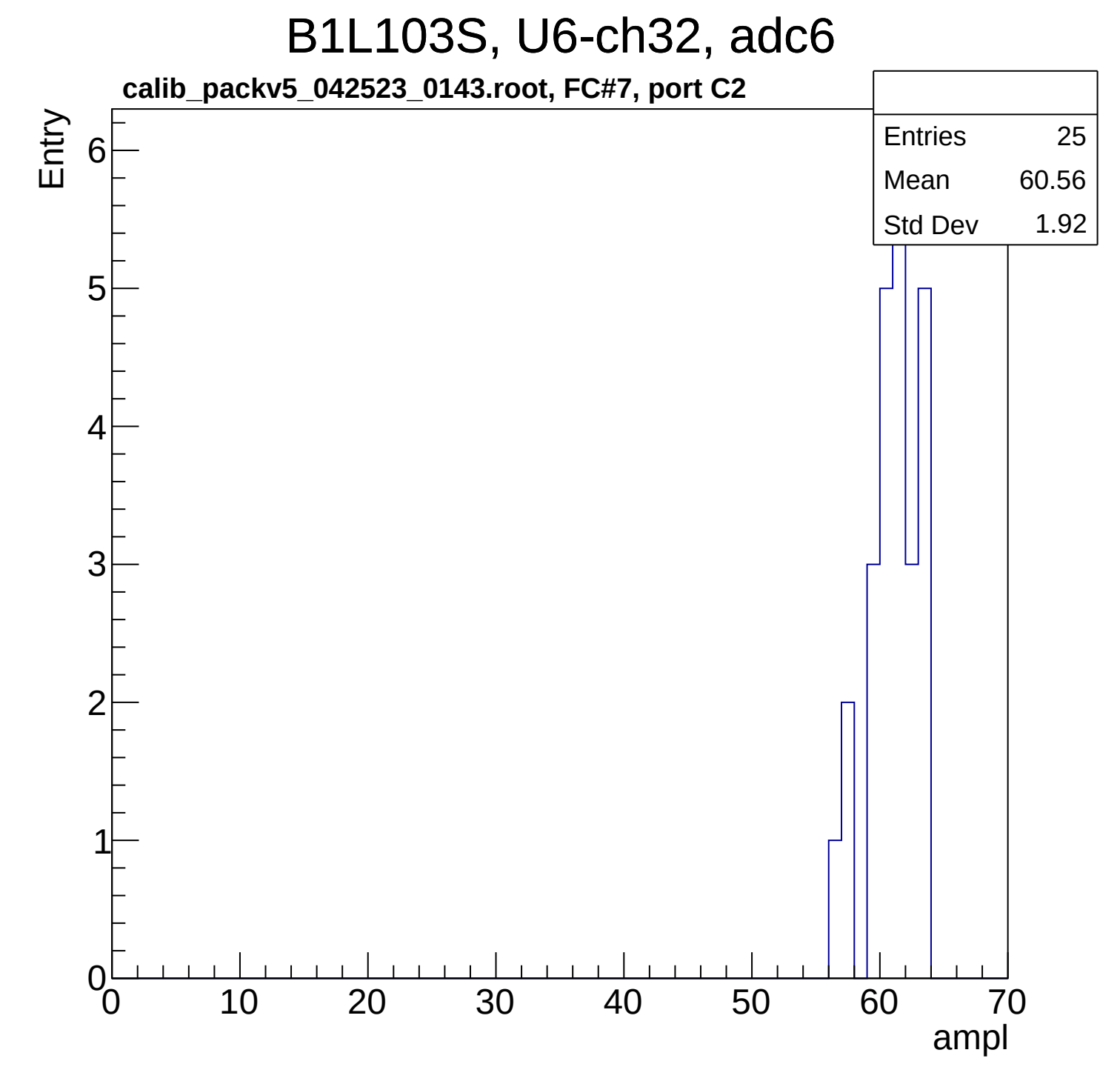
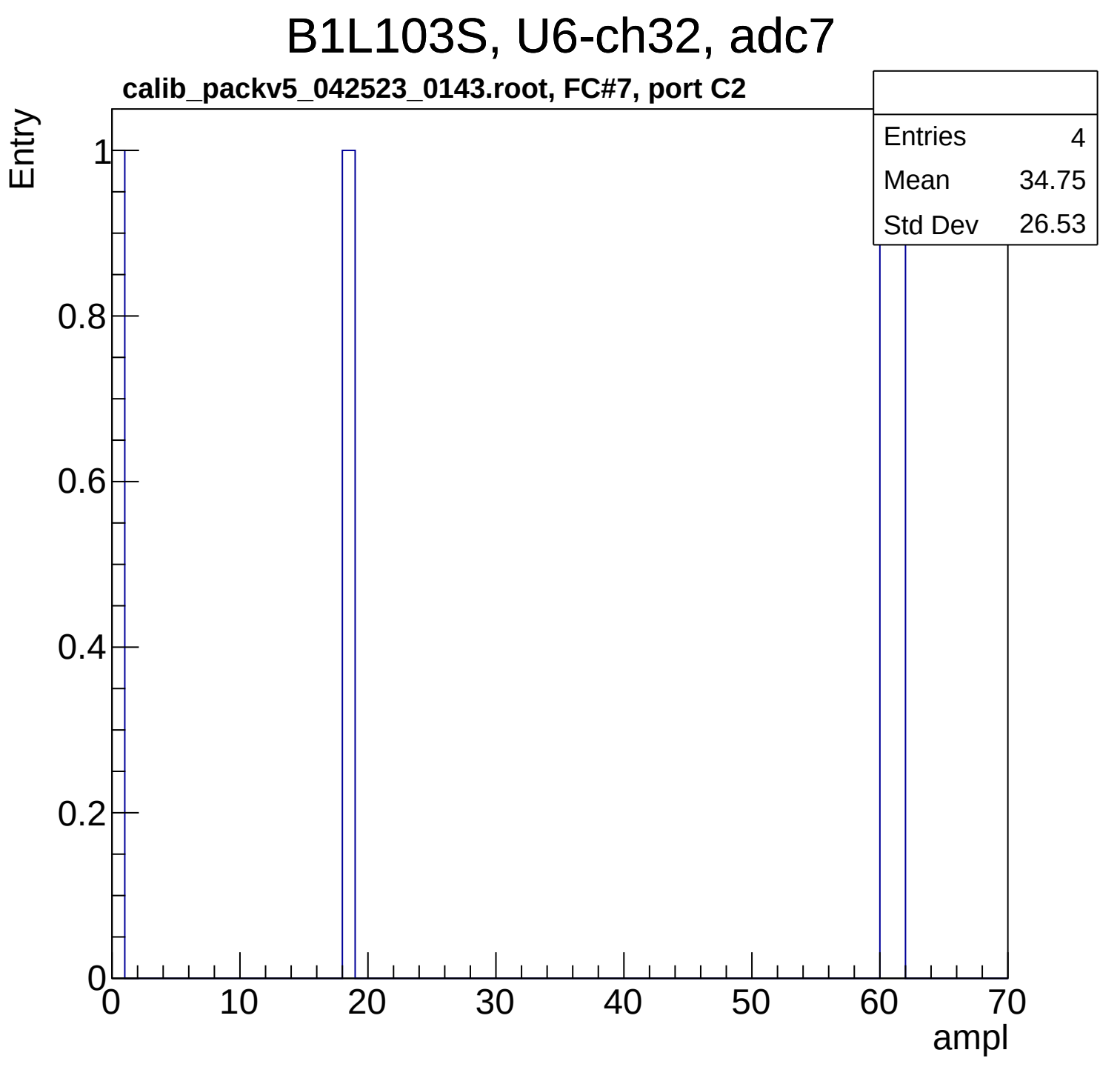
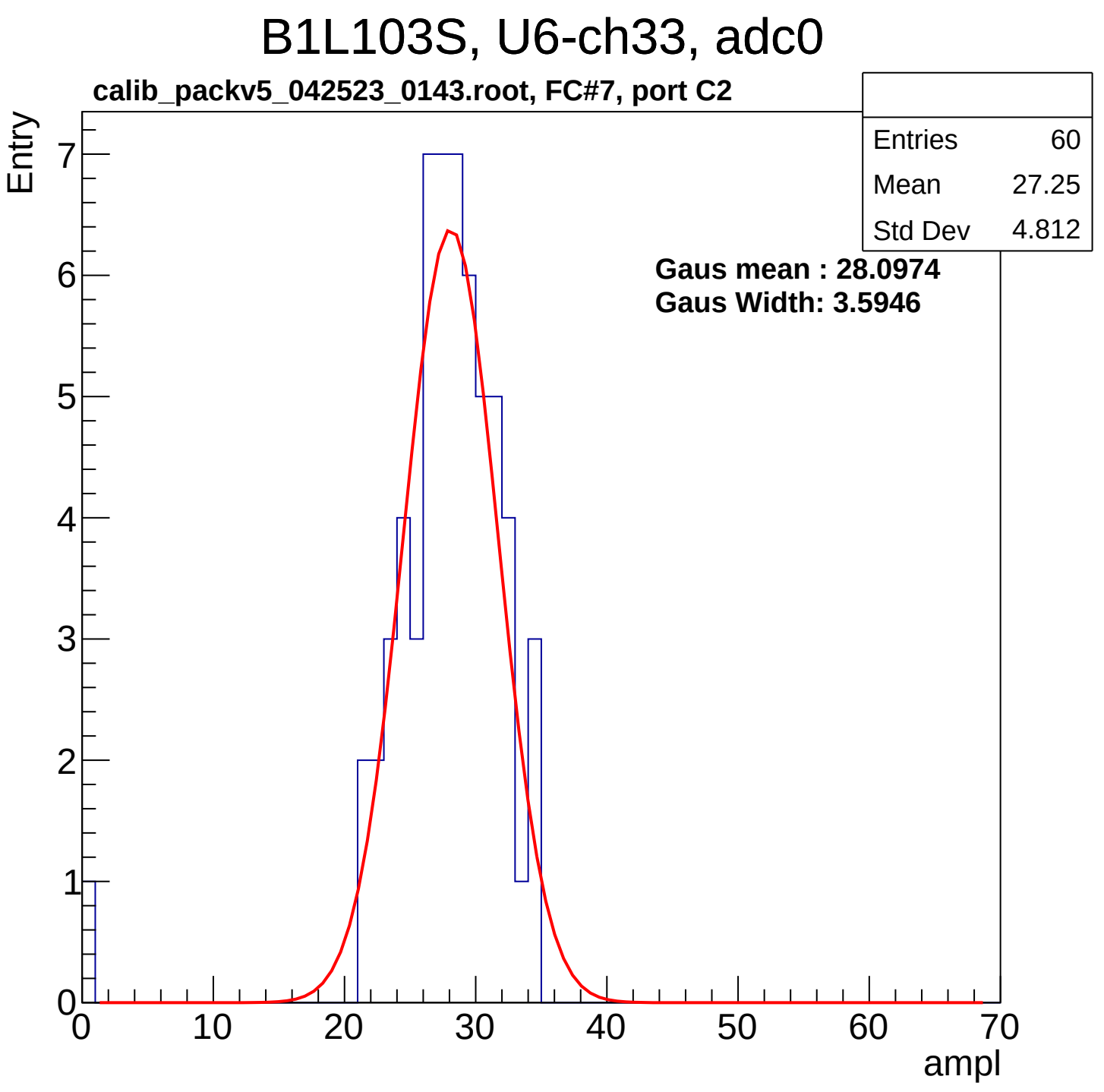
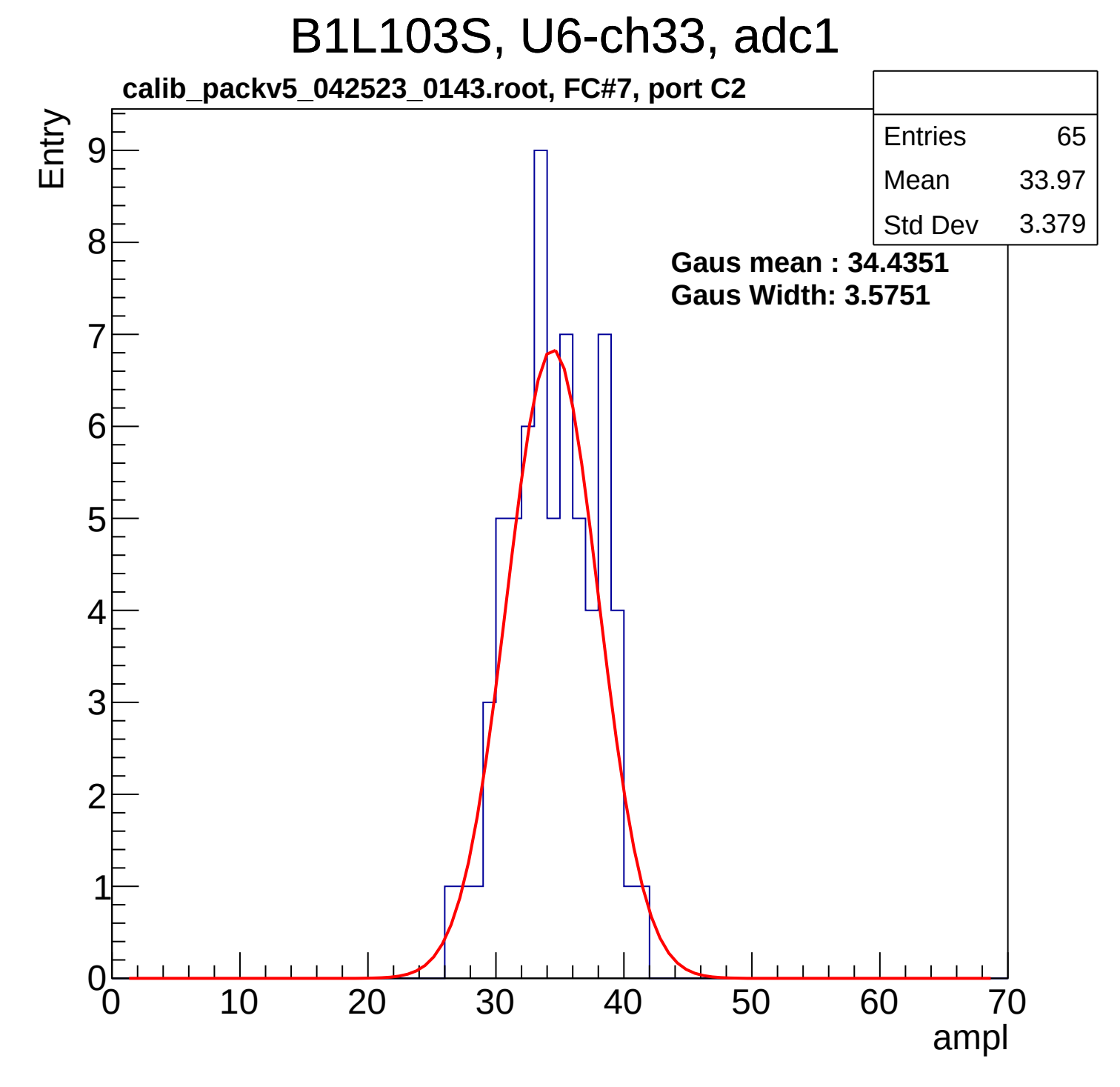
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

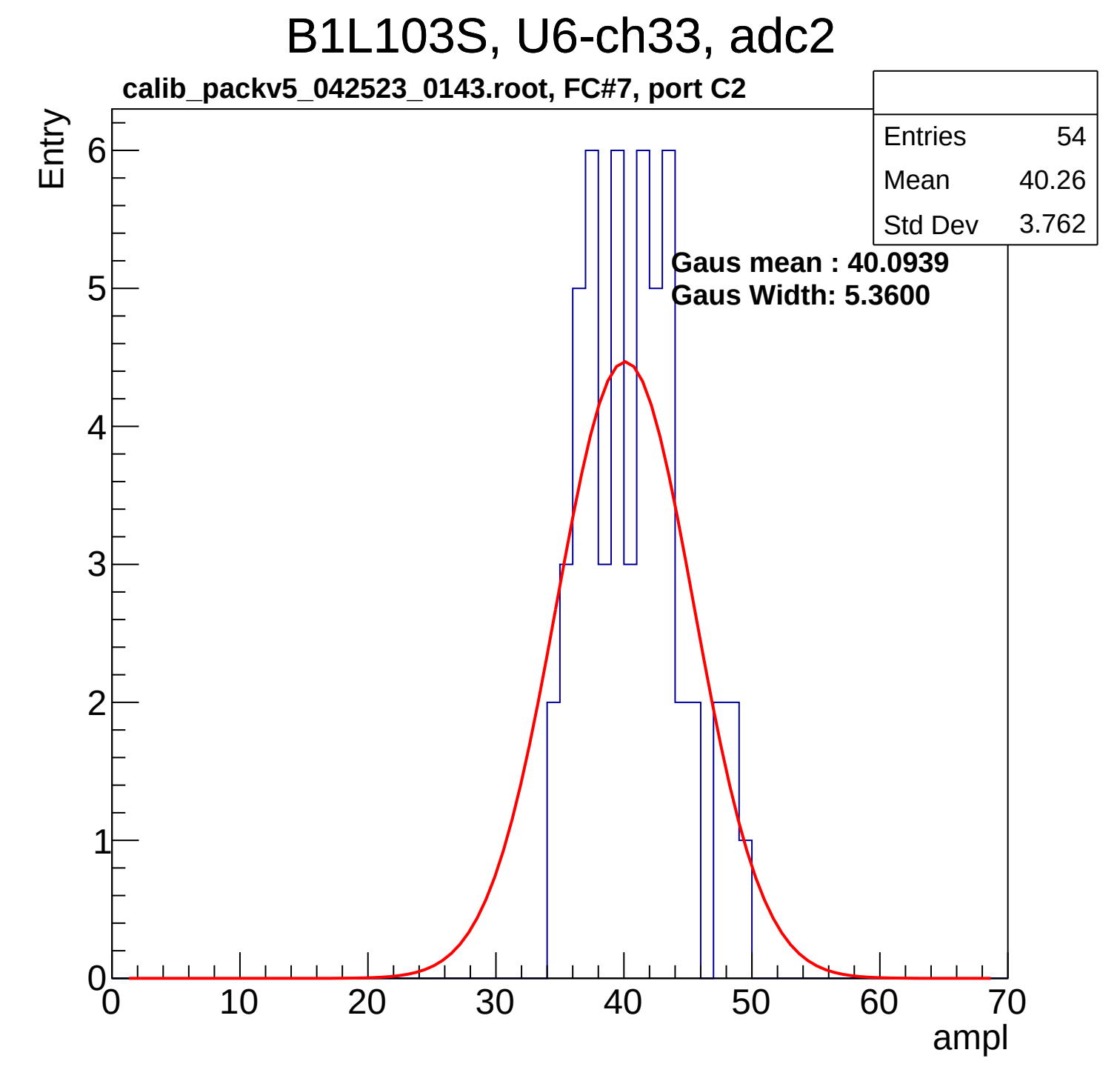

B1L103S, U6-ch32, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 56.92
Std Dev 8.624
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

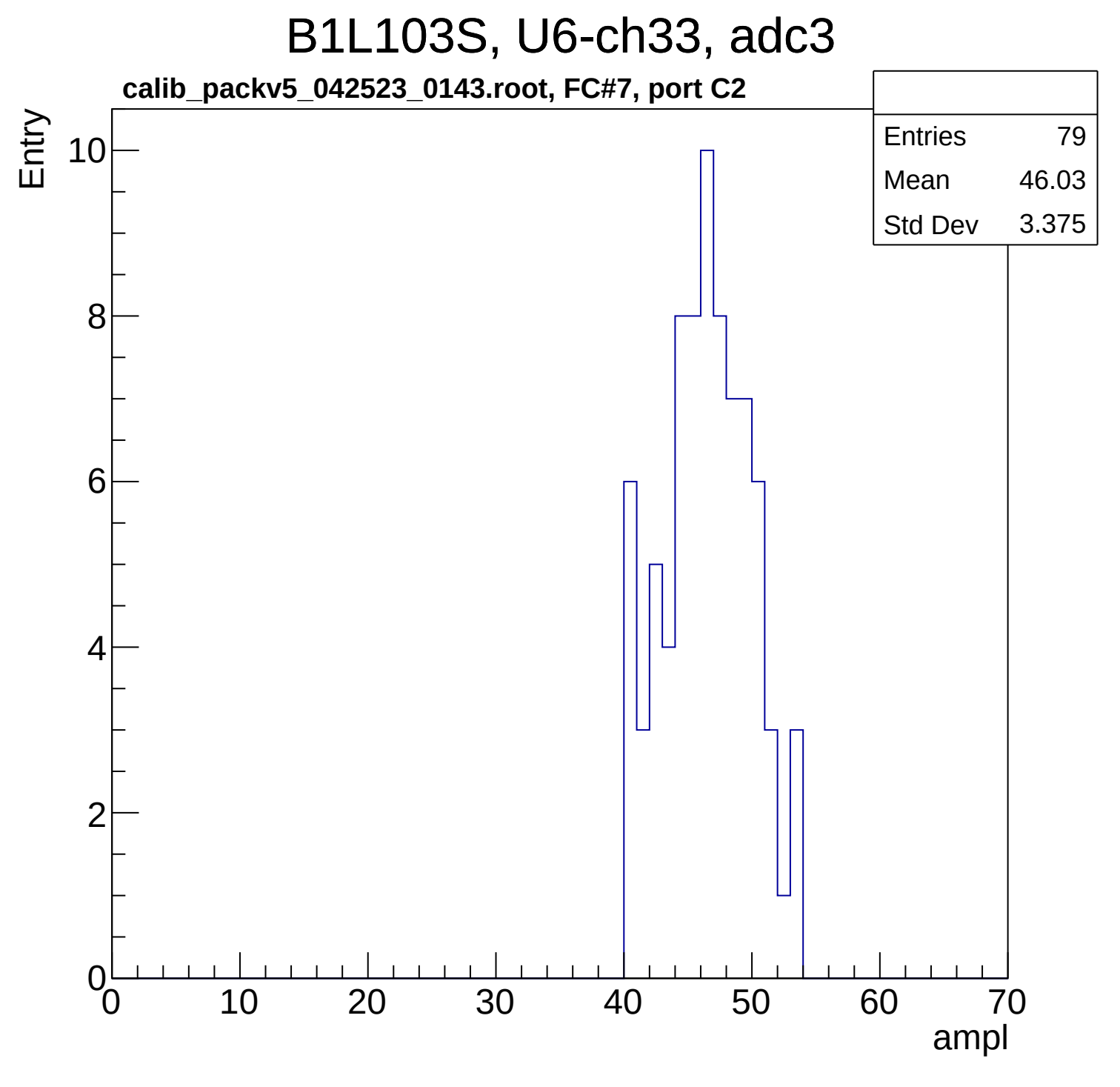

B1L103S, U6-ch32, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 25
Mean 60.56
Std Dev 1.92
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch32, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 34.75
Std Dev 26.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U6-ch33, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 27.25
Std Dev 4.812
Gaus mean : 28.0974
Gaus Width: 3.5946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch33, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 33.97
Std Dev 3.379
Gaus mean : 34.4351
Gaus Width: 3.5751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch33, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 40.26
Std Dev 3.762
Gaus mean : 40.0939
Gaus Width: 5.3600
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch33, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 46.03
Std Dev 3.375
Entry
ampl
0
2
4
6
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0
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20
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40
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60
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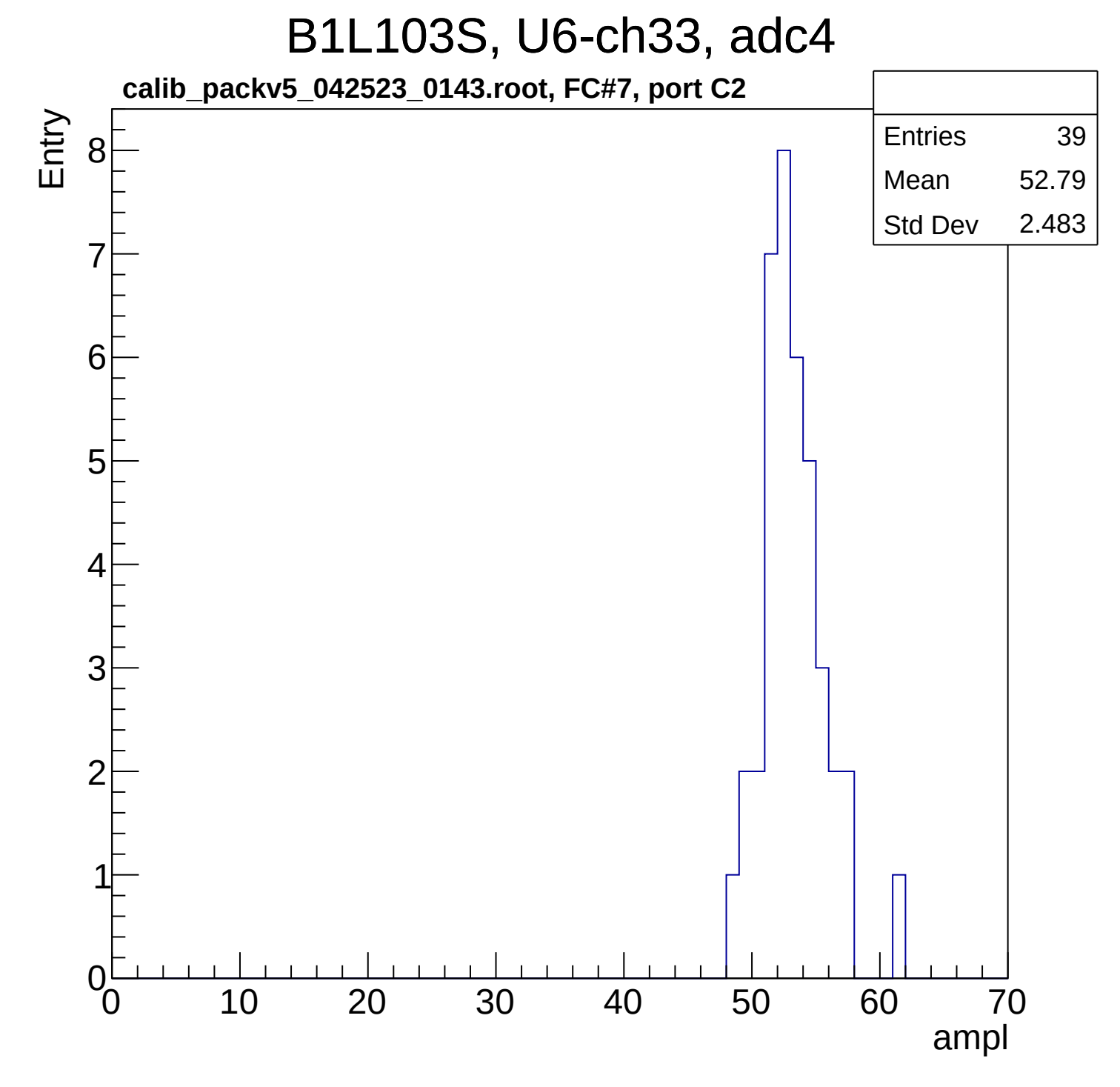
B1L103S, U6-ch33, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 52.79
Std Dev 2.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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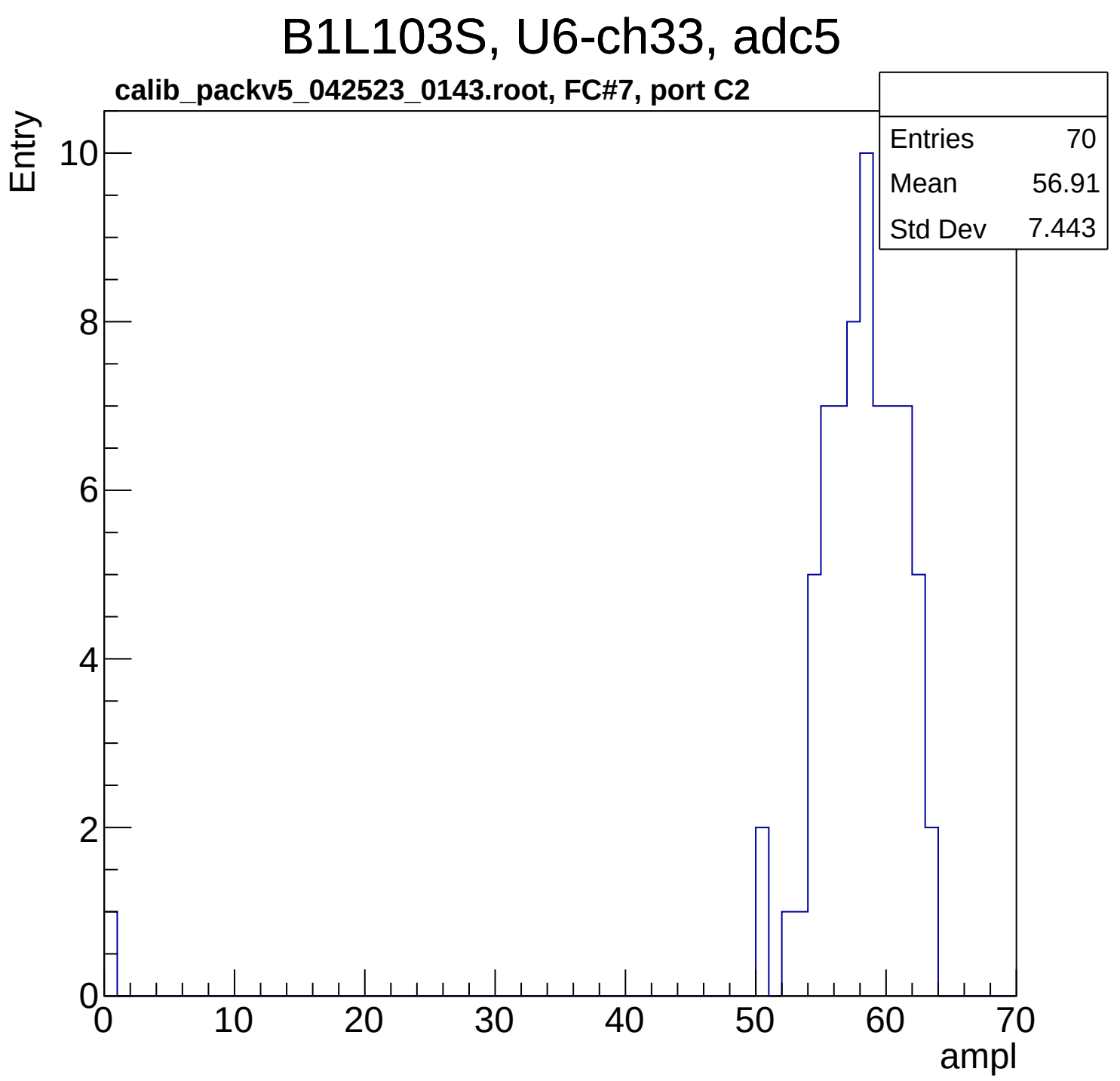
B1L103S, U6-ch33, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 56.91
Std Dev 7.443
Entry
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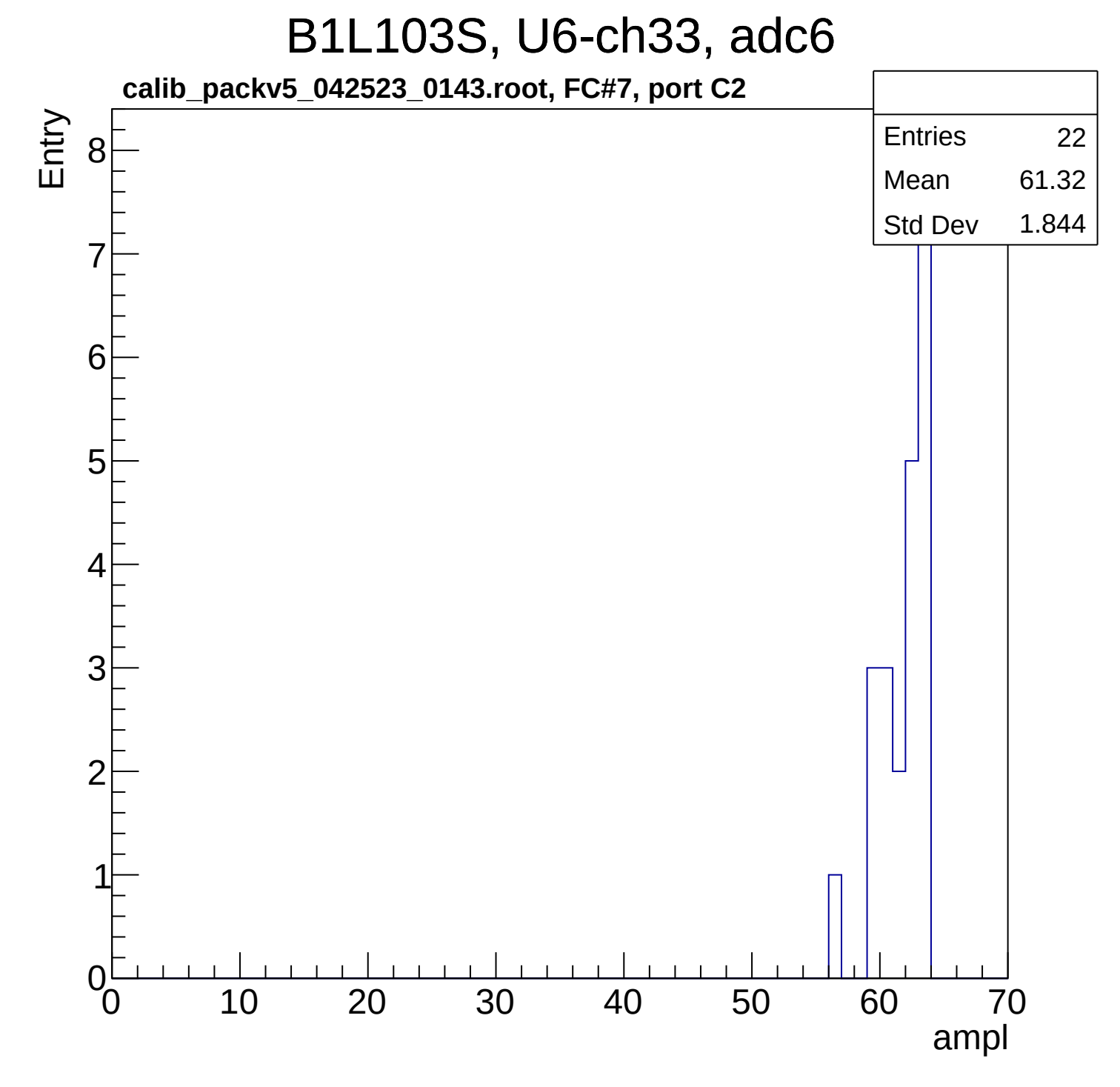
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch33, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 61.32
Std Dev 1.844
Entry
ampl
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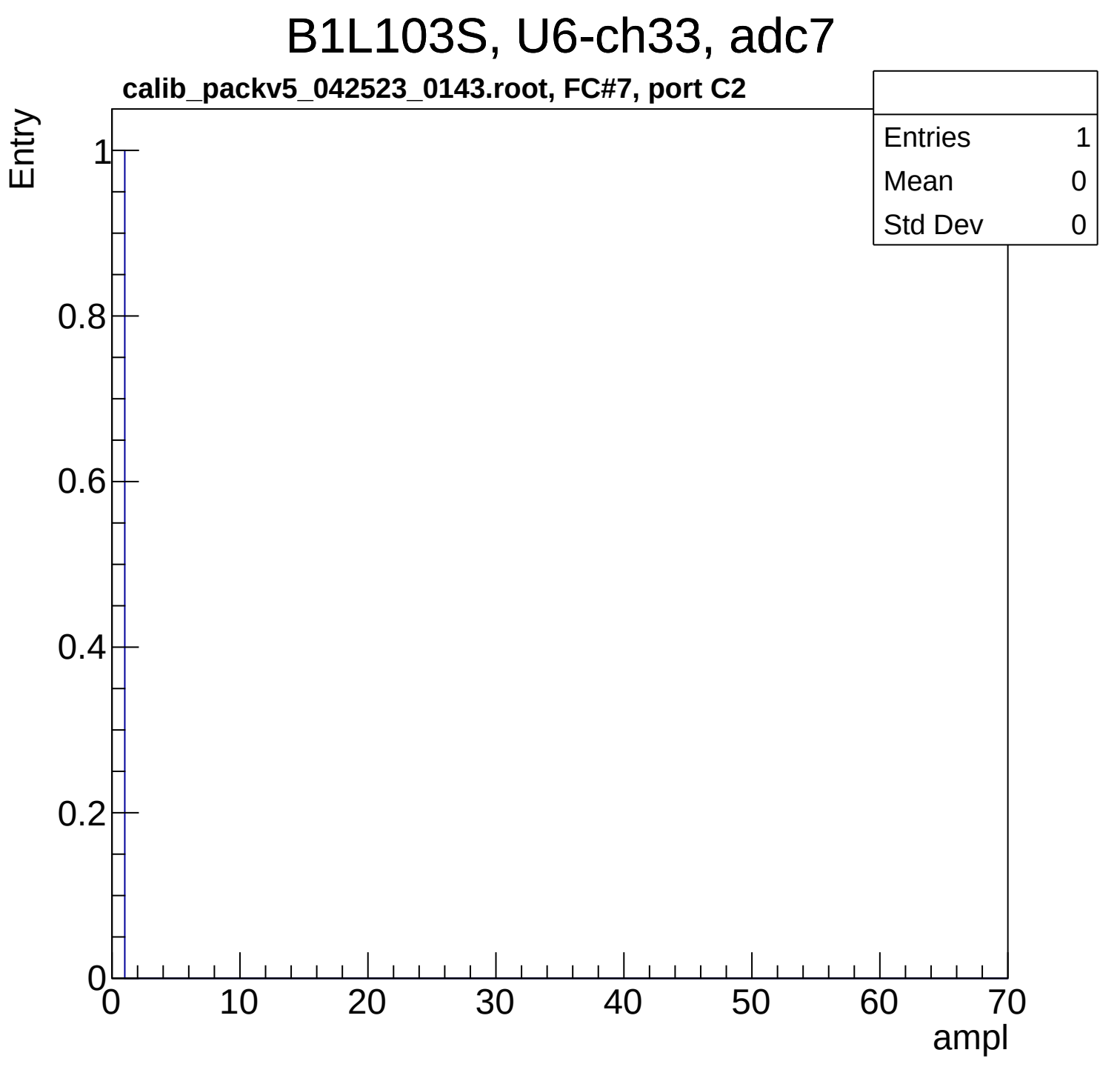
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch33, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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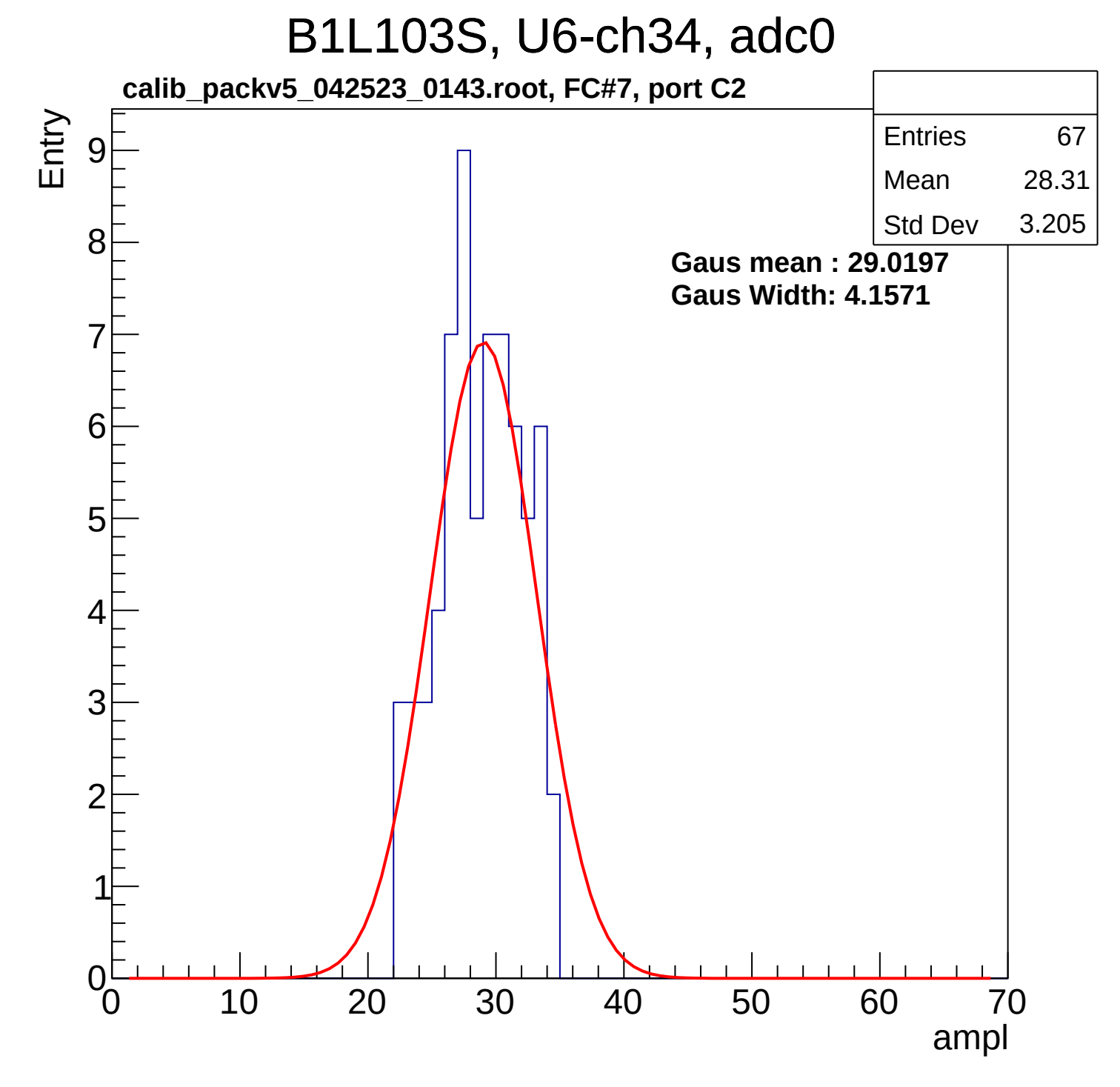
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch34, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.31
Std Dev 3.205
Gaus mean : 29.0197
Gaus Width: 4.1571
Entry
ampl
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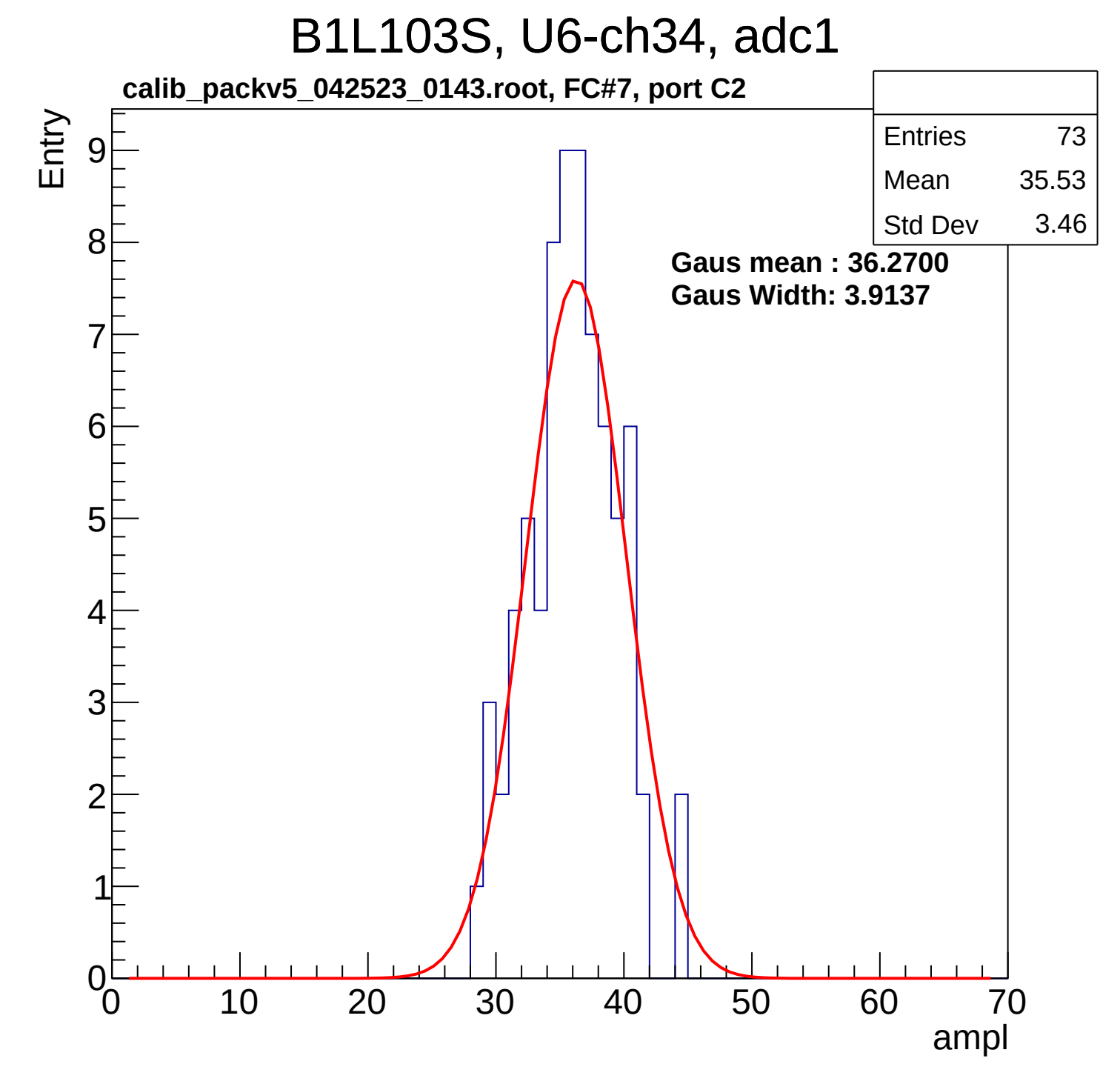
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch34, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 35.53
Std Dev 3.46
Gaus mean : 36.2700
Gaus Width: 3.9137
Entry
ampl
0
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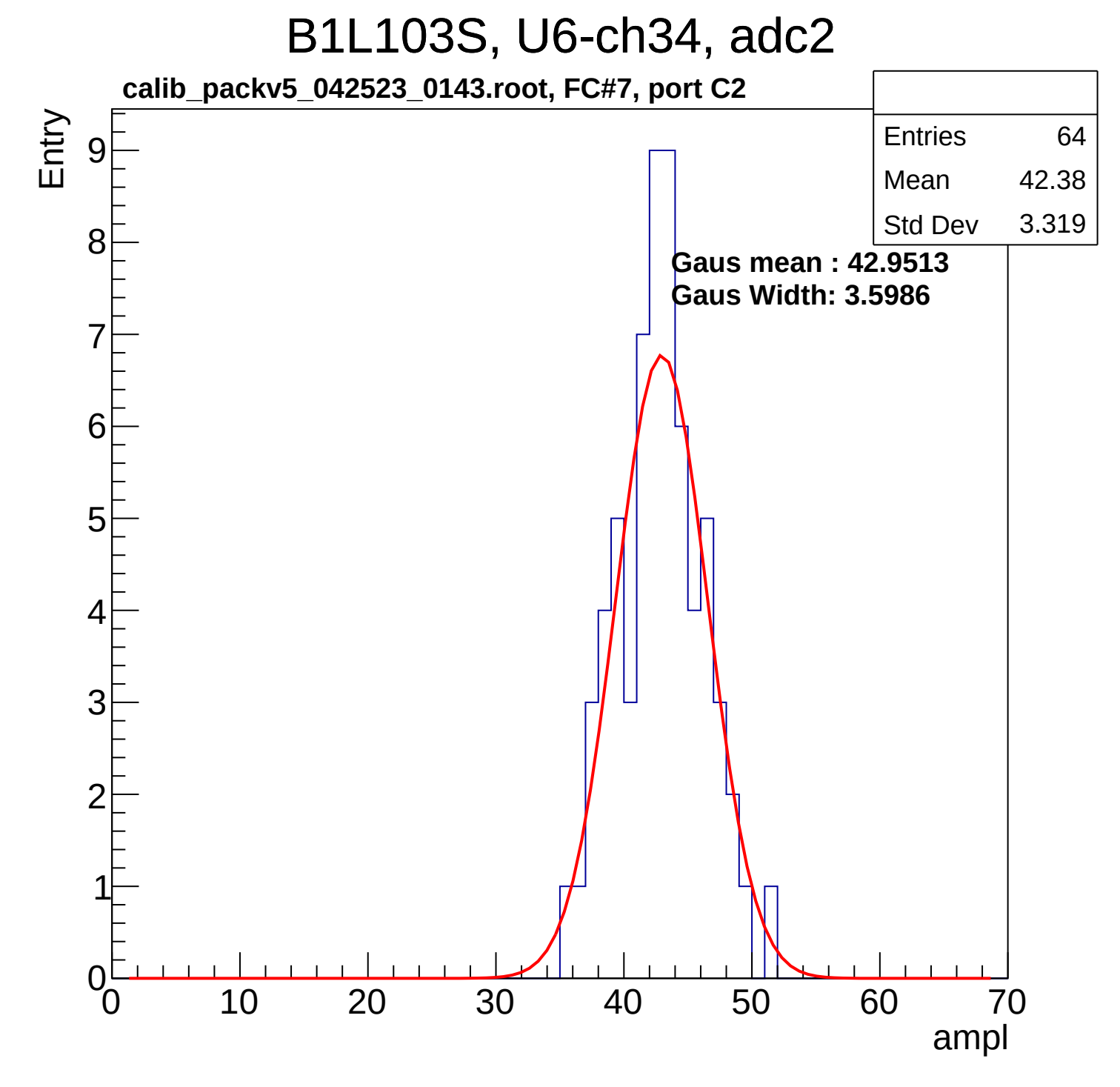
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch34, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 42.38
Std Dev 3.319
Gaus mean : 42.9513
Gaus Width: 3.5986
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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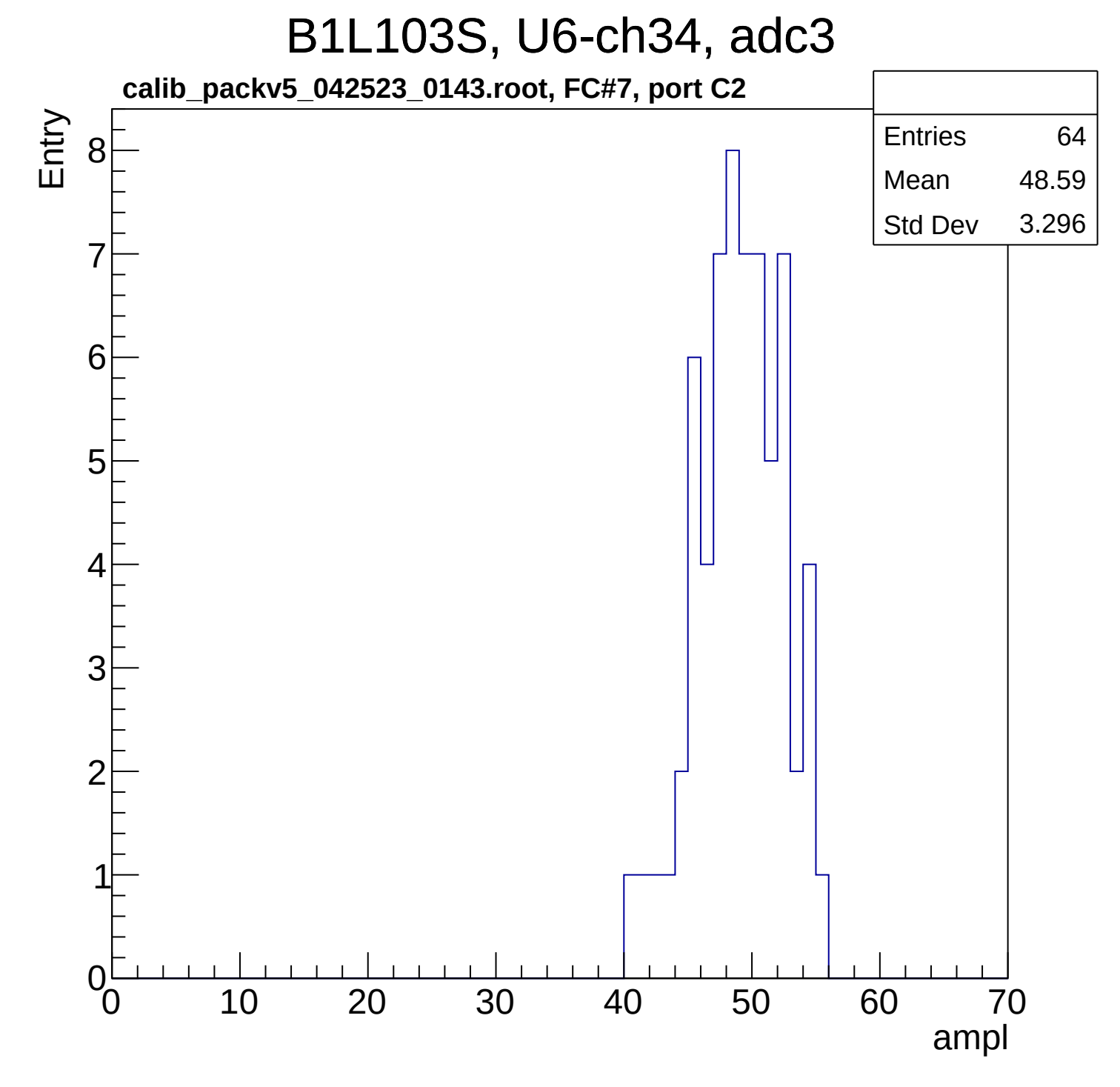
B1L103S, U6-ch34, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.59
Std Dev 3.296
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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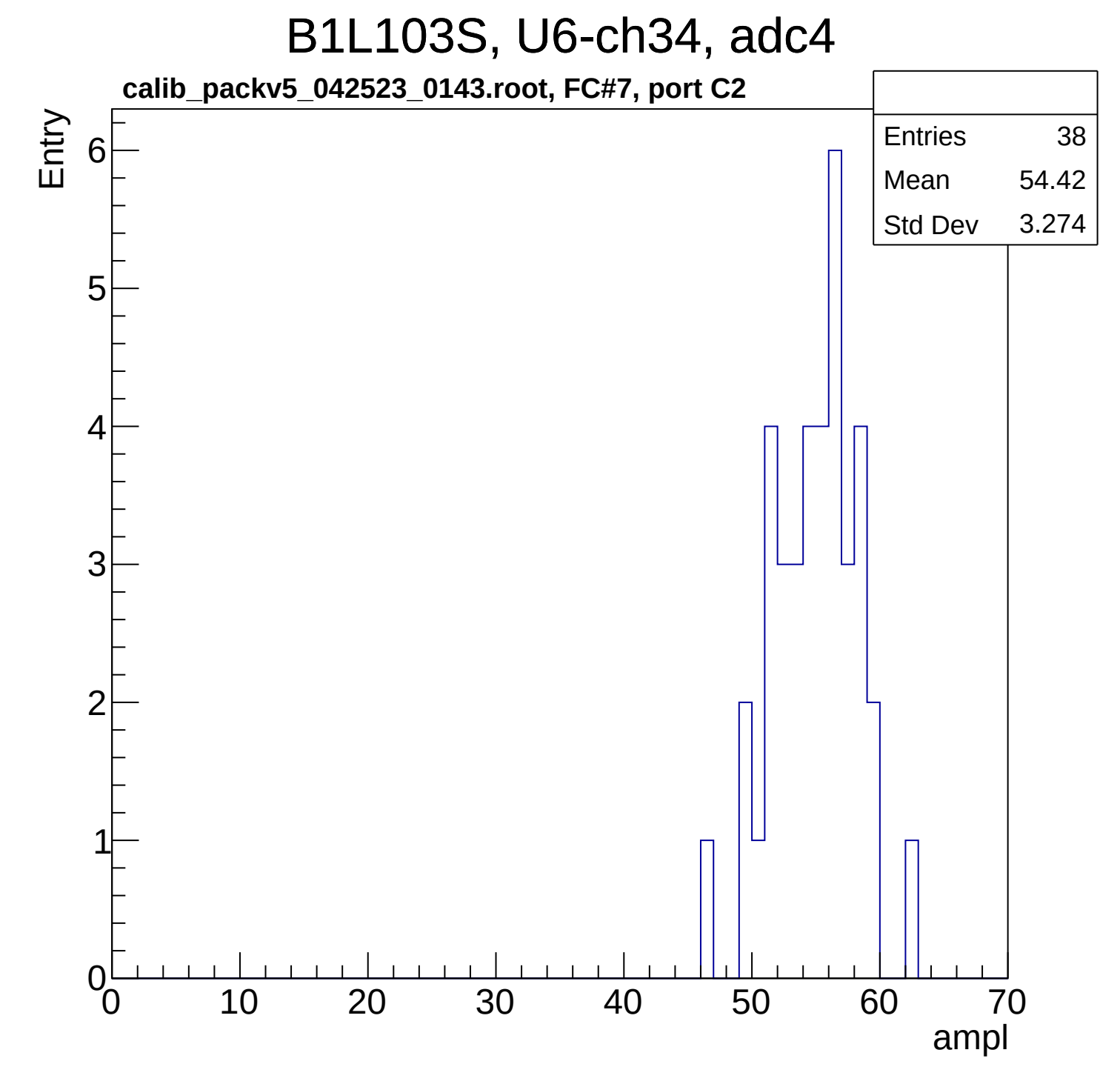
B1L103S, U6-ch34, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 54.42
Std Dev 3.274
Entry
ampl
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2
3
4
5
6
0
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20
30
40
50
60
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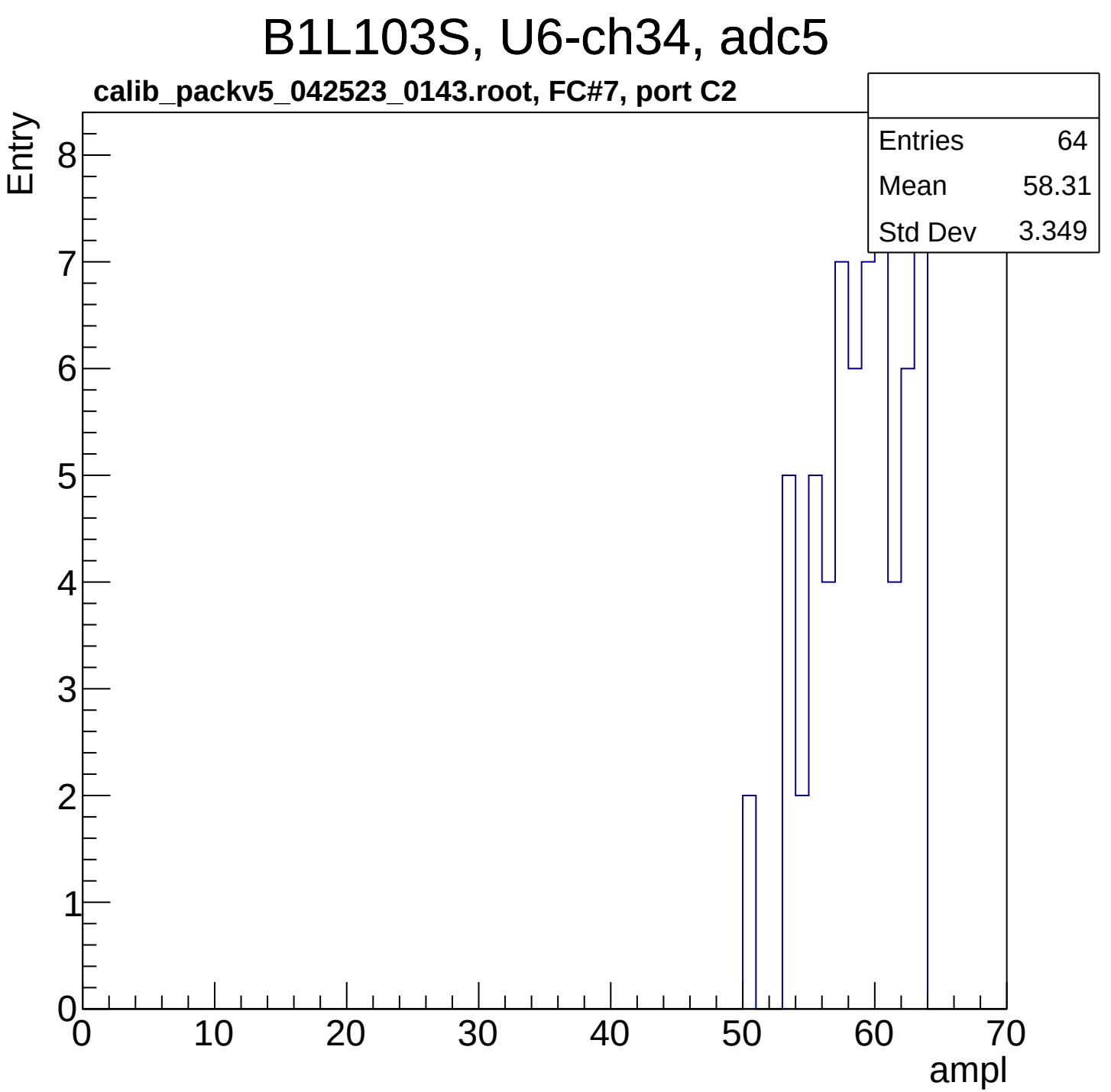
B1L103S, U6-ch34, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 58.31
Std Dev 3.349
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
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60
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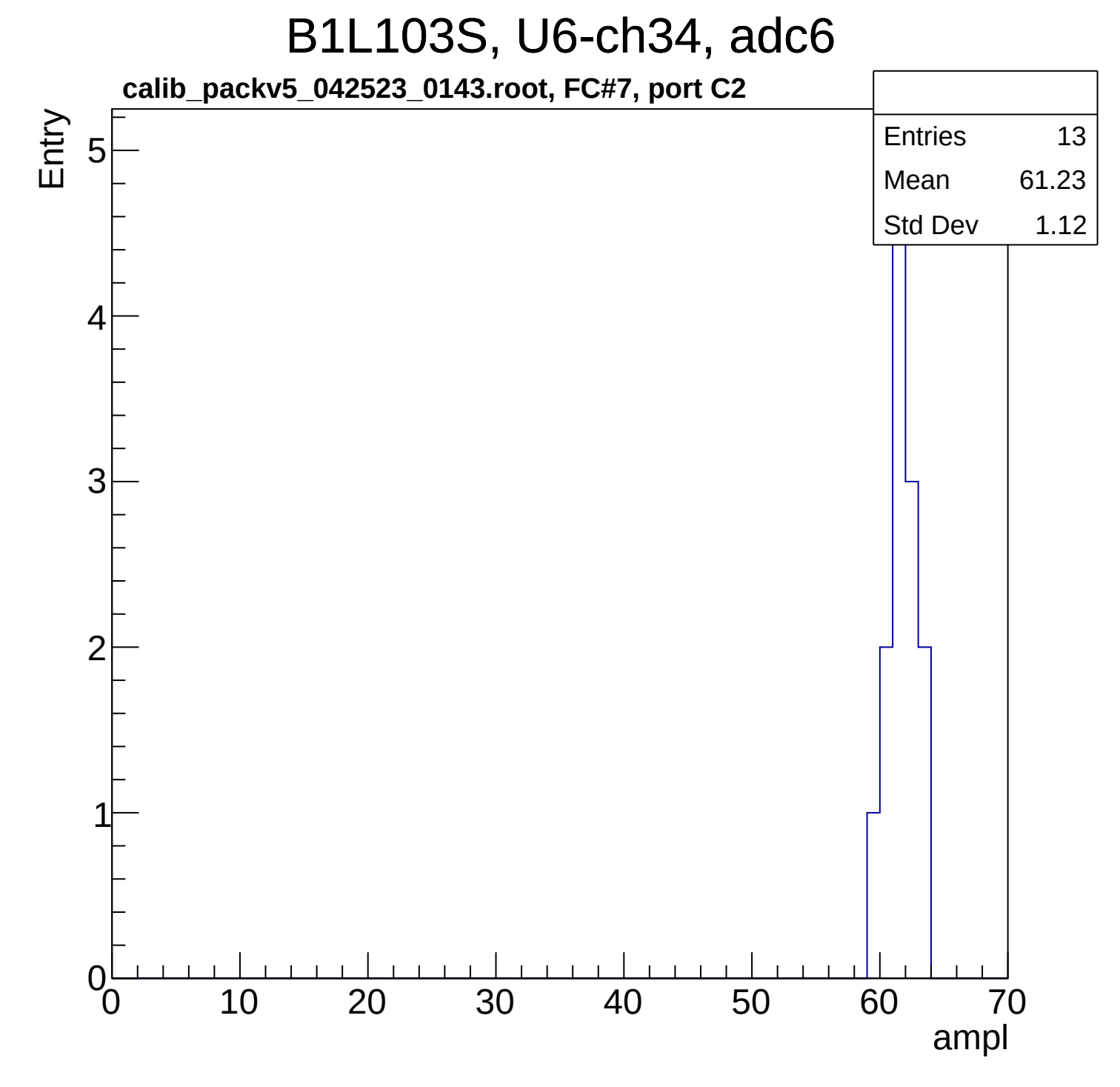
B1L103S, U6-ch34, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.23
Std Dev 1.12
Entry
0
1
2
3
4
5
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10
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50
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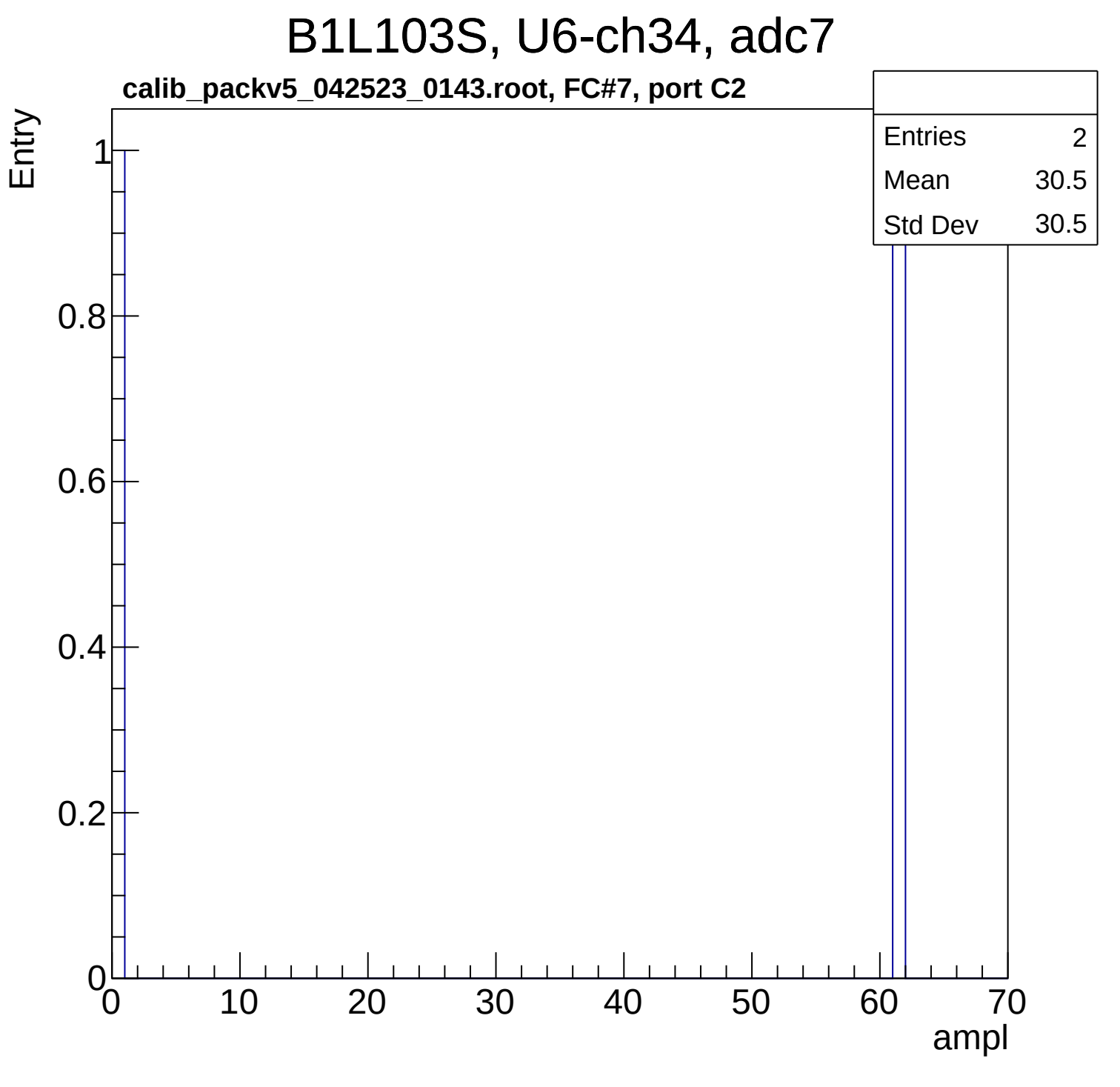
70
ampl


B1L103S, U6-ch34, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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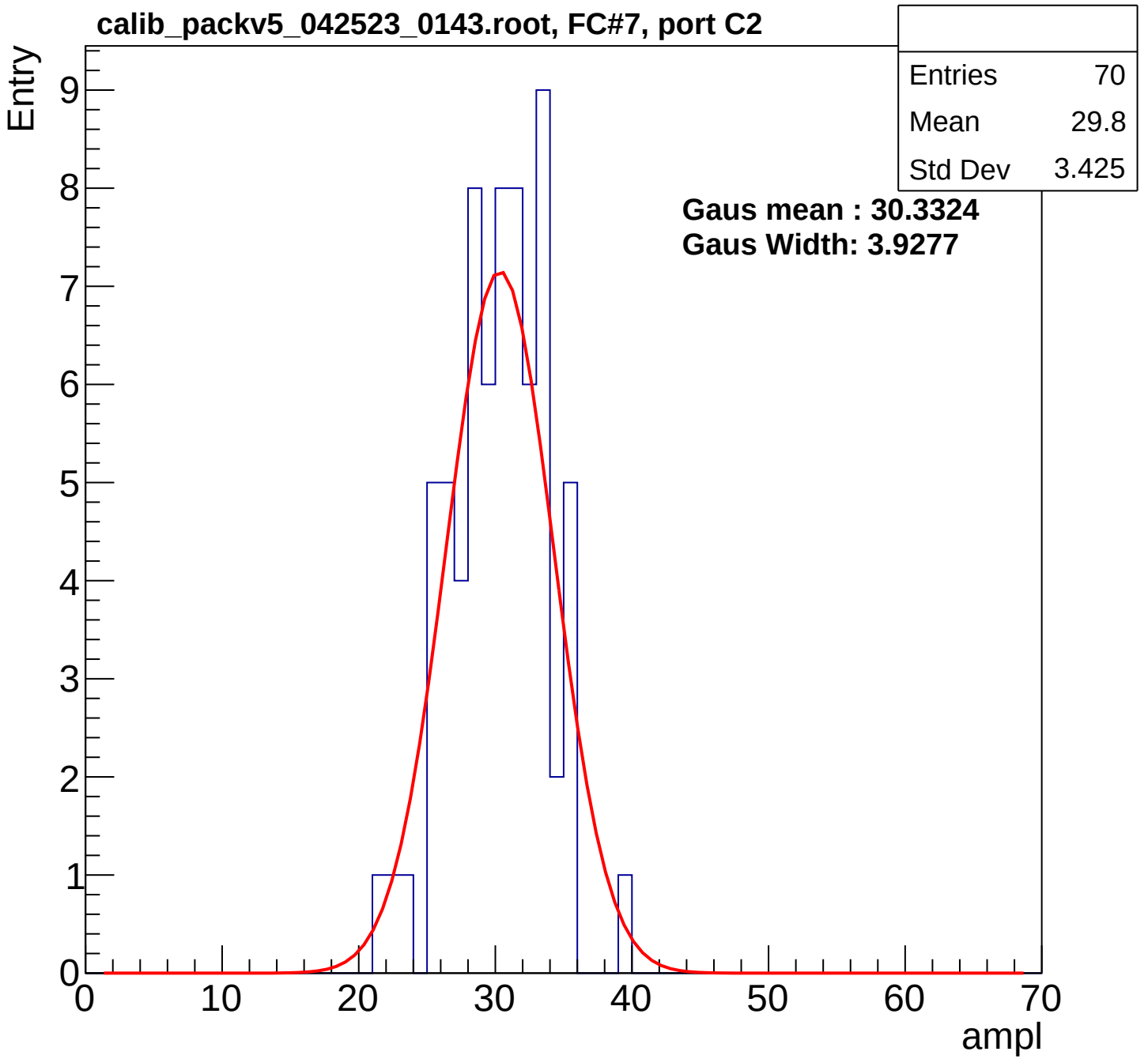
50
60
70
ampl


B1L103S, U6-ch35, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.8
Std Dev 3.425
Gaus mean : 30.3324
Gaus Width: 3.9277
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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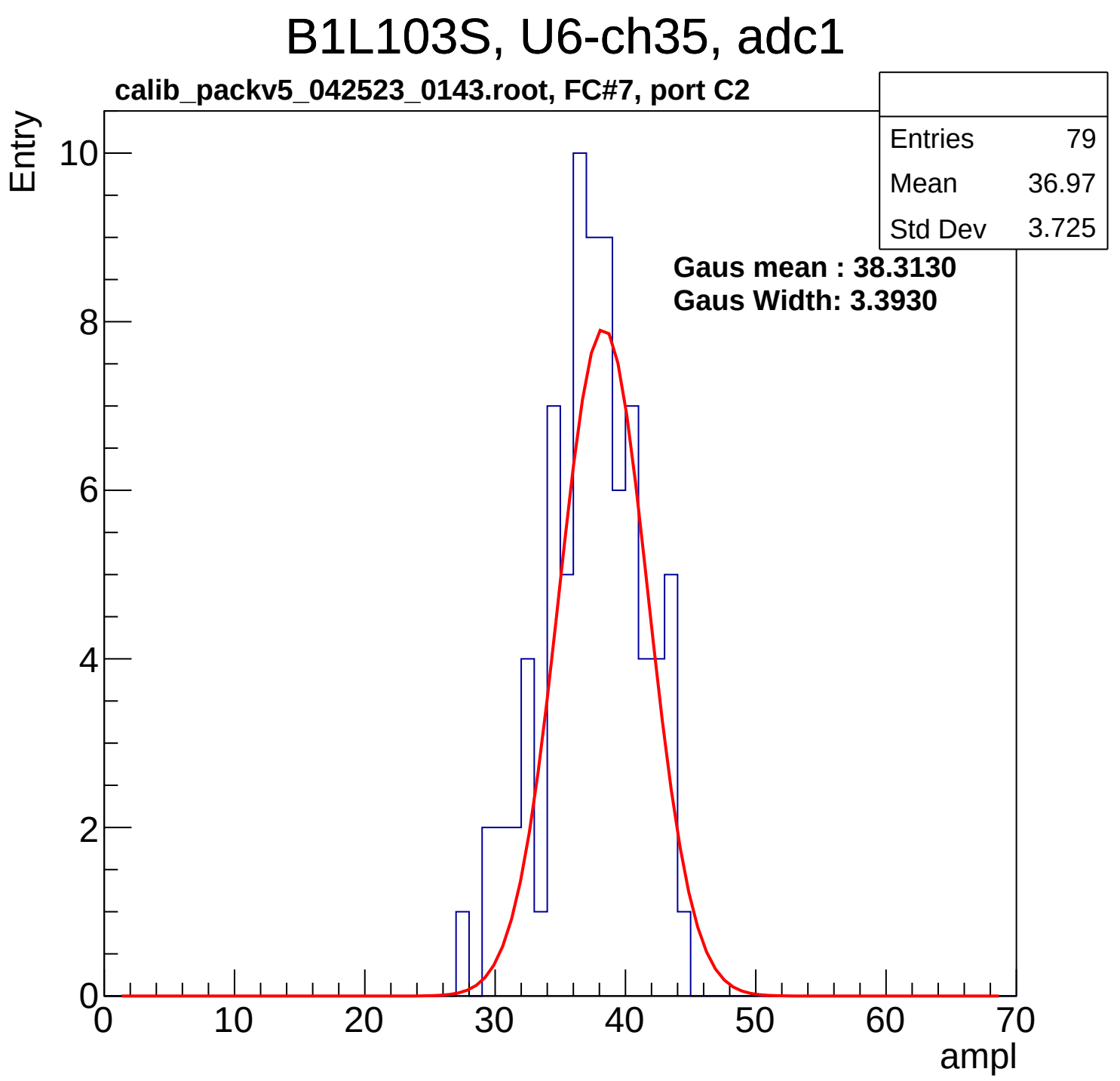
B1L103S, U6-ch35, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 36.97
Std Dev 3.725
Gaus mean : 38.3130
Gaus Width: 3.3930
Entry
ampl
0
2
4
6
8
10
0
10
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40
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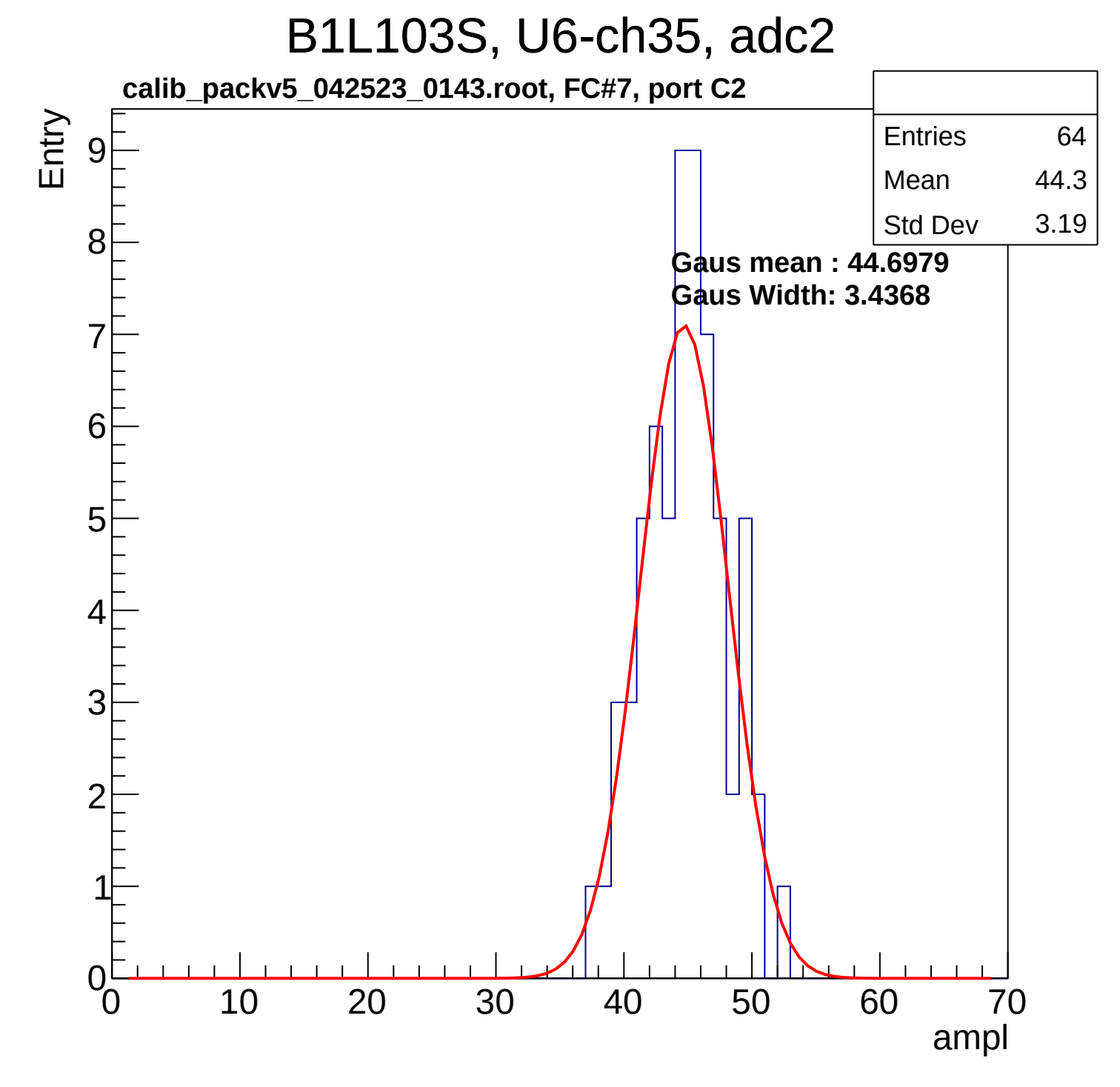
B1L103S, U6-ch35, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.3
Std Dev 3.19
Gaus mean : 44.6979
Gaus Width: 3.4368
Entry
ampl
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2
3
4
5
6
7
8
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0
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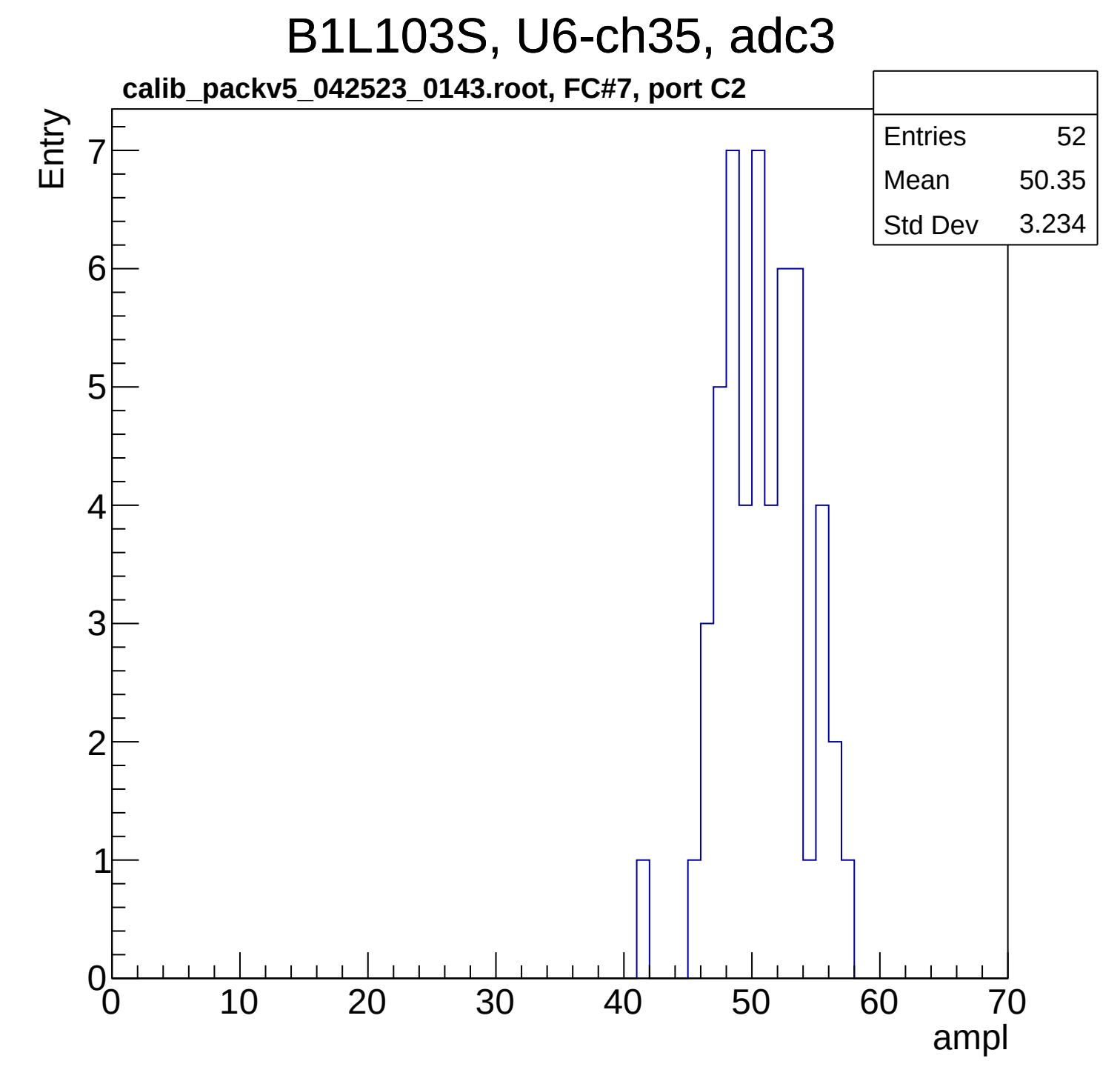
B1L103S, U6-ch35, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 50.35
Std Dev 3.234
Entry
ampl
0
1
2
3
4
5
6
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0
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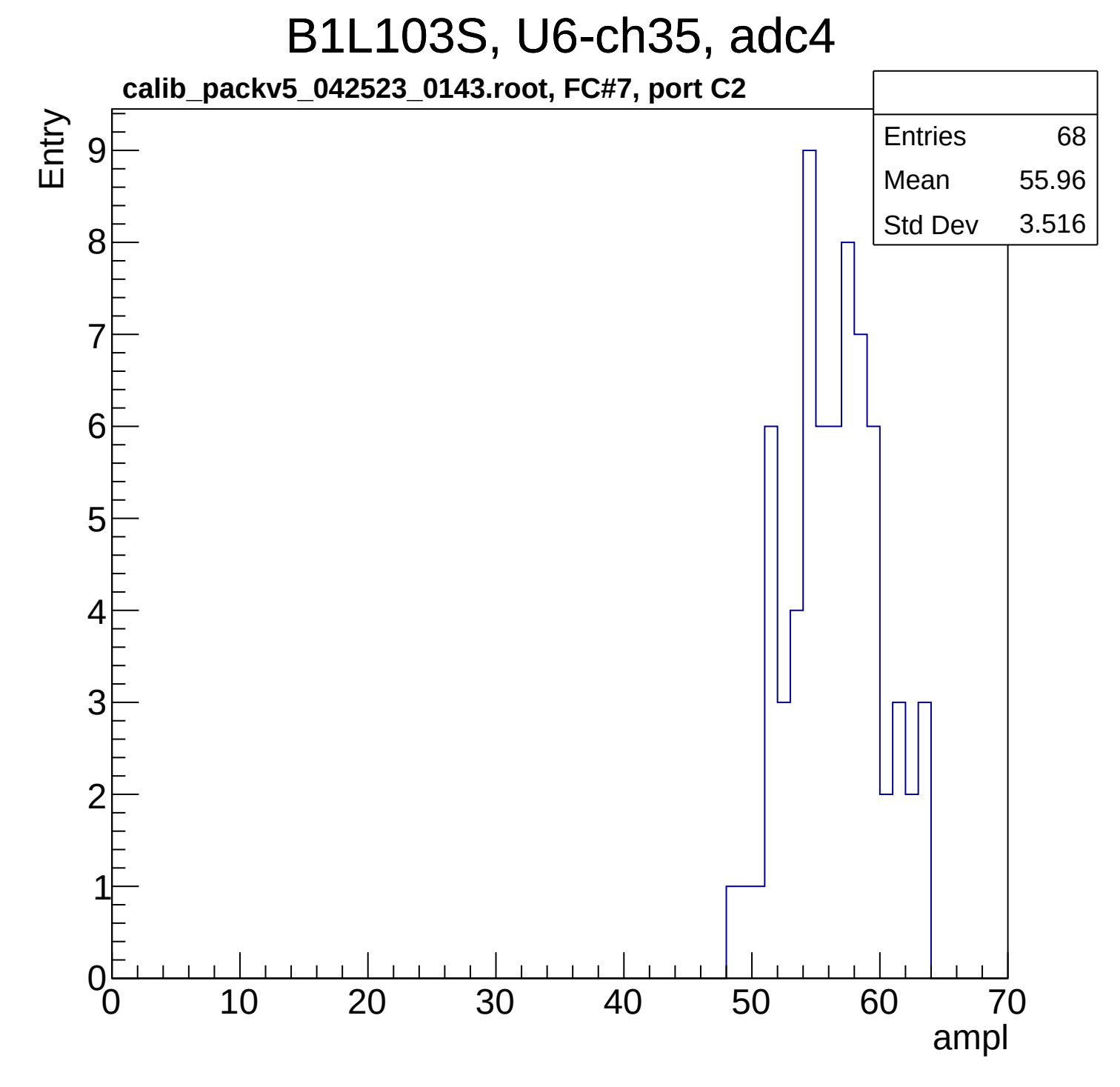
B1L103S, U6-ch35, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 55.96
Std Dev 3.516
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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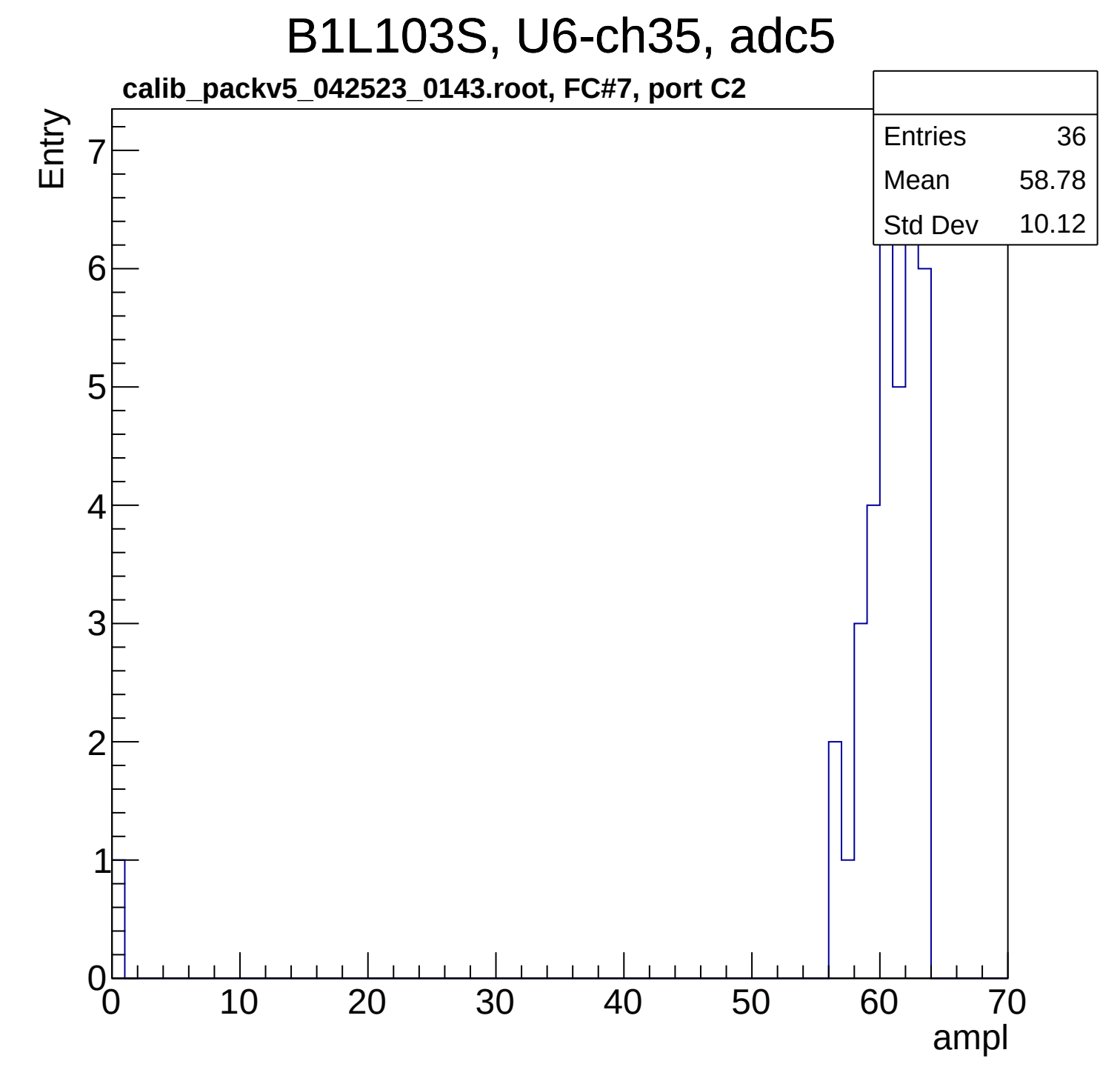
B1L103S, U6-ch35, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.78
Std Dev 10.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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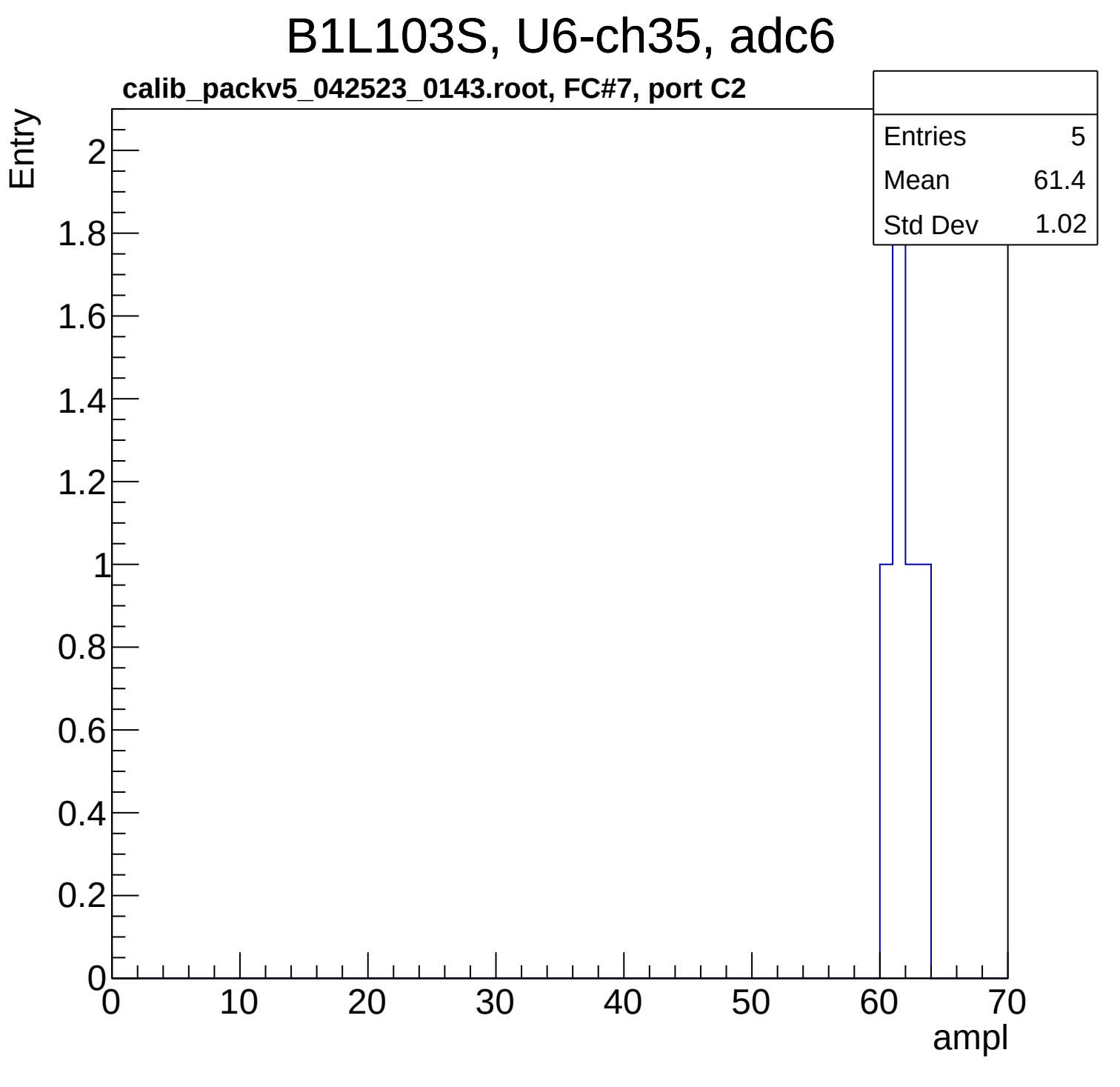
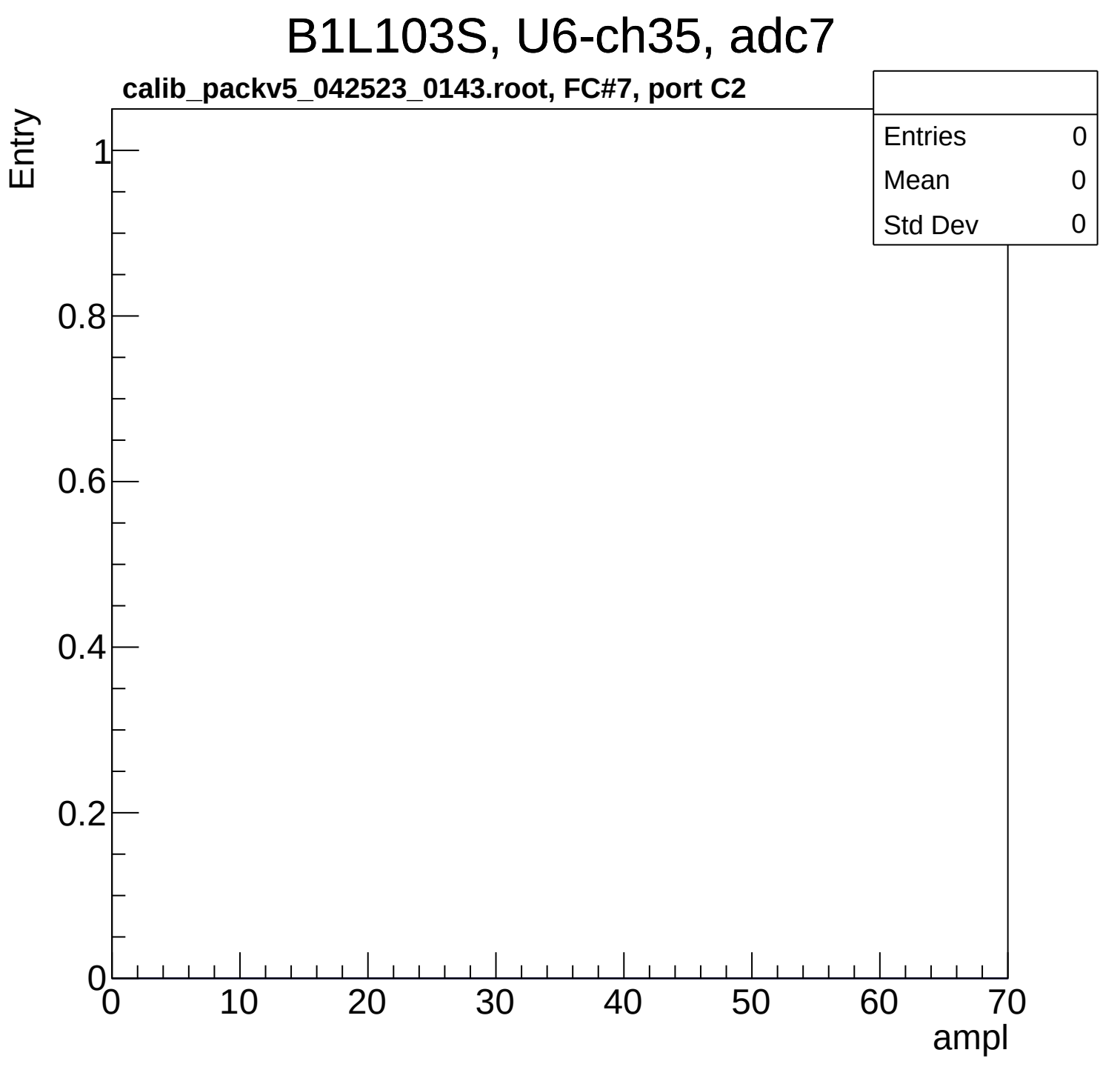
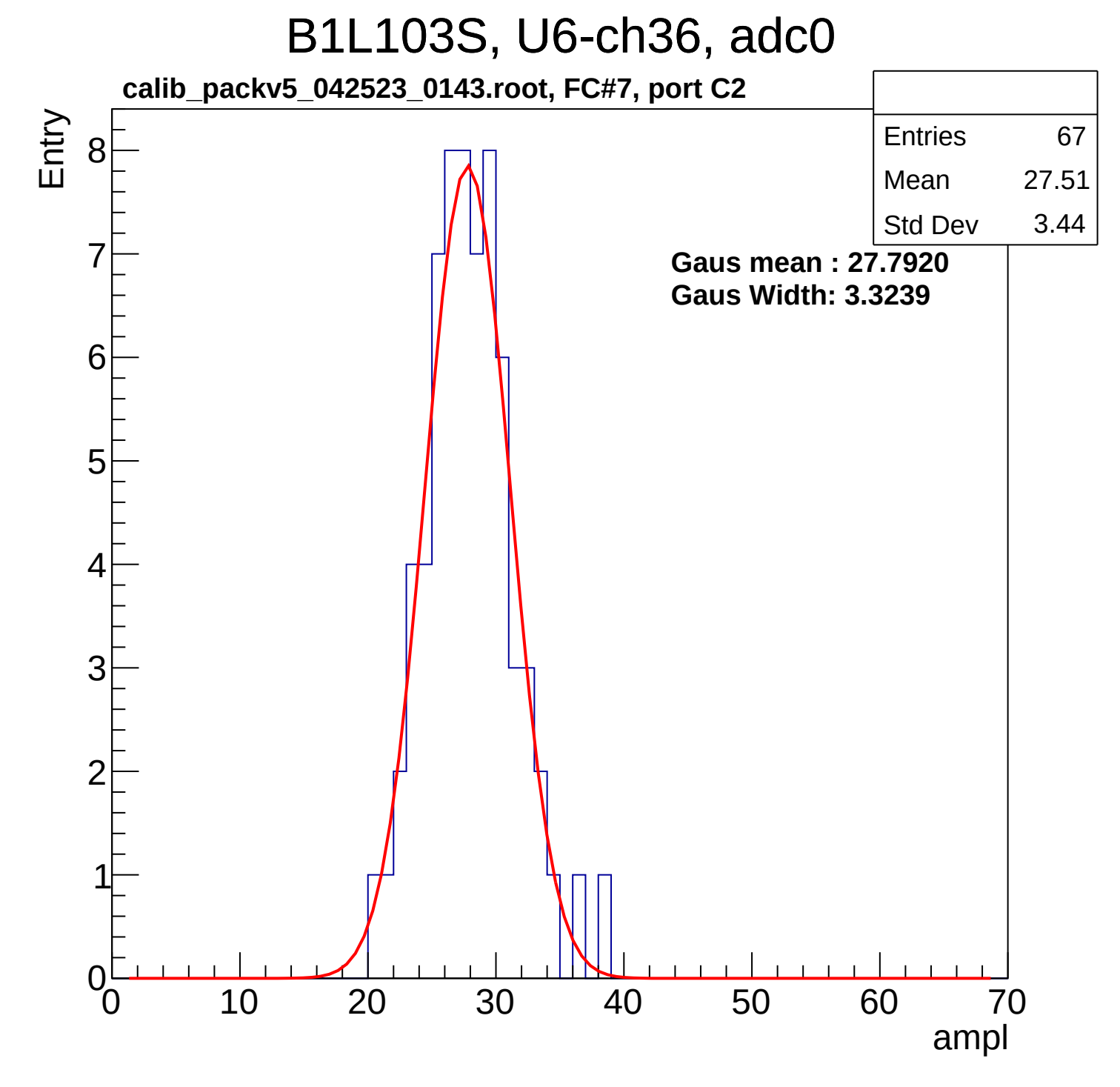
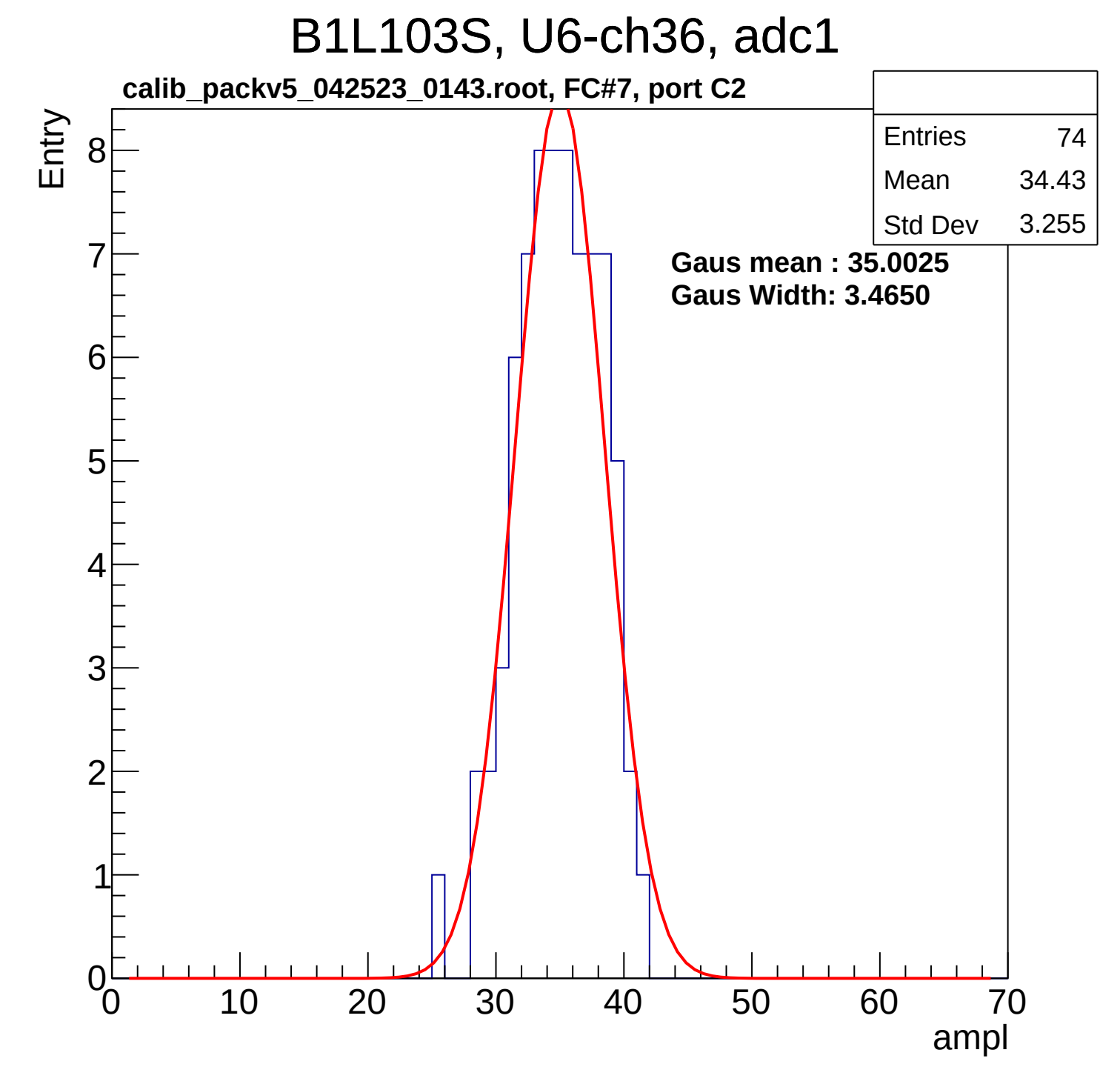

B1L103S, U6-ch35, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.4
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch35, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch36, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 27.51
Std Dev 3.44
Gaus mean : 27.7920
Gaus Width: 3.3239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch36, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 34.43
Std Dev 3.255
Gaus mean : 35.0025
Gaus Width: 3.4650
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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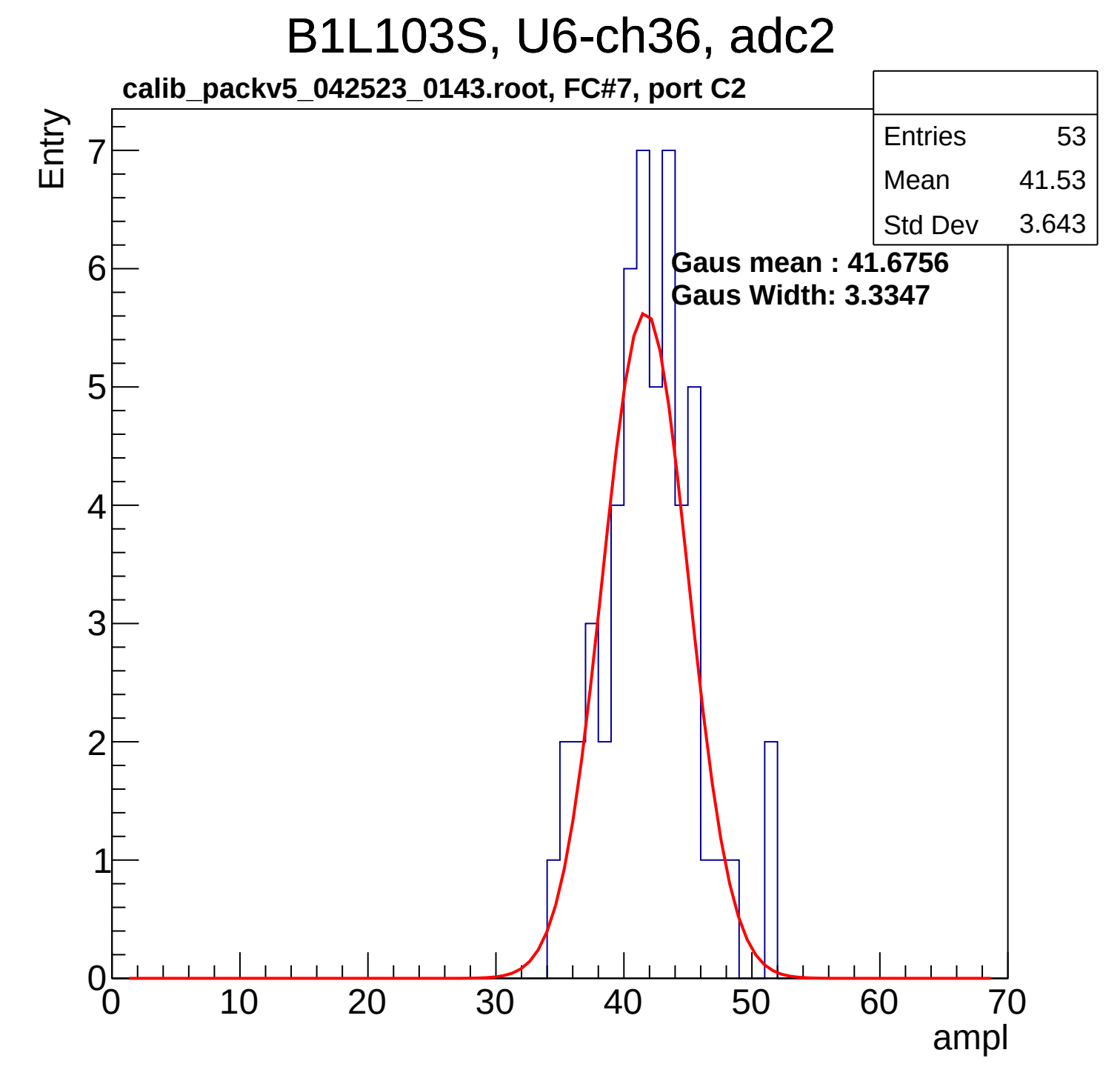
B1L103S, U6-ch36, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 41.53
Std Dev 3.643
Gaus mean : 41.6756
Gaus Width: 3.3347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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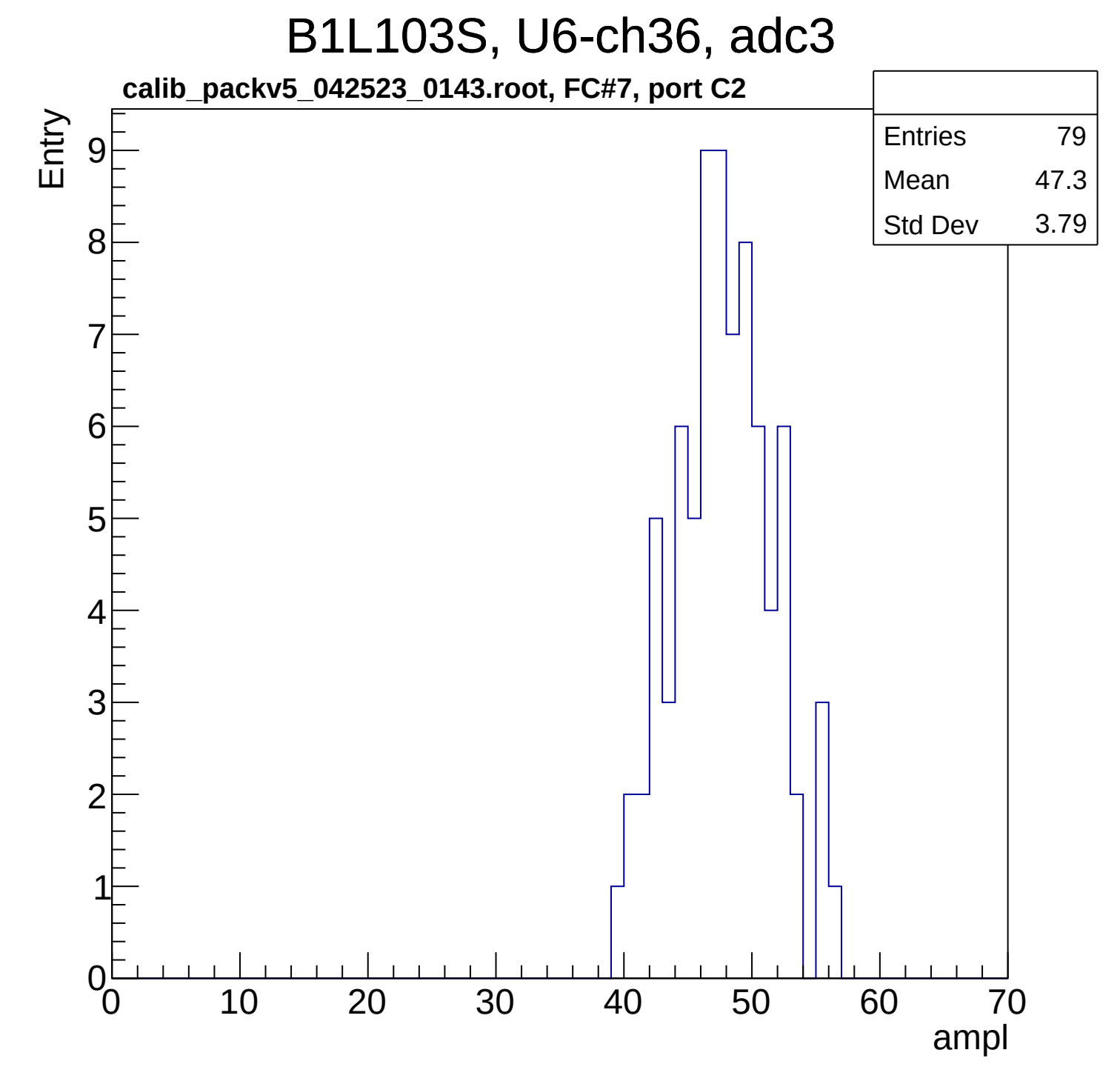
B1L103S, U6-ch36, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 47.3
Std Dev 3.79
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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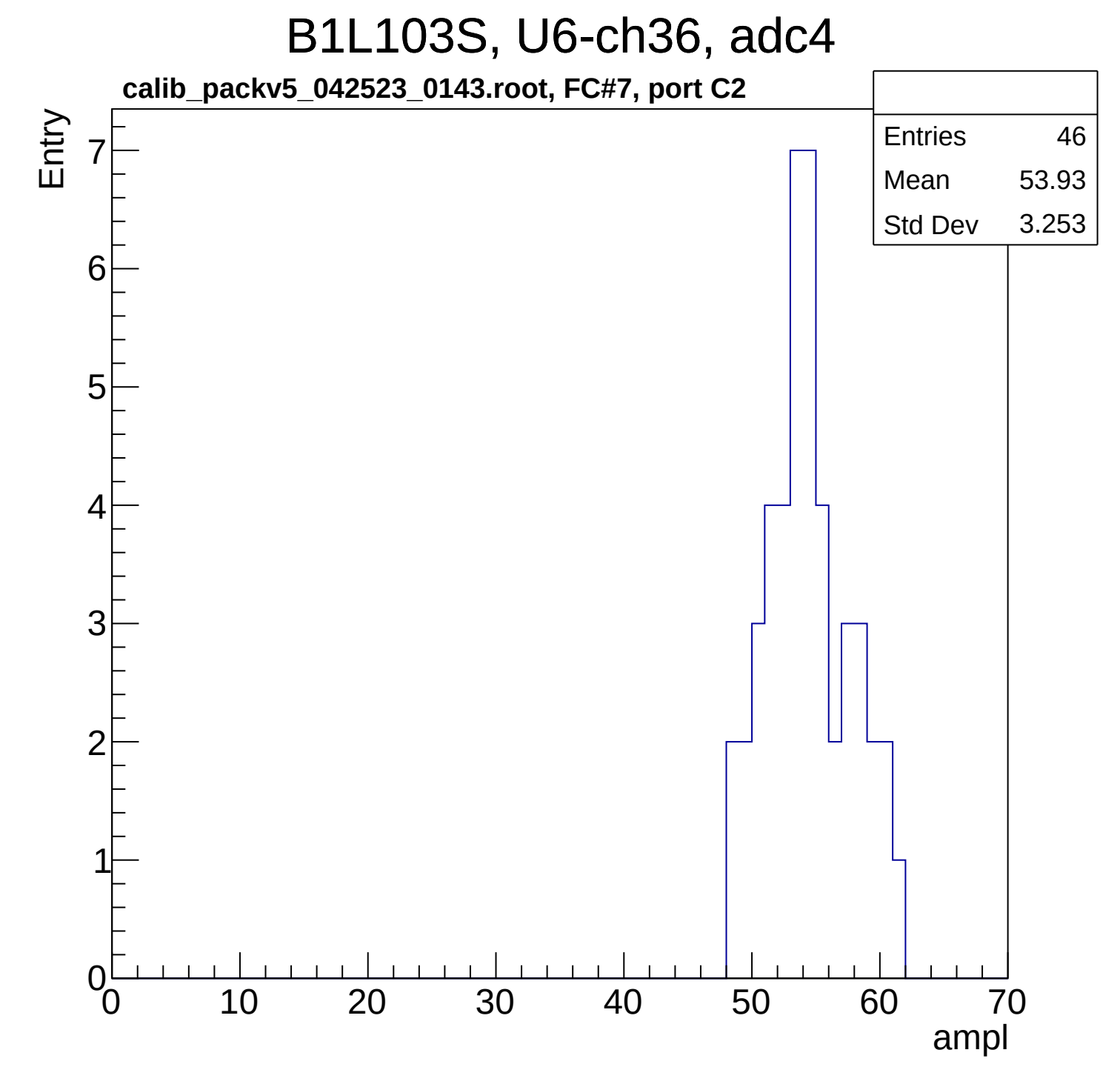
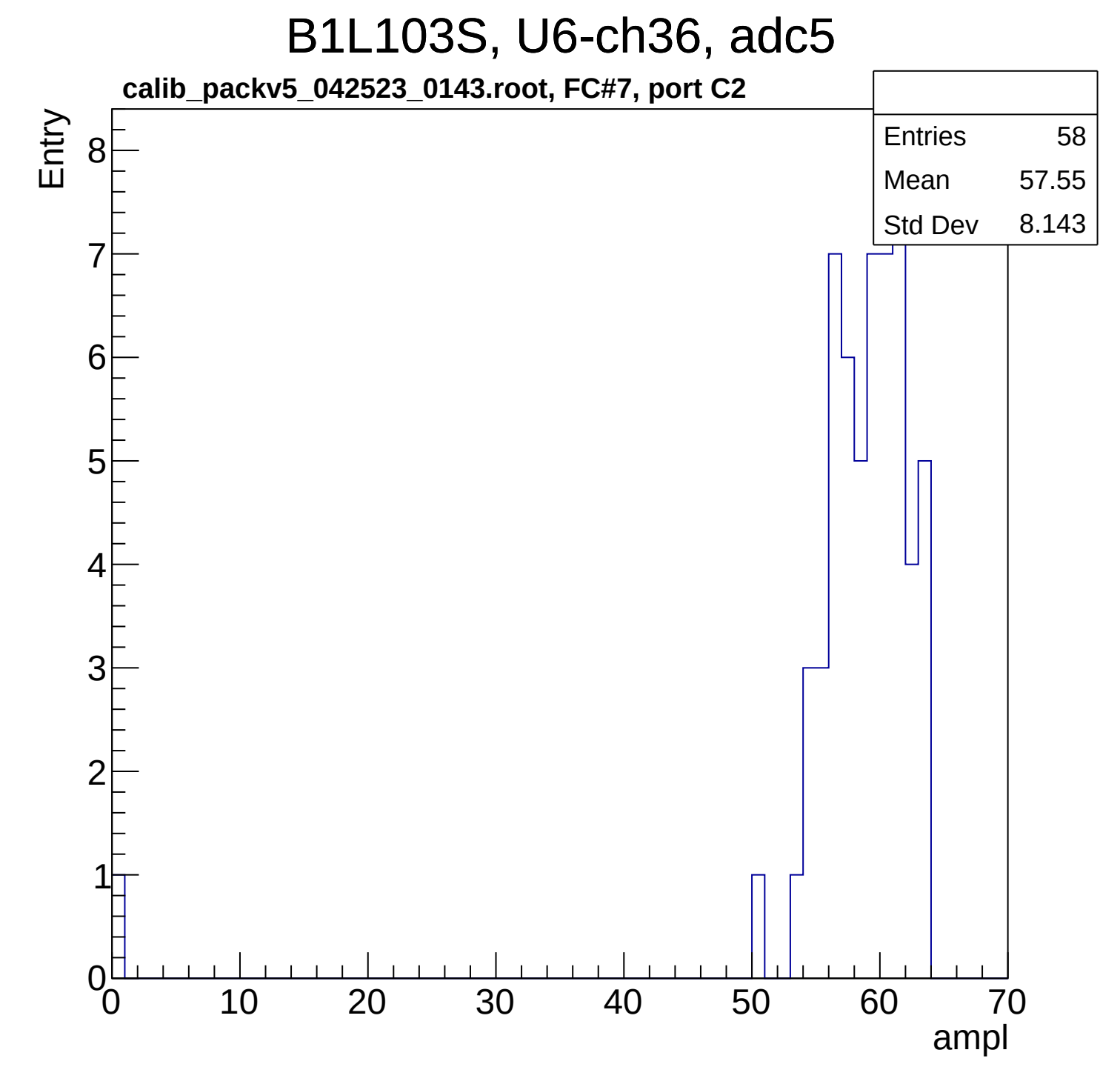

B1L103S, U6-ch36, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 53.93
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch36, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.55
Std Dev 8.143
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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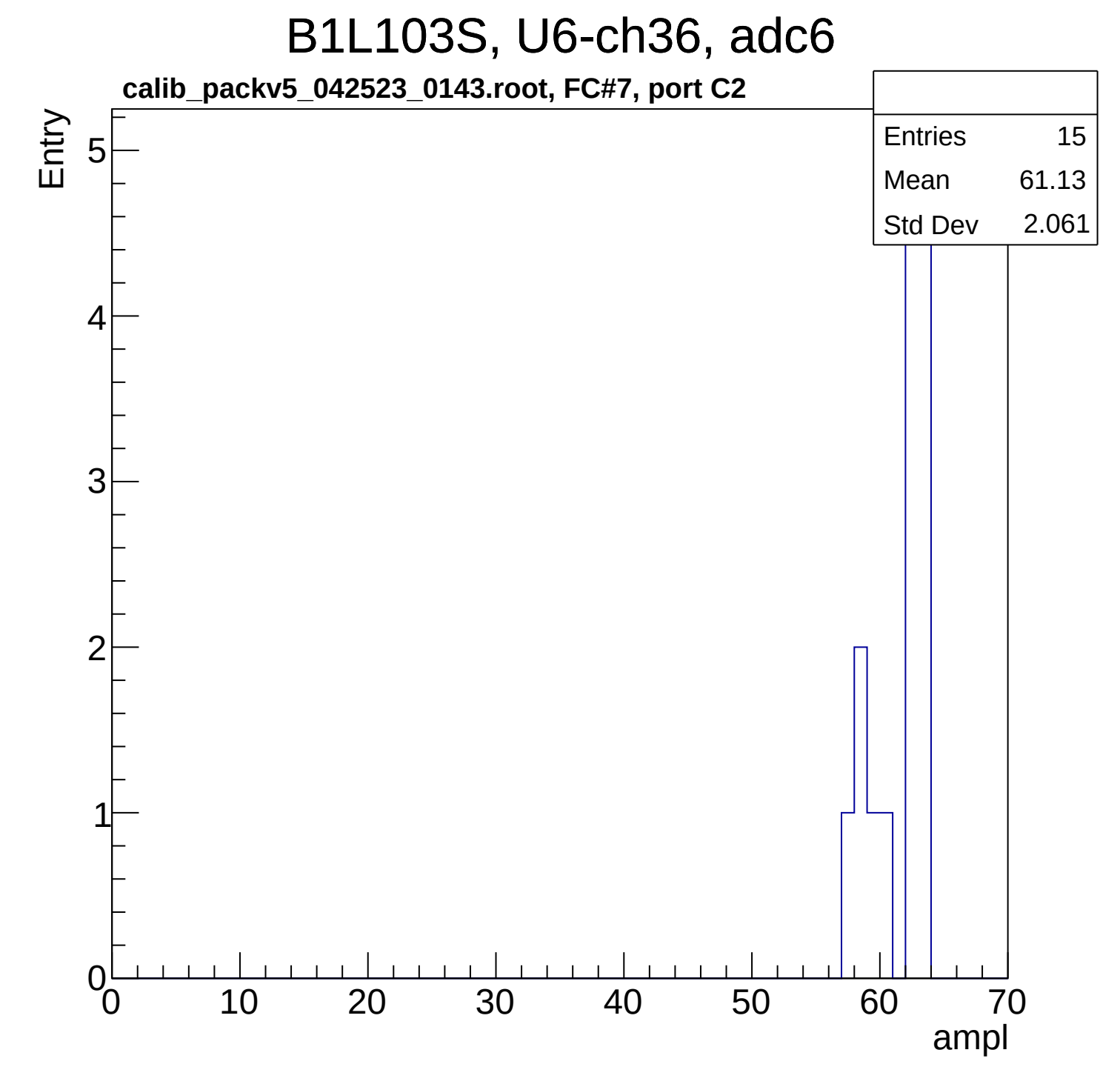
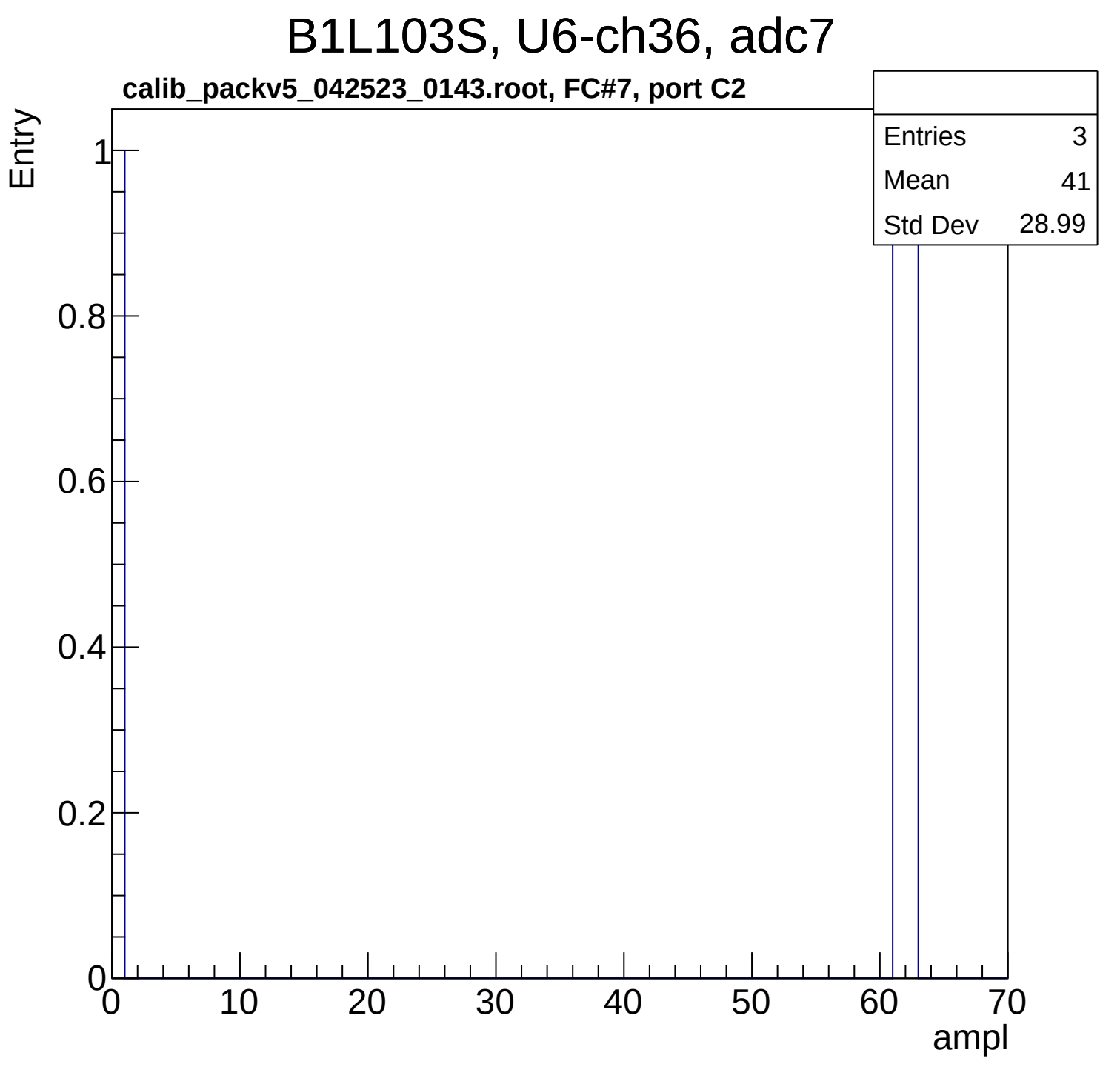
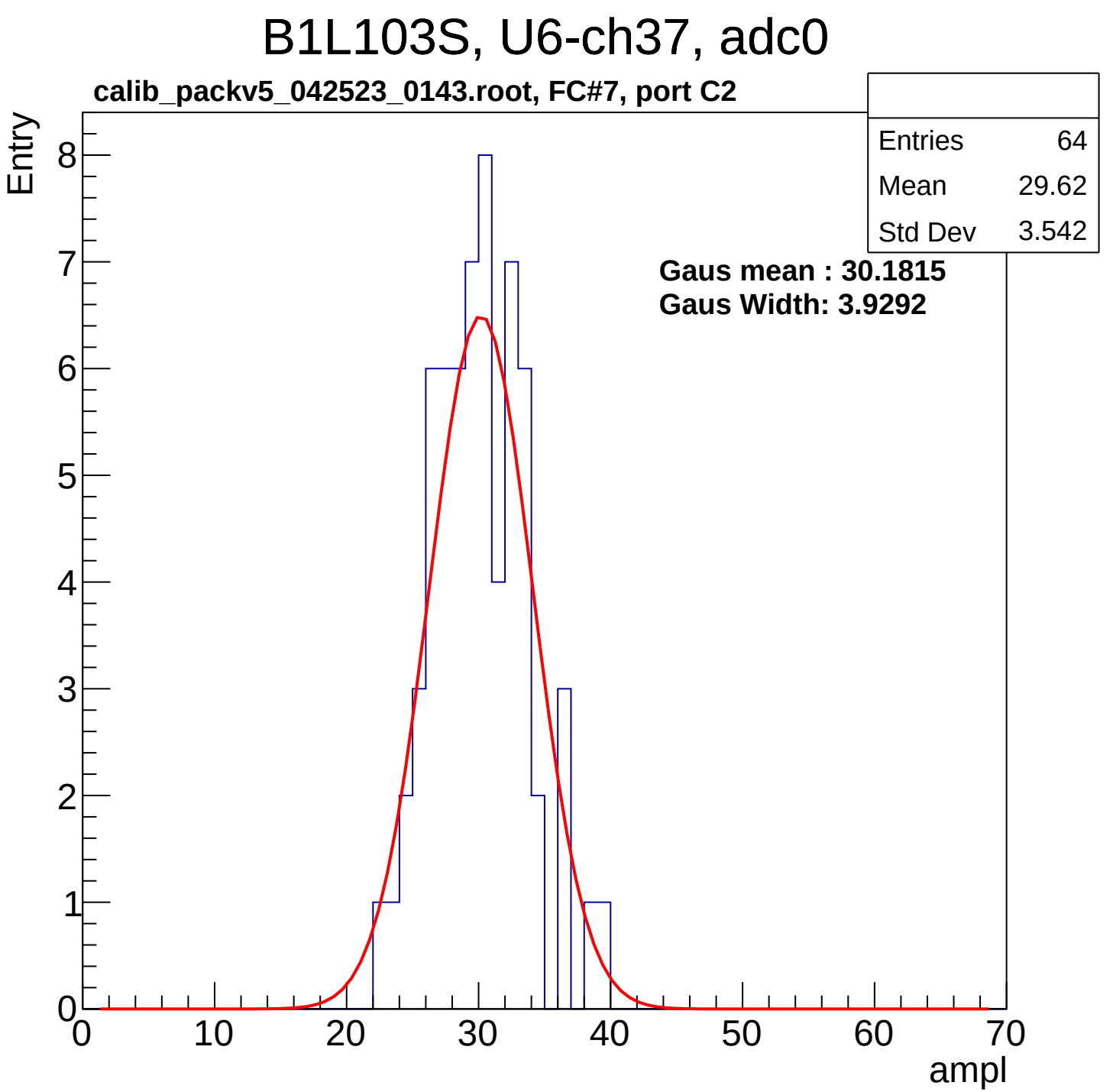
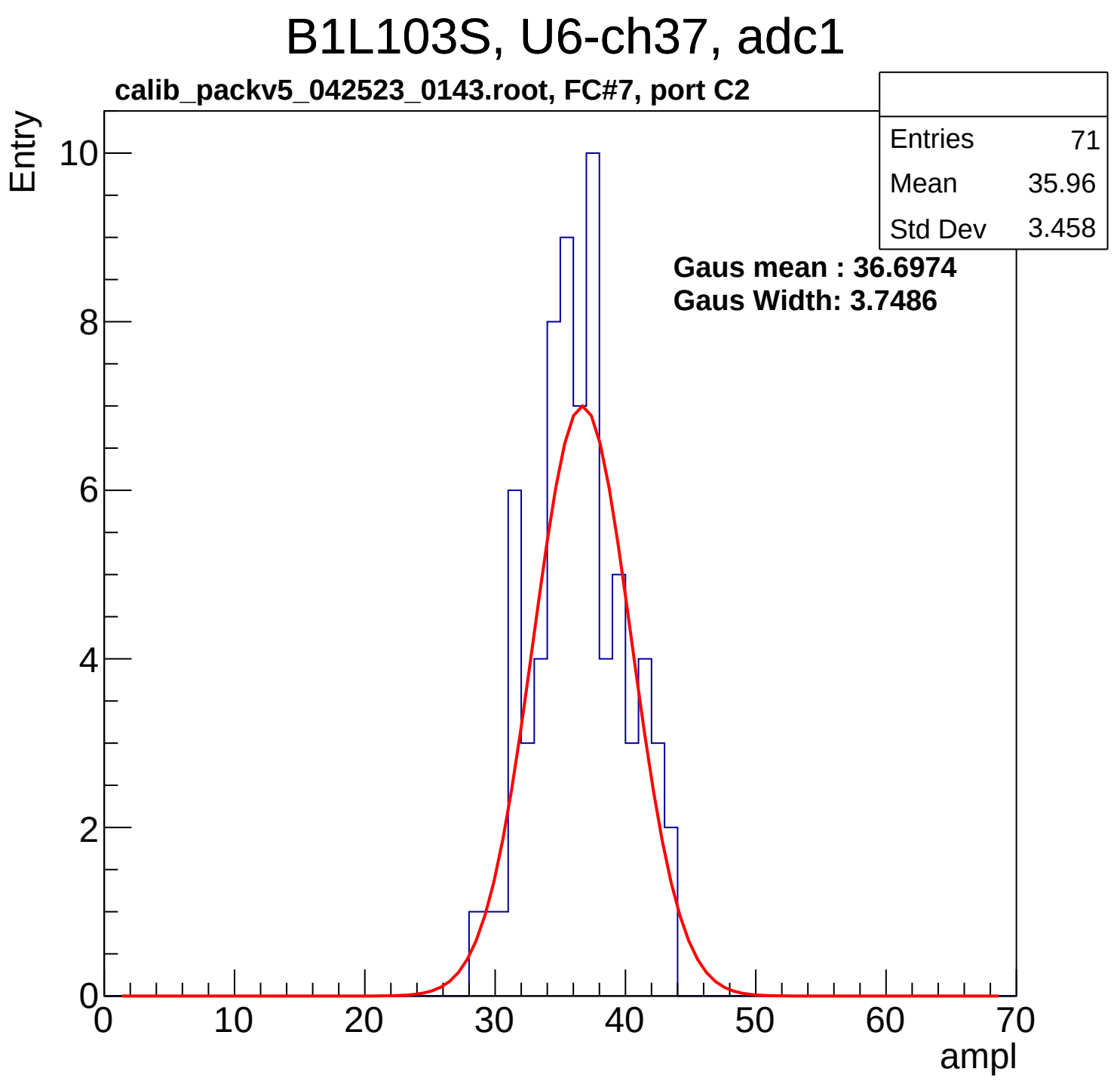
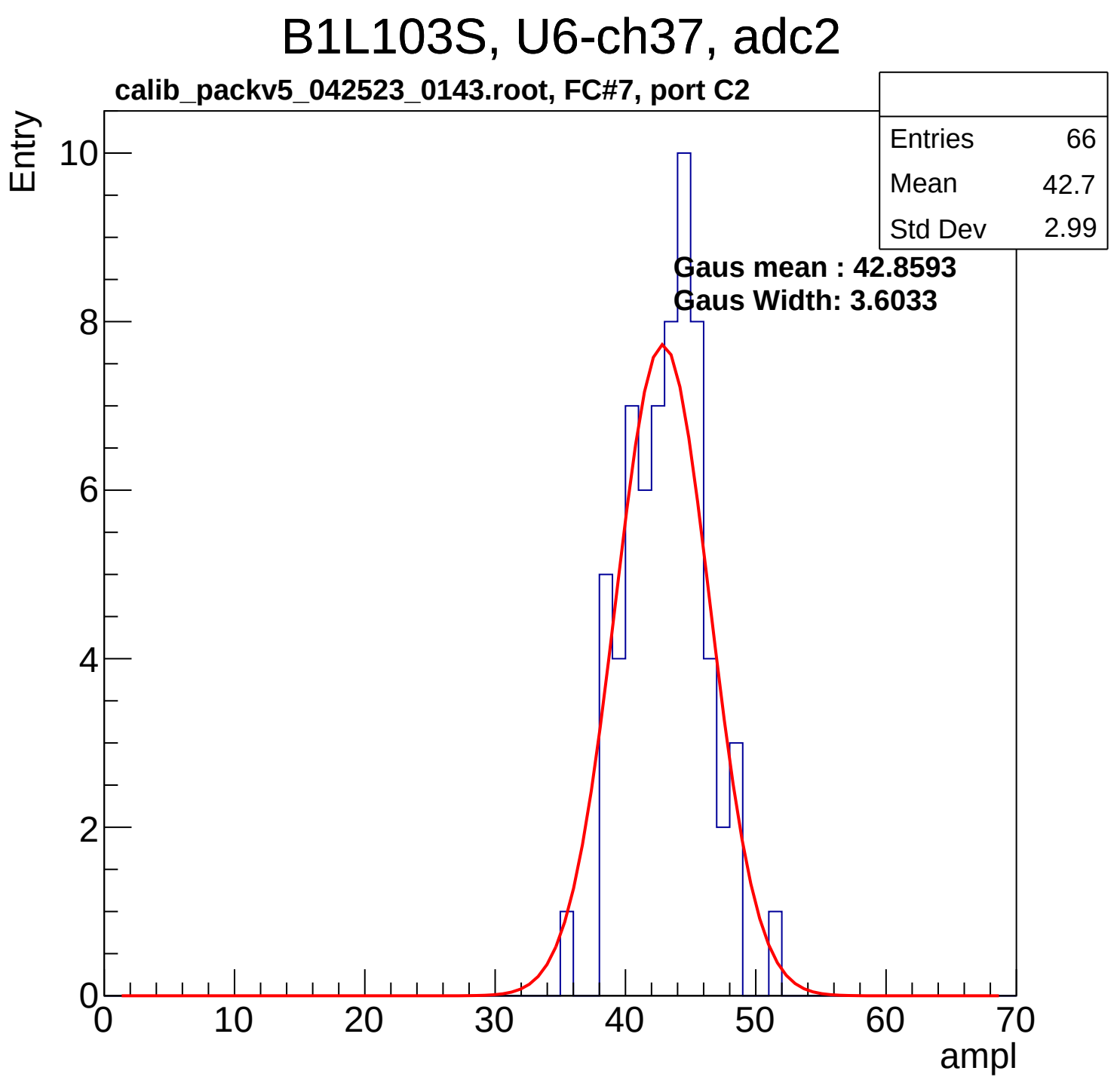
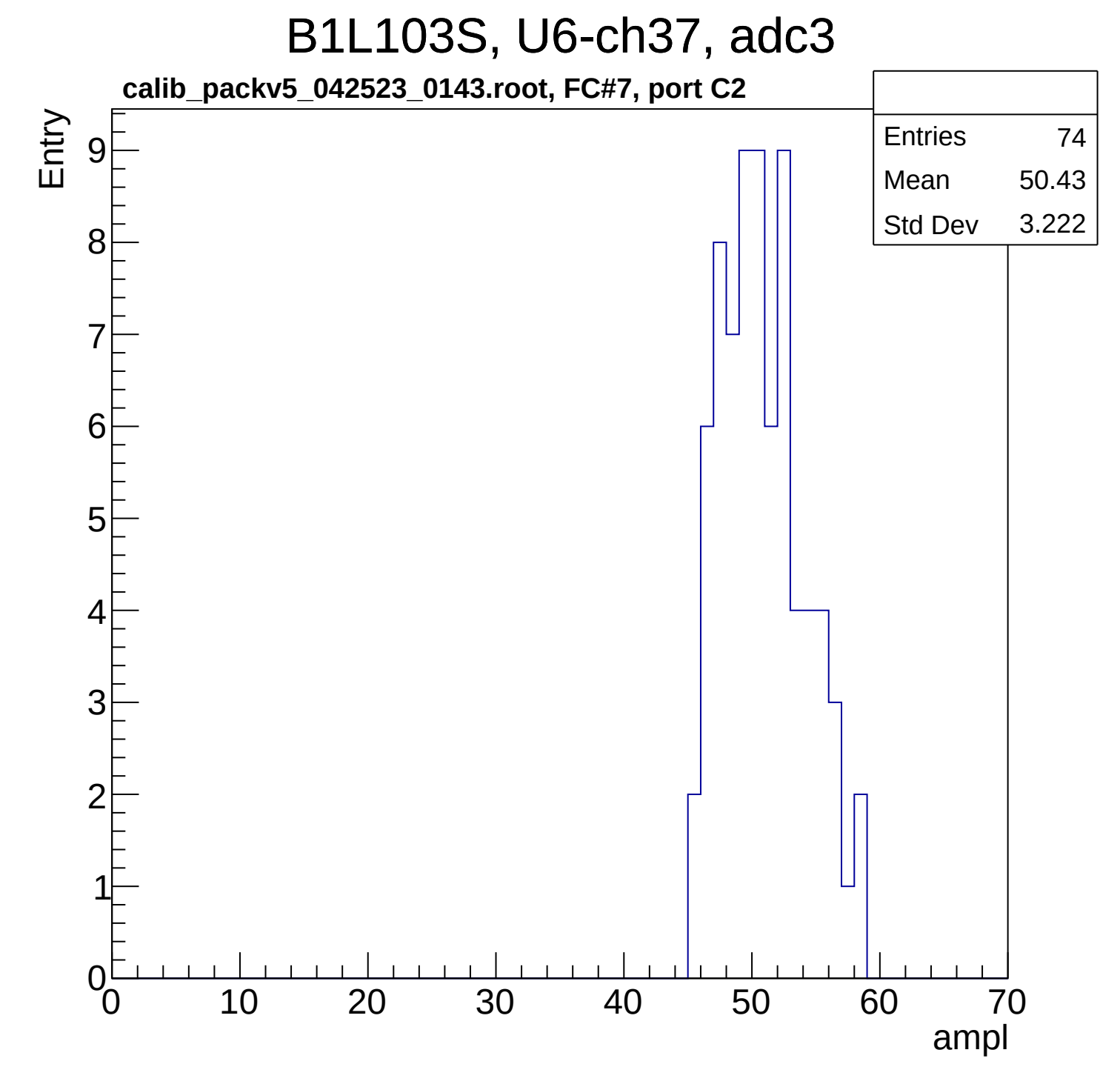

B1L103S, U6-ch36, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 61.13
Std Dev 2.061
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch36, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch37, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 29.62
Std Dev 3.542
Gaus mean : 30.1815
Gaus Width: 3.9292
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch37, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 35.96
Std Dev 3.458
Gaus mean : 36.6974
Gaus Width: 3.7486
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch37, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.7
Std Dev 2.99
Gaus mean : 42.8593
Gaus Width: 3.6033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch37, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 50.43
Std Dev 3.222
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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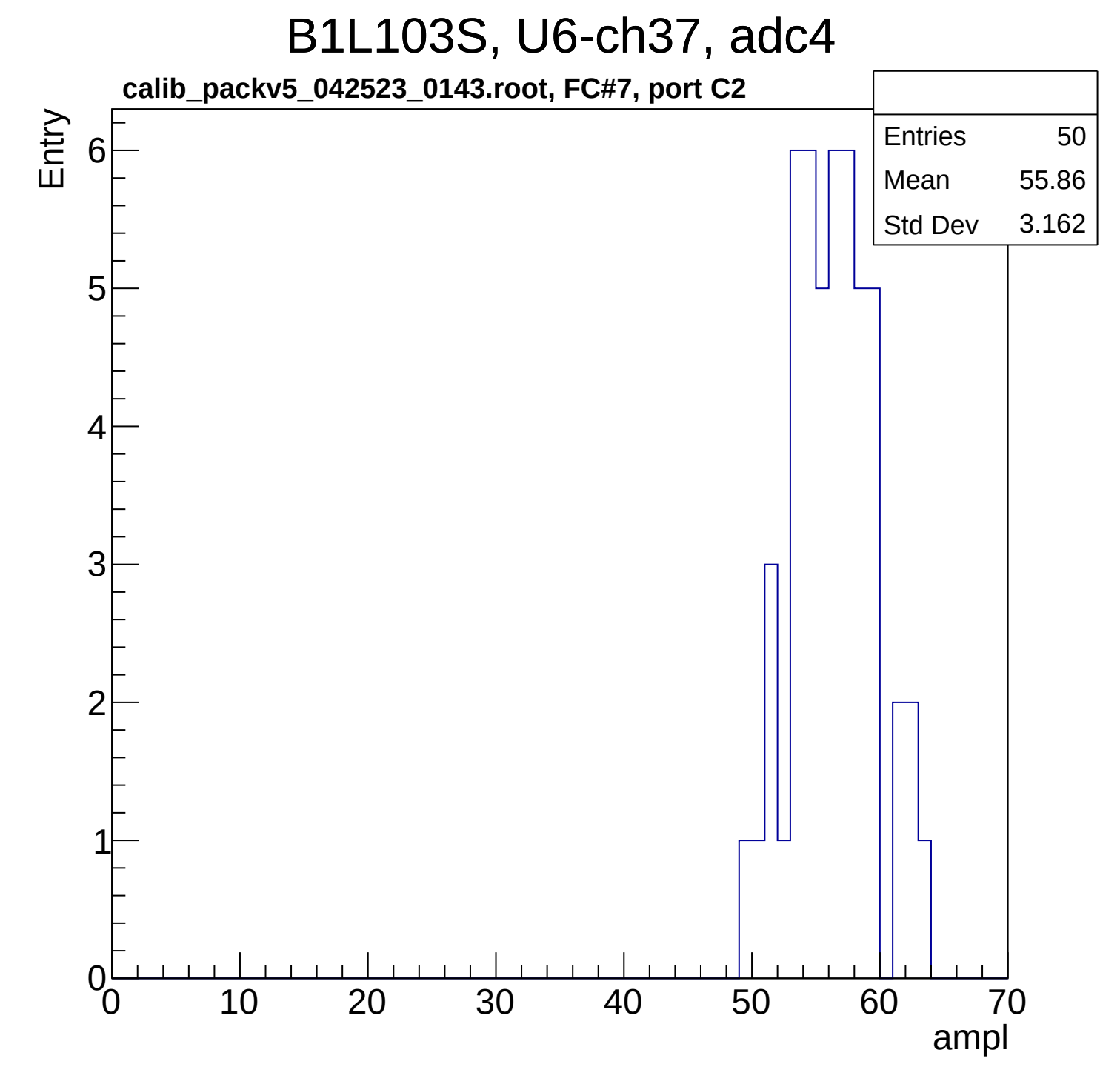
B1L103S, U6-ch37, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 55.86
Std Dev 3.162
Entry
ampl
0
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3
4
5
6
0
10
20
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40
50
60
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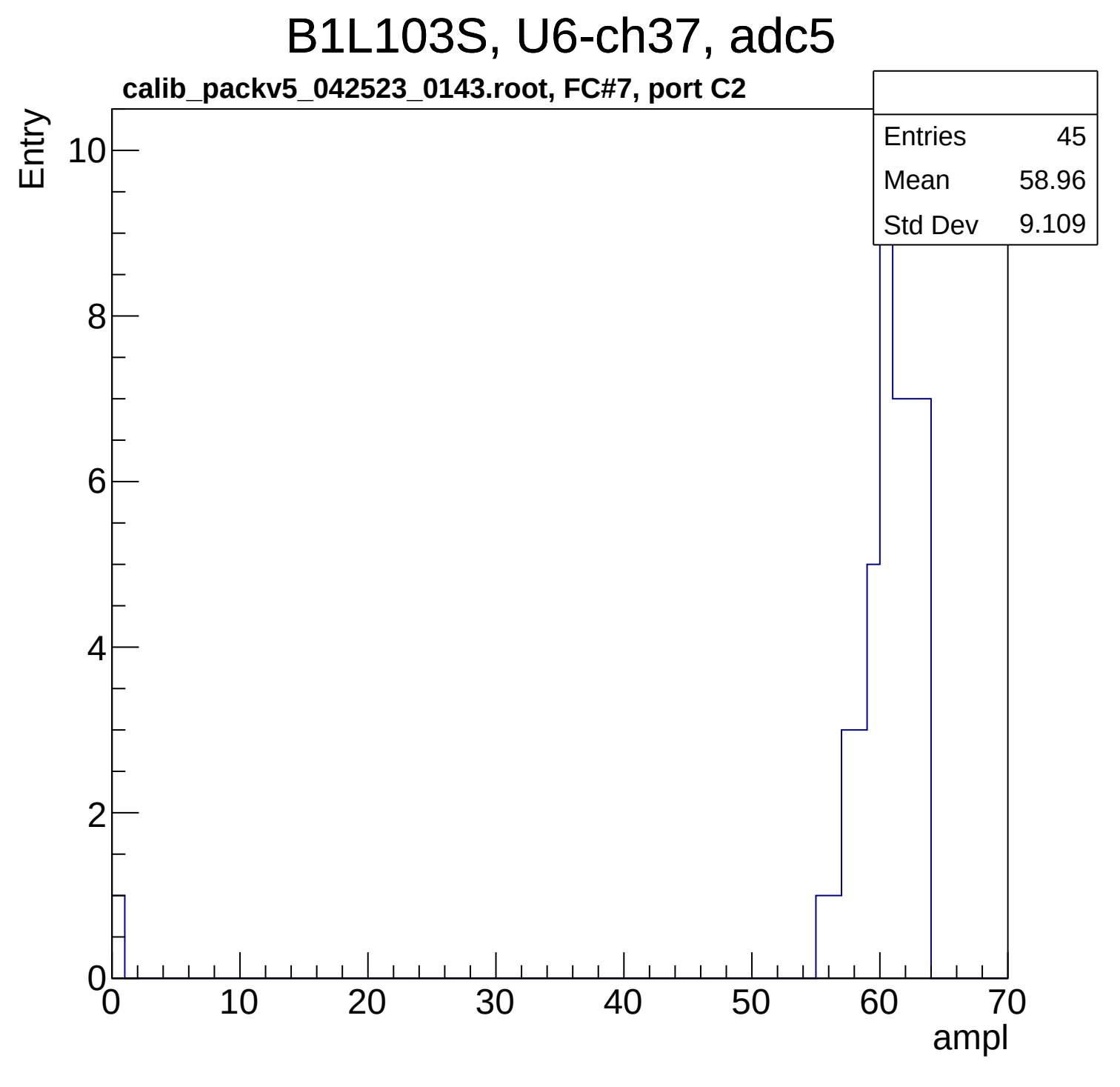
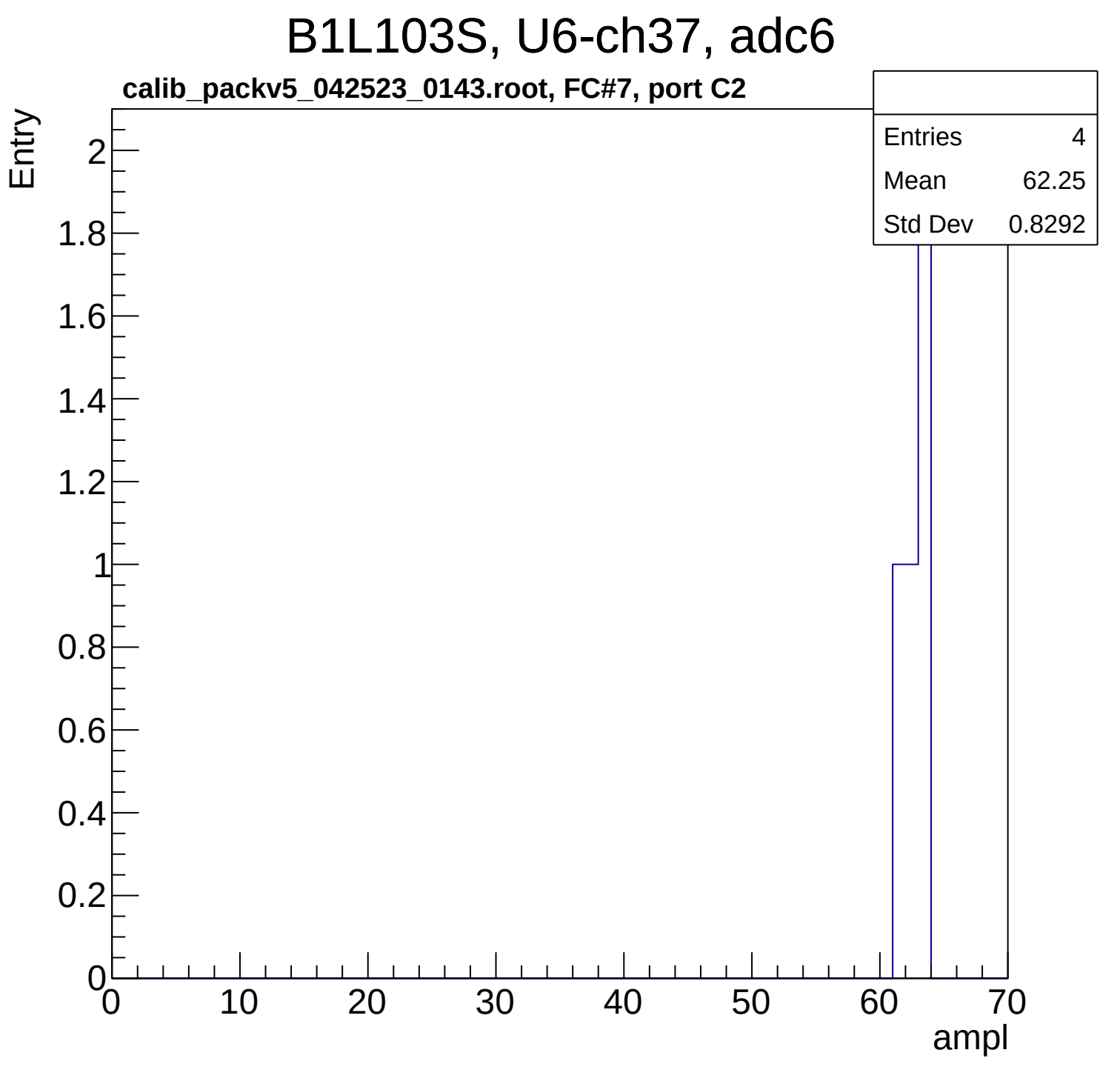

B1L103S, U6-ch37, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.96
Std Dev 9.109
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch37, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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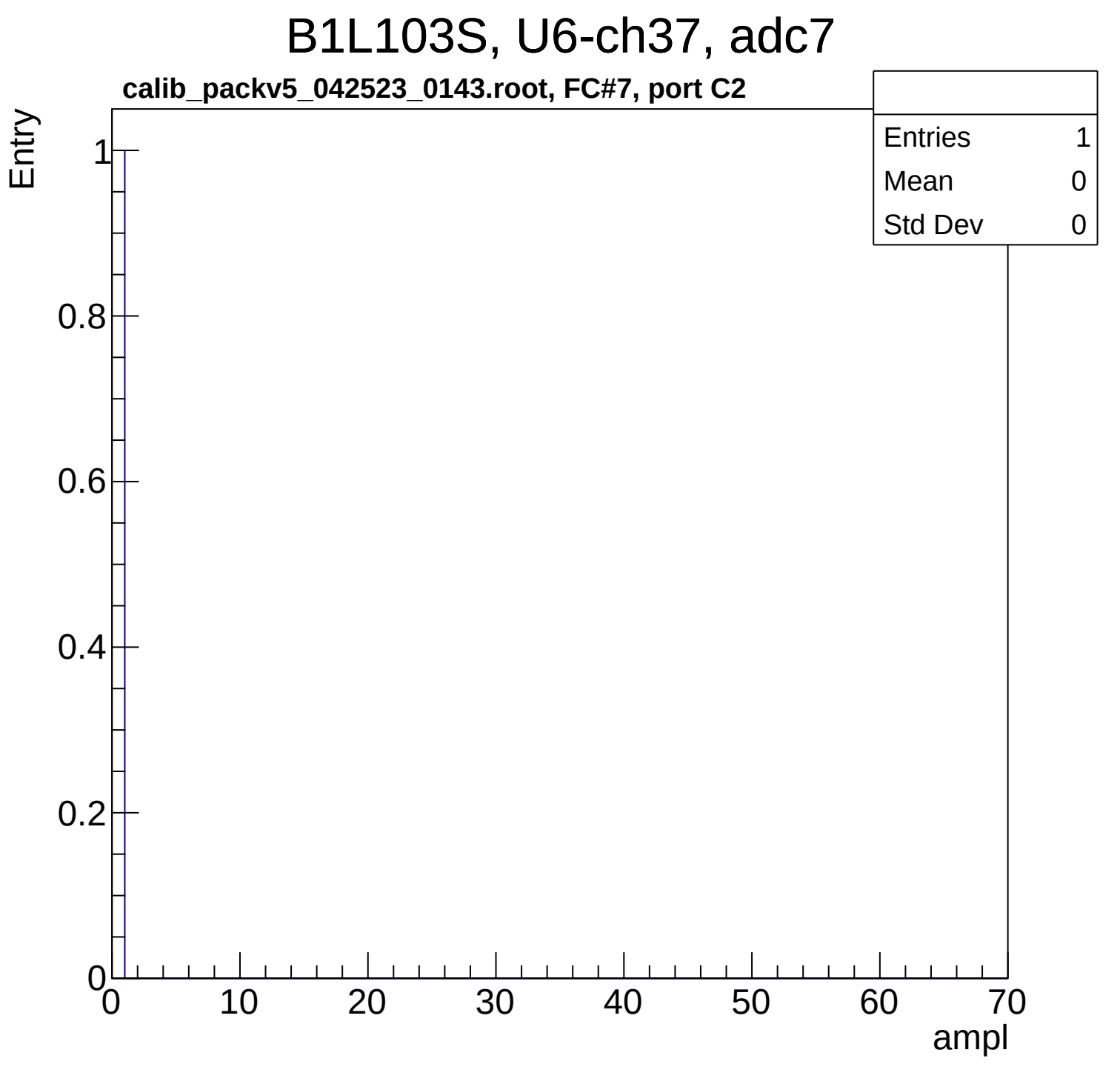
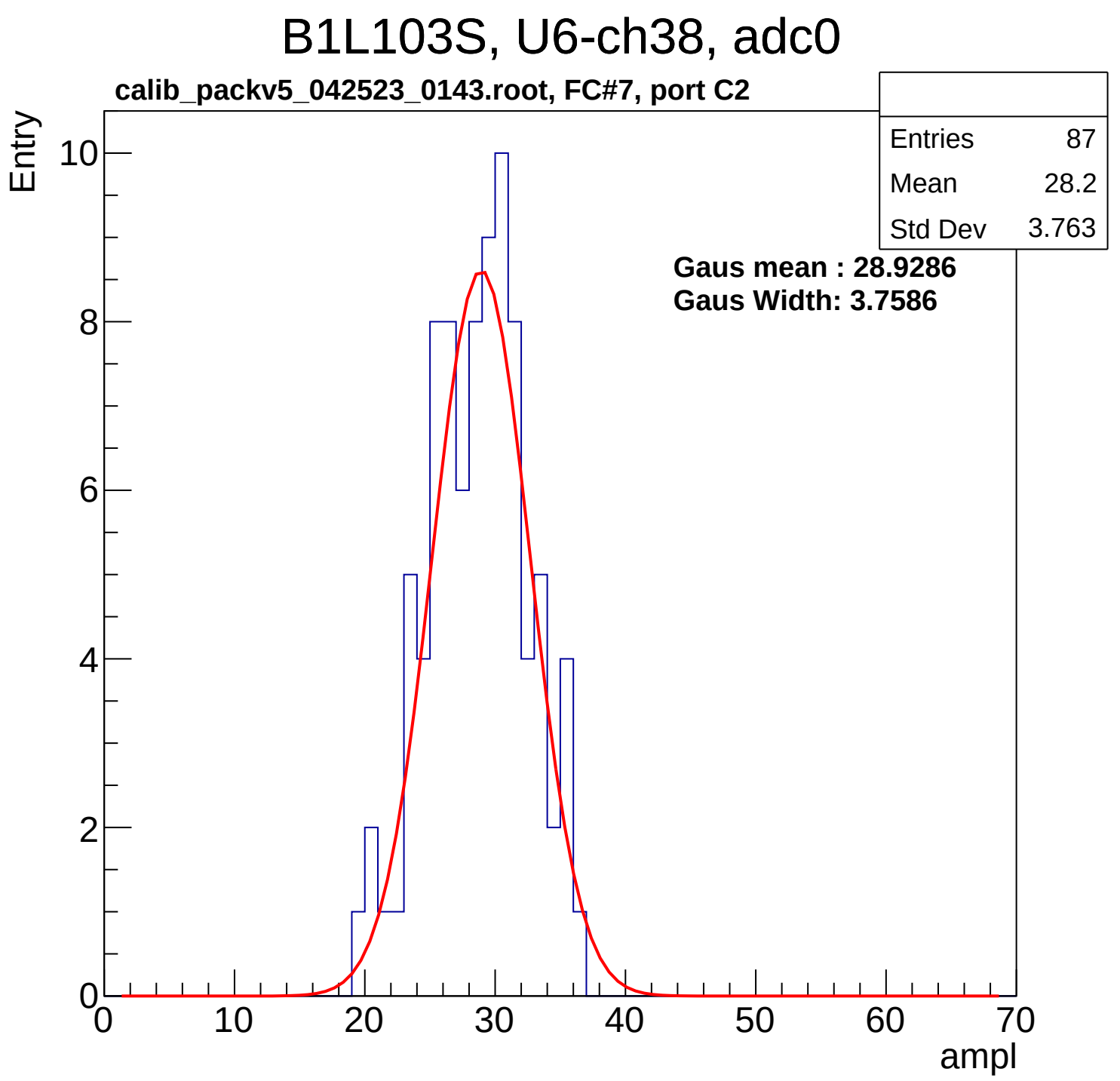

B1L103S, U6-ch37, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch38, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 28.2
Std Dev 3.763
Gaus mean : 28.9286
Gaus Width: 3.7586
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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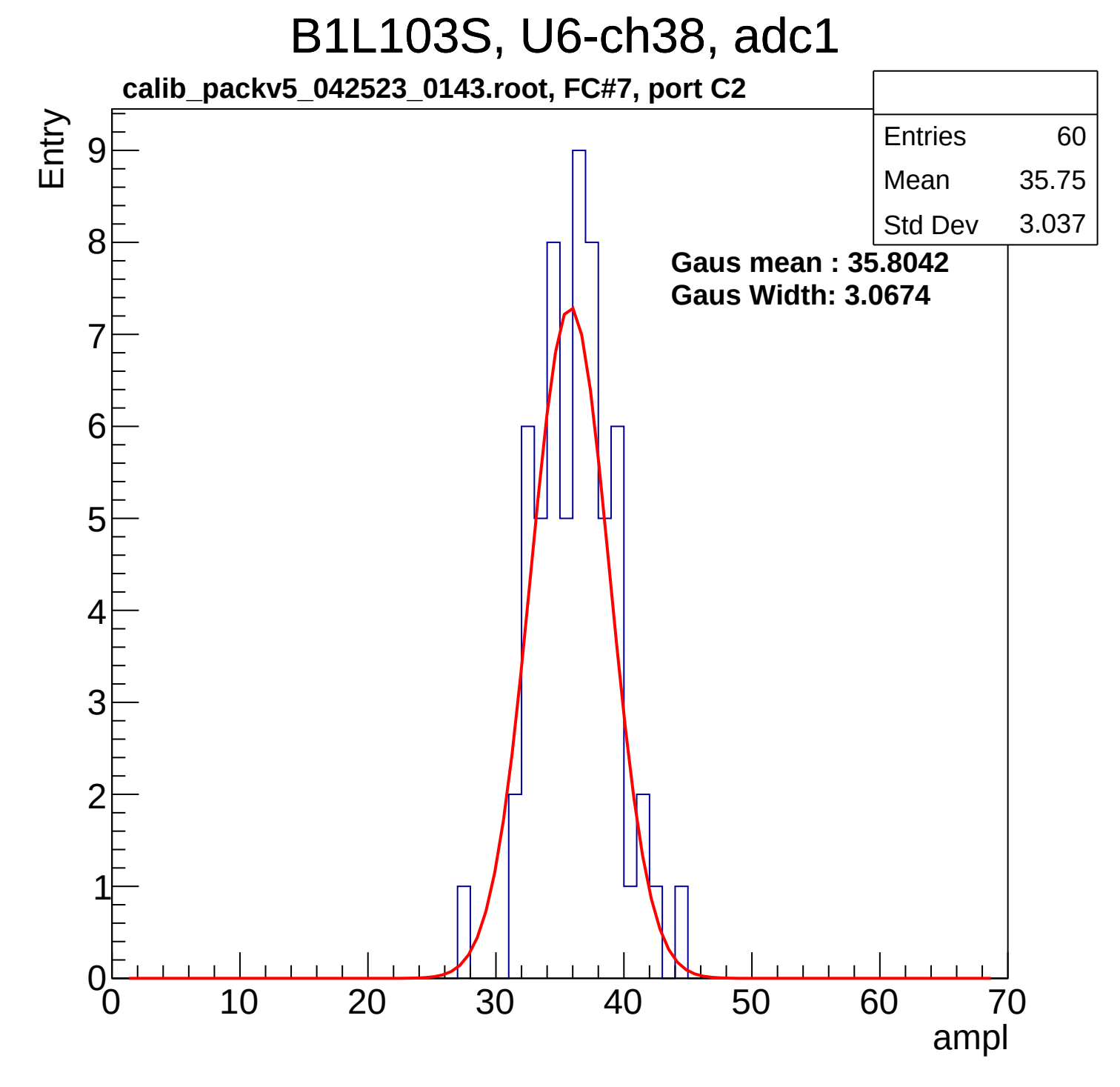
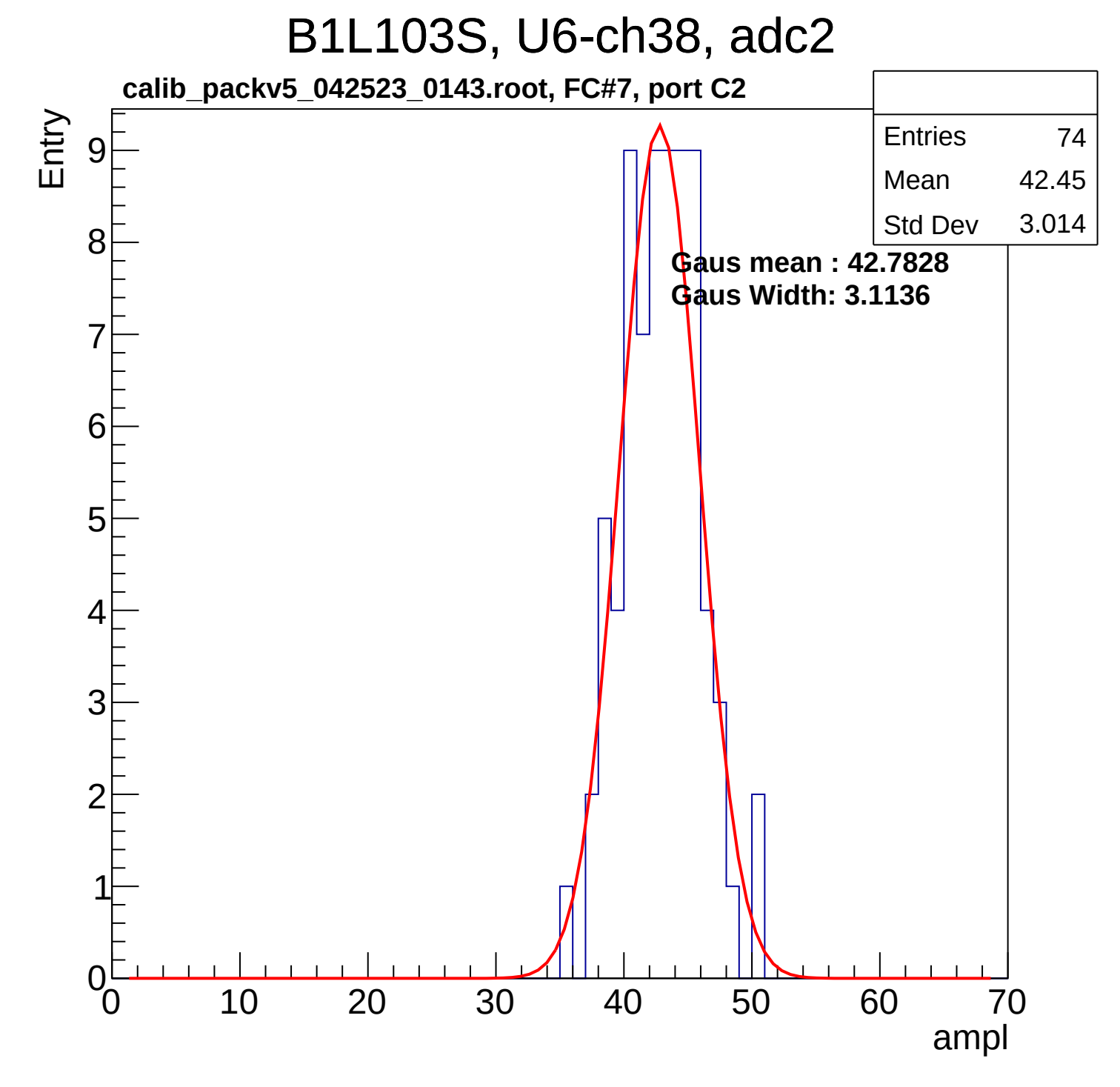

B1L103S, U6-ch38, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 35.75
Std Dev 3.037
Gaus mean : 35.8042
Gaus Width: 3.0674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch38, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 42.45
Std Dev 3.014
Gaus mean : 42.7828
Gaus Width: 3.1136
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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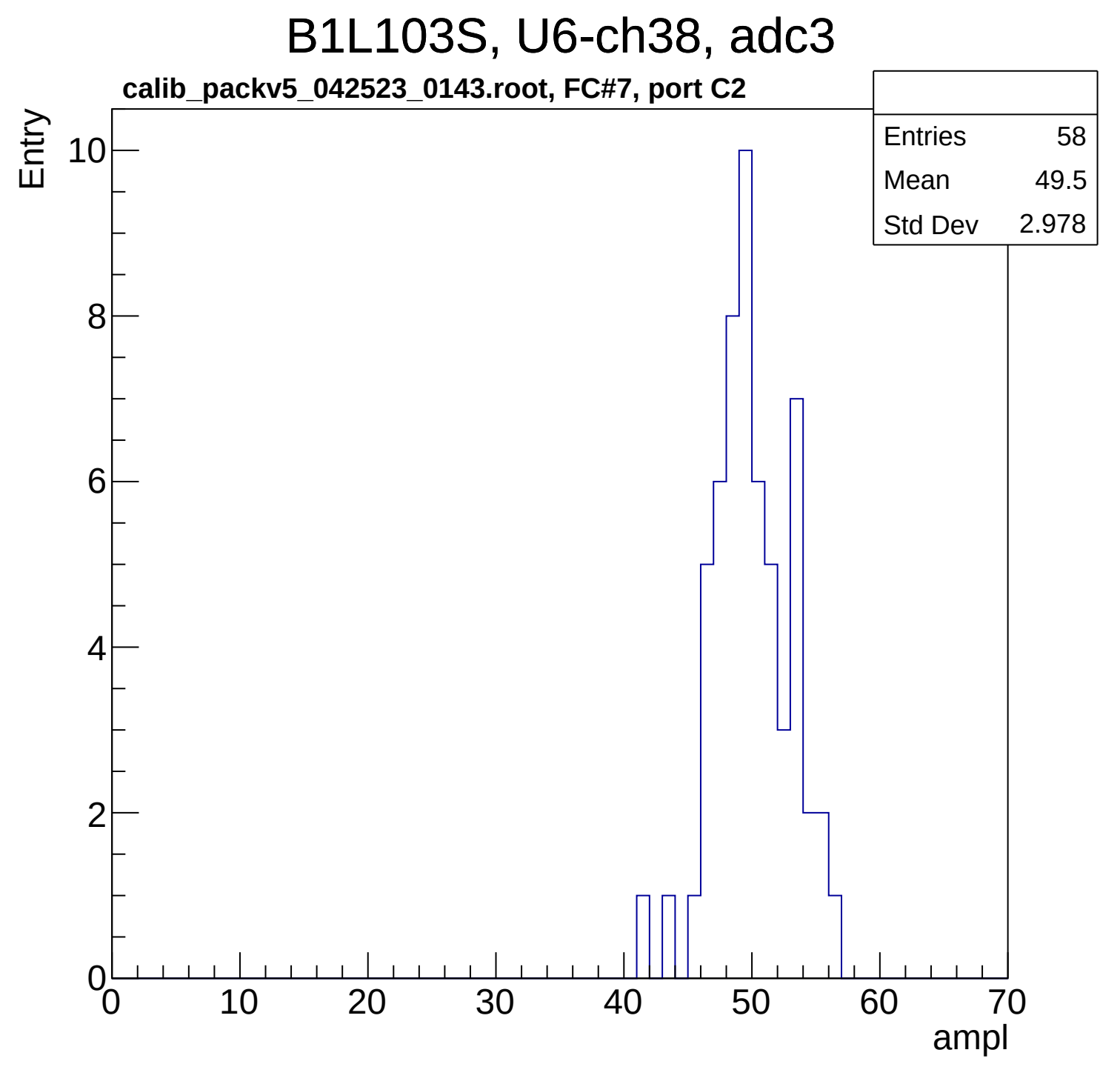
B1L103S, U6-ch38, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.5
Std Dev 2.978
Entry
ampl
0
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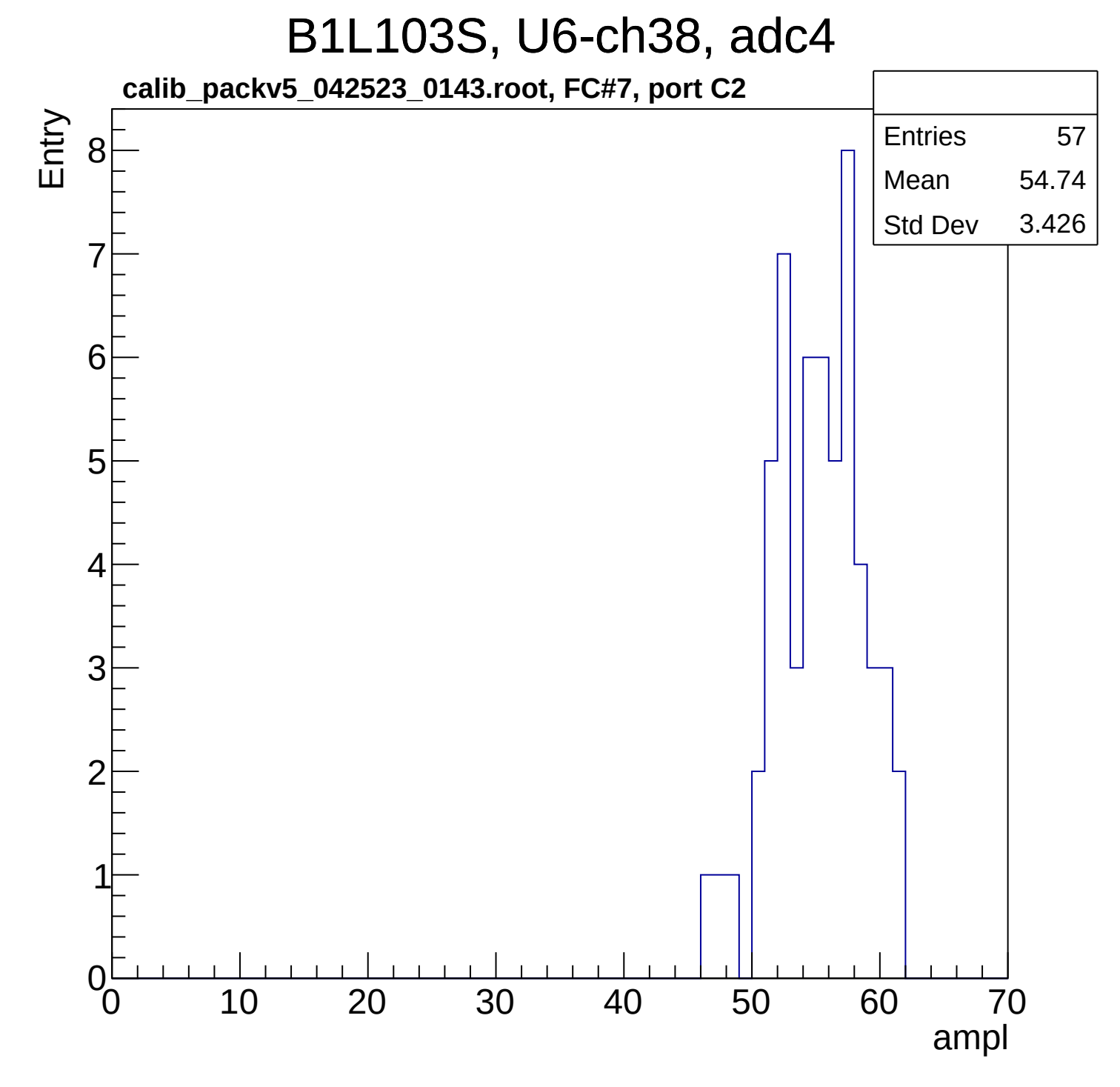
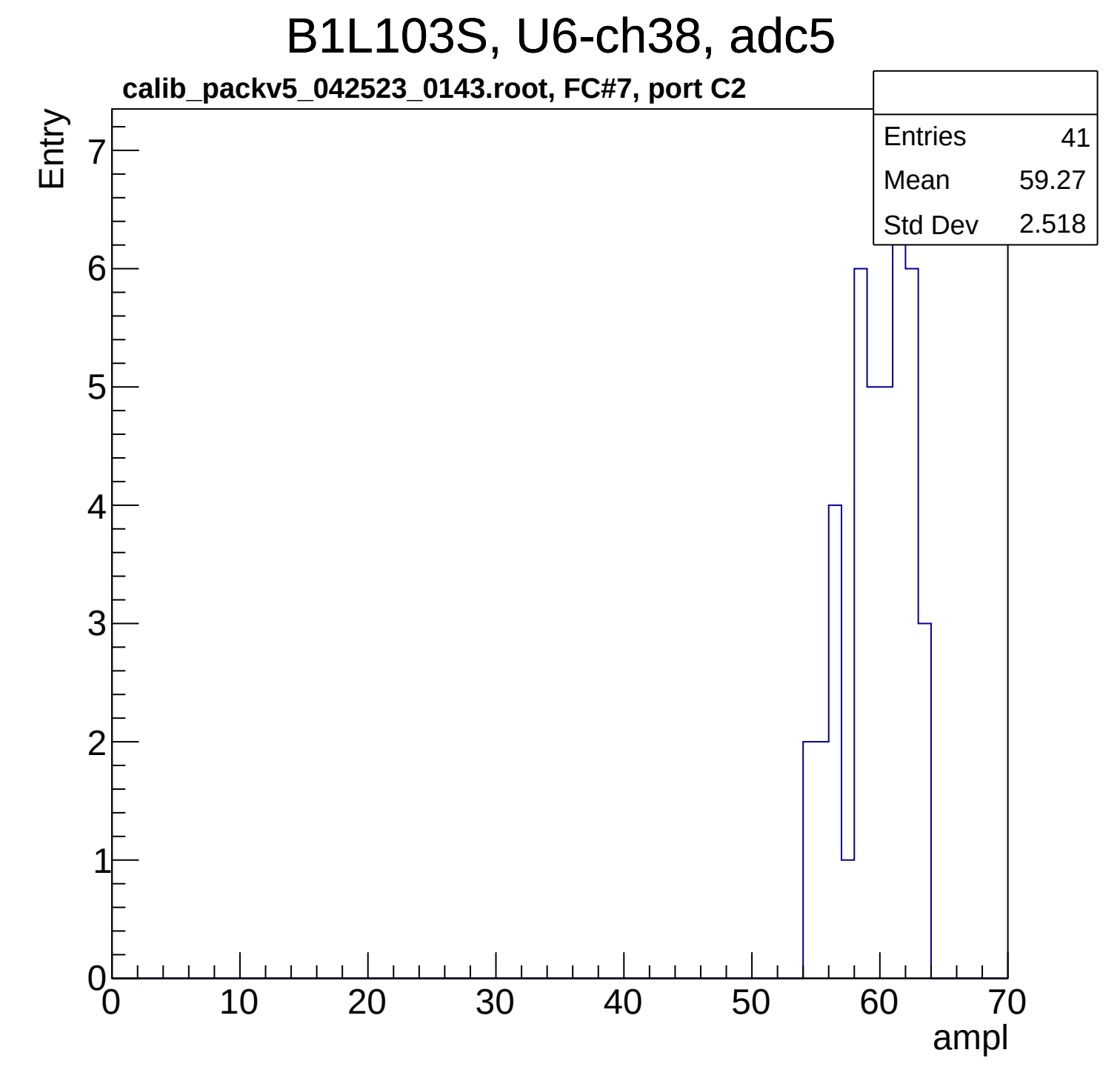
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch38, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 54.74
Std Dev 3.426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch38, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.27
Std Dev 2.518
Entry
ampl
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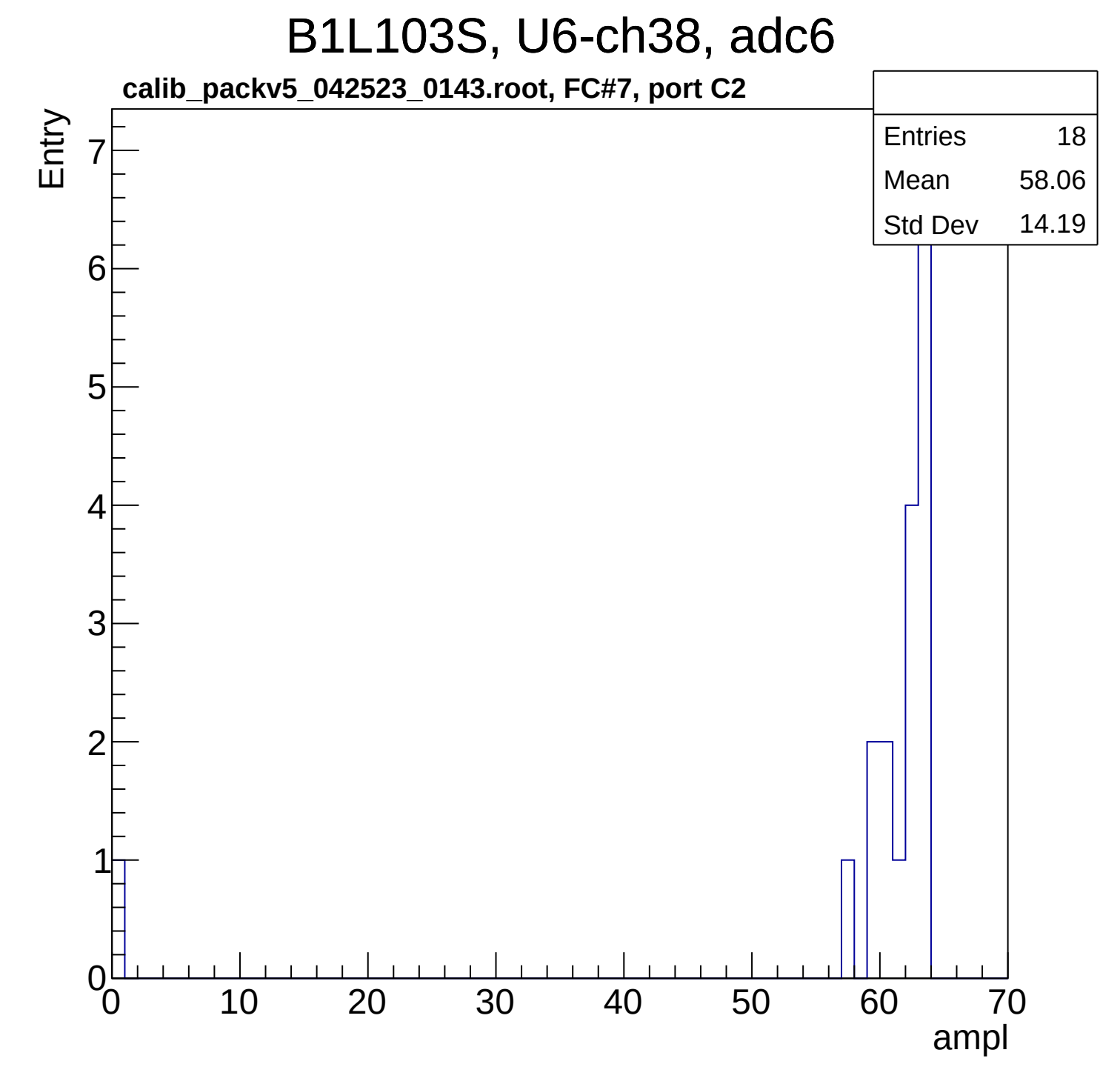
B1L103S, U6-ch38, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 58.06
Std Dev 14.19
Entry
ampl
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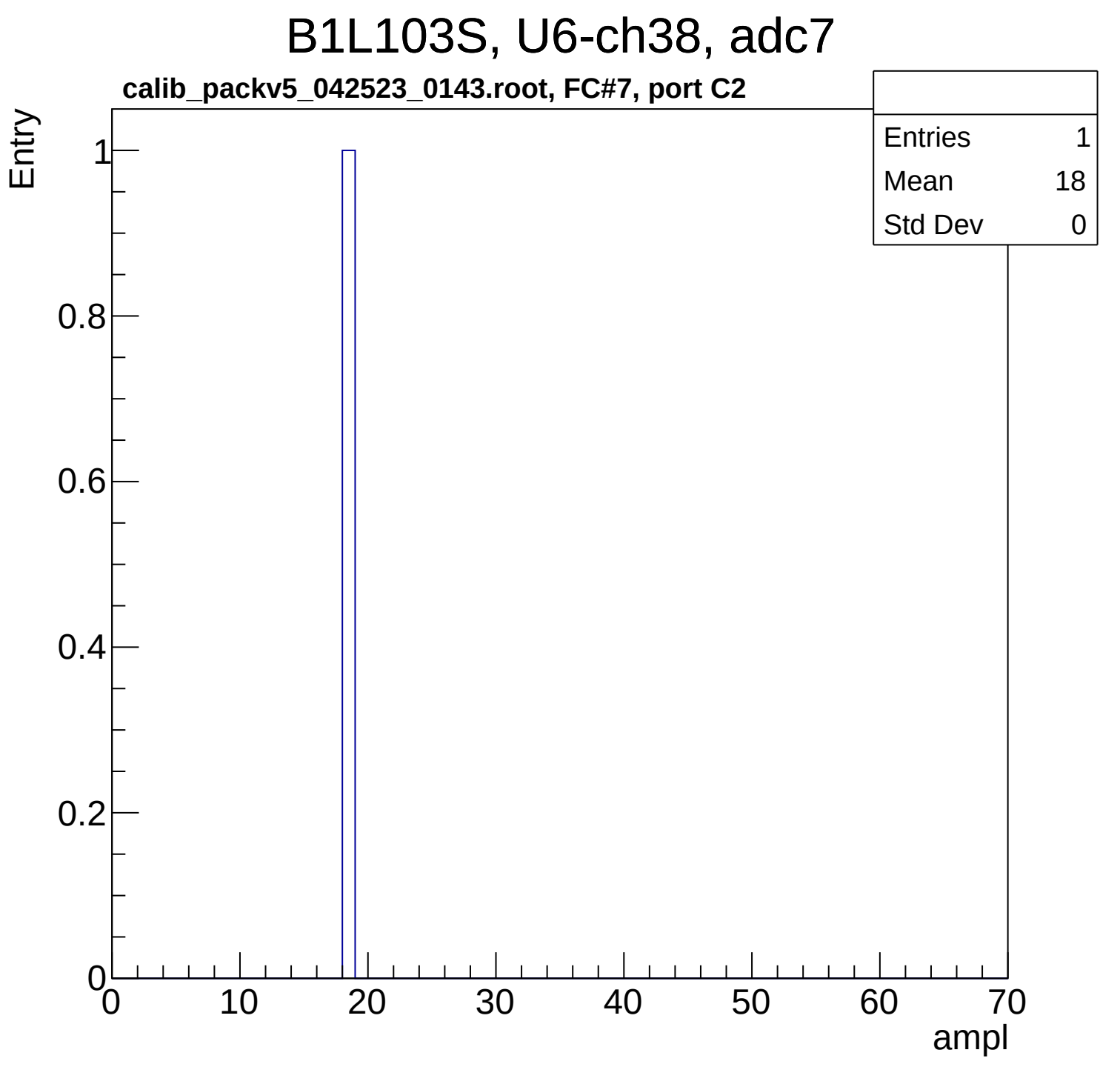
B1L103S, U6-ch38, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 18
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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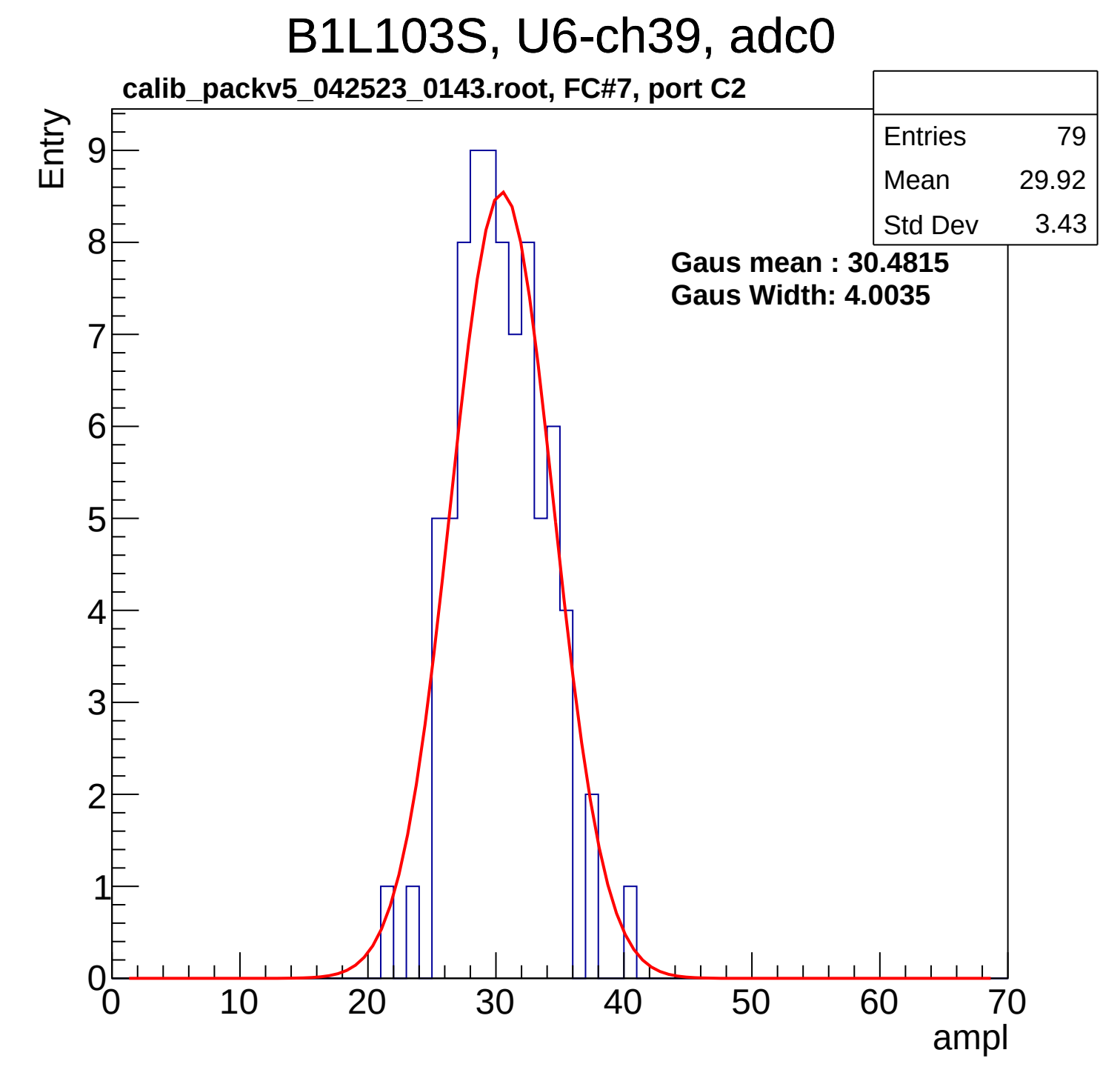
50
60
70
ampl


B1L103S, U6-ch39, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.92
Std Dev 3.43
Gaus mean : 30.4815
Gaus Width: 4.0035
Entry
ampl
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2
3
4
5
6
7
8
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0
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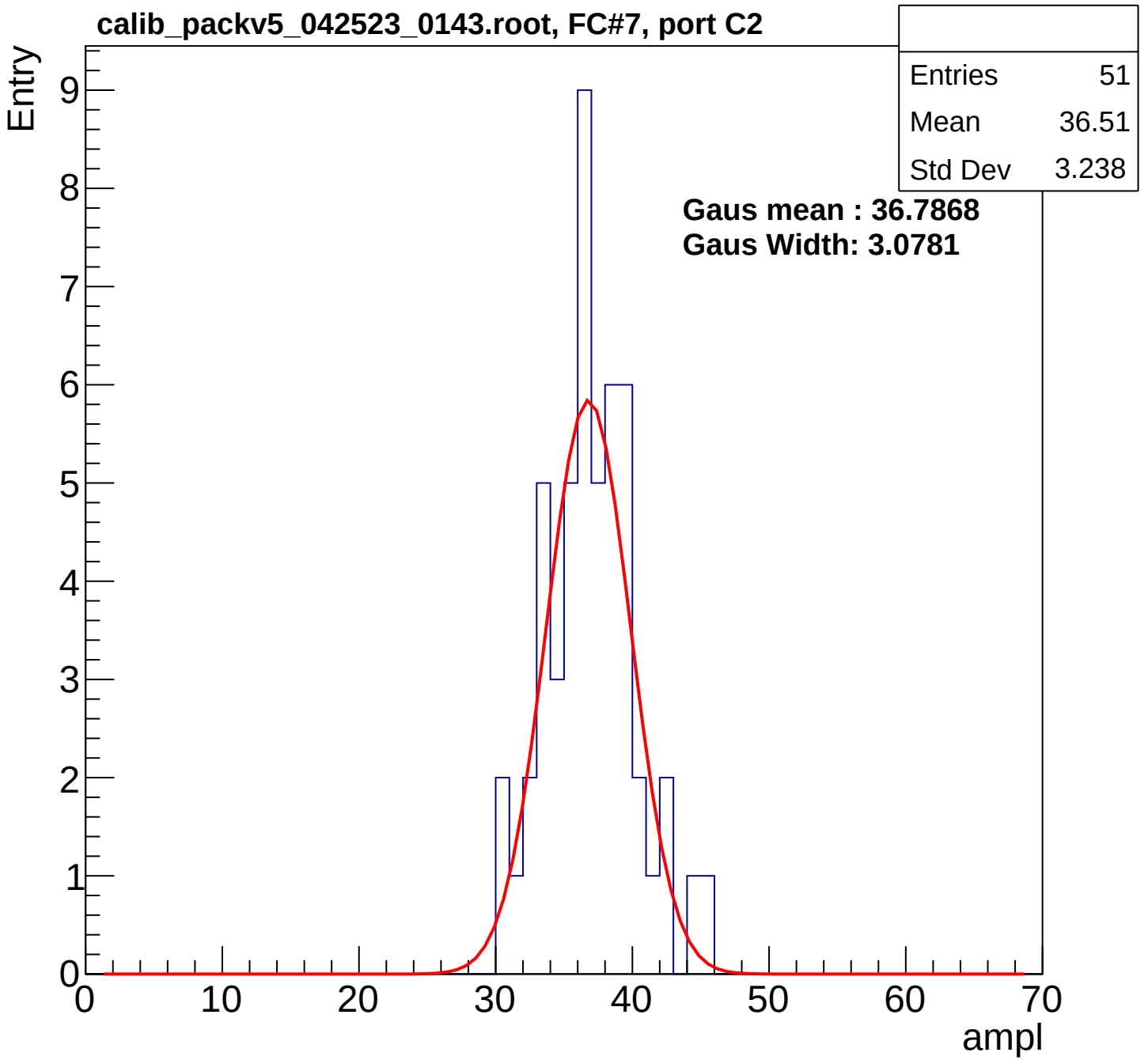
B1L103S, U6-ch39, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 36.51
Std Dev 3.238
Gaus mean : 36.7868
Gaus Width: 3.0781
Entry
ampl
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2
3
4
5
6
7
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0
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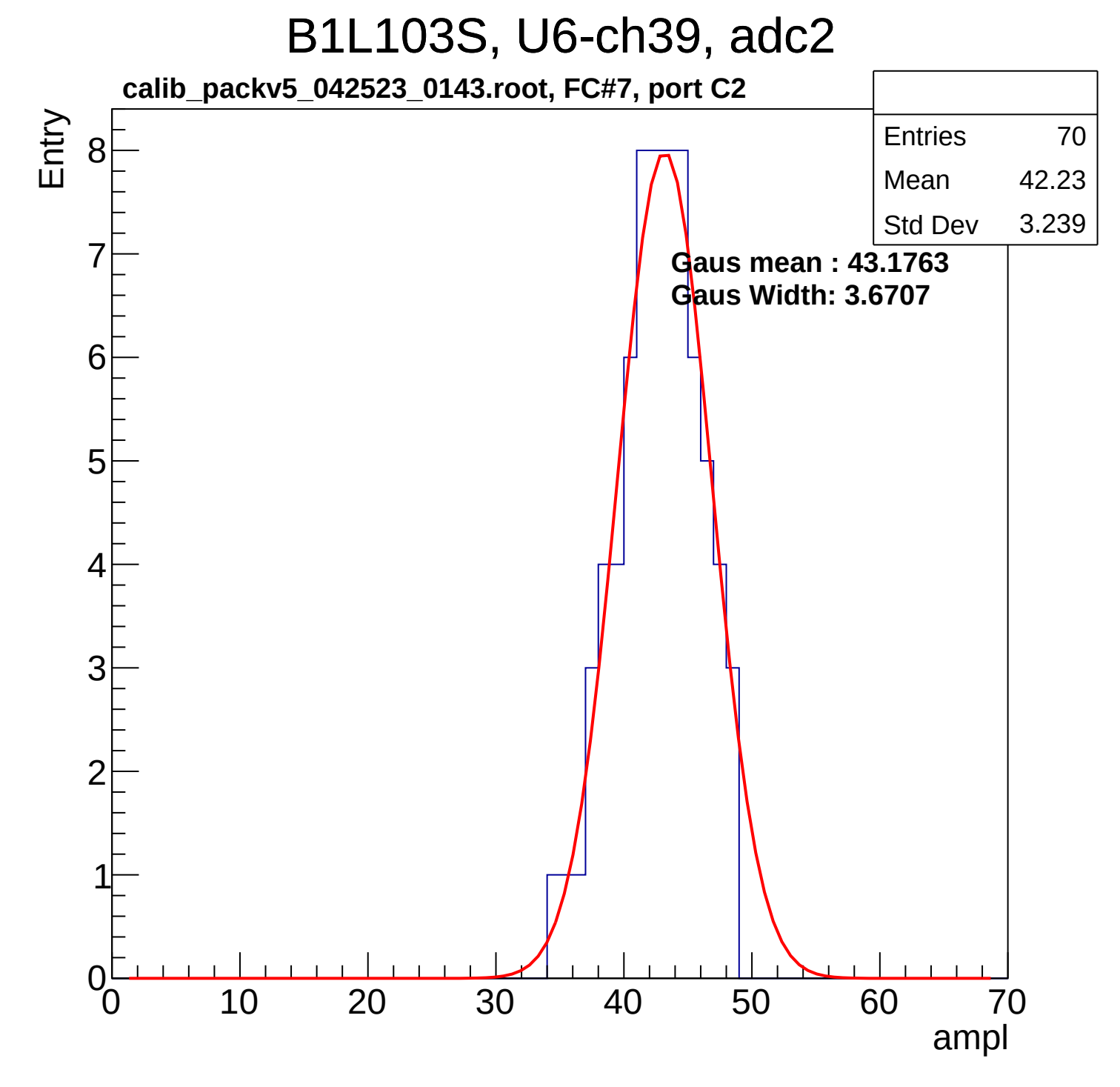
B1L103S, U6-ch39, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.23
Std Dev 3.239
Gaus mean : 43.1763
Gaus Width: 3.6707
Entry
ampl
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1
2
3
4
5
6
7
8
0
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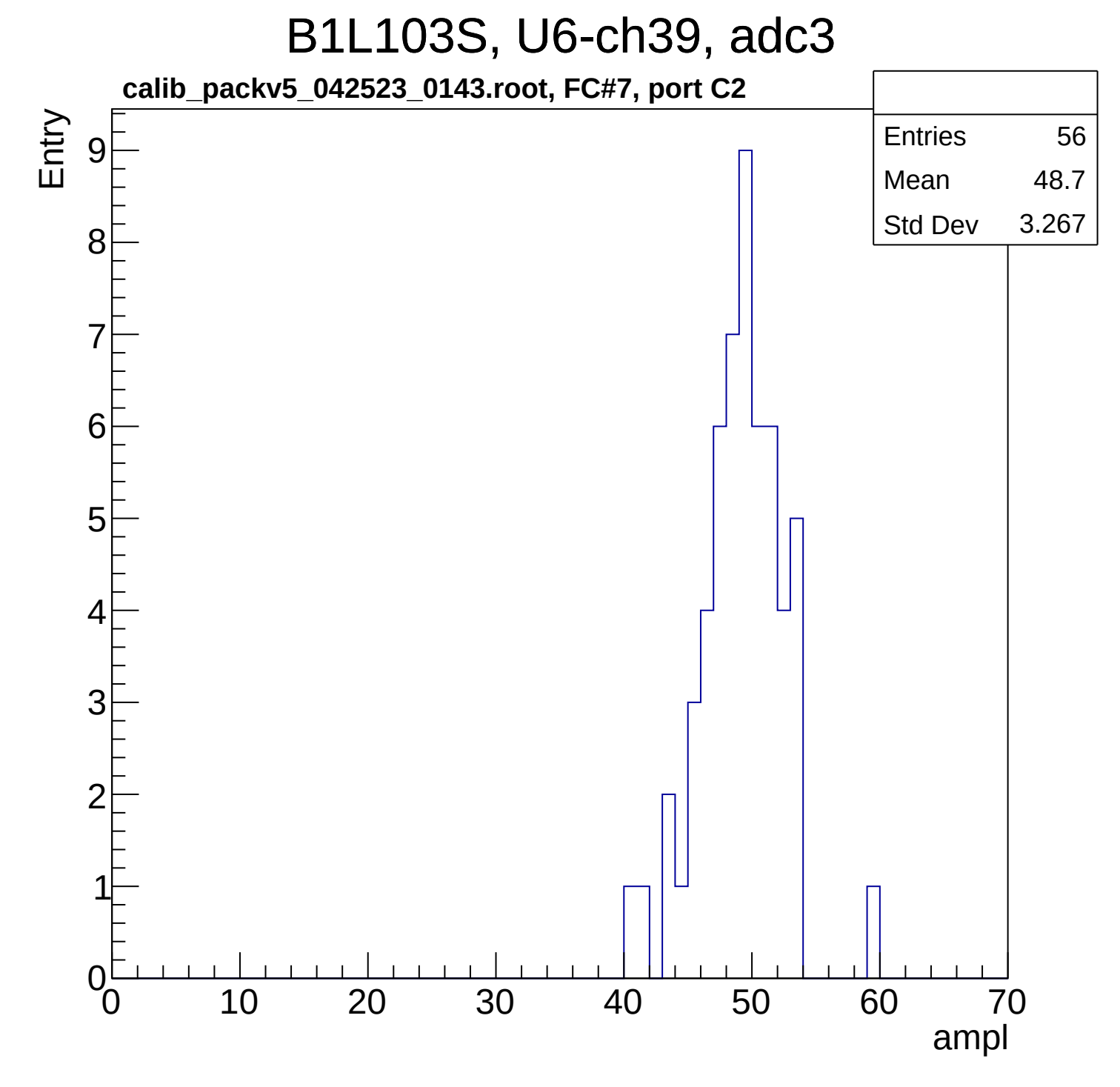
B1L103S, U6-ch39, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 48.7
Std Dev 3.267
Entry
ampl
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2
3
4
5
6
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0
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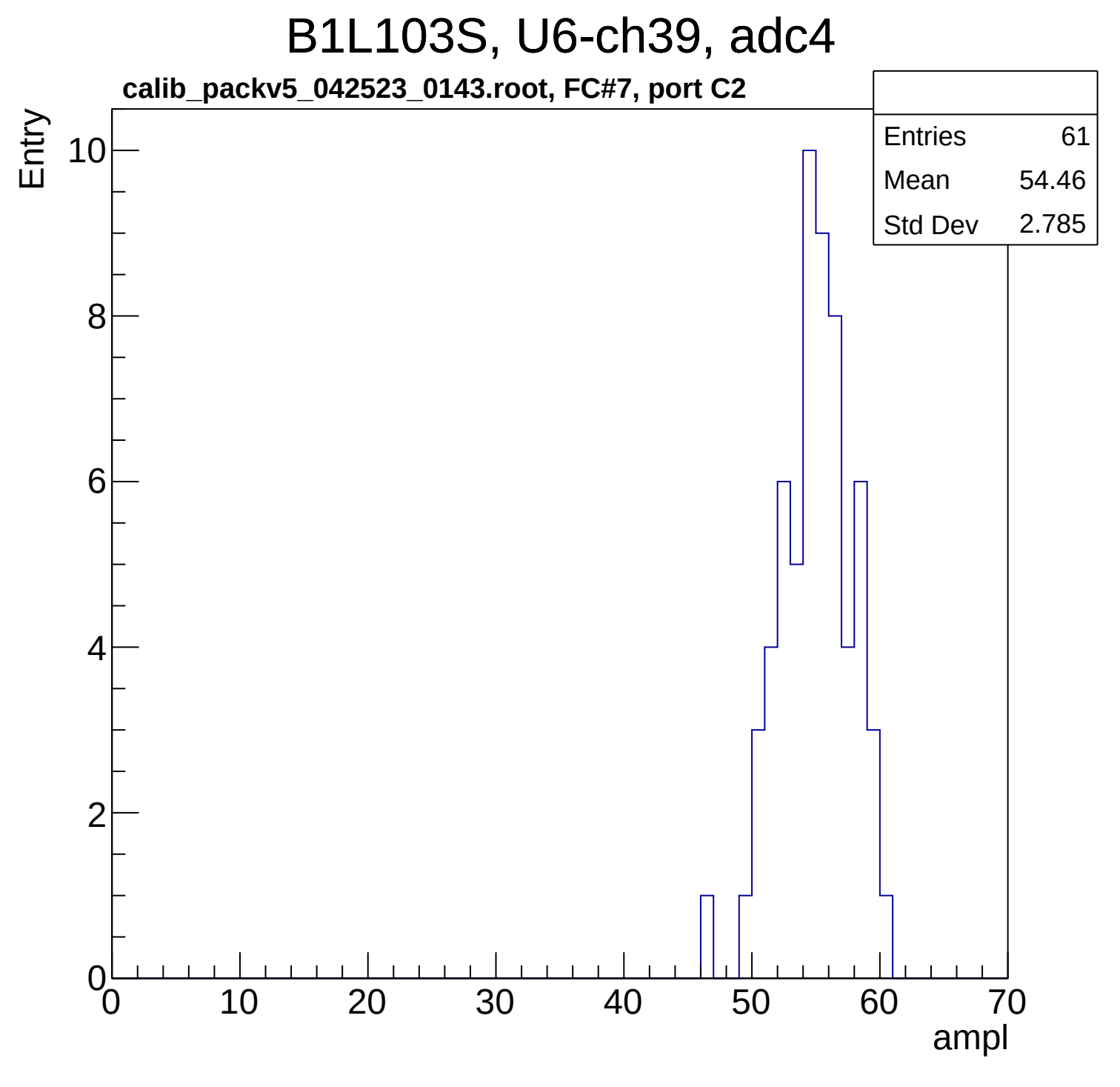
B1L103S, U6-ch39, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 54.46
Std Dev 2.785
Entry
ampl
0
2
4
6
8
10
0
10
20
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60
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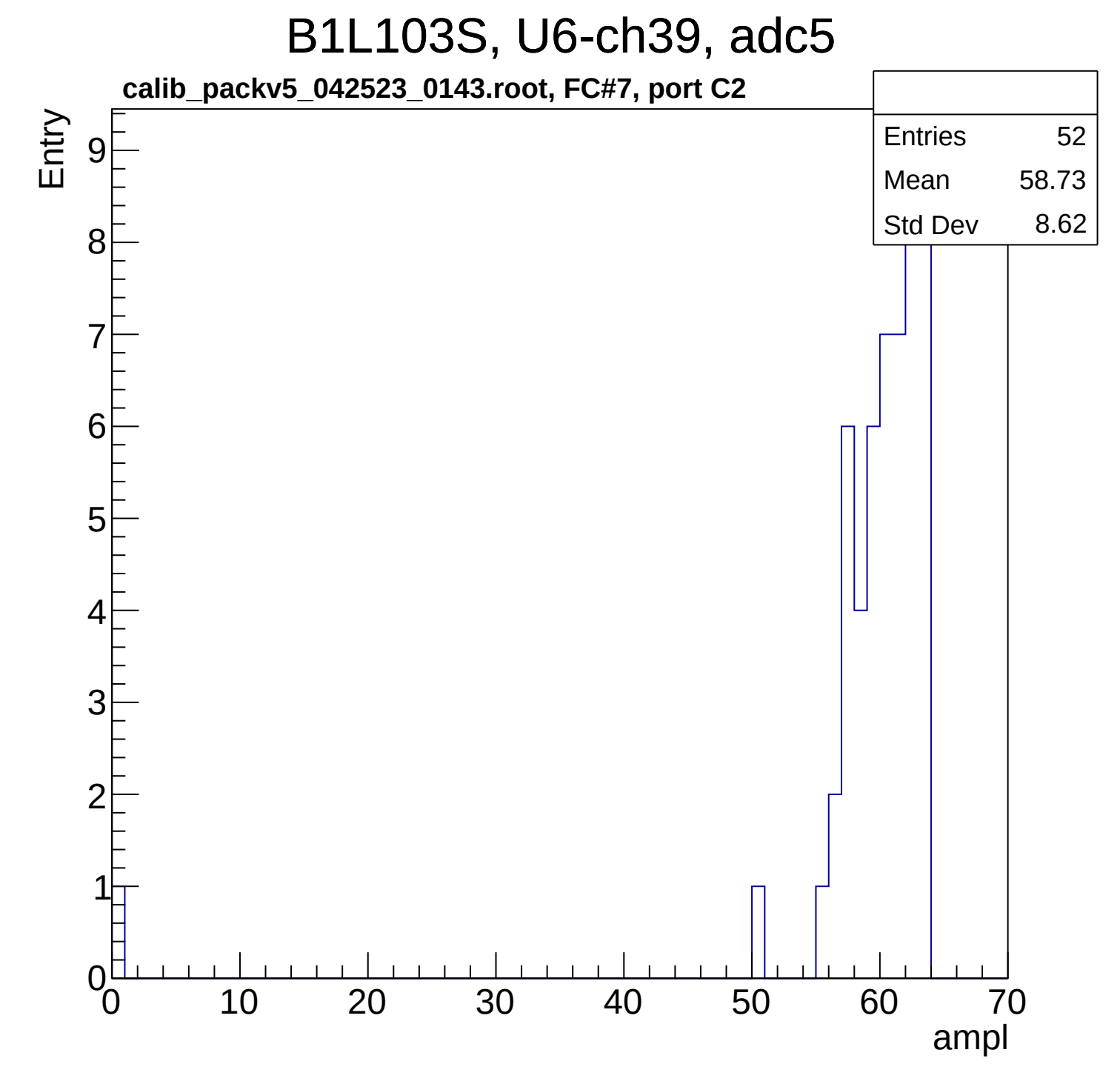
B1L103S, U6-ch39, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.73
Std Dev 8.62
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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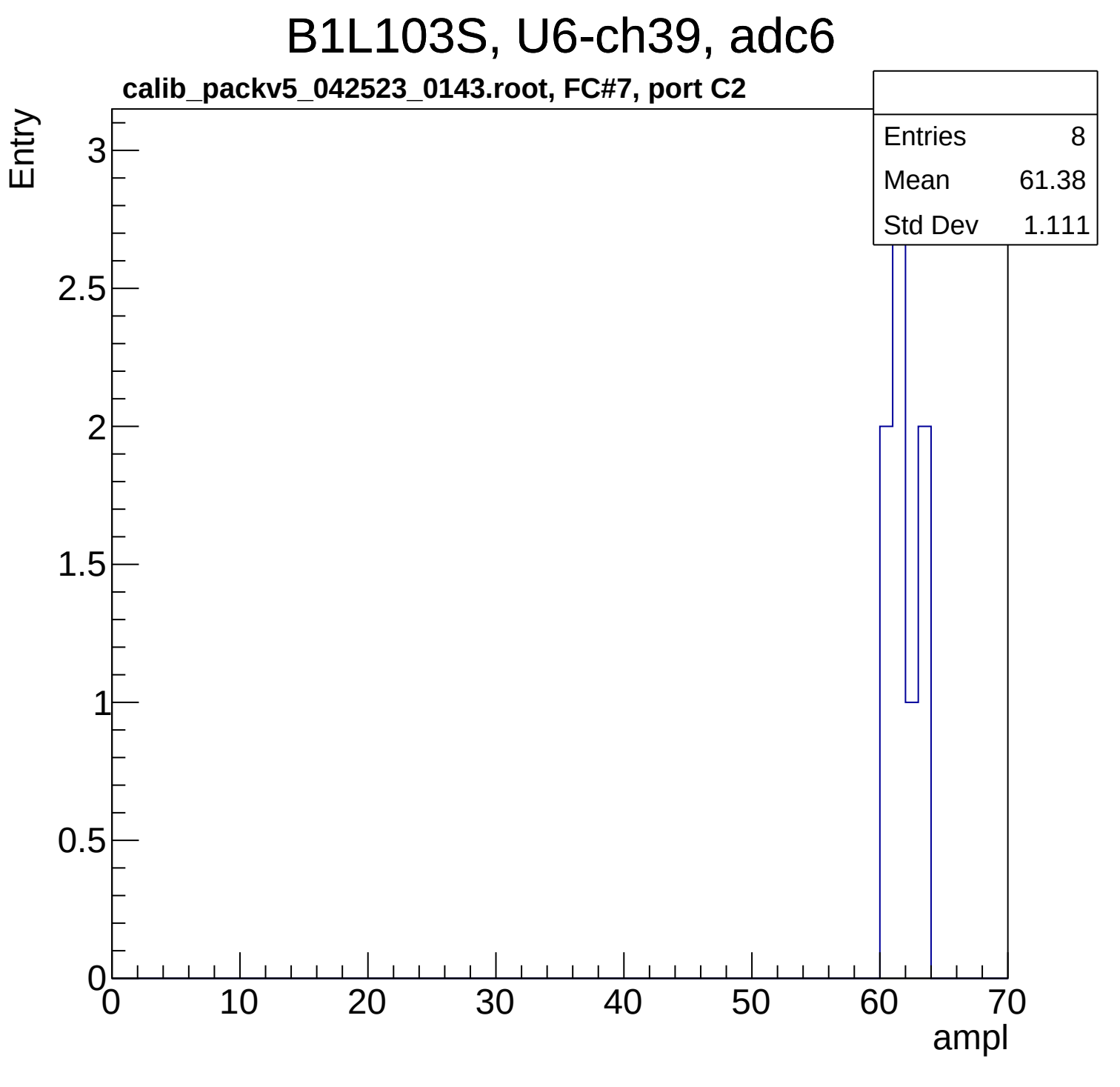
B1L103S, U6-ch39, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.38
Std Dev 1.111
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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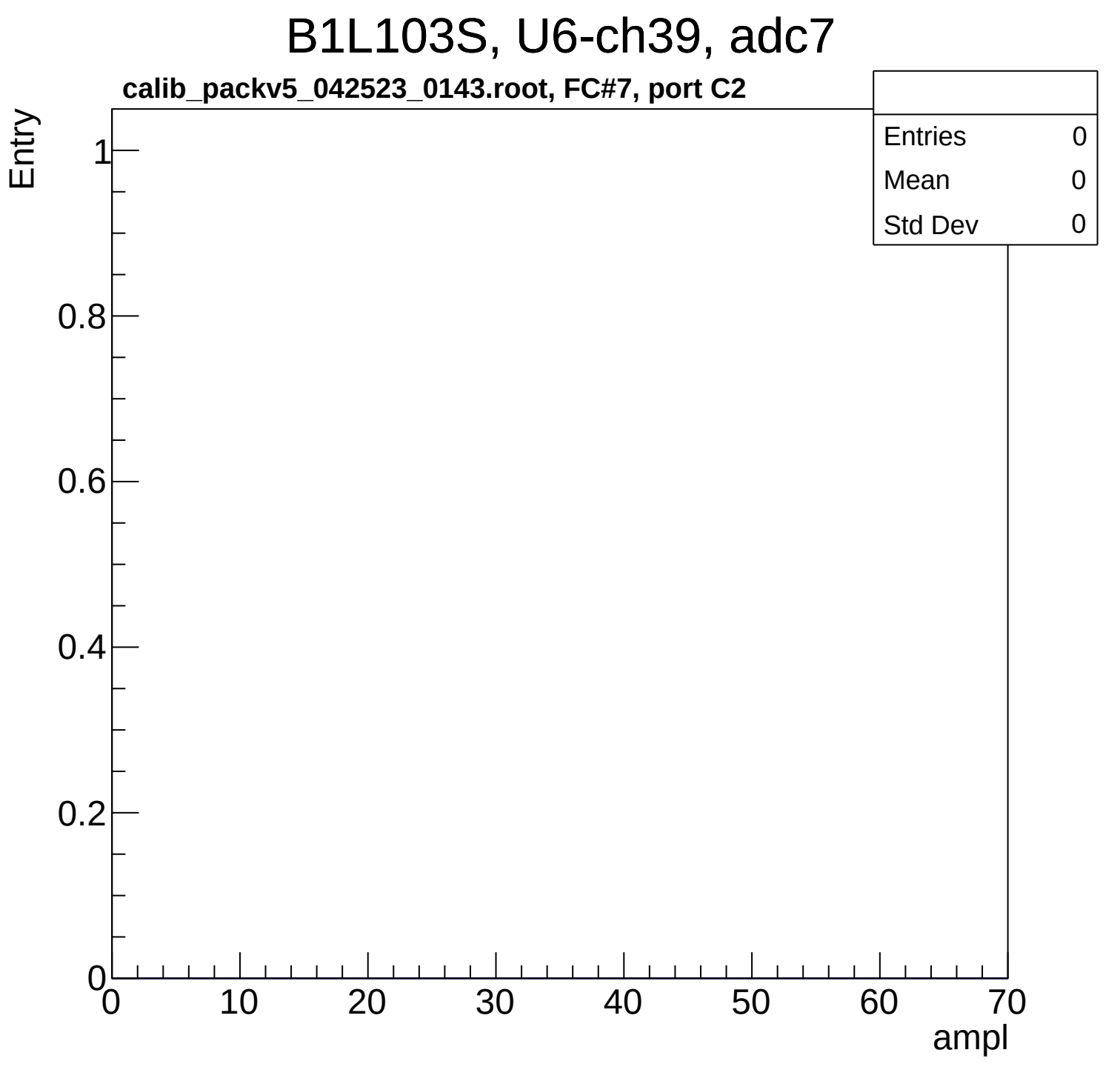
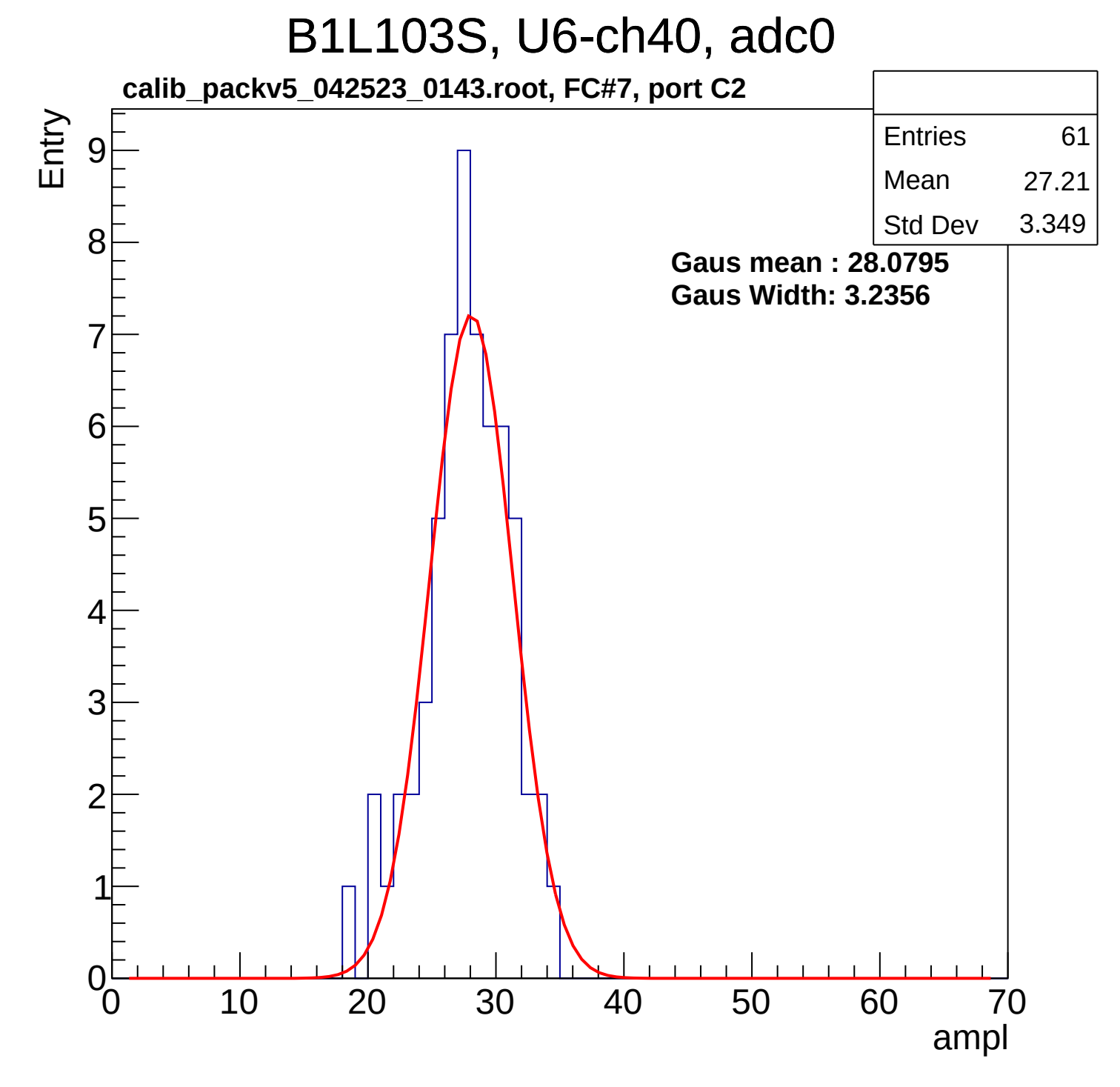

B1L103S, U6-ch39, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch40, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 27.21
Std Dev 3.349
Gaus mean : 28.0795
Gaus Width: 3.2356
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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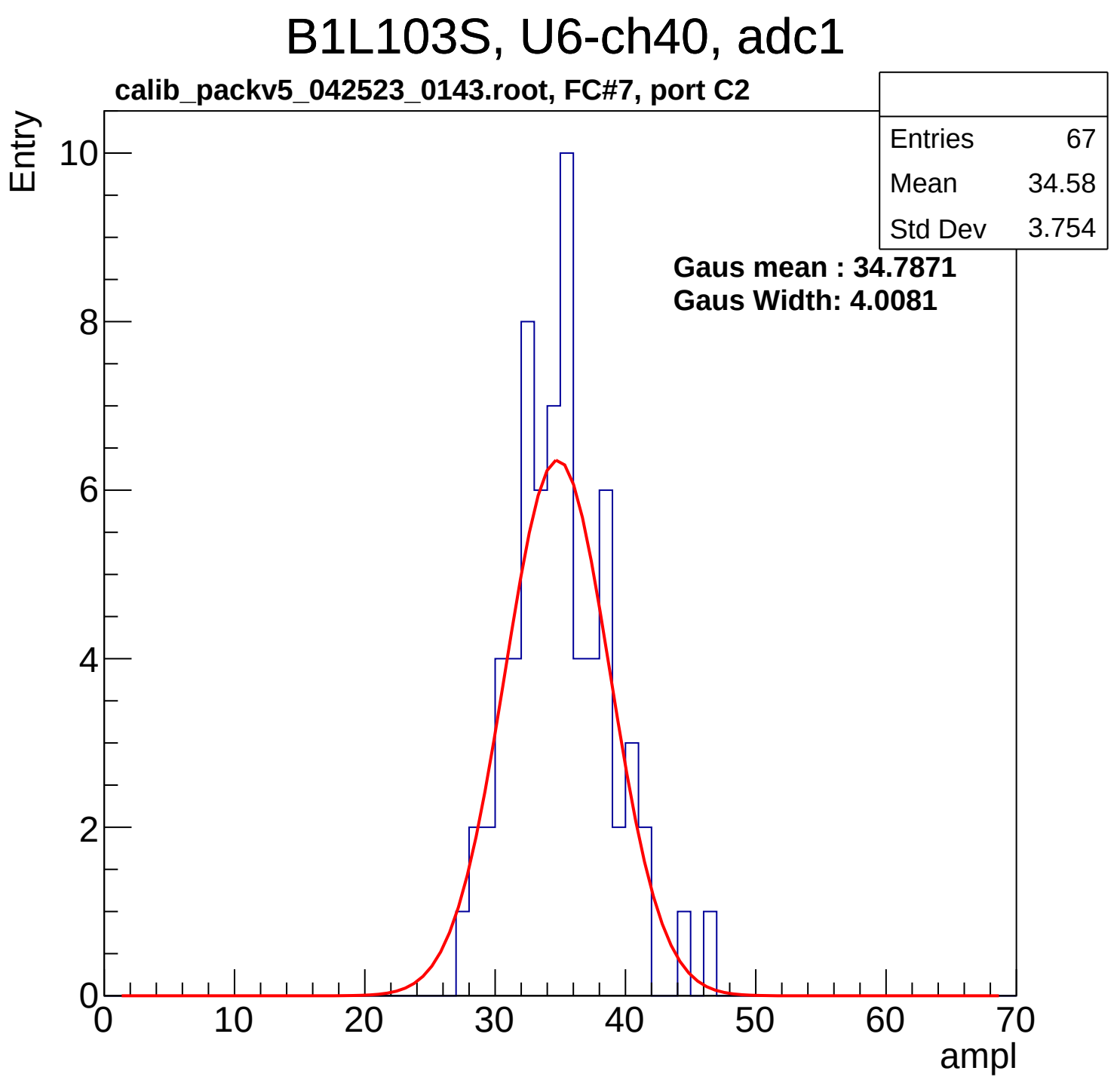
B1L103S, U6-ch40, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 34.58
Std Dev 3.754
Gaus mean : 34.7871
Gaus Width: 4.0081
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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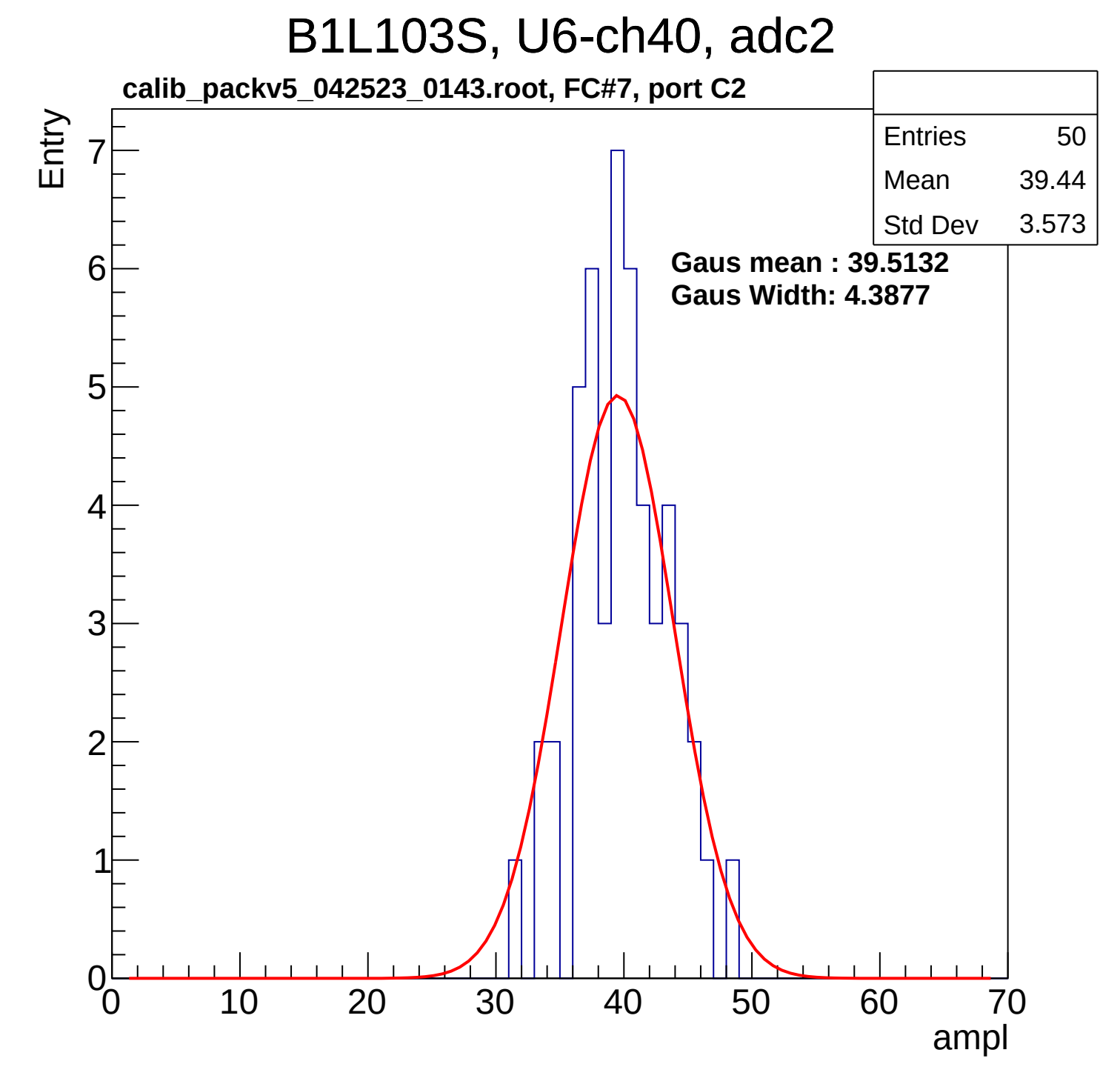
B1L103S, U6-ch40, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 39.44
Std Dev 3.573
Gaus mean : 39.5132
Gaus Width: 4.3877
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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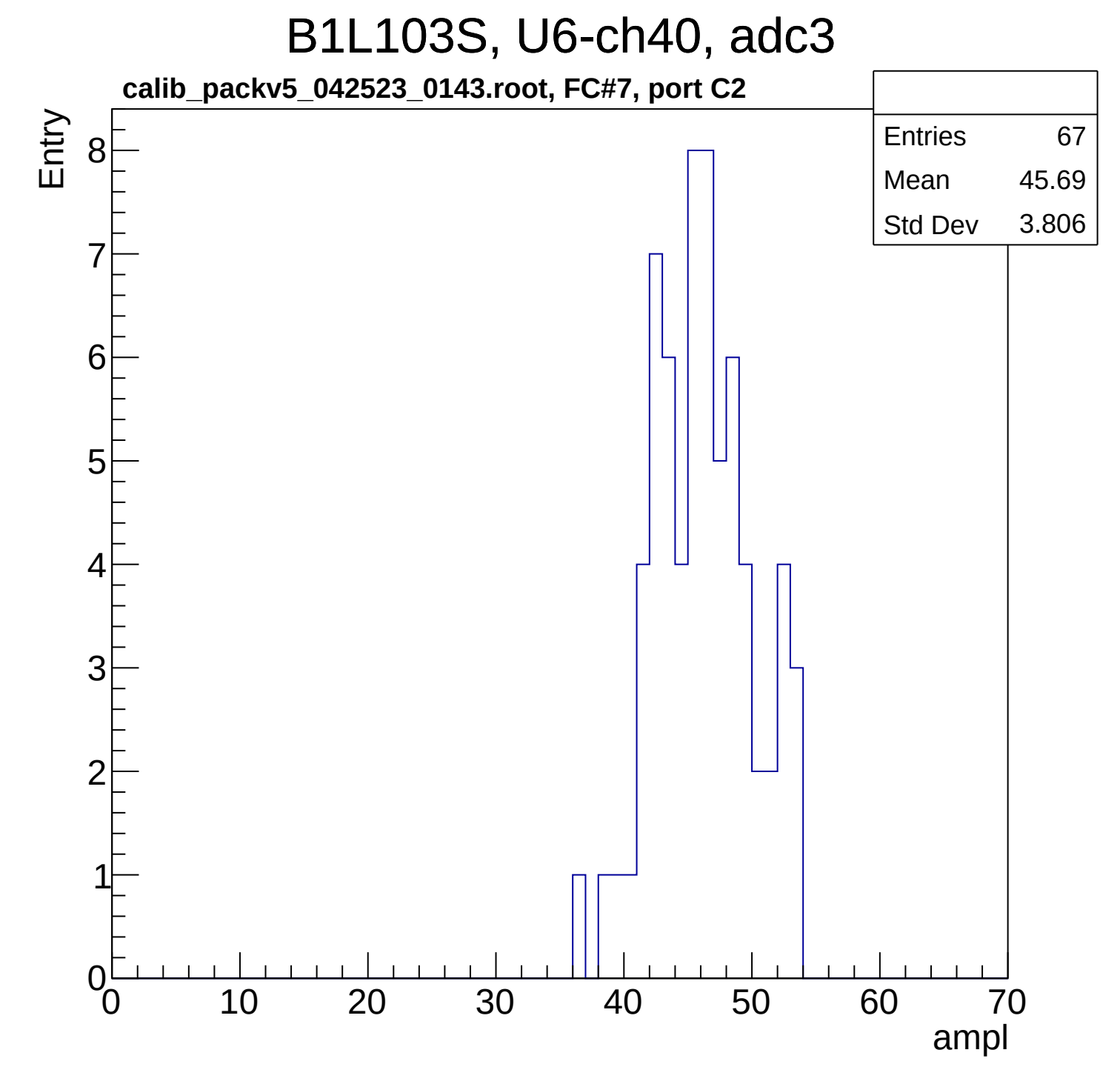
B1L103S, U6-ch40, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 45.69
Std Dev 3.806
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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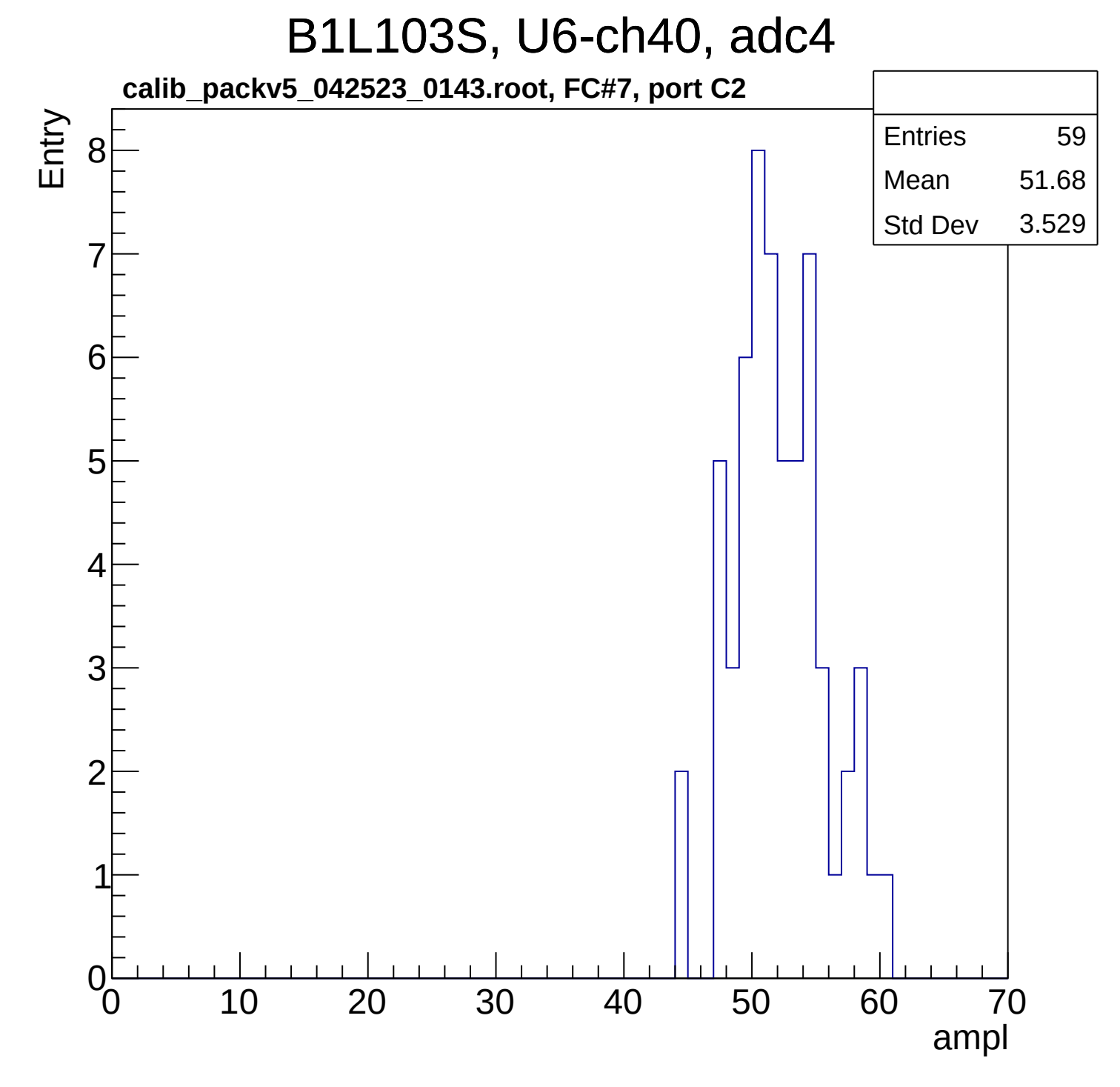
B1L103S, U6-ch40, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 51.68
Std Dev 3.529
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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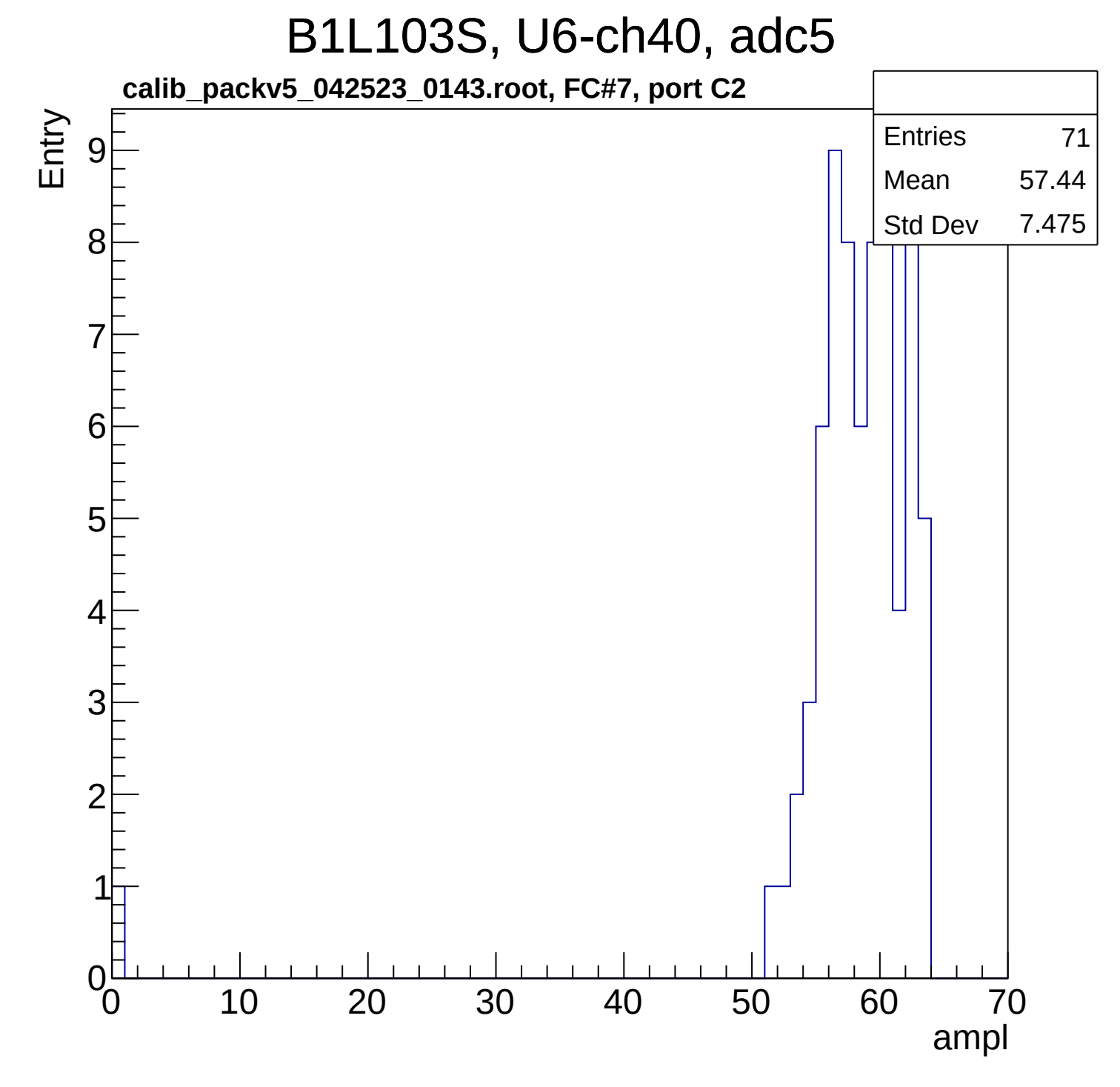
B1L103S, U6-ch40, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 57.44
Std Dev 7.475
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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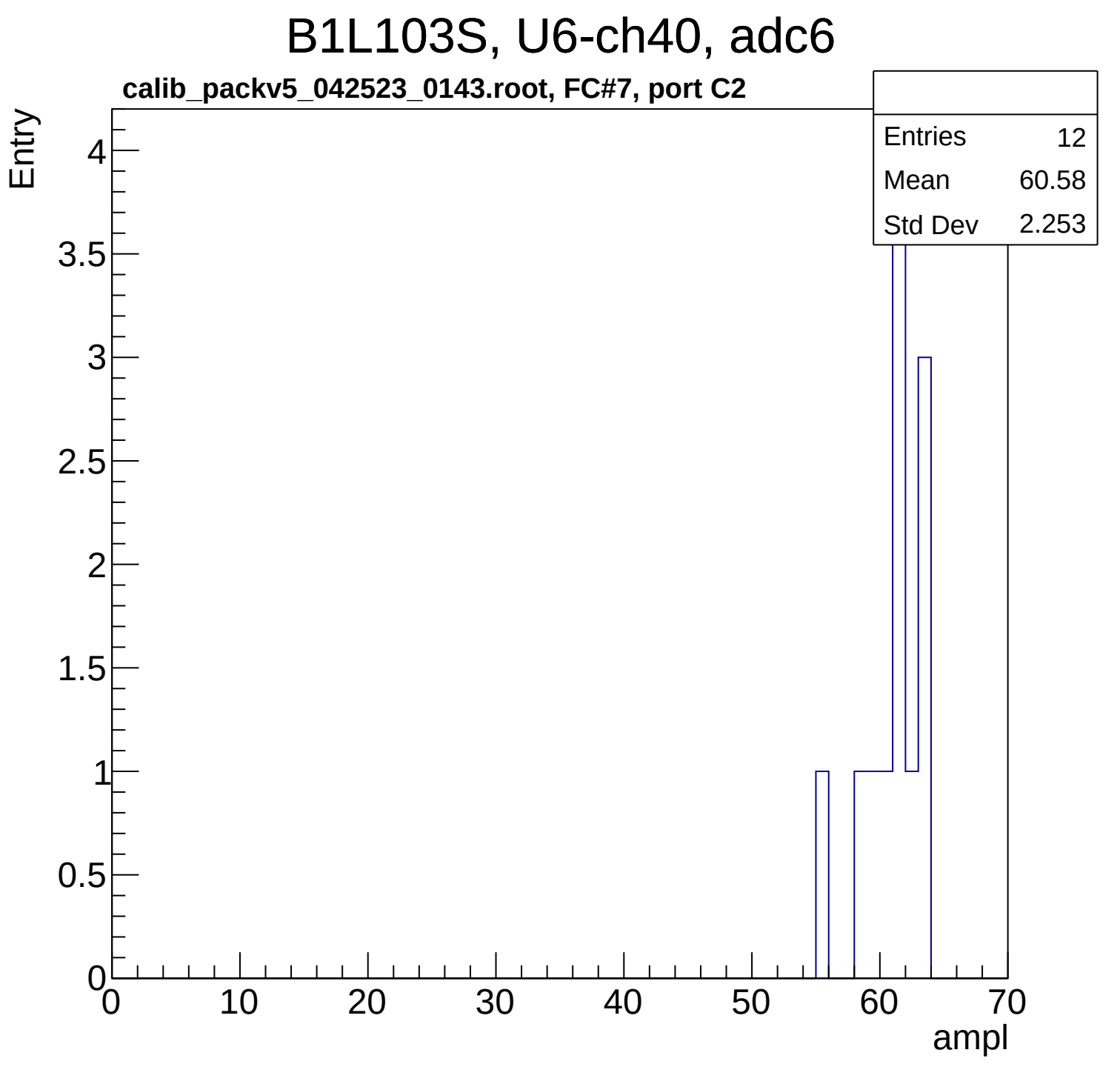
B1L103S, U6-ch40, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 60.58
Std Dev 2.253
Entry
ampl
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1.5
2
2.5
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3.5
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0
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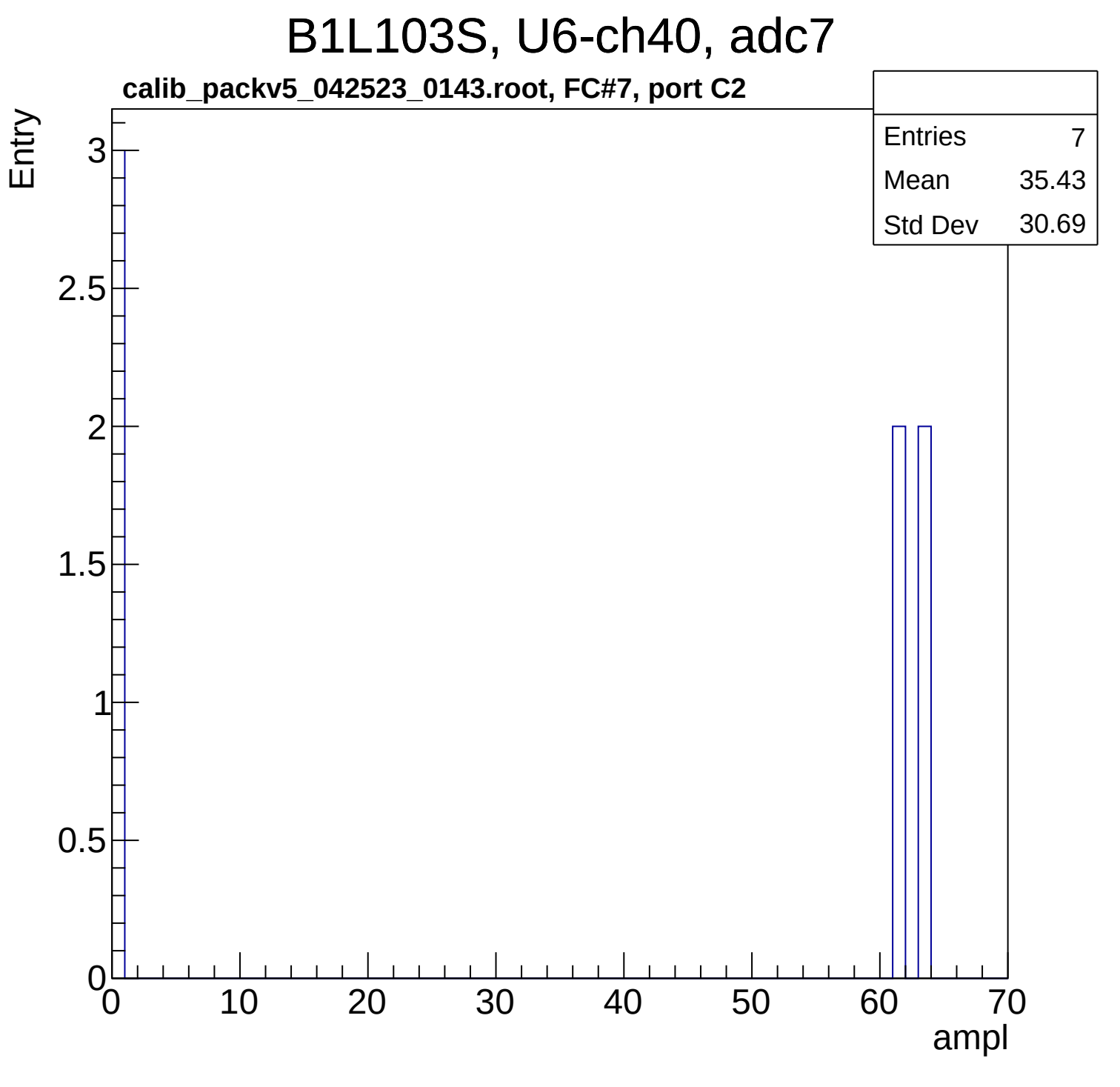
B1L103S, U6-ch40, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 35.43
Std Dev 30.69
Entry
ampl
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1
1.5
2
2.5
3
0
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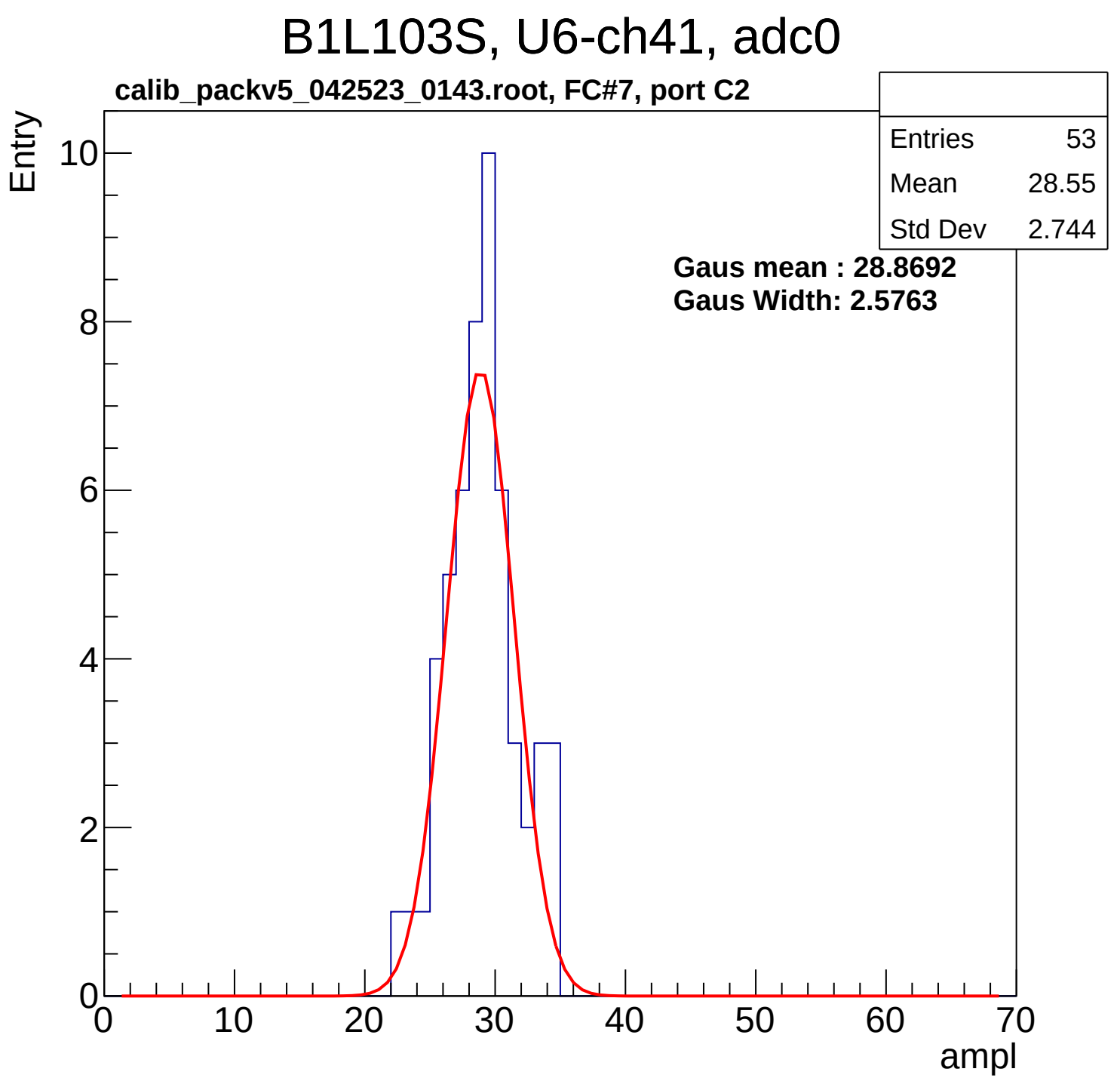
B1L103S, U6-ch41, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 28.55
Std Dev 2.744
Gaus mean : 28.8692
Gaus Width: 2.5763
Entry
ampl
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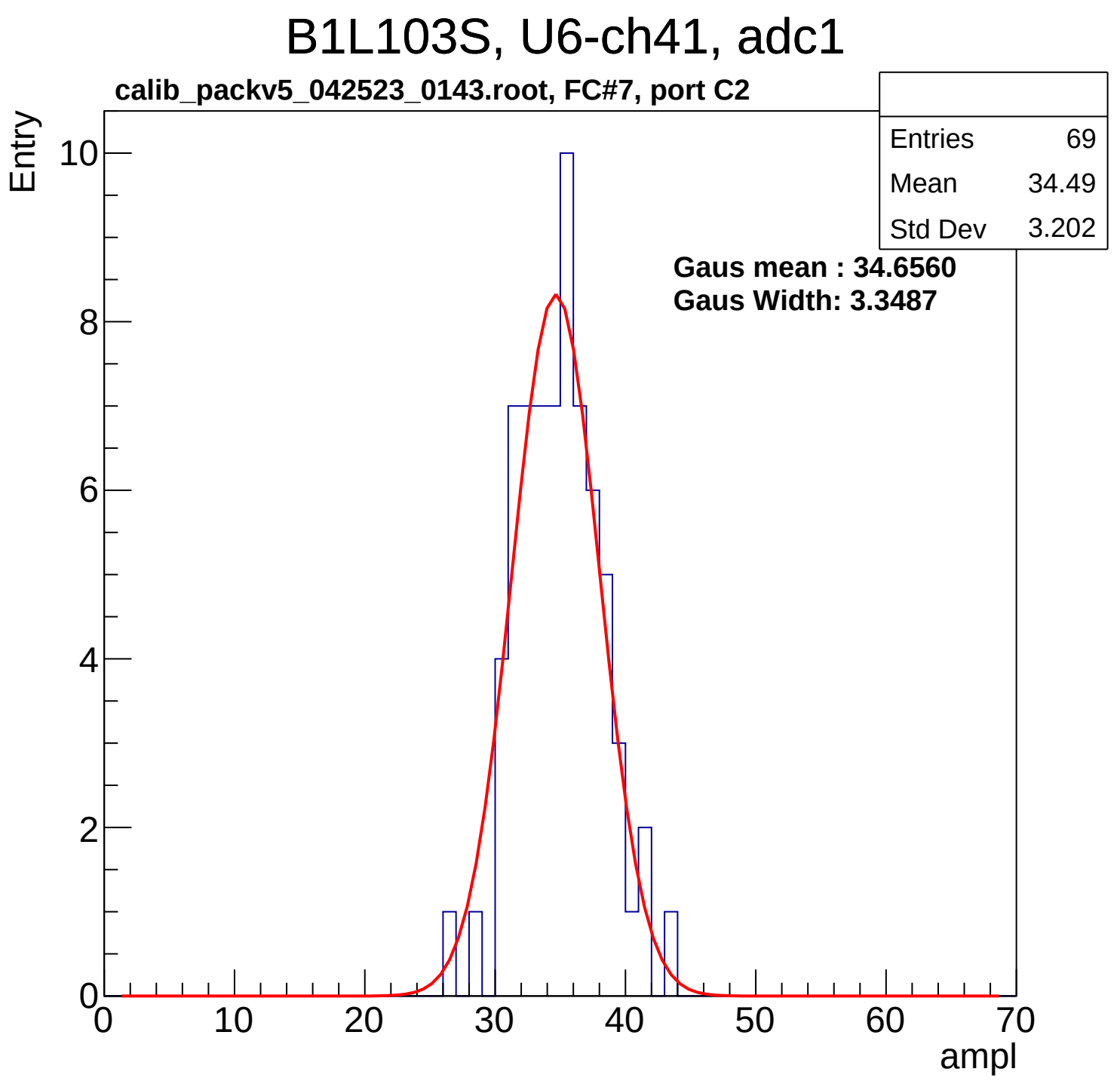
B1L103S, U6-ch41, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 34.49
Std Dev 3.202
Gaus mean : 34.6560
Gaus Width: 3.3487
Entry
ampl
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6
8
10
0
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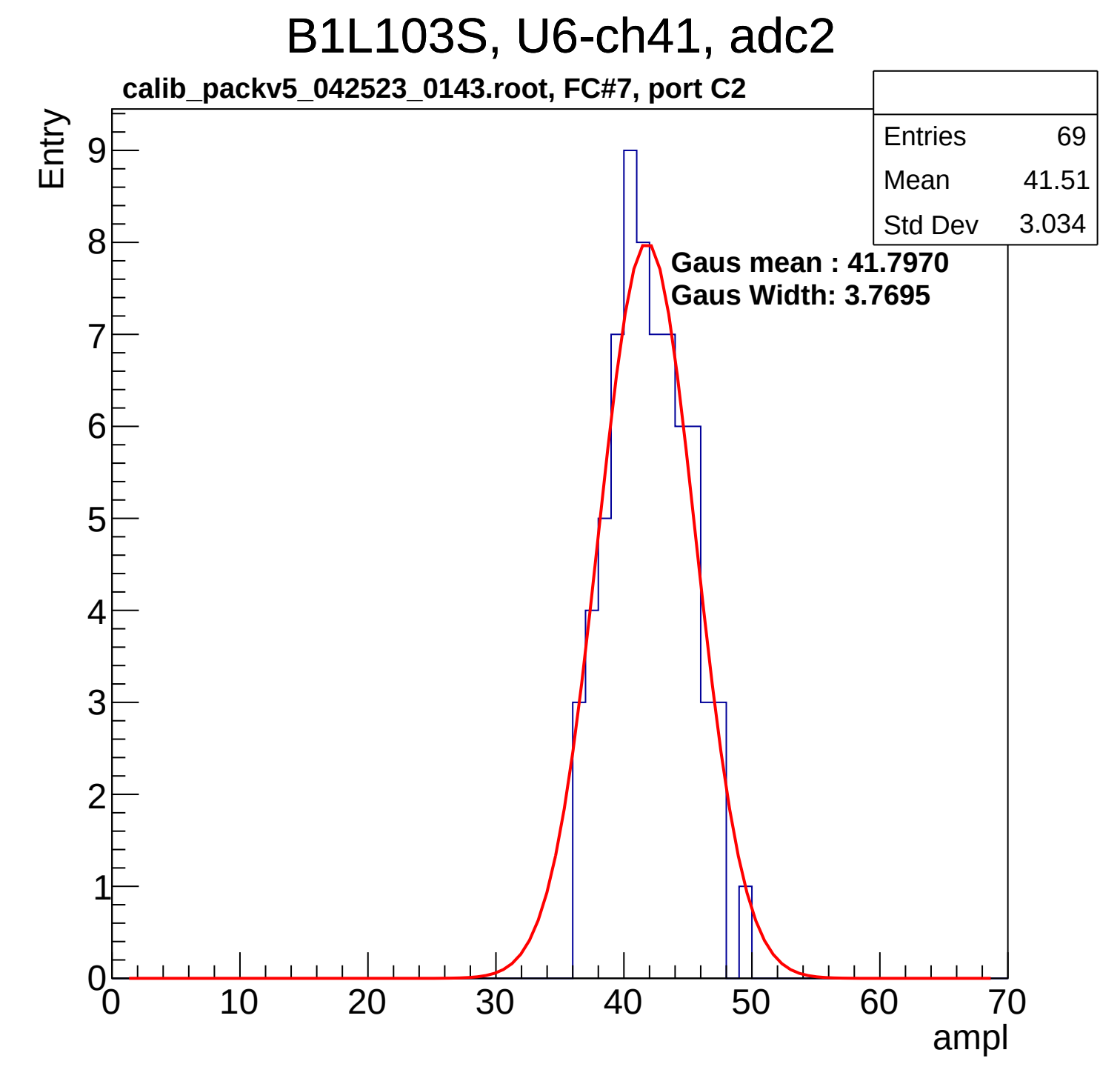
20
30
40
50
60
70


B1L103S, U6-ch41, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 41.51
Std Dev 3.034
Gaus mean : 41.7970
Gaus Width: 3.7695
Entry
ampl
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2
3
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5
6
7
8
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0
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60
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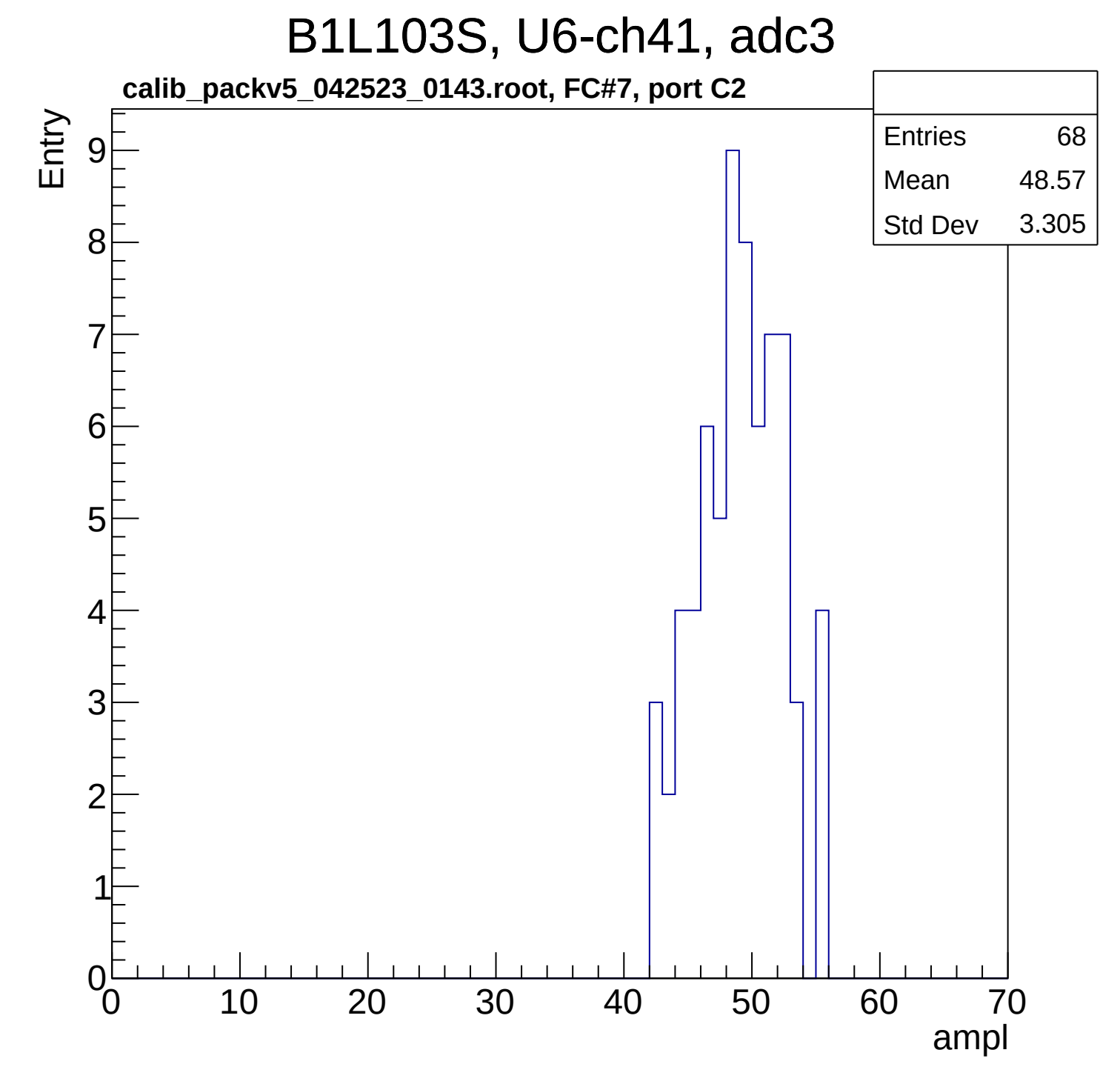
B1L103S, U6-ch41, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 48.57
Std Dev 3.305
Entry
ampl
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6
7
8
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0
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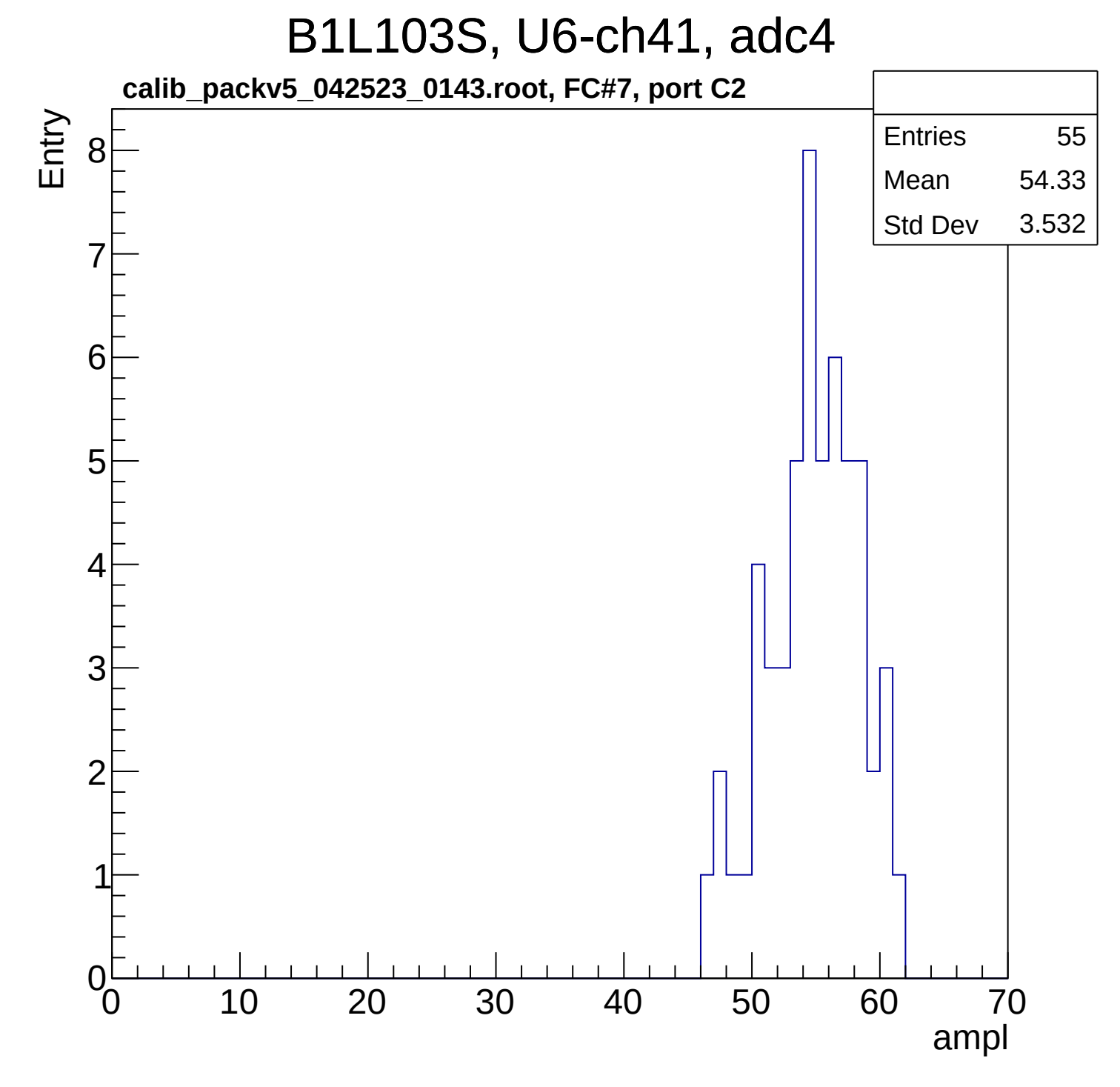
B1L103S, U6-ch41, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.33
Std Dev 3.532
Entry
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5
6
7
8
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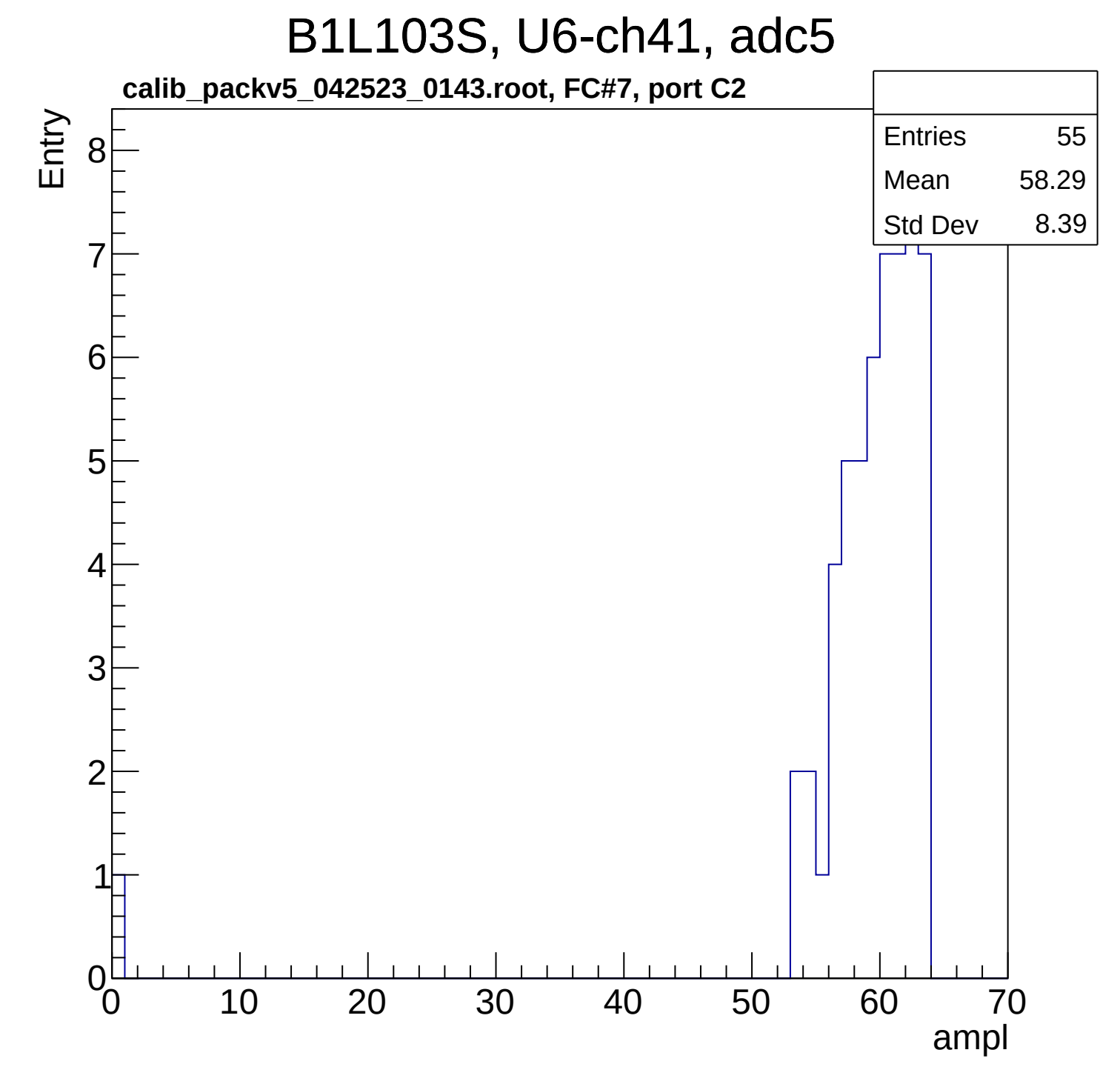
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch41, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.29
Std Dev 8.39
Entry
ampl
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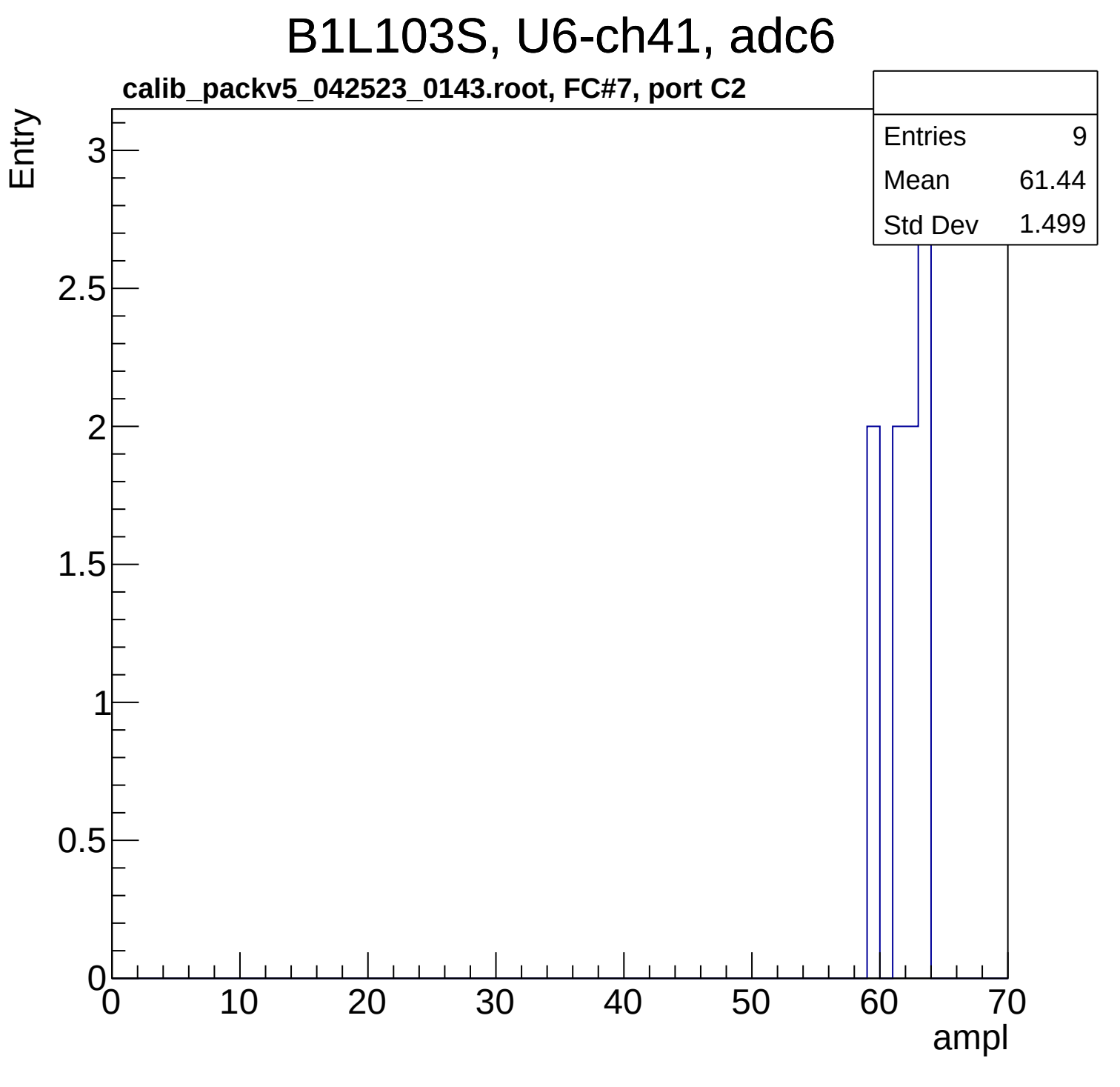
B1L103S, U6-ch41, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.44
Std Dev 1.499
Entry
ampl
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1
1.5
2
2.5
3
0
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20
30
40
50
60
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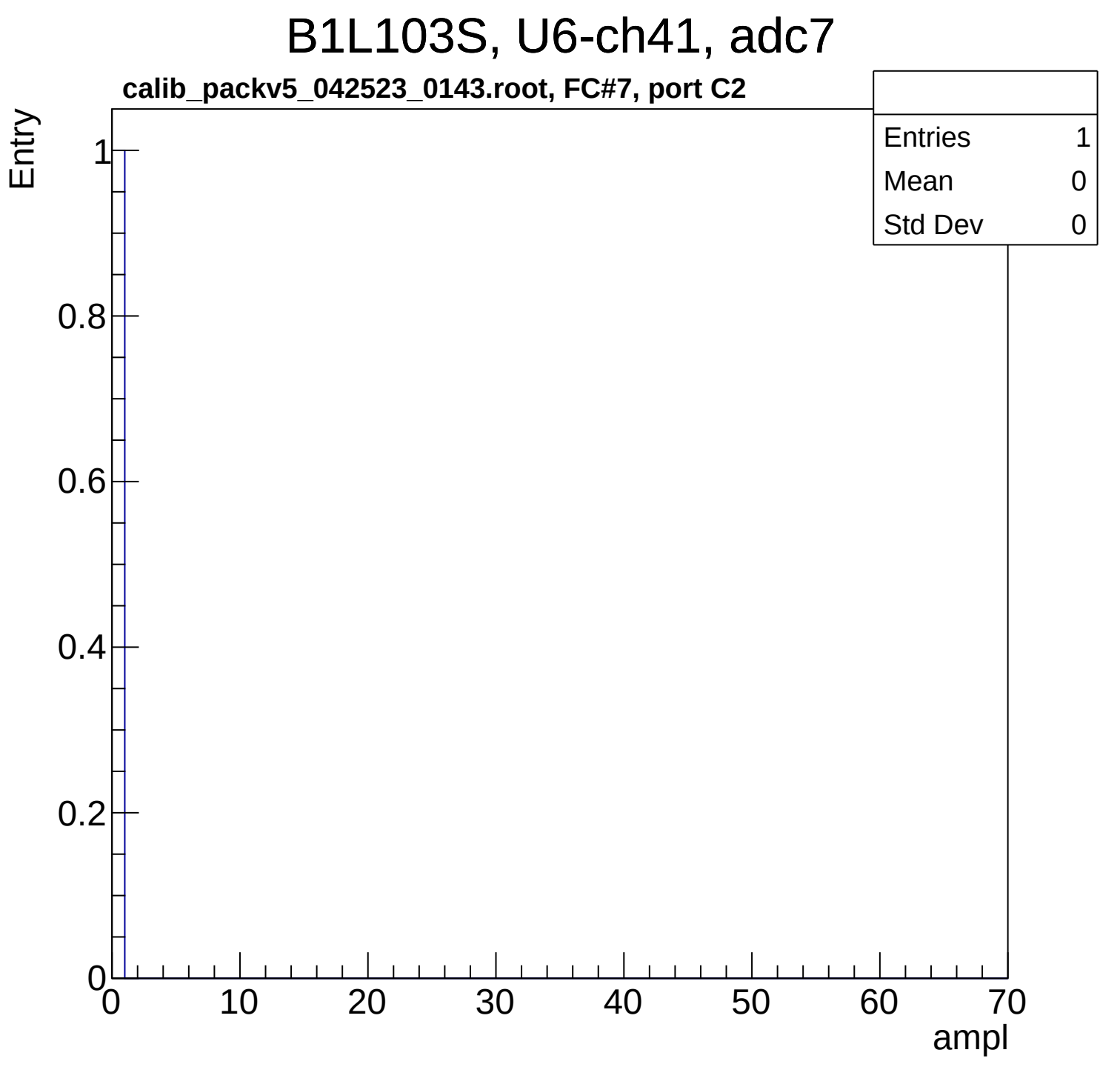
B1L103S, U6-ch41, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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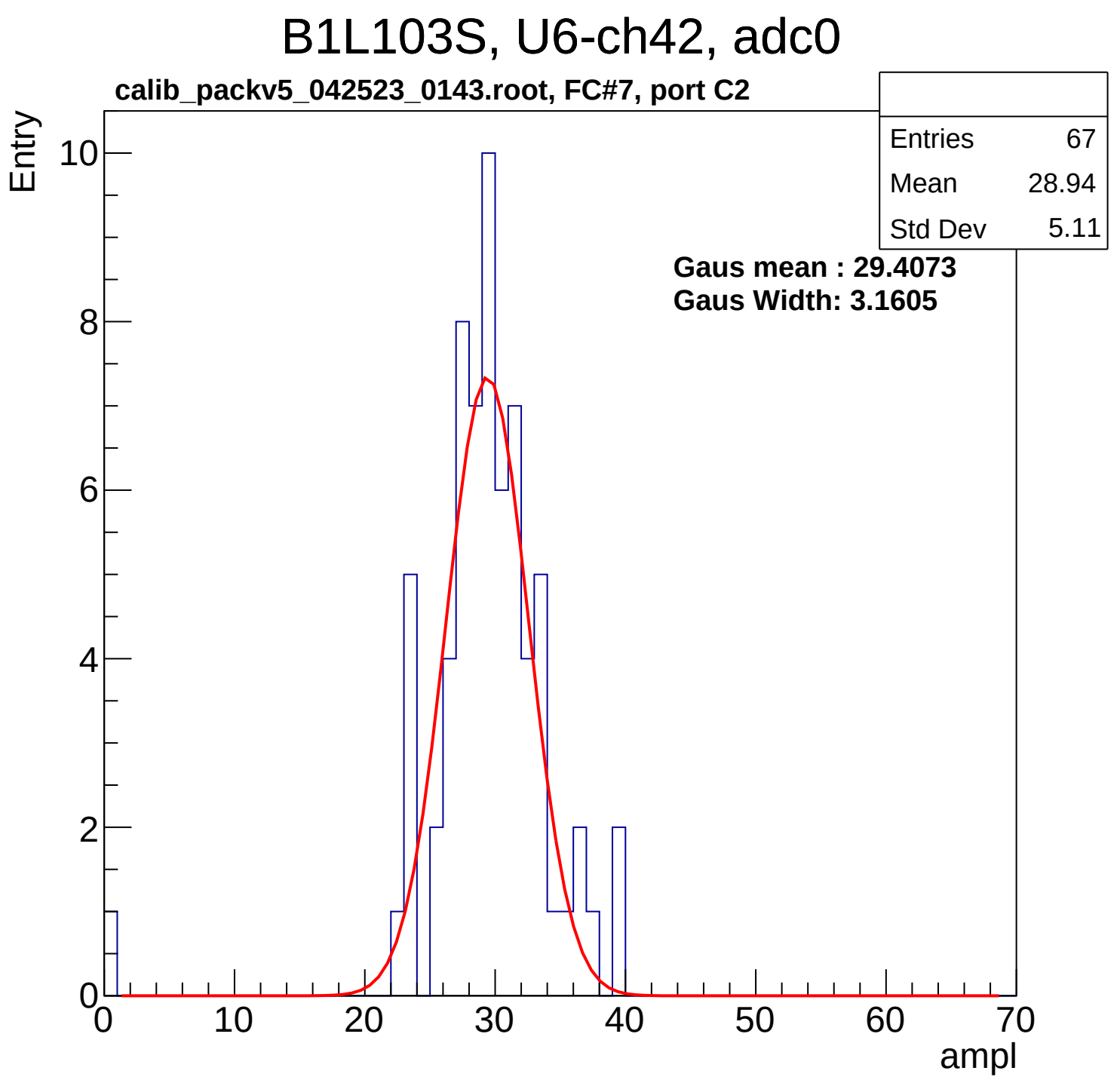
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch42, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.94
Std Dev 5.11
Gaus mean : 29.4073
Gaus Width: 3.1605
Entry
ampl
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4
6
8
10
0
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40
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60
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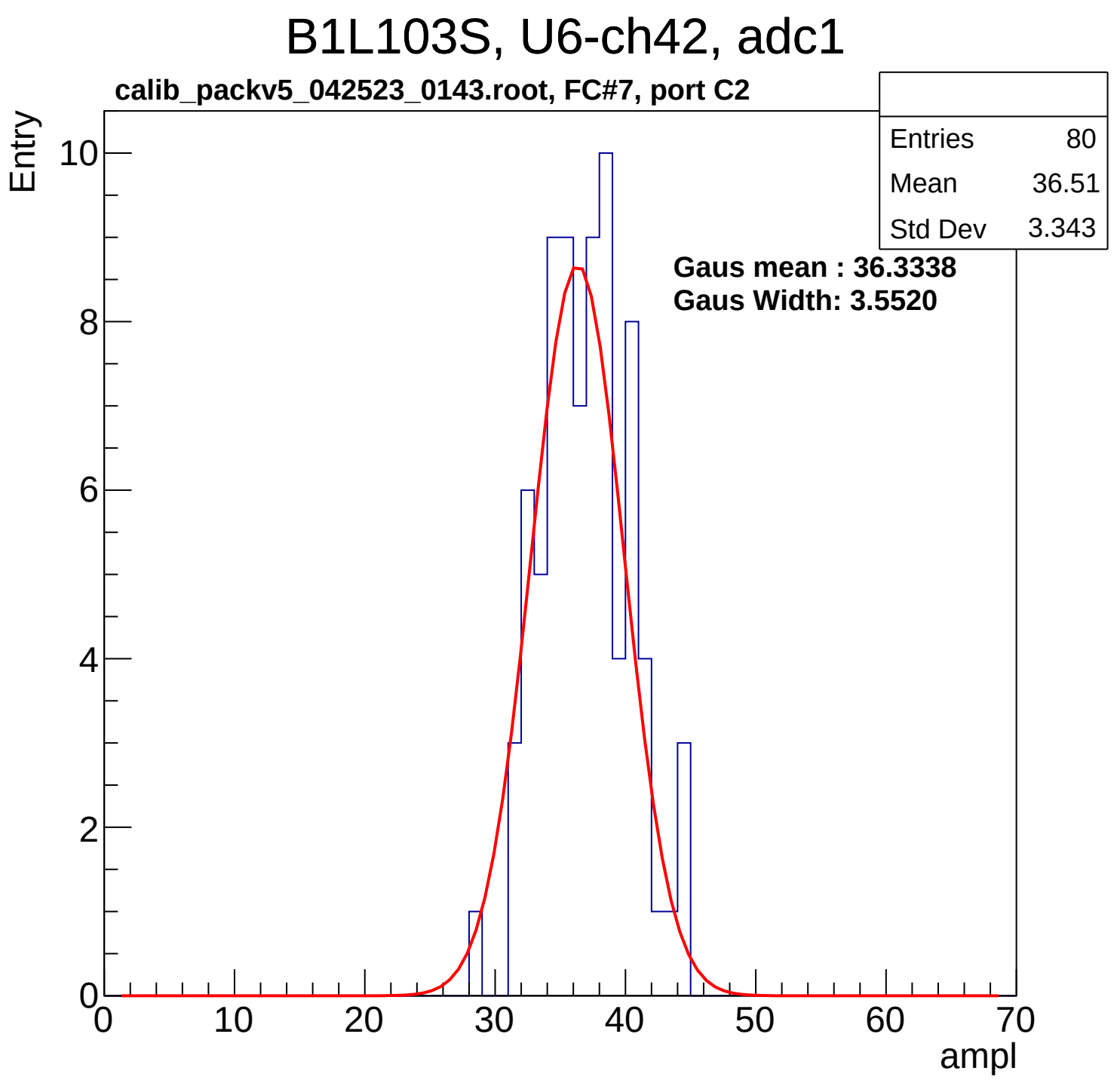
B1L103S, U6-ch42, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 36.51
Std Dev 3.343
Gaus mean : 36.3338
Gaus Width: 3.5520
Entry
ampl
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4
6
8
10
0
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20
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40
50
60
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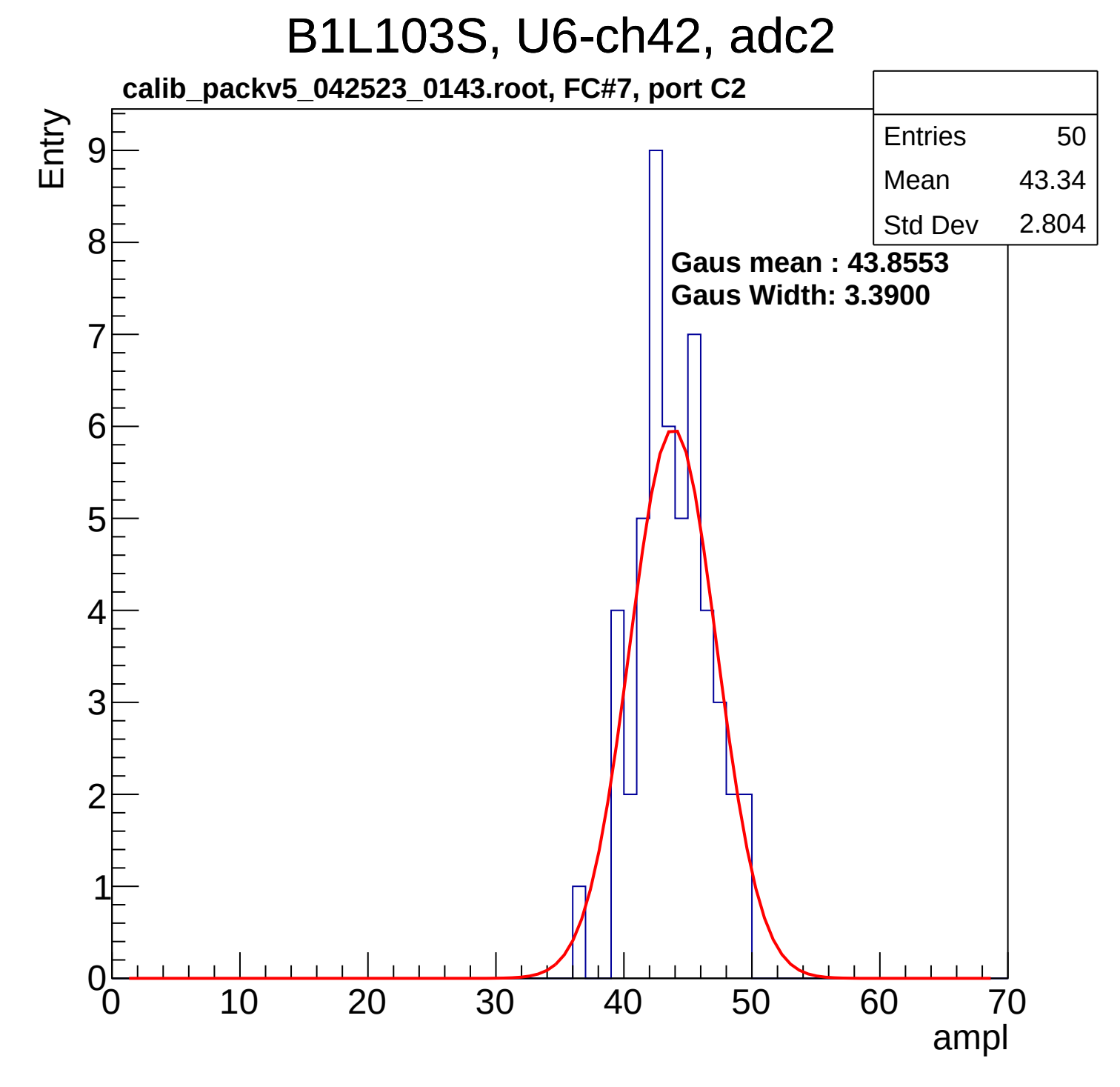
B1L103S, U6-ch42, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 43.34
Std Dev 2.804
Gaus mean : 43.8553
Gaus Width: 3.3900
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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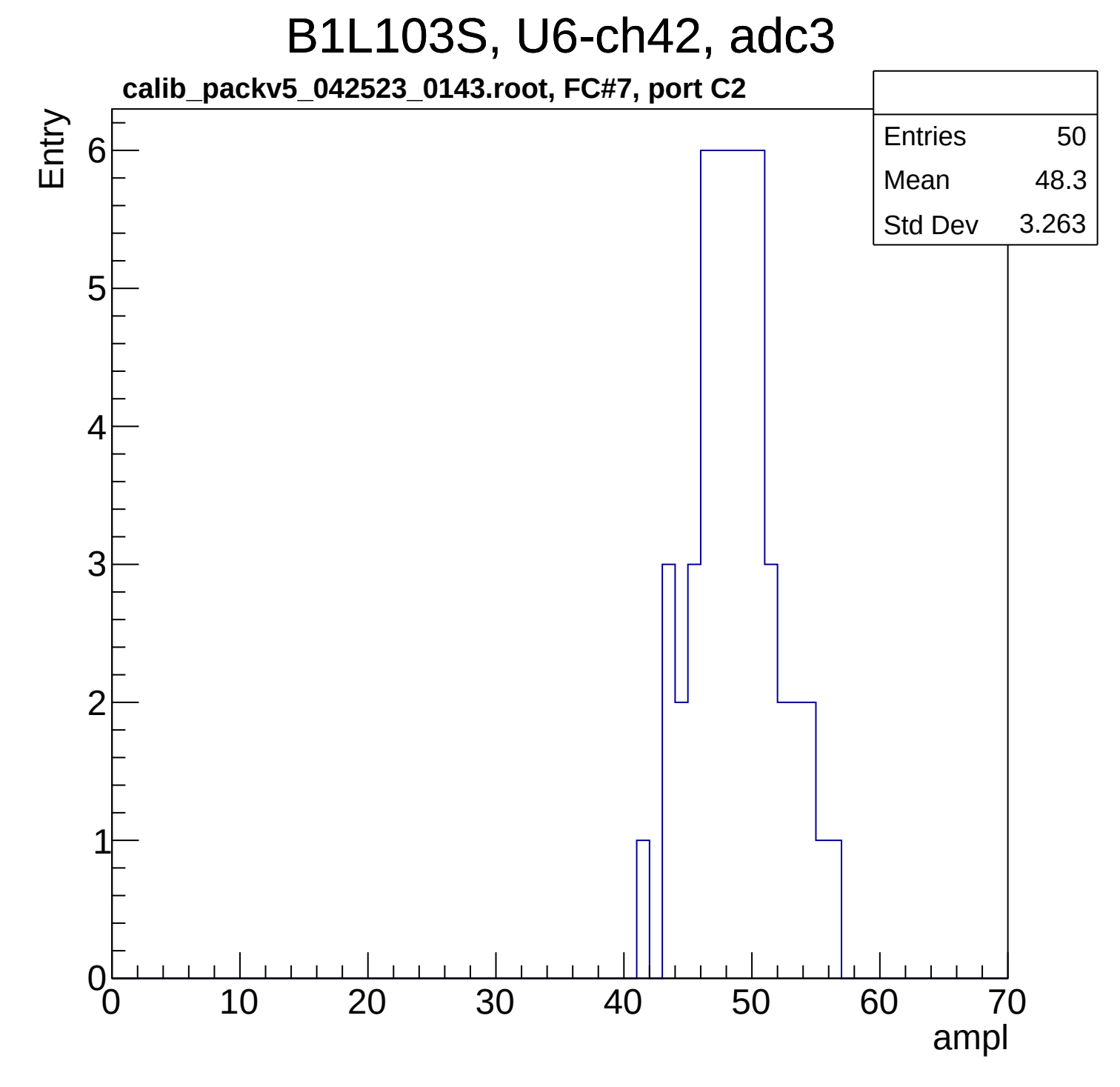
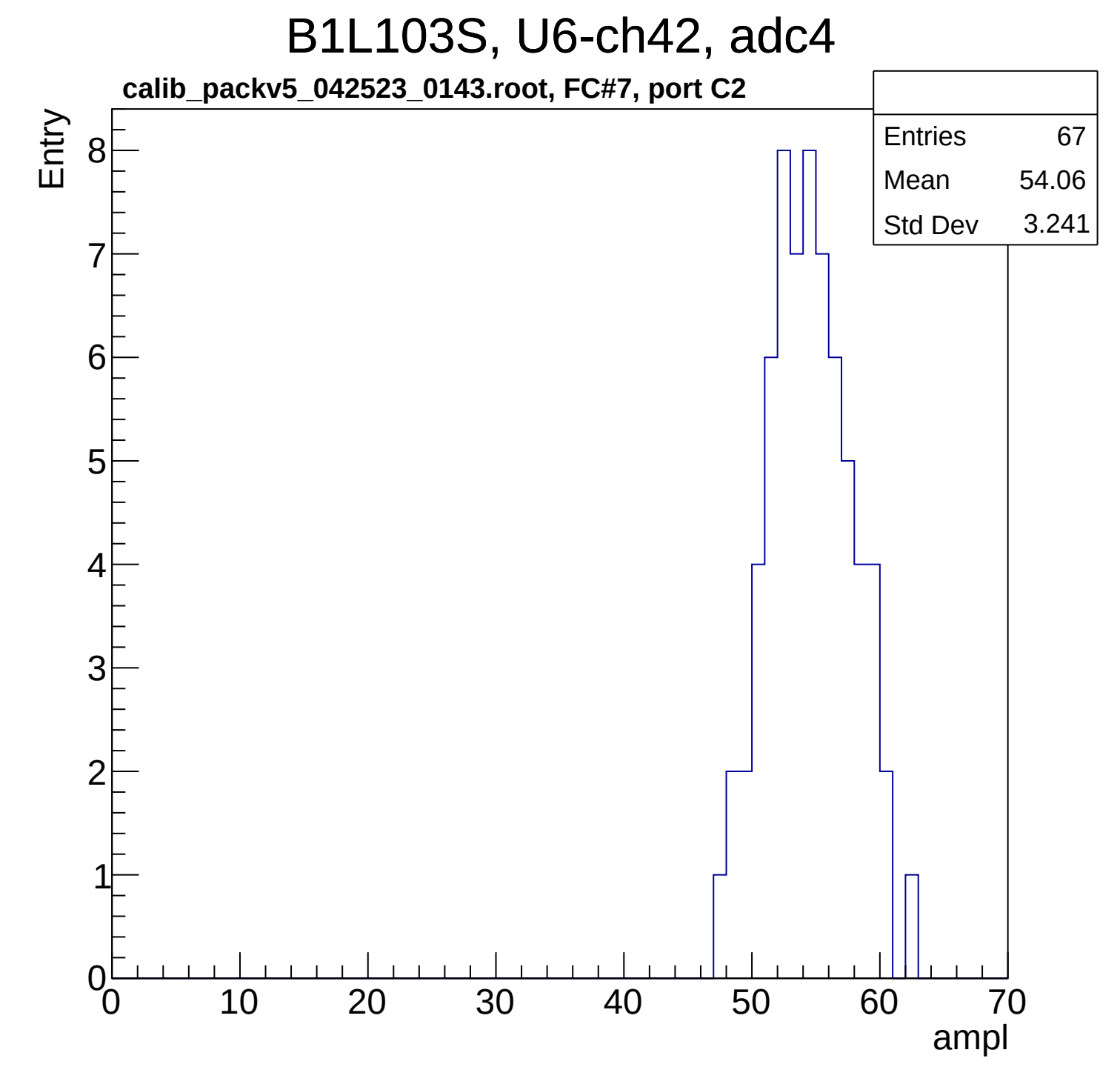

B1L103S, U6-ch42, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 48.3
Std Dev 3.263
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch42, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.06
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
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7
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0
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40
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60
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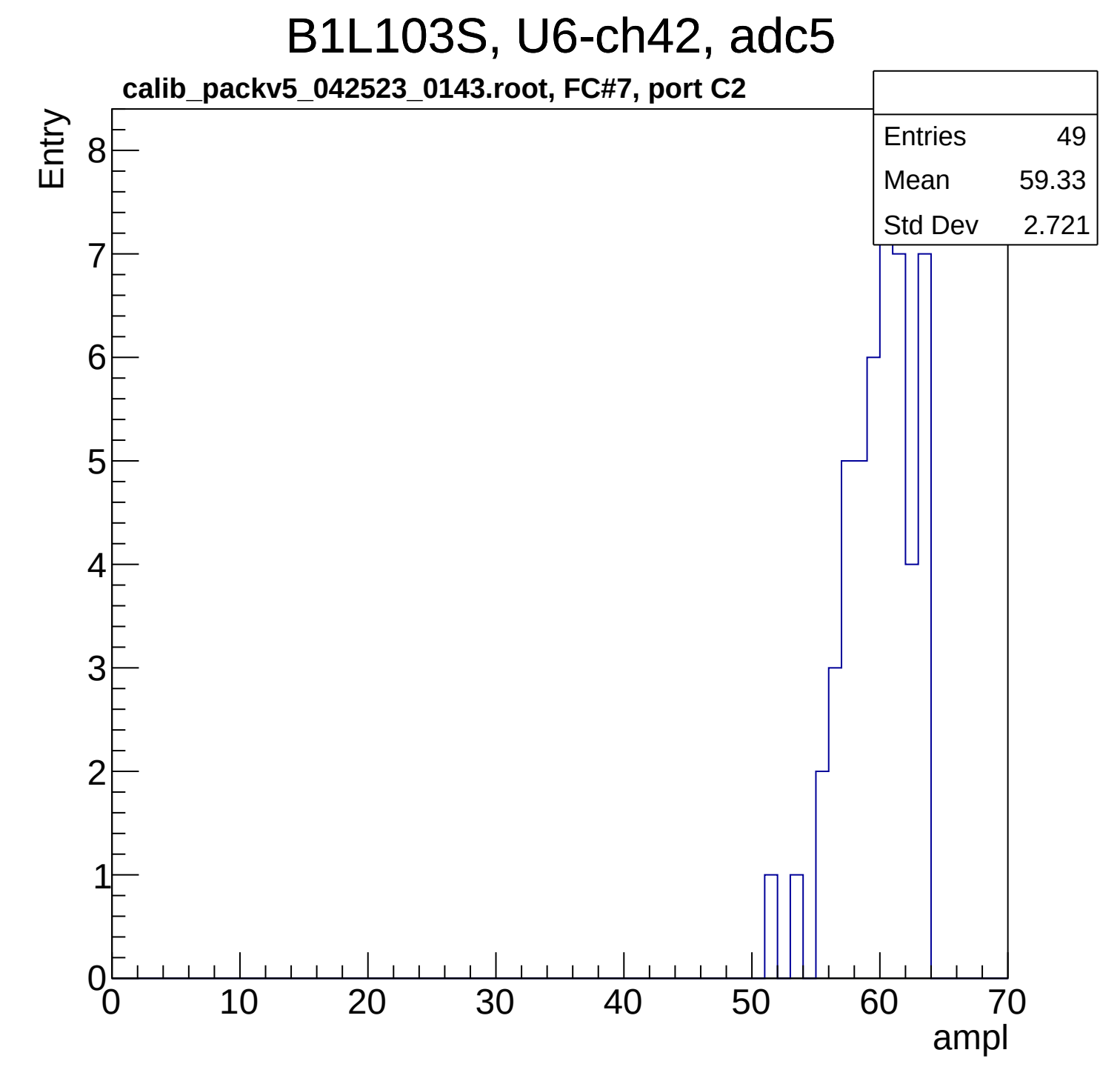
B1L103S, U6-ch42, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.33
Std Dev 2.721
Entry
ampl
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2
3
4
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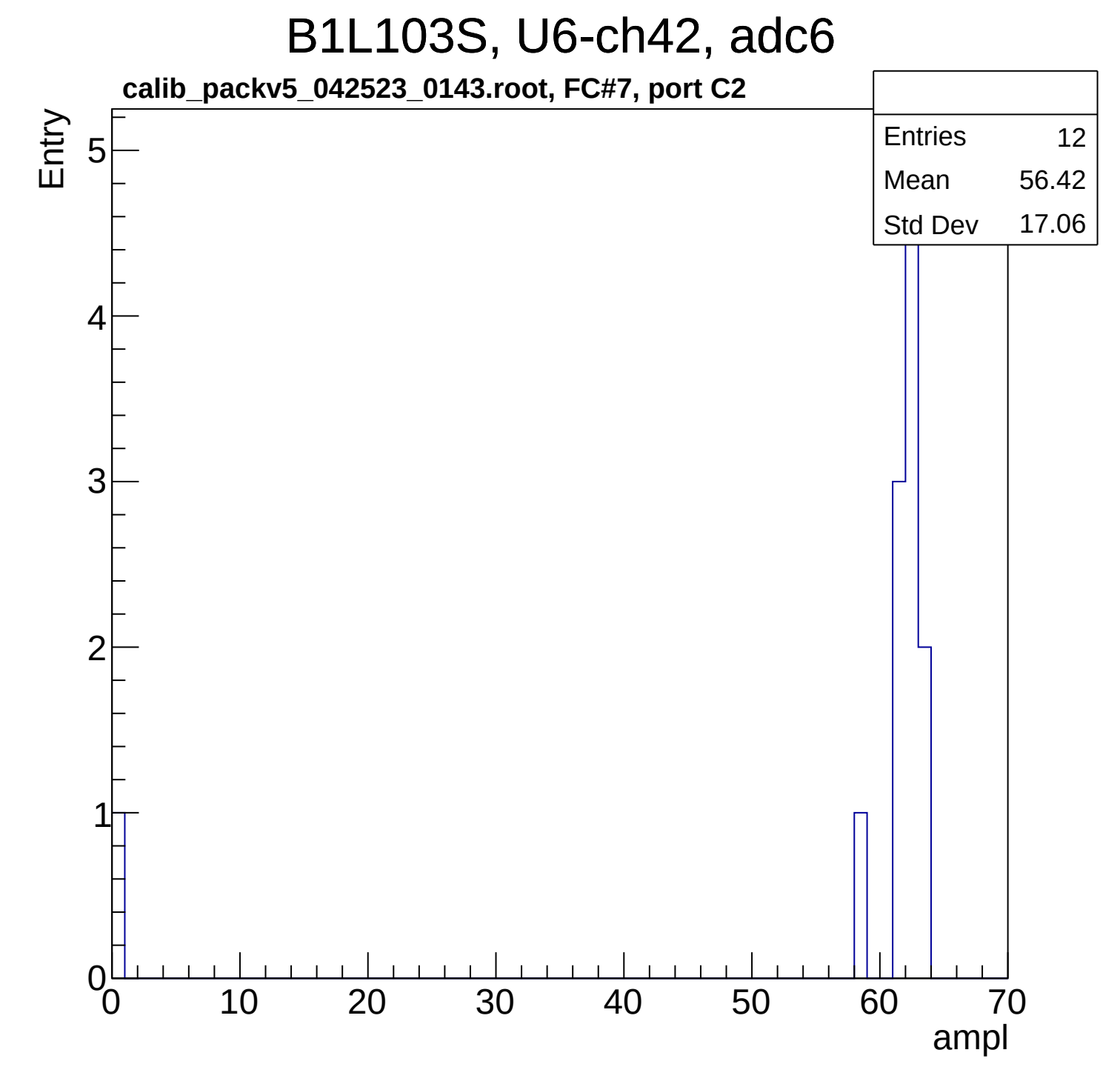
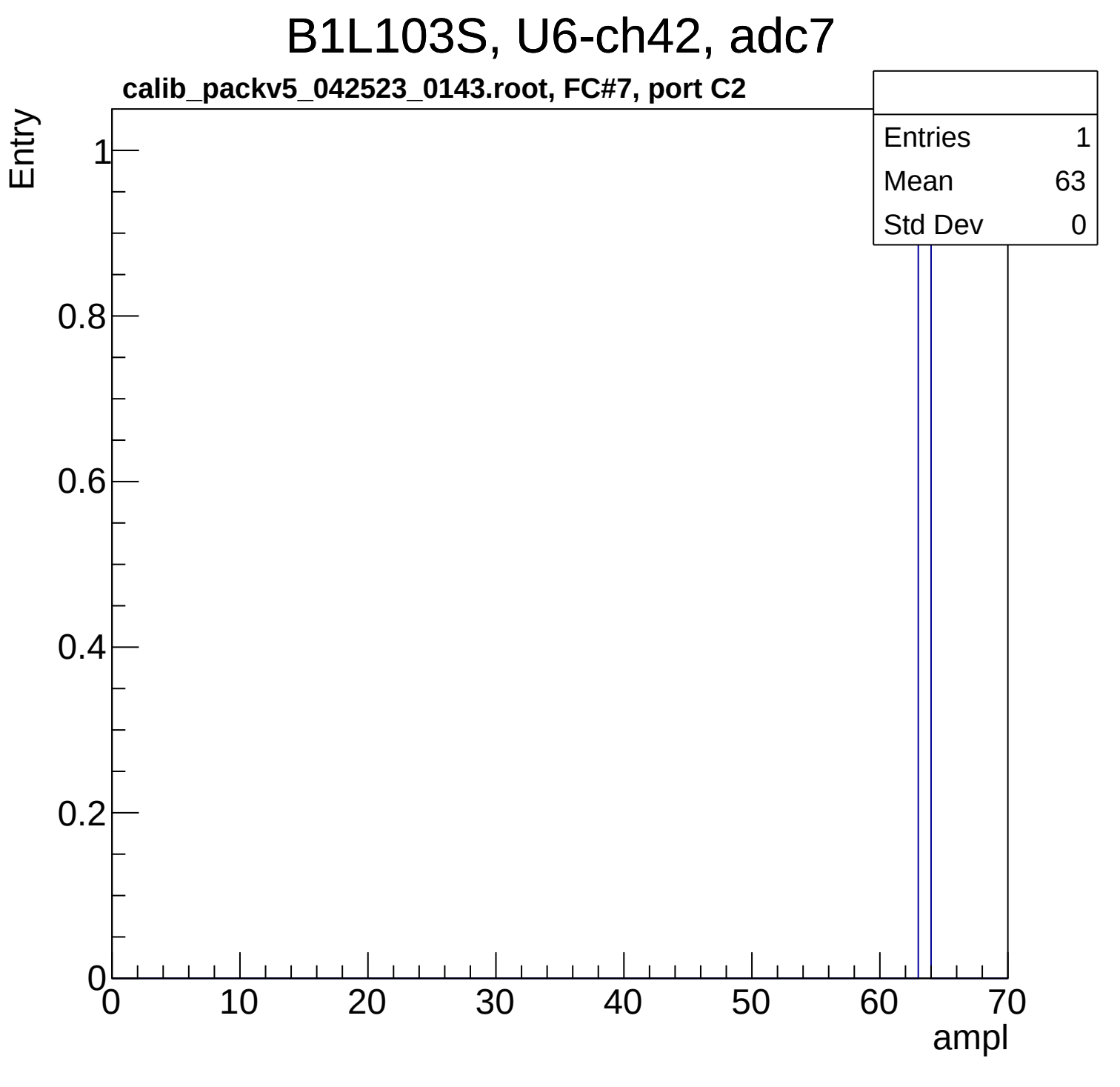
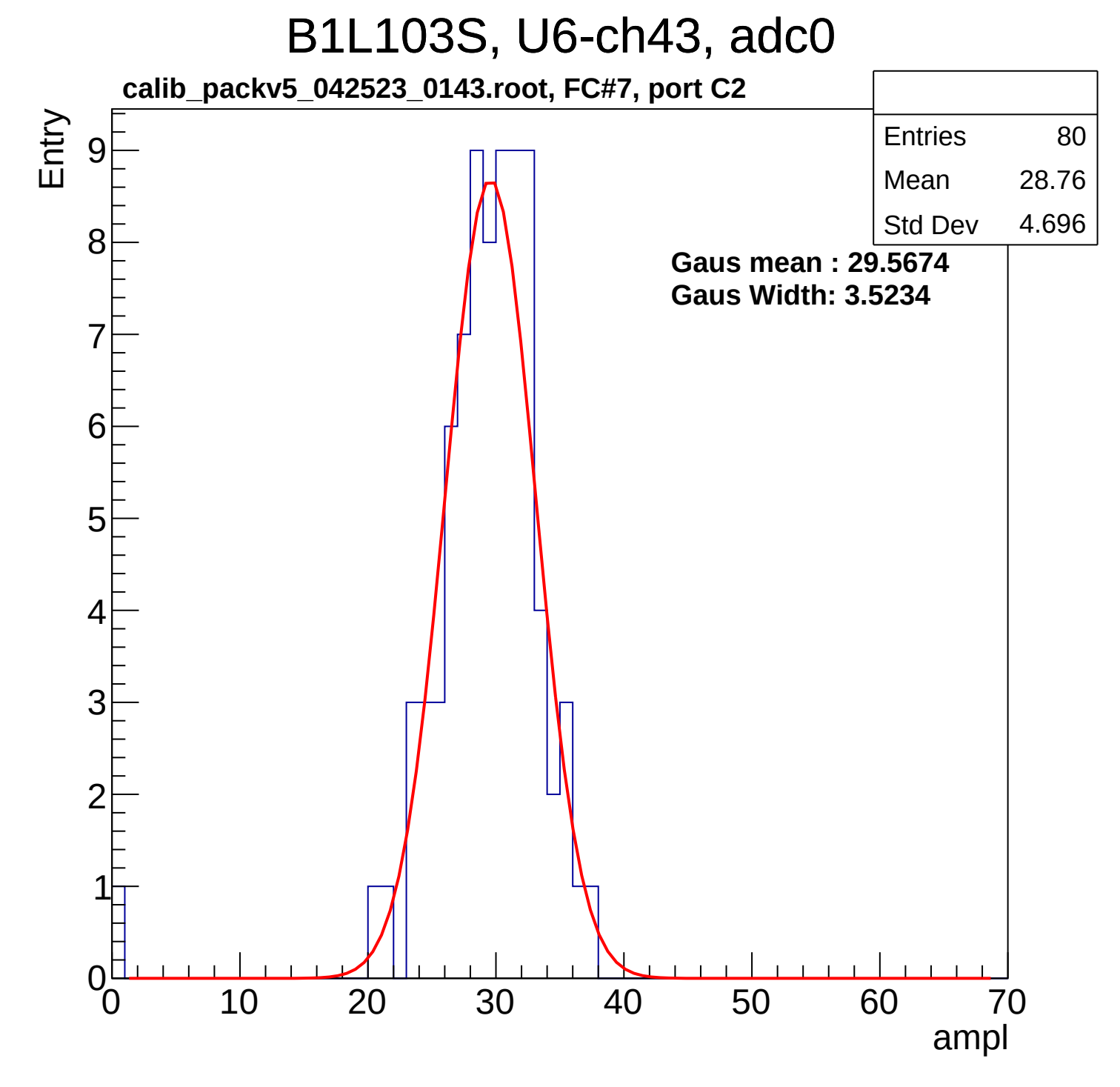

B1L103S, U6-ch42, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.42
Std Dev 17.06
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch42, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch43, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 28.76
Std Dev 4.696
Gaus mean : 29.5674
Gaus Width: 3.5234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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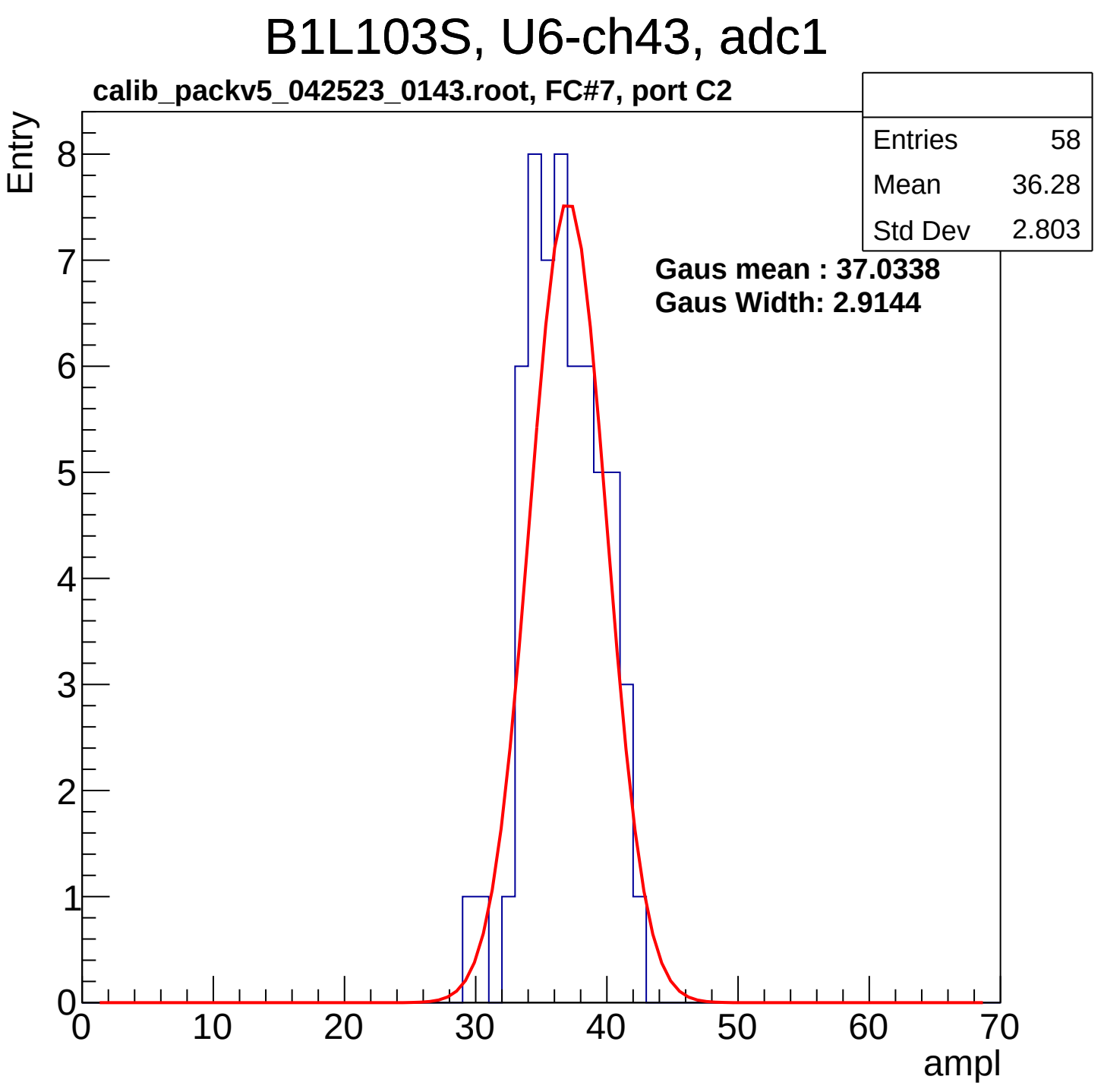
B1L103S, U6-ch43, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 36.28
Std Dev 2.803
Gaus mean : 37.0338
Gaus Width: 2.9144
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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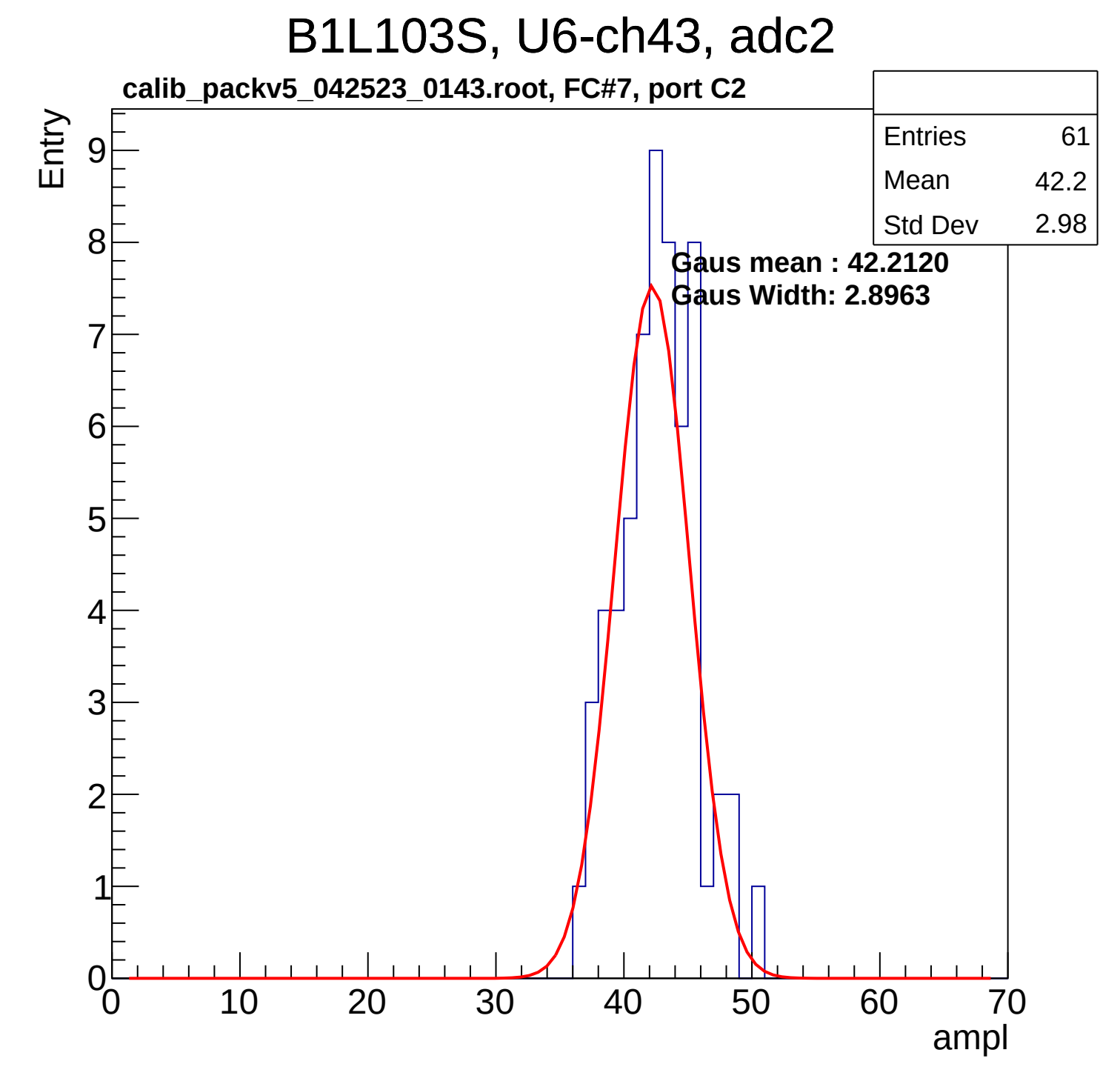
B1L103S, U6-ch43, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.2
Std Dev 2.98
Gaus mean : 42.2120
Gaus Width: 2.8963
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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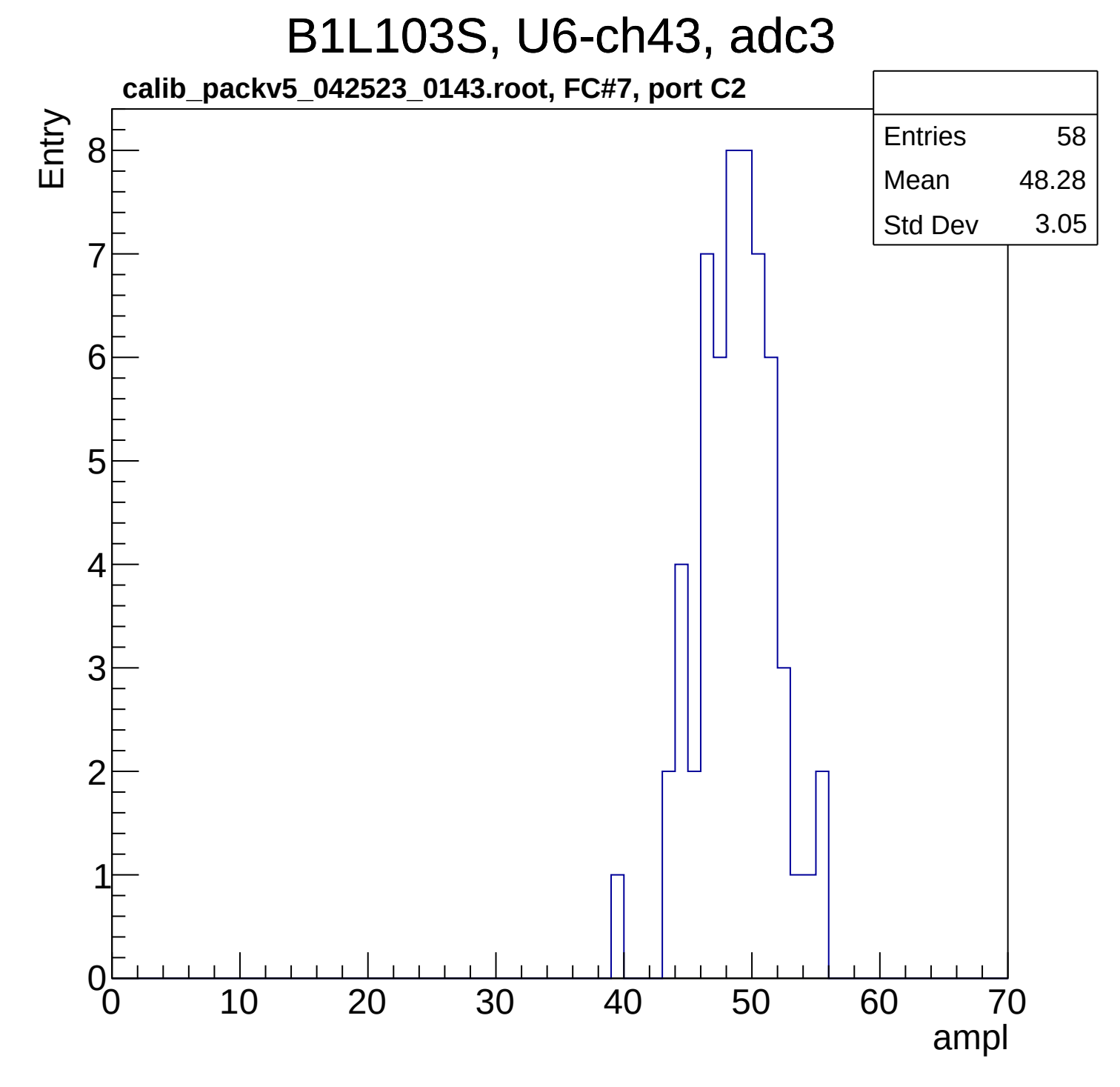
B1L103S, U6-ch43, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 48.28
Std Dev 3.05
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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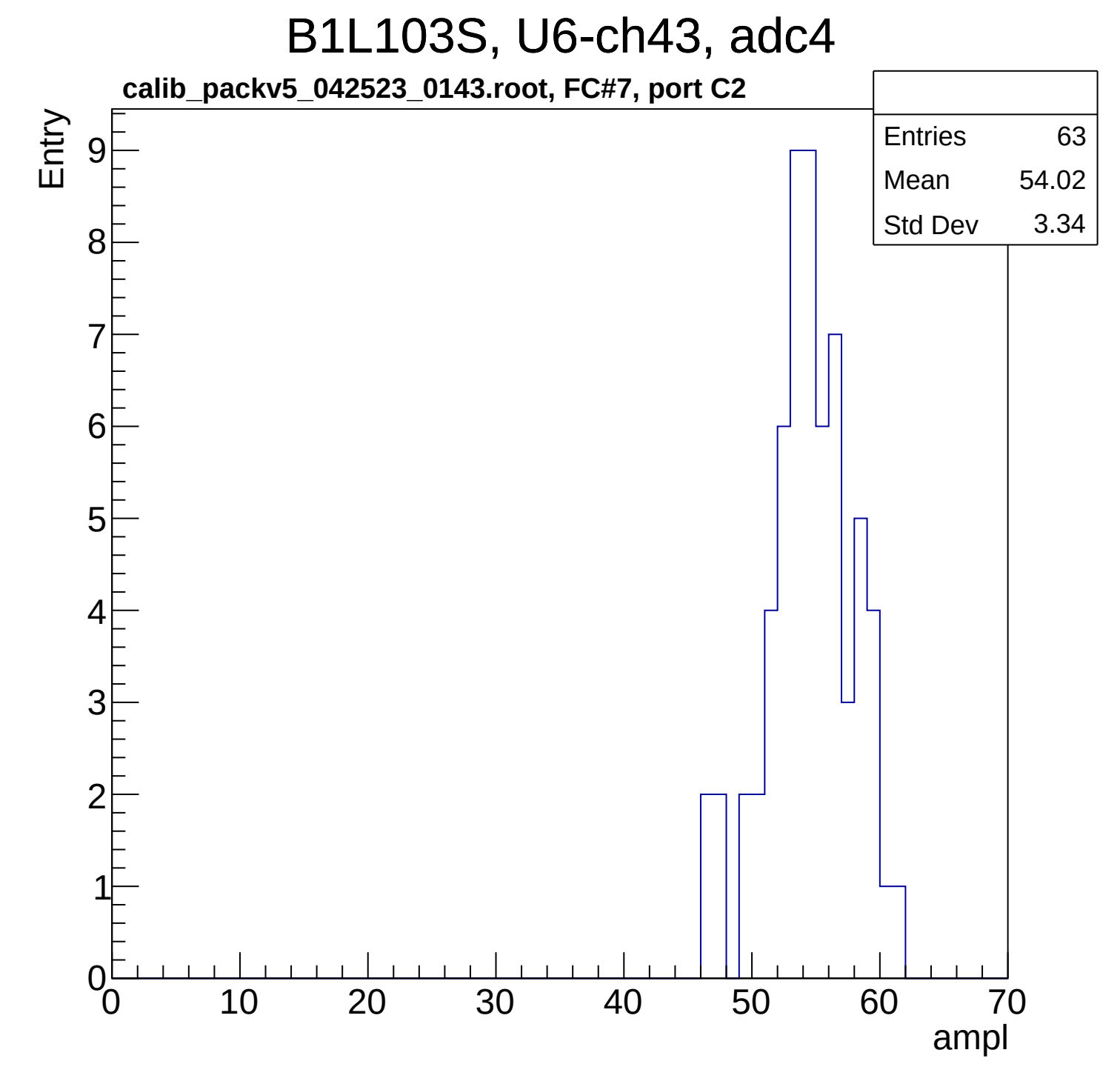
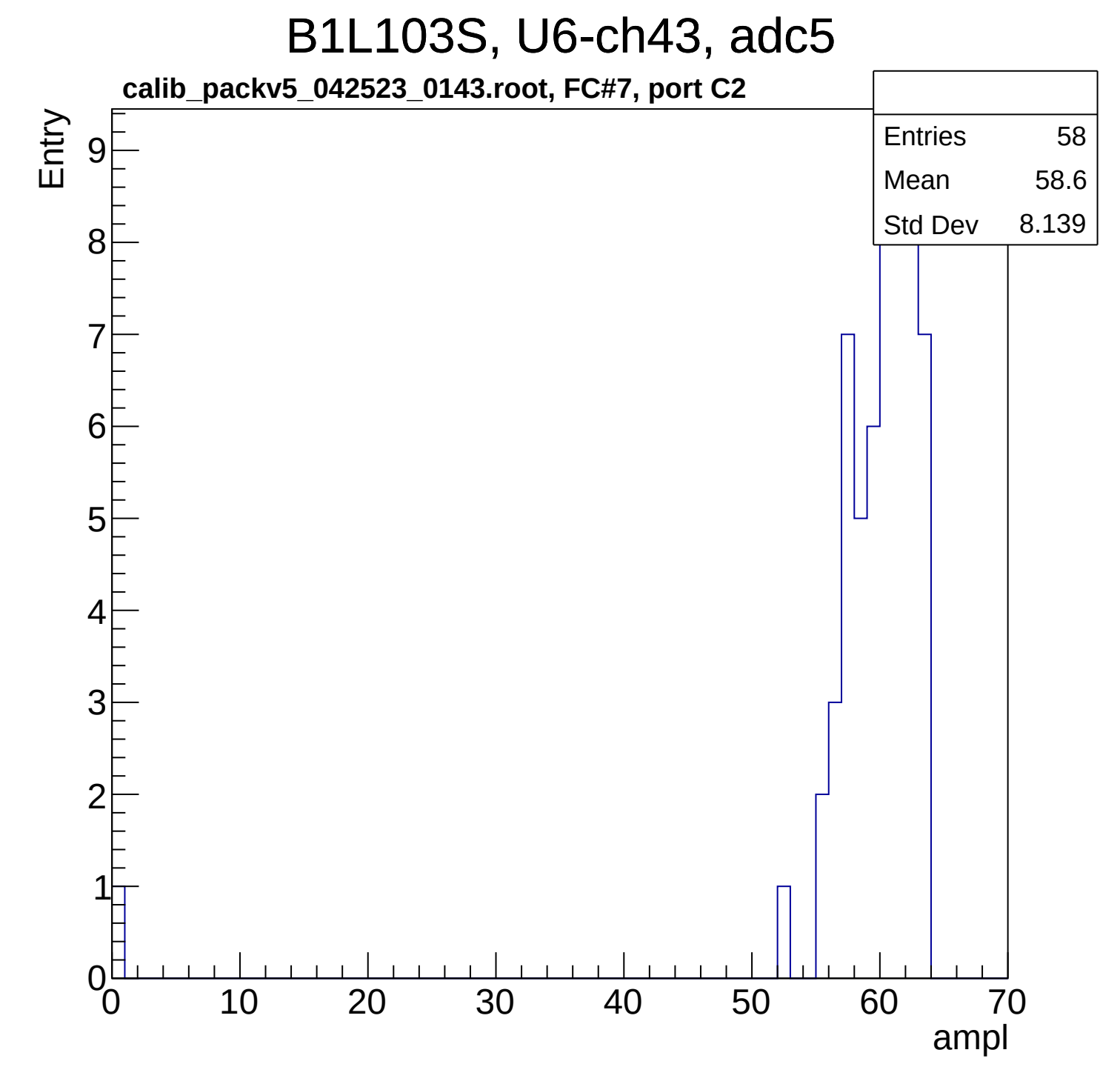

B1L103S, U6-ch43, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.02
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch43, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 58.6
Std Dev 8.139
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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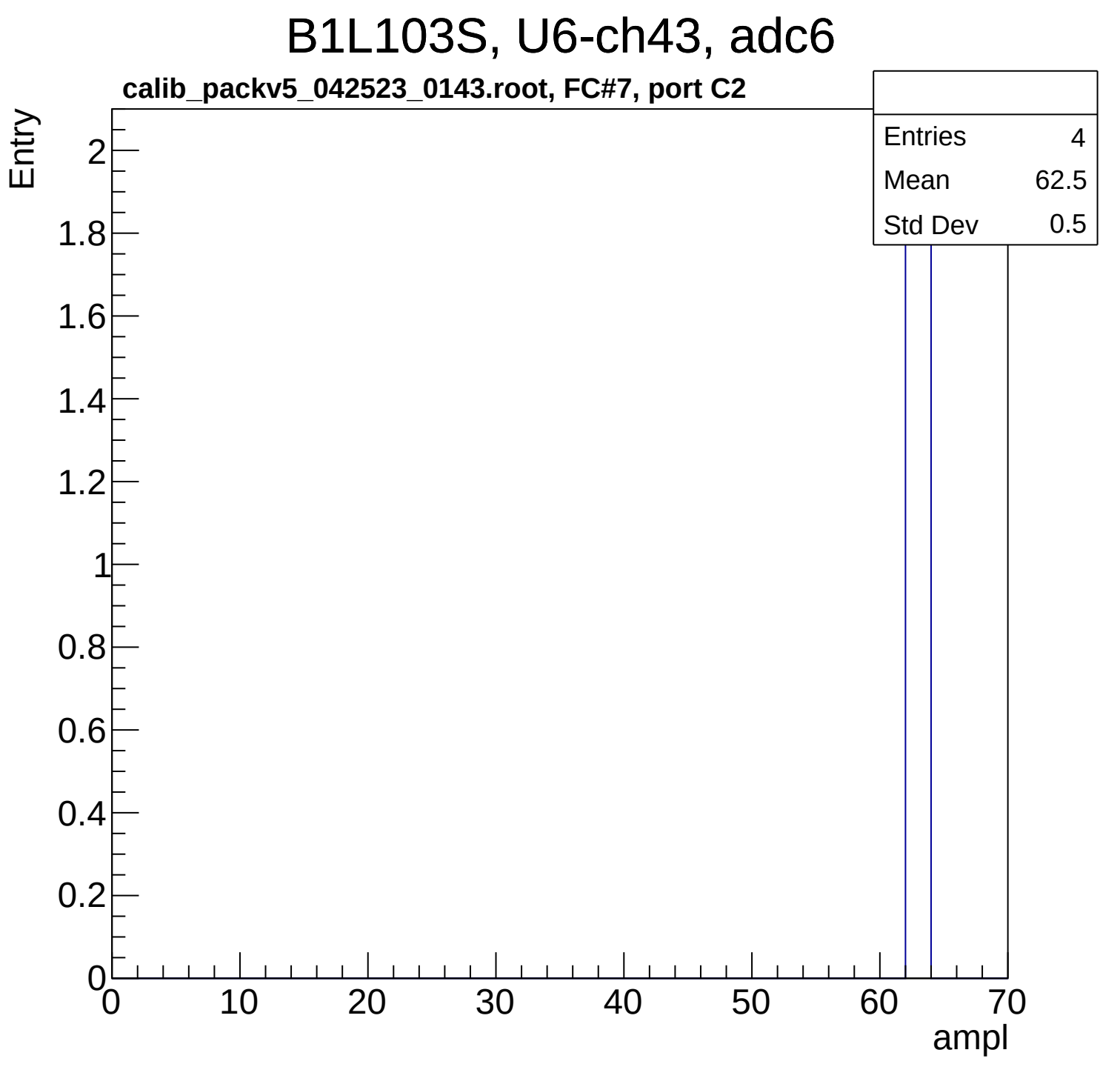
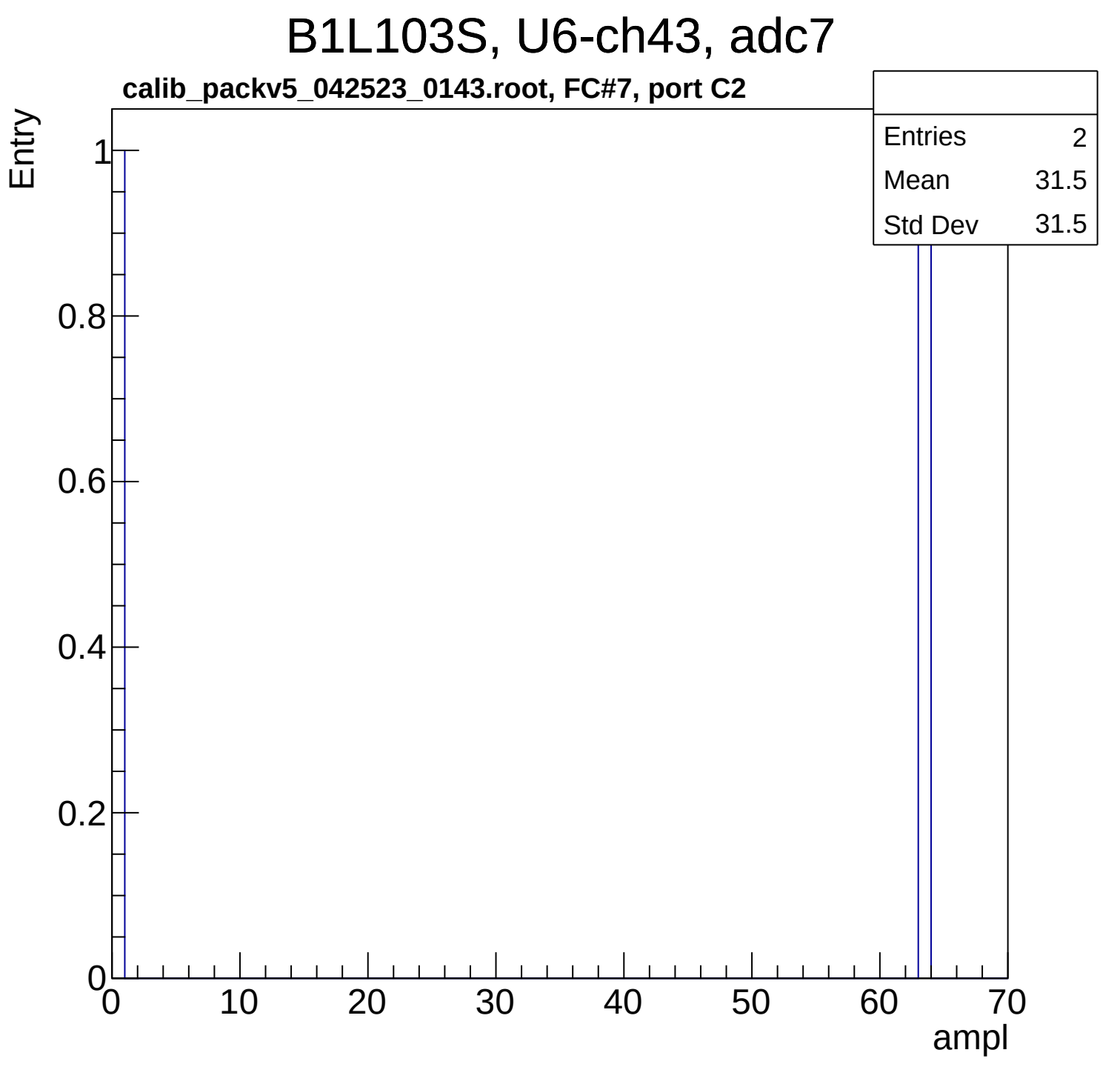
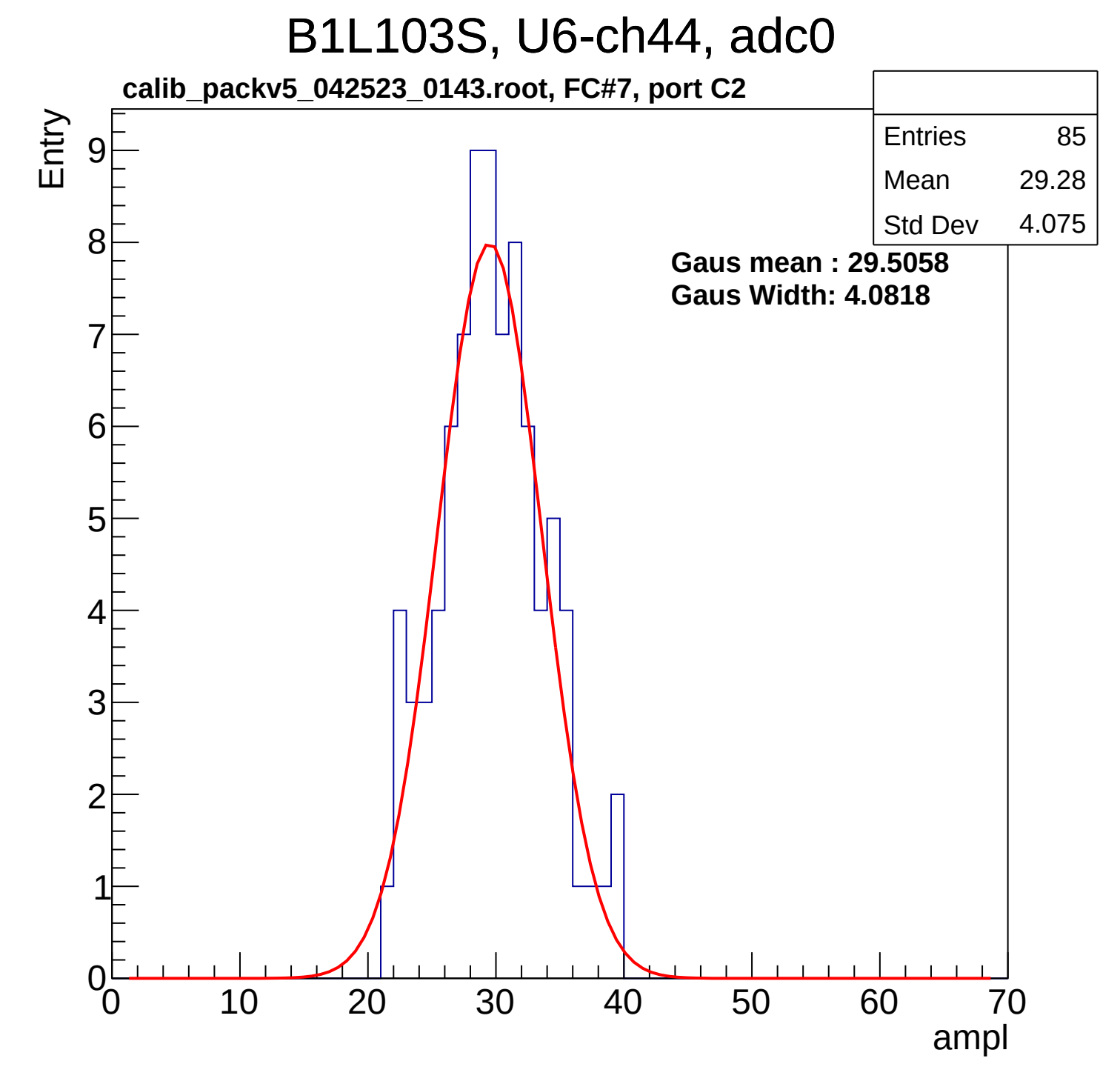
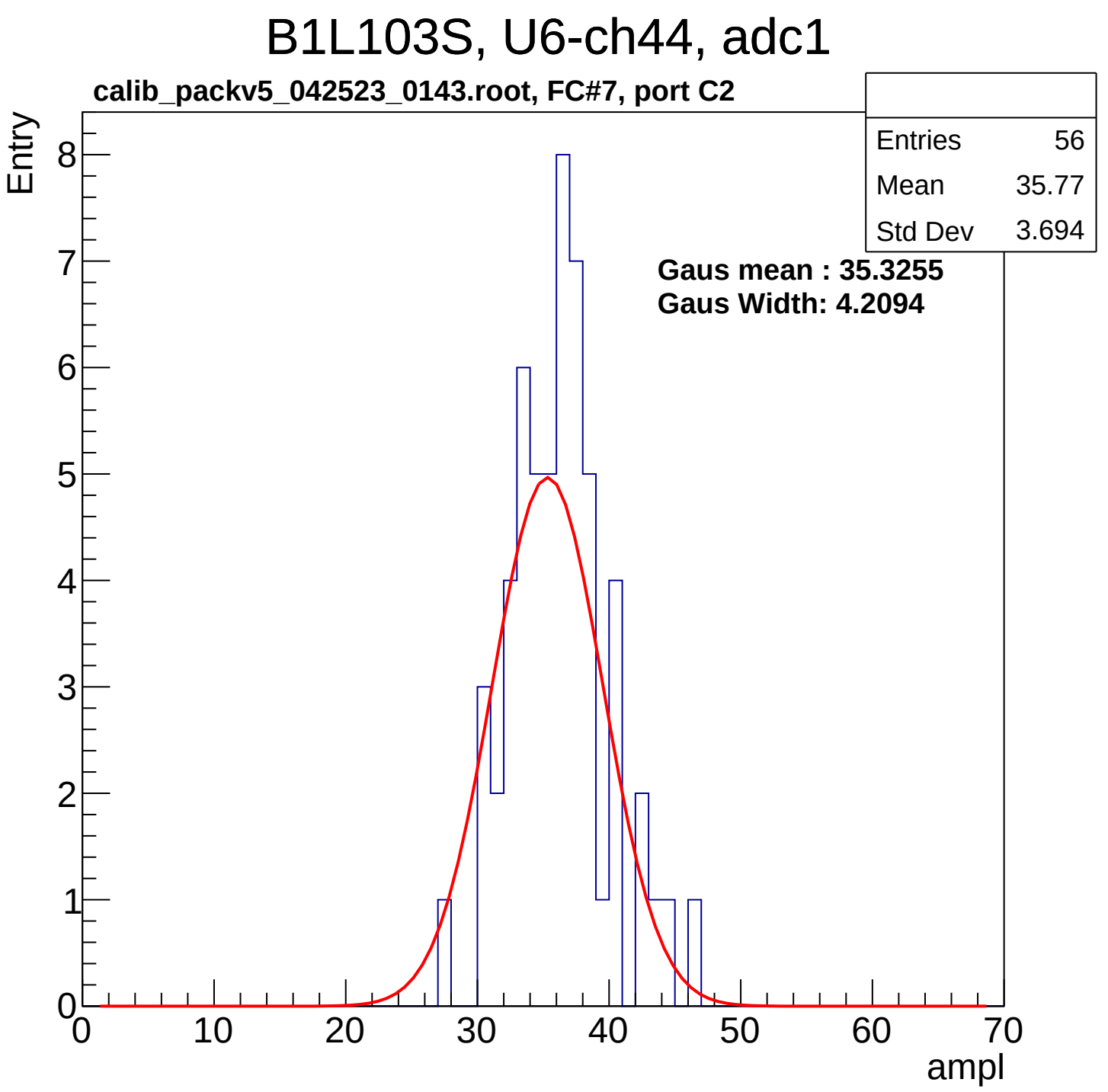
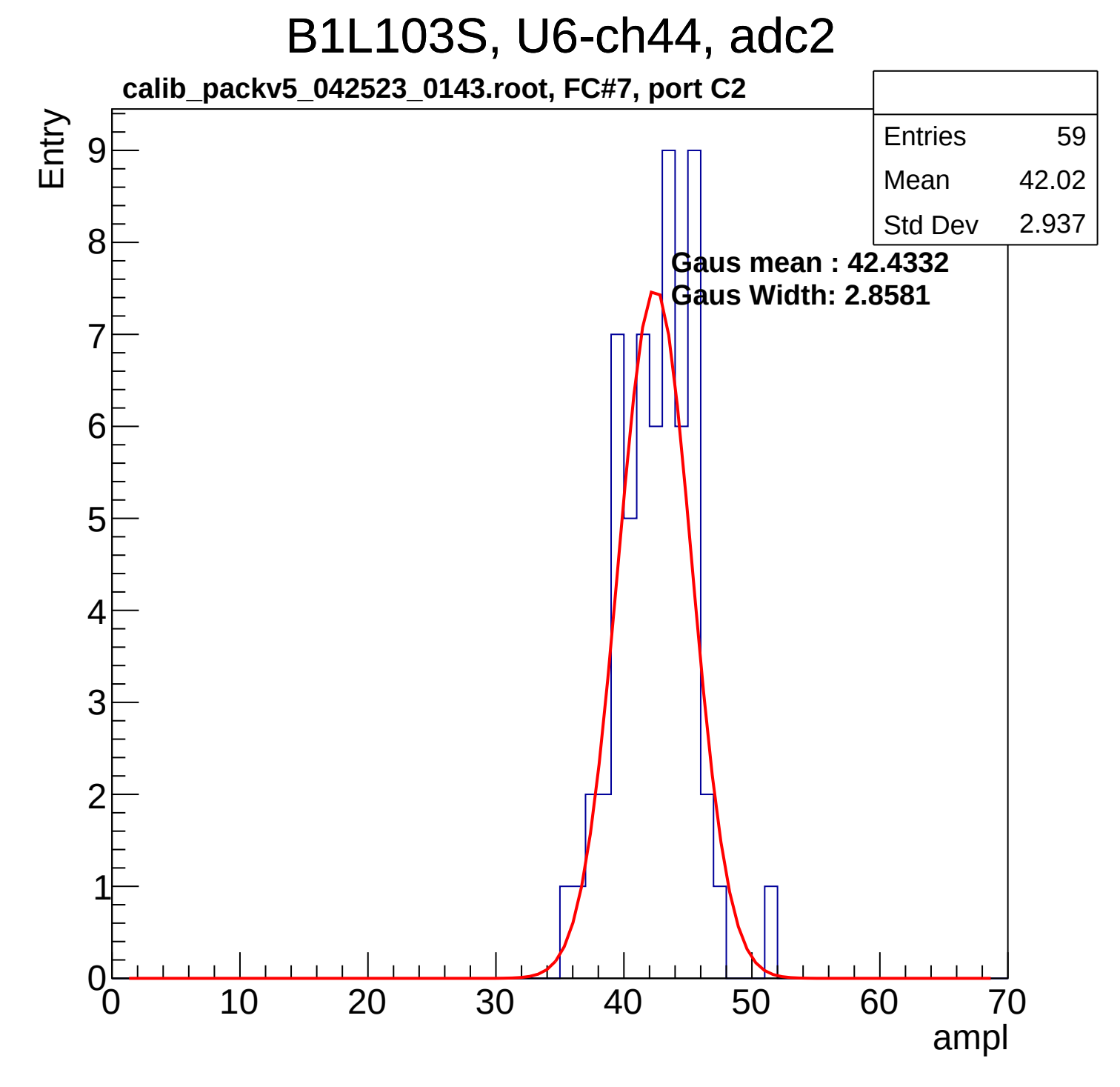
60
70


B1L103S, U6-ch43, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch43, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch44, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 29.28
Std Dev 4.075
Gaus mean : 29.5058
Gaus Width: 4.0818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch44, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 35.77
Std Dev 3.694
Gaus mean : 35.3255
Gaus Width: 4.2094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch44, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42.02
Std Dev 2.937
Gaus mean : 42.4332
Gaus Width: 2.8581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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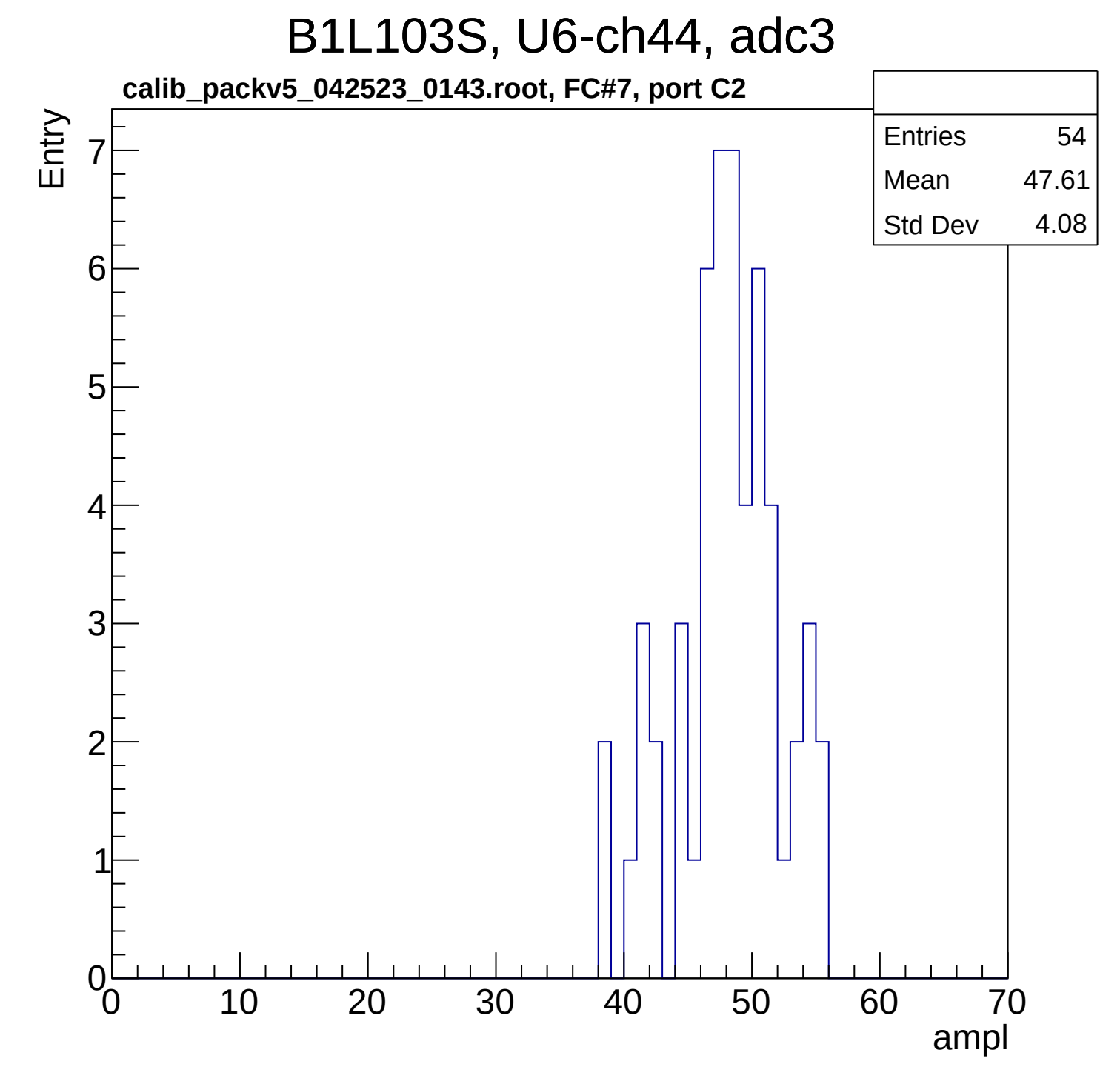
B1L103S, U6-ch44, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 47.61
Std Dev 4.08
Entry
ampl
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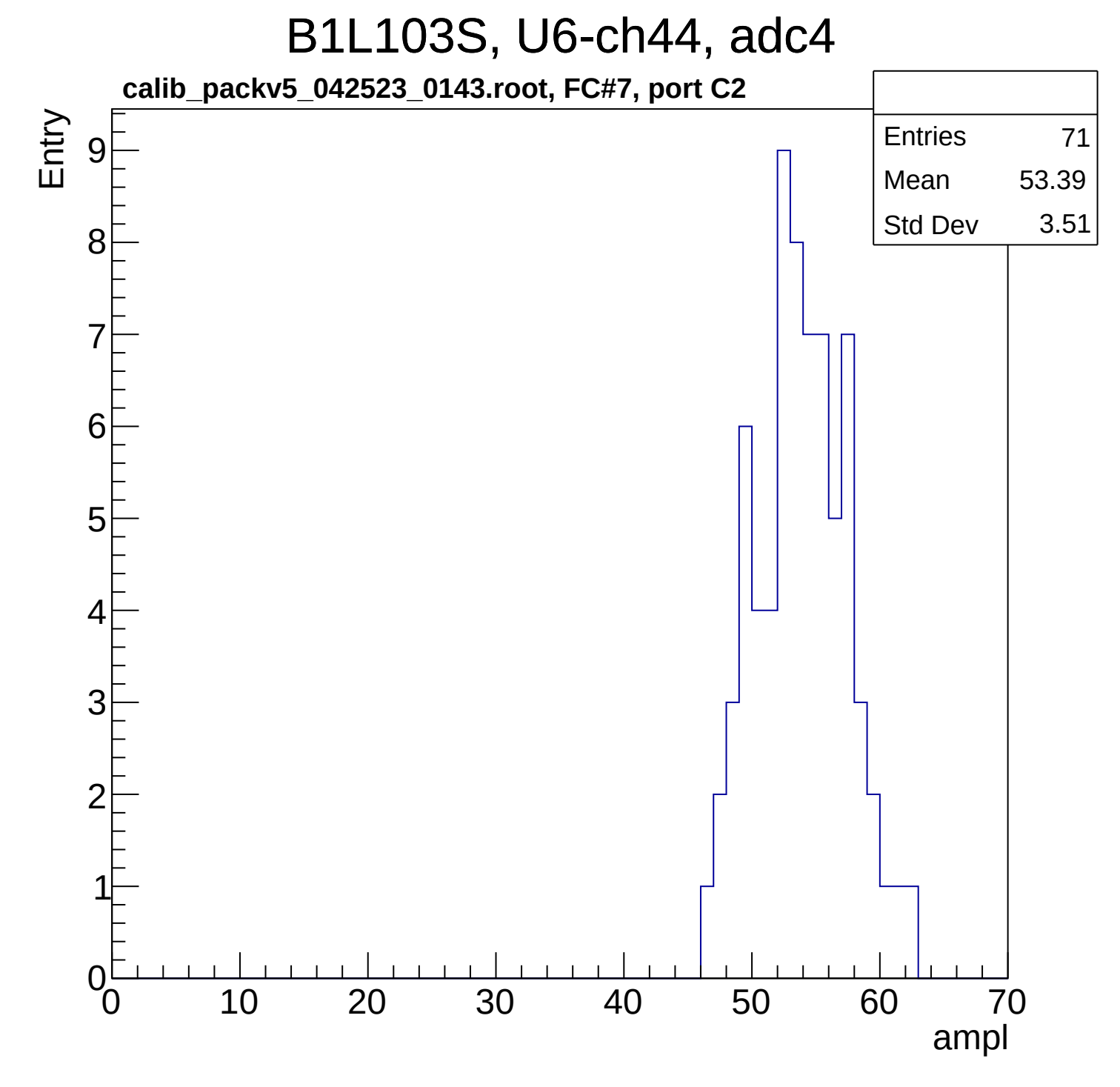
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch44, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 53.39
Std Dev 3.51
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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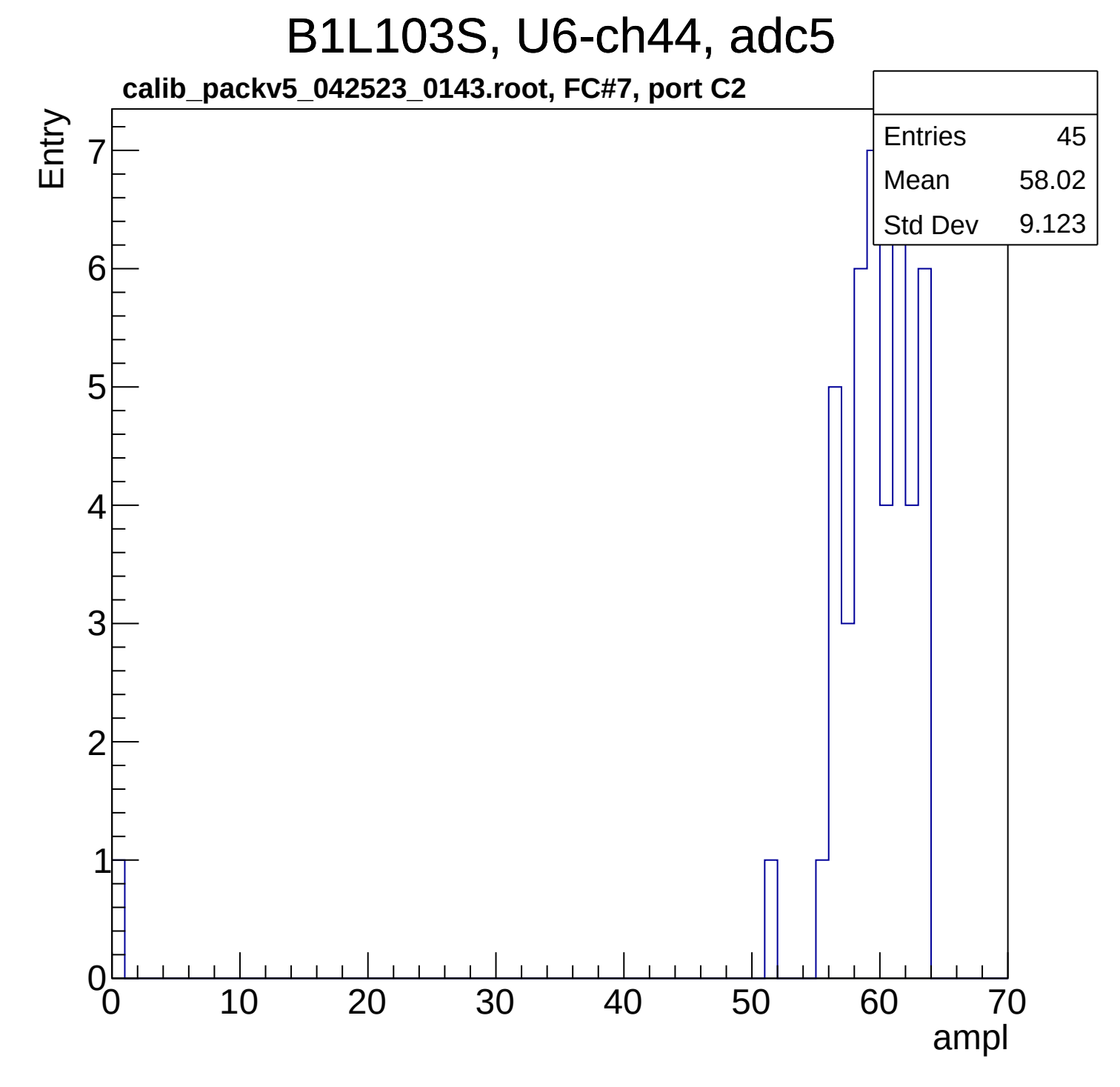
B1L103S, U6-ch44, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.02
Std Dev 9.123
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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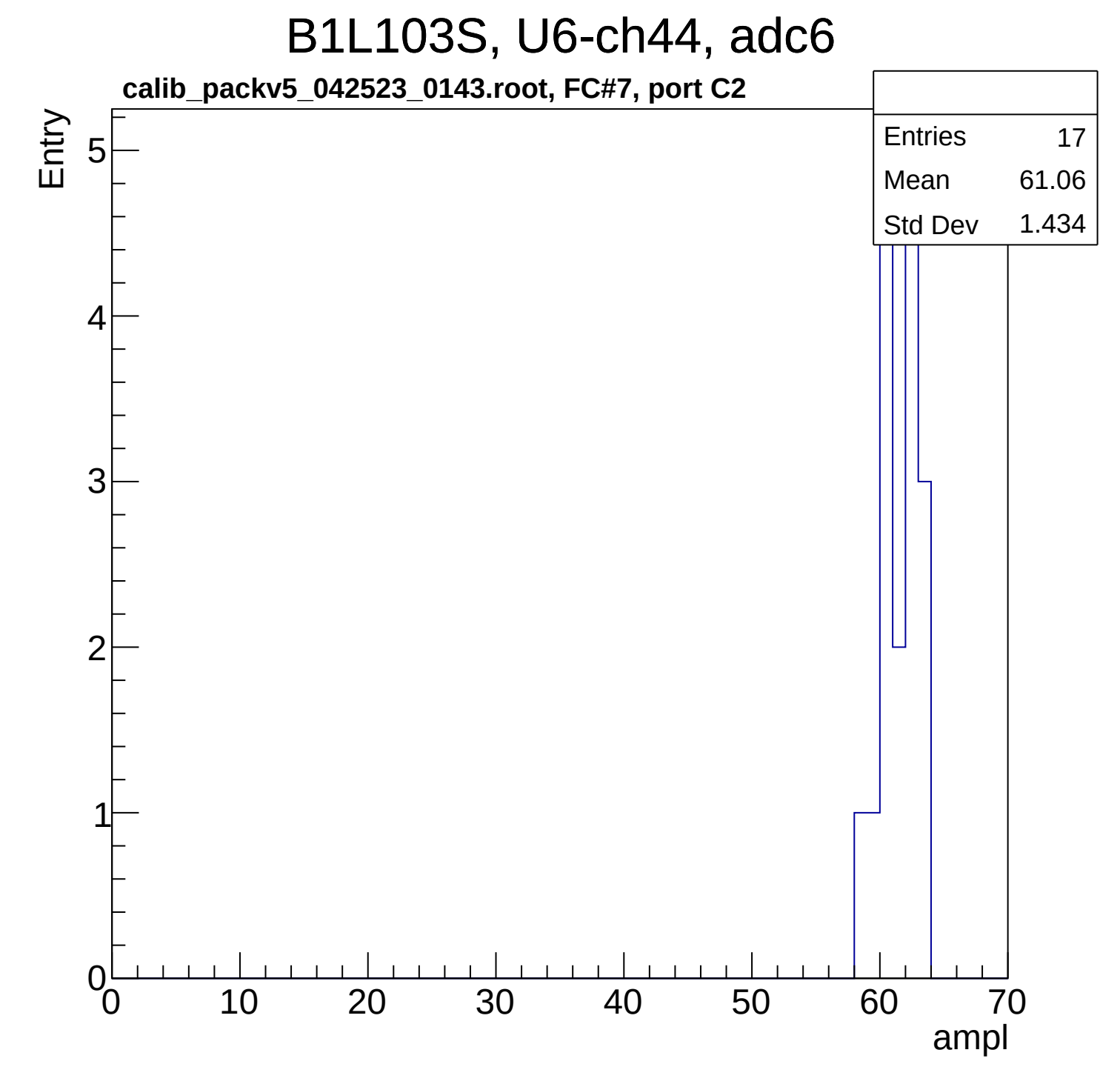
B1L103S, U6-ch44, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 61.06
Std Dev 1.434
Entry
ampl
0
1
2
3
4
5
0
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20
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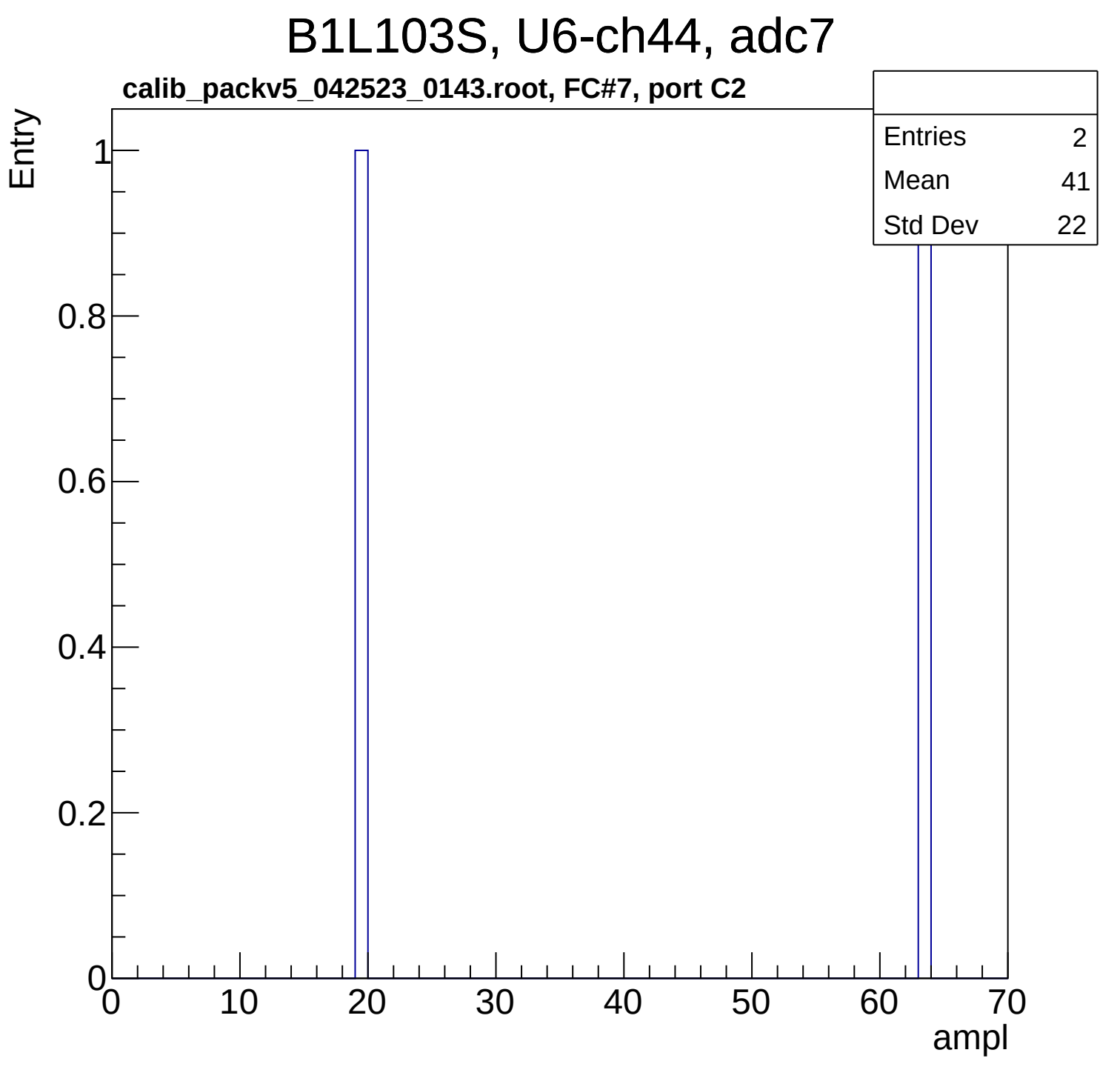
40
50
60
70


B1L103S, U6-ch44, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 41
Std Dev 22
Entry
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0.6
0.4
0.2
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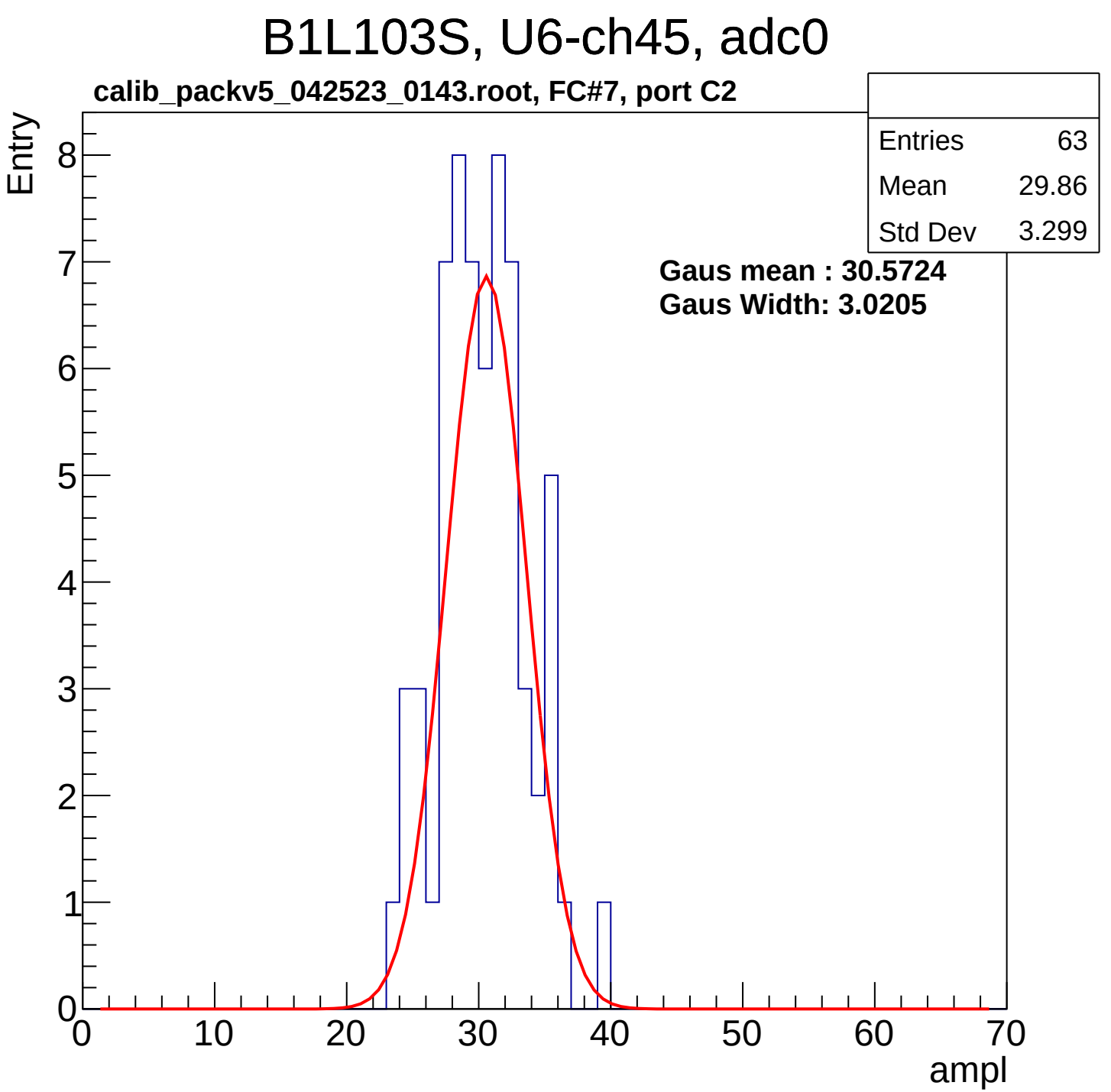
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch45, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.86
Std Dev 3.299
Gaus mean : 30.5724
Gaus Width: 3.0205
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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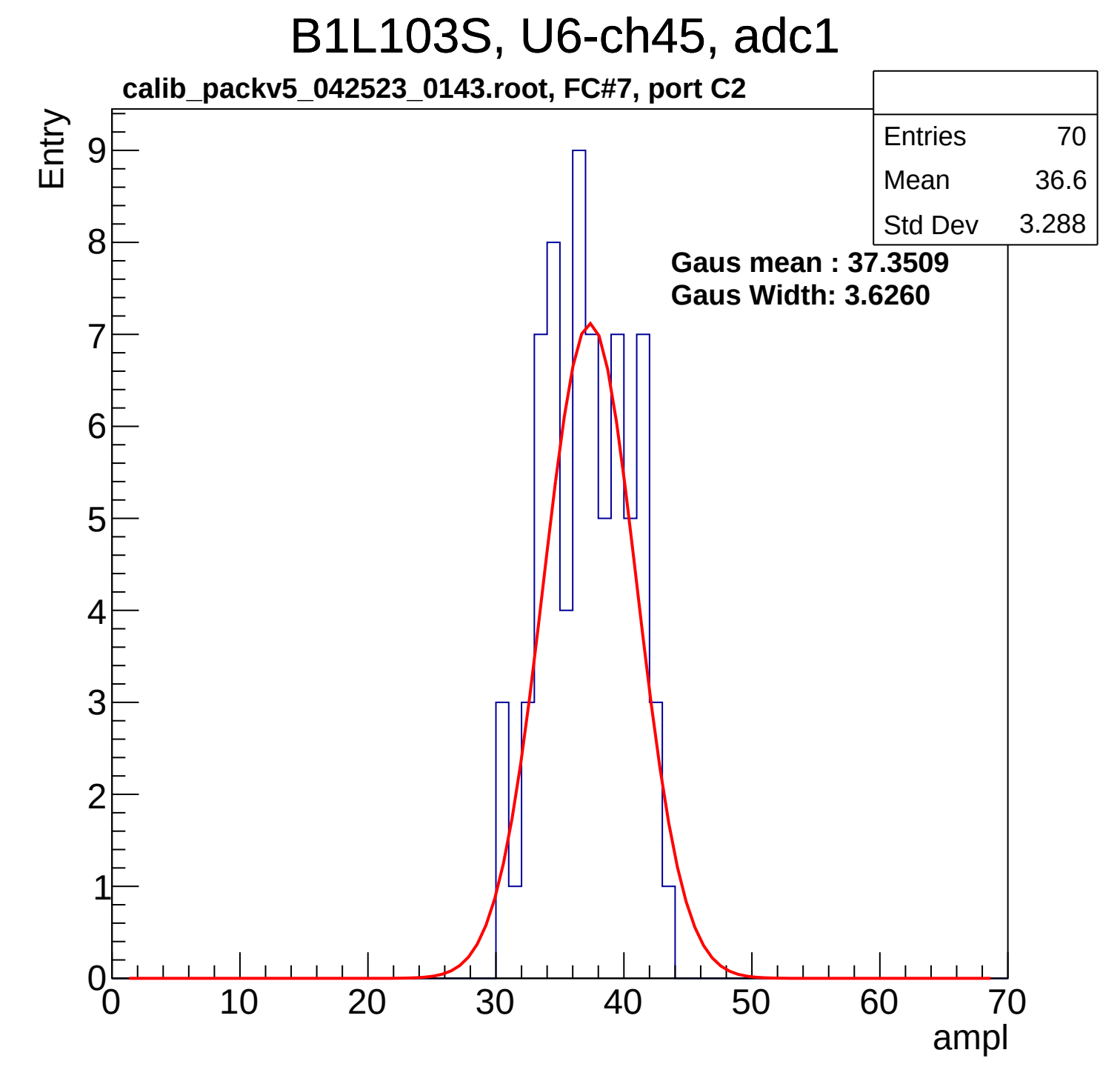
B1L103S, U6-ch45, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 36.6
Std Dev 3.288
Gaus mean : 37.3509
Gaus Width: 3.6260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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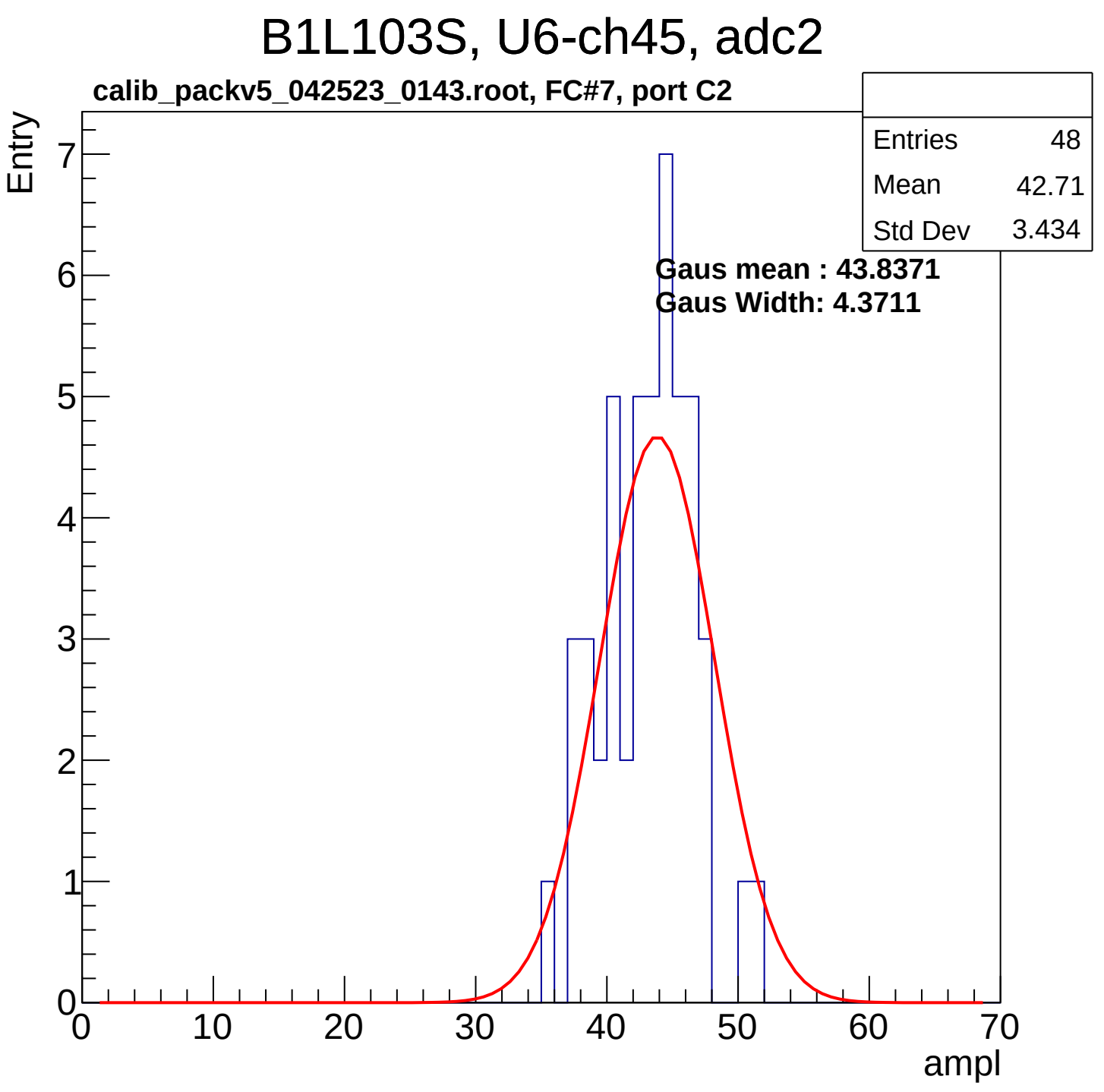
B1L103S, U6-ch45, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 42.71
Std Dev 3.434
Gaus mean : 43.8371
Gaus Width: 4.3711
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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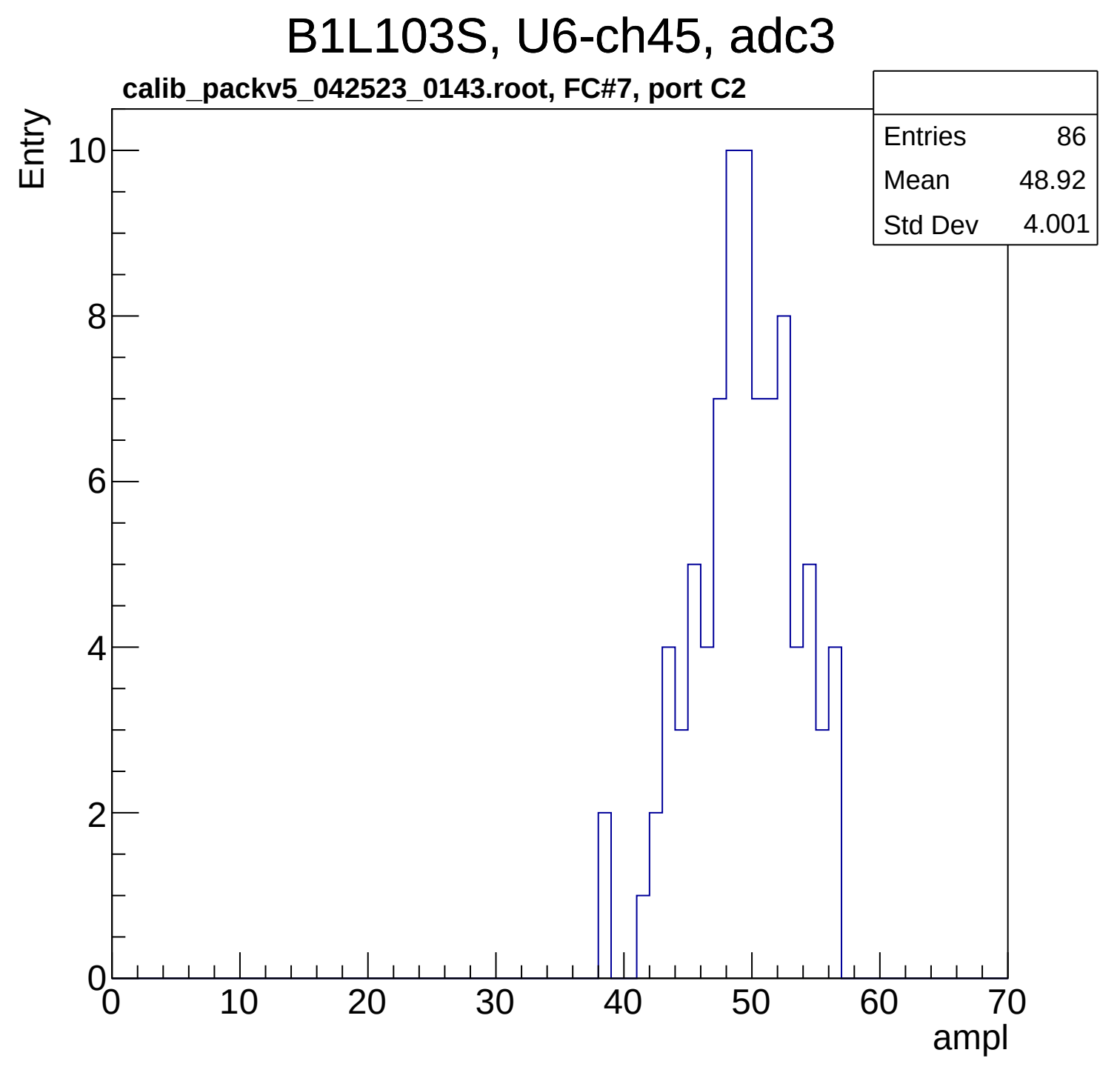
B1L103S, U6-ch45, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 48.92
Std Dev 4.001
Entry
ampl
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4
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0
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40
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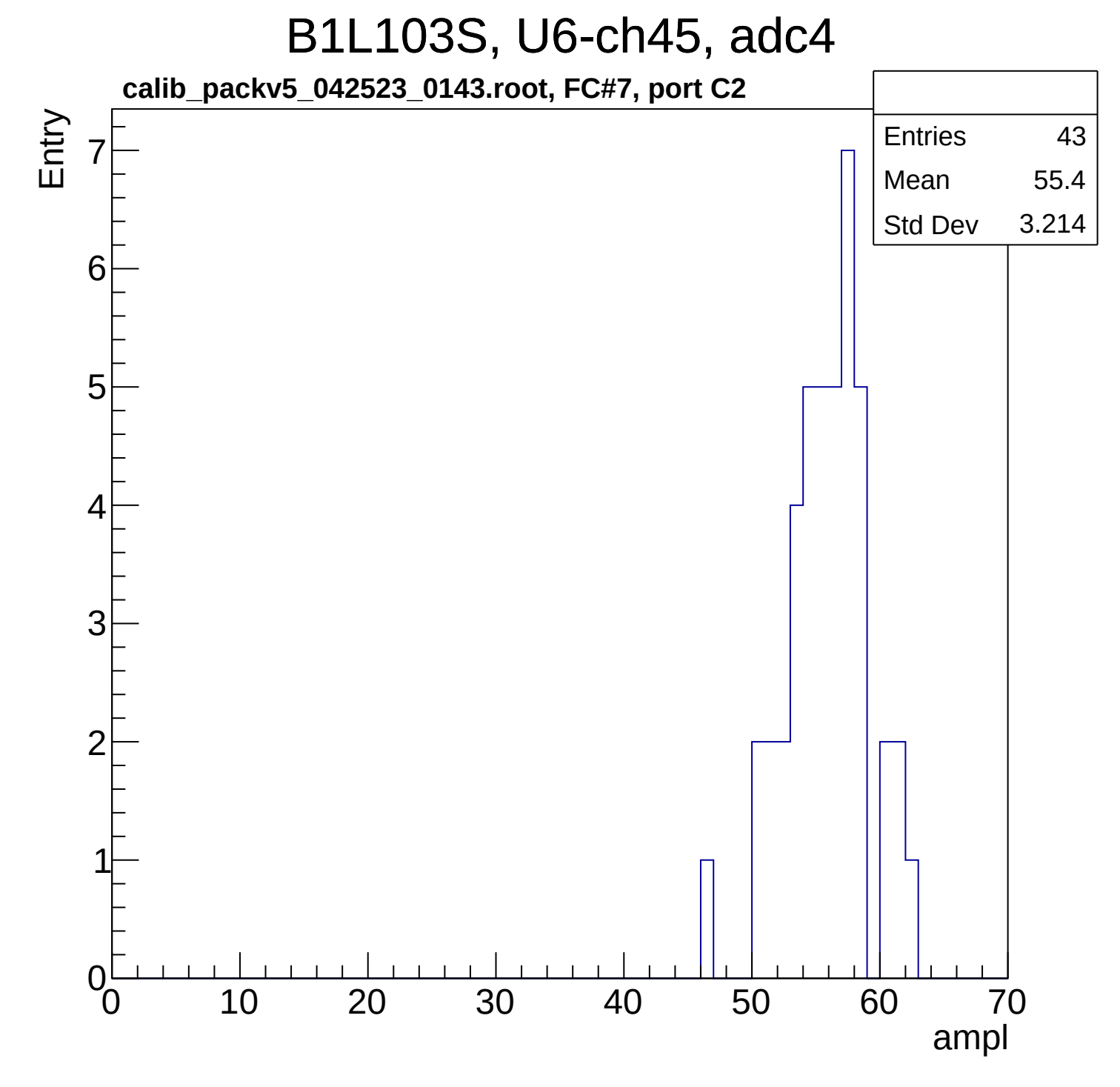
60
70


B1L103S, U6-ch45, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 55.4
Std Dev 3.214
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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60
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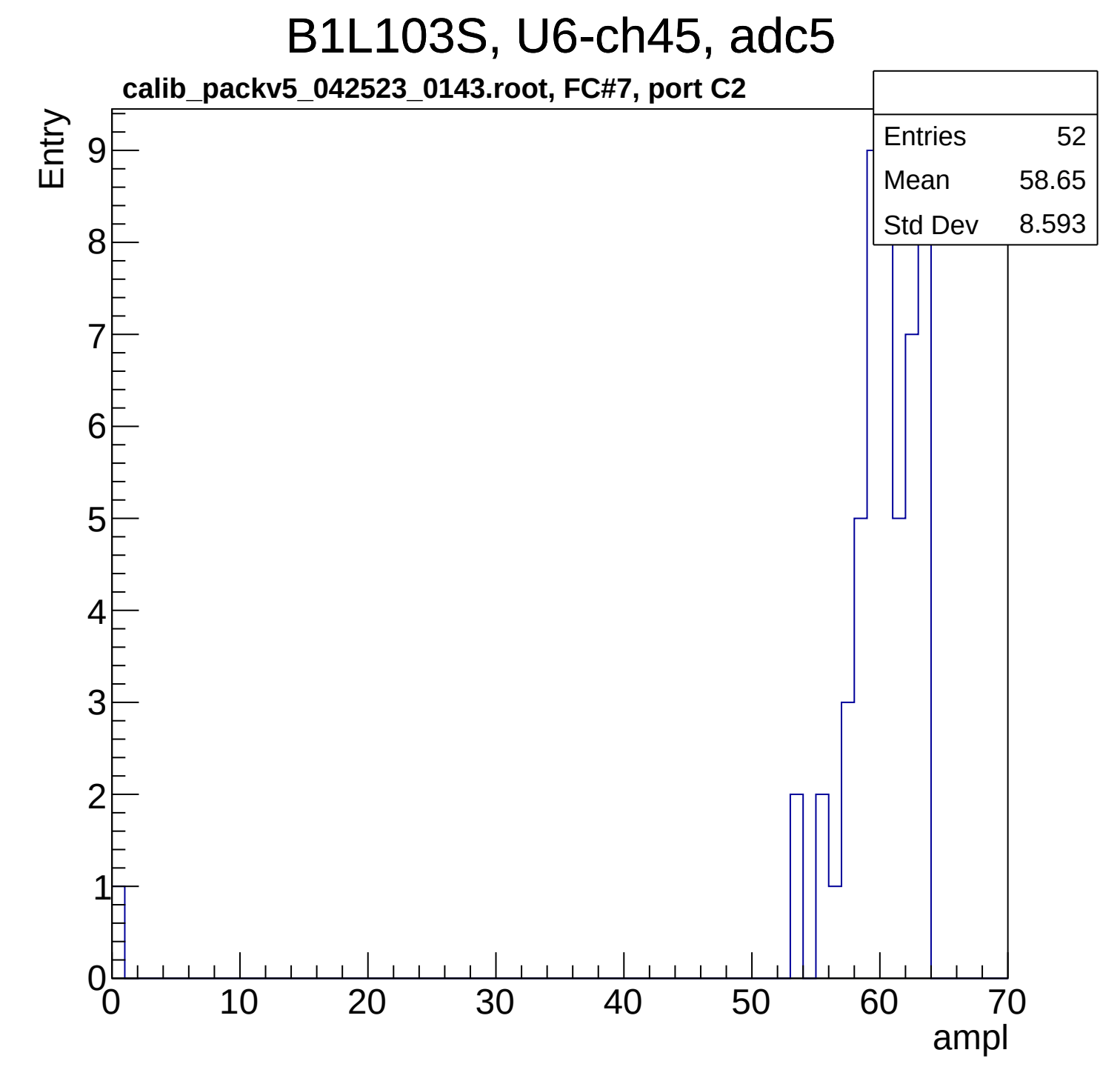
B1L103S, U6-ch45, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.65
Std Dev 8.593
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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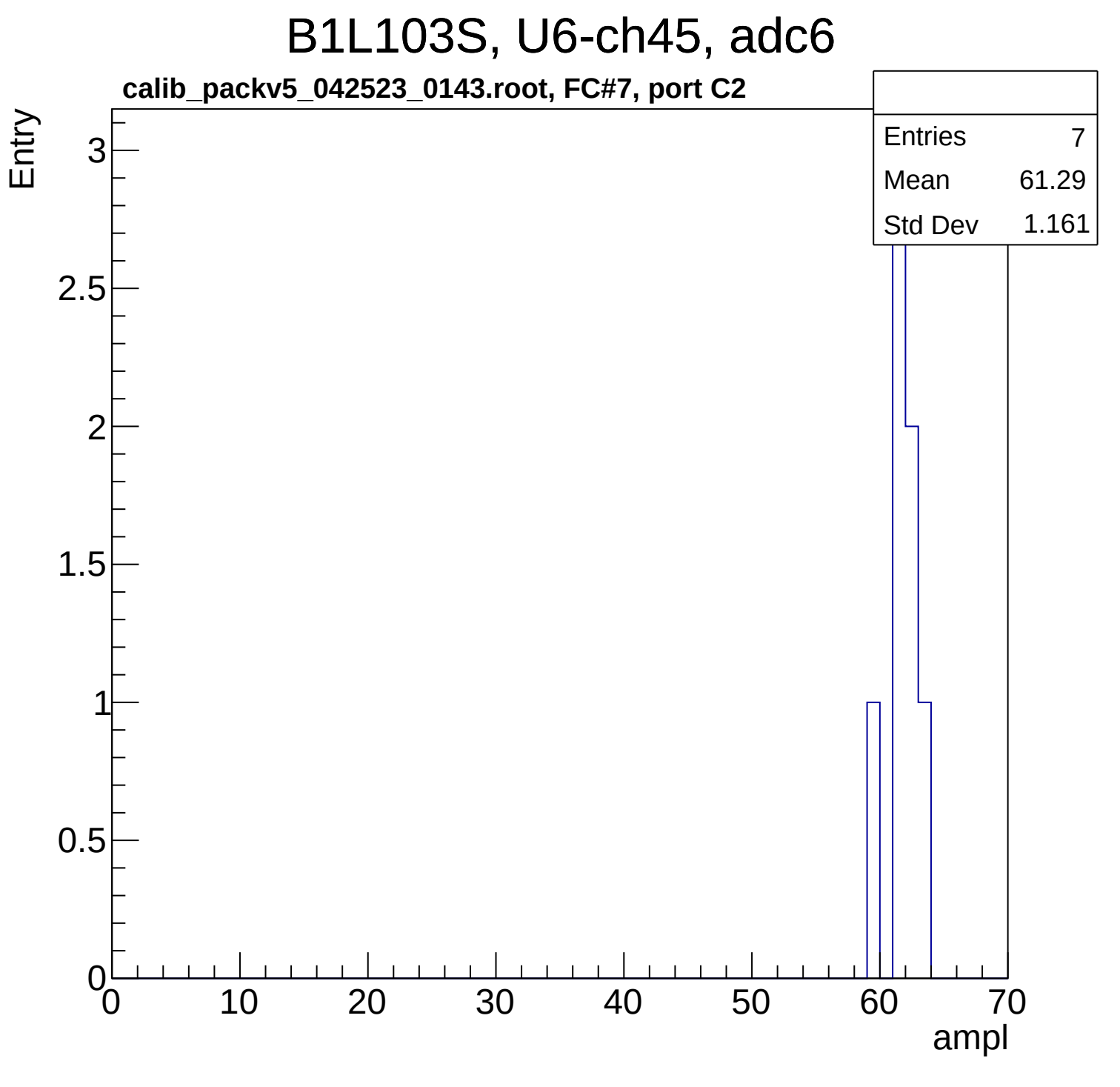
B1L103S, U6-ch45, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.29
Std Dev 1.161
Entry
ampl
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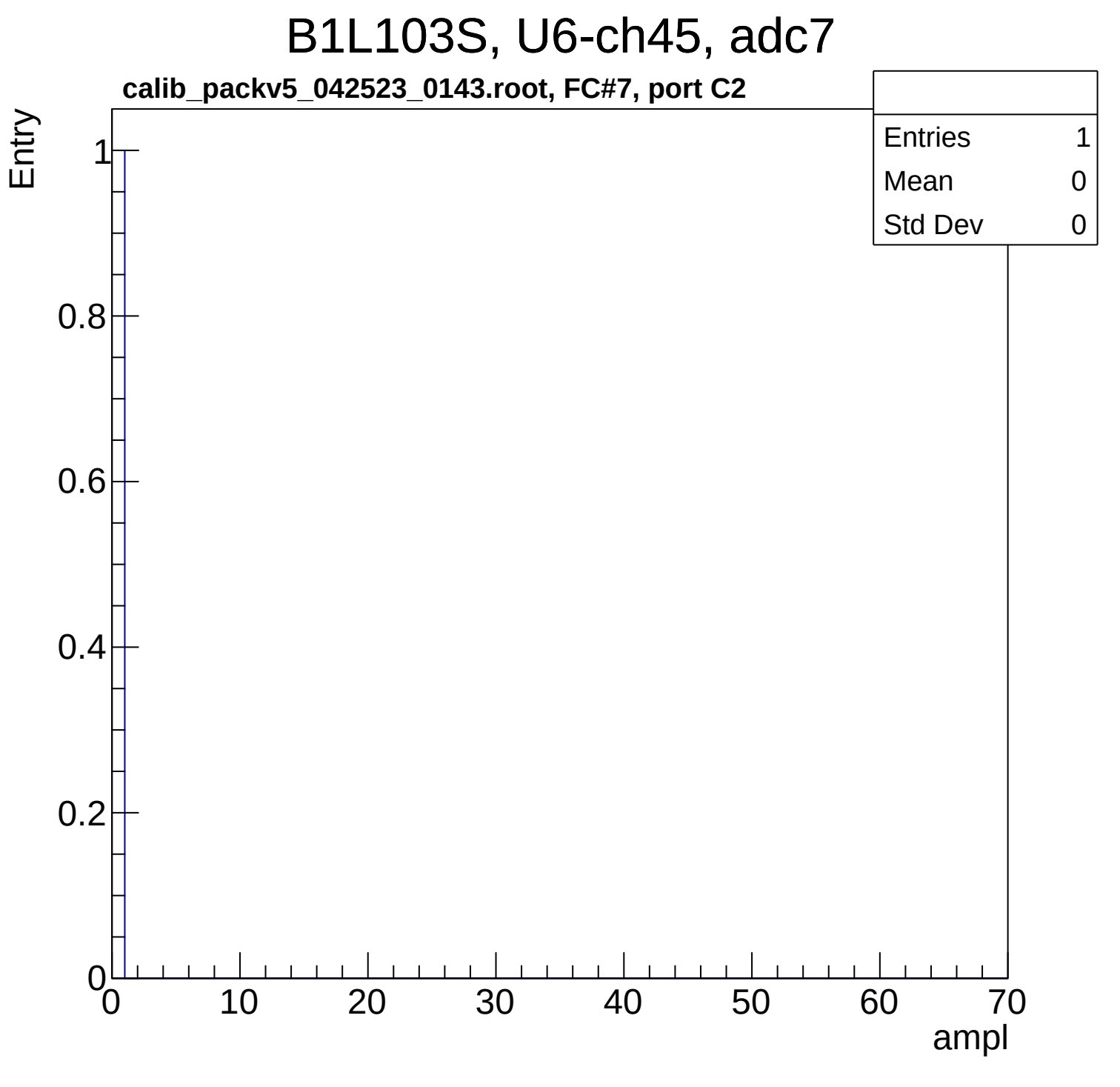
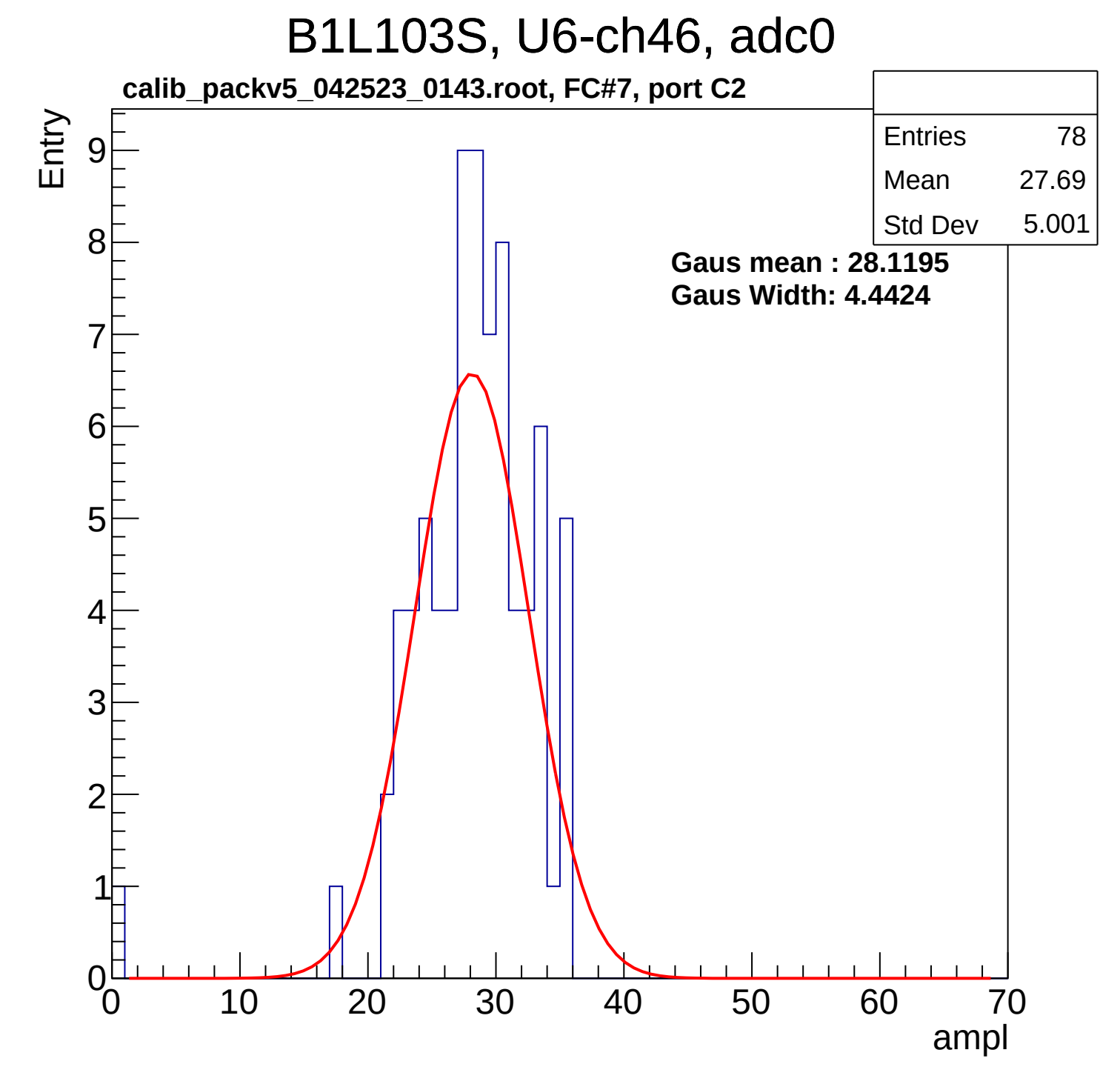
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch45, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch46, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 27.69
Std Dev 5.001
Gaus mean : 28.1195
Gaus Width: 4.4424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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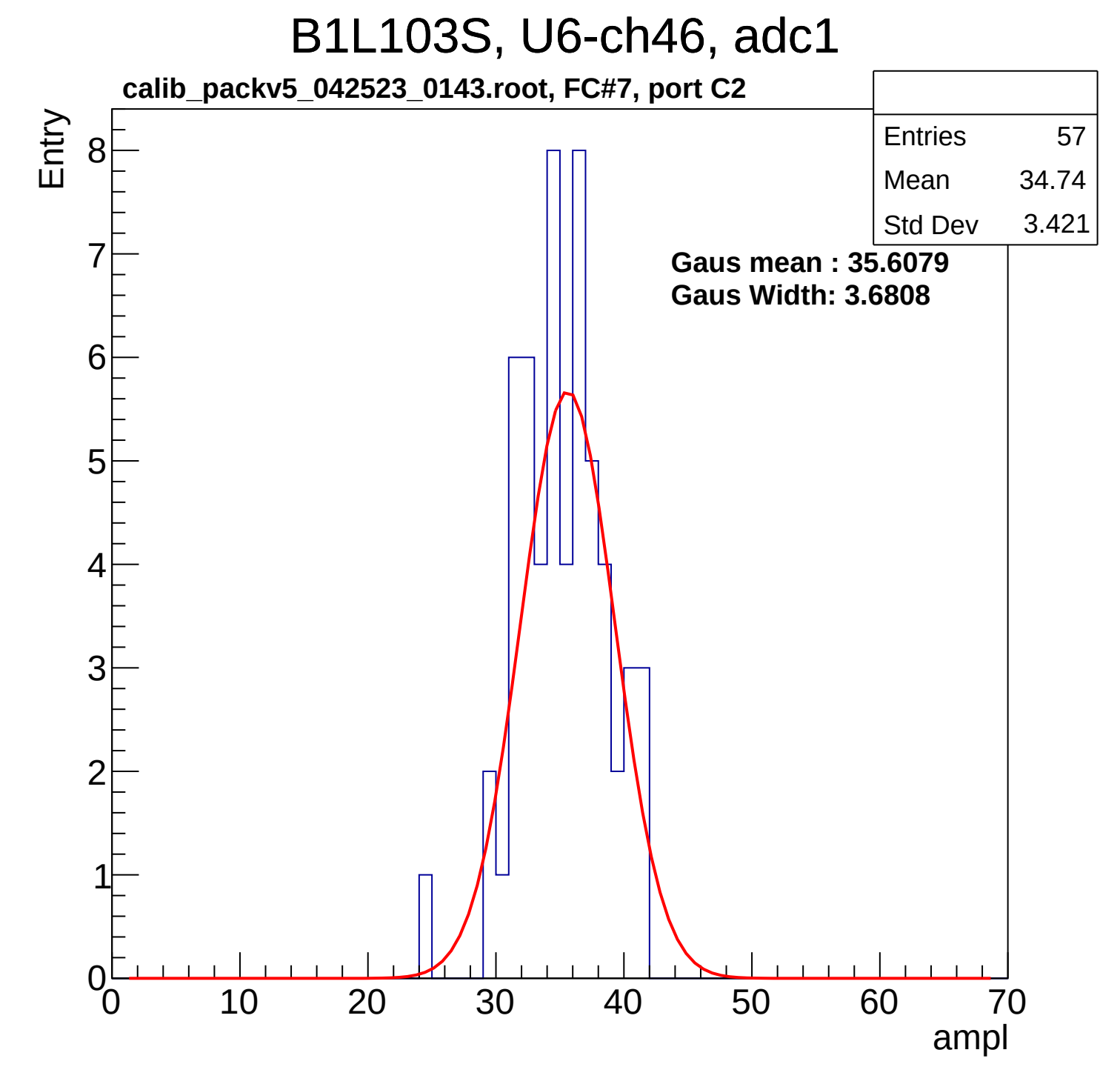
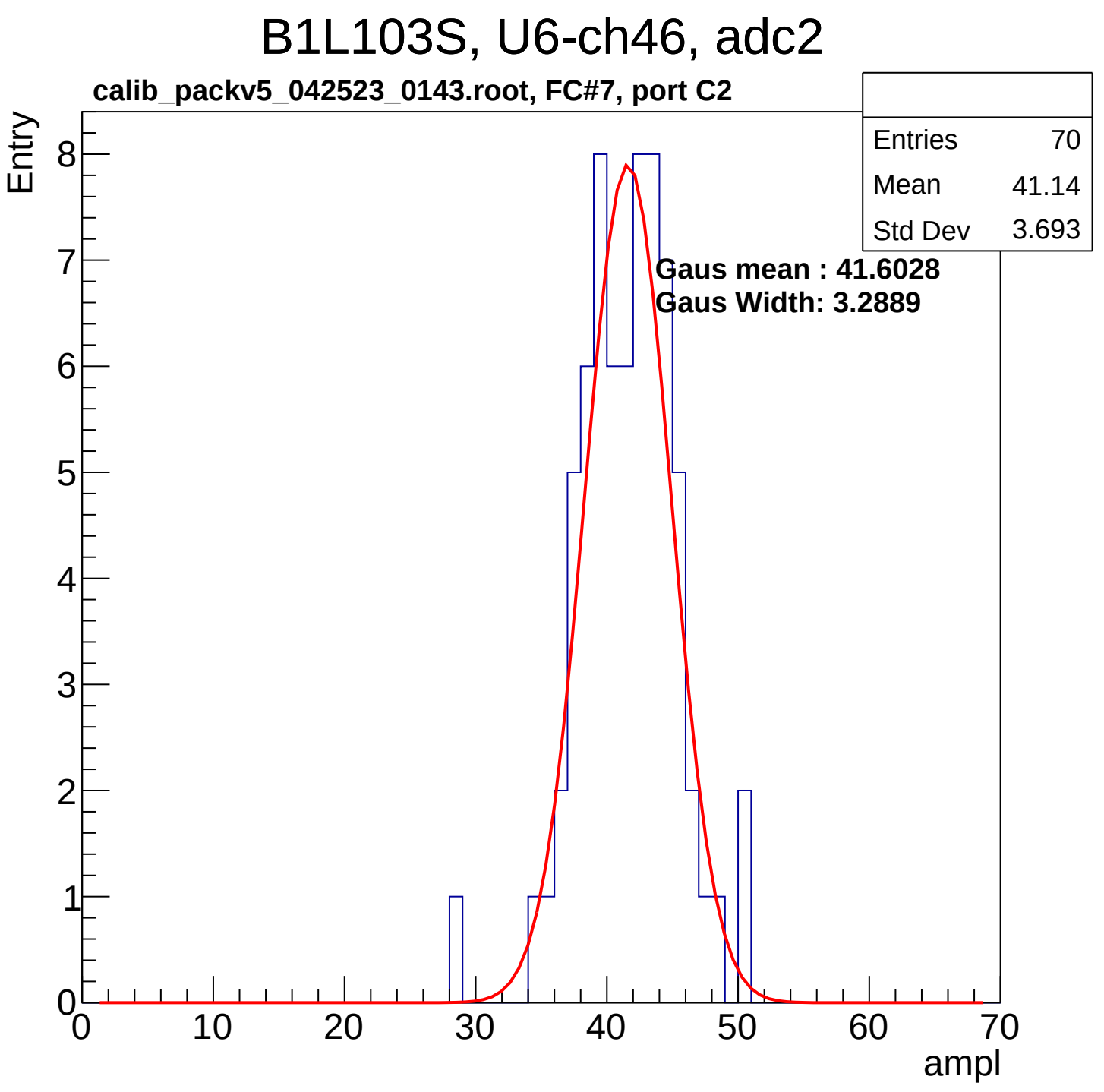

B1L103S, U6-ch46, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 34.74
Std Dev 3.421
Gaus mean : 35.6079
Gaus Width: 3.6808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch46, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 41.14
Std Dev 3.693
Gaus mean : 41.6028
Gaus Width: 3.2889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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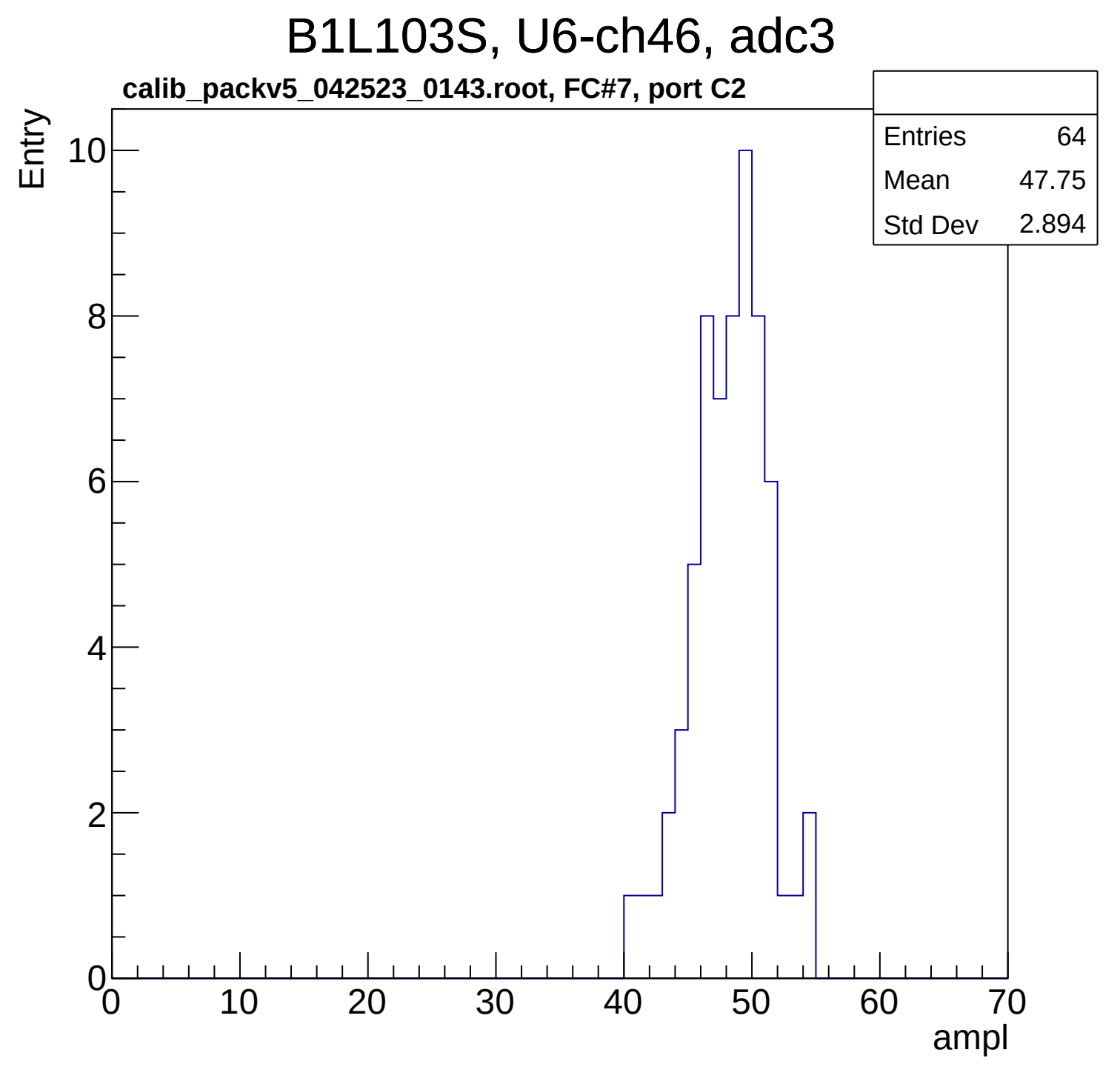
B1L103S, U6-ch46, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.75
Std Dev 2.894
Entry
ampl
0
2
4
6
8
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0
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60
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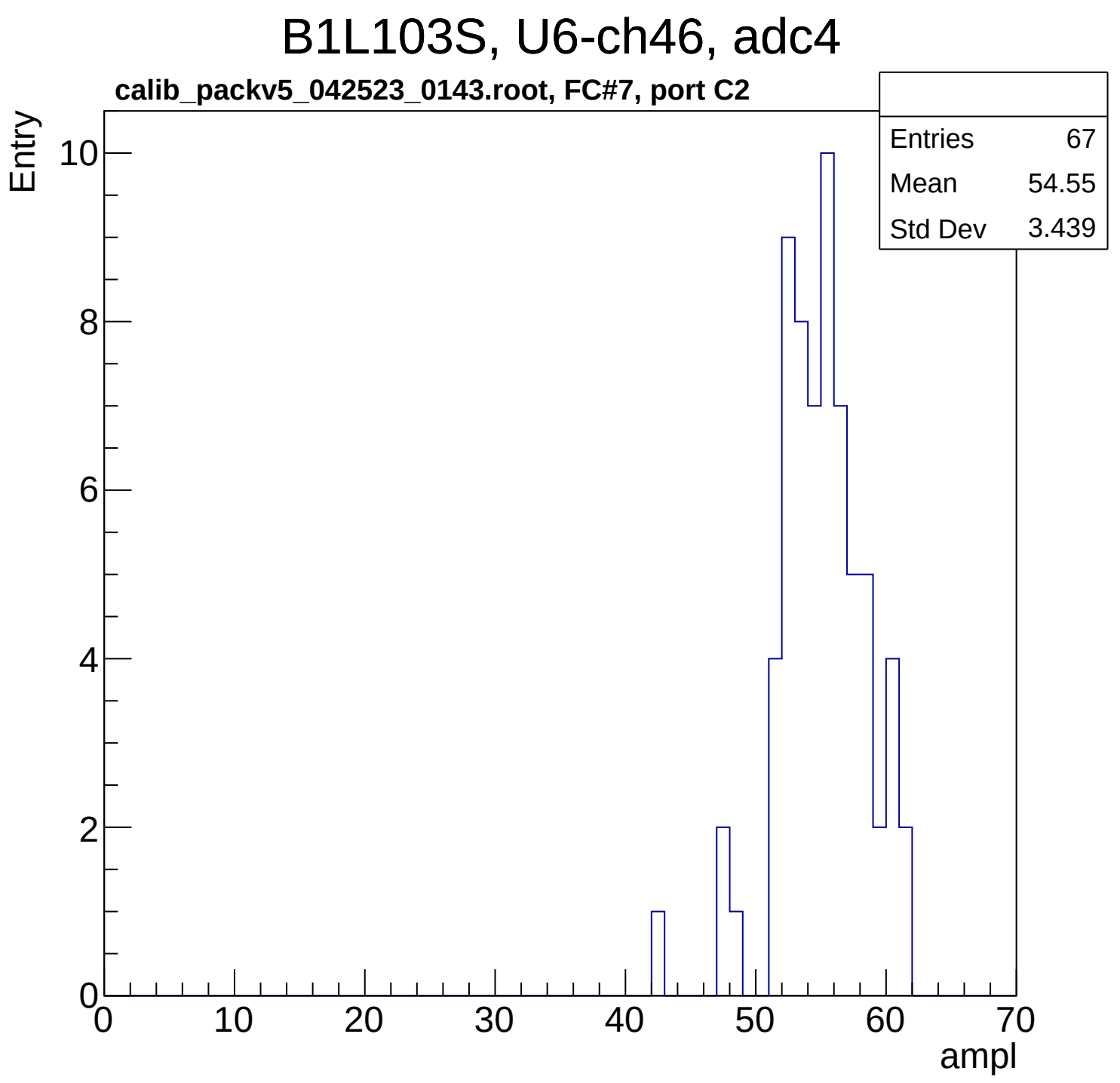
B1L103S, U6-ch46, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.55
Std Dev 3.439
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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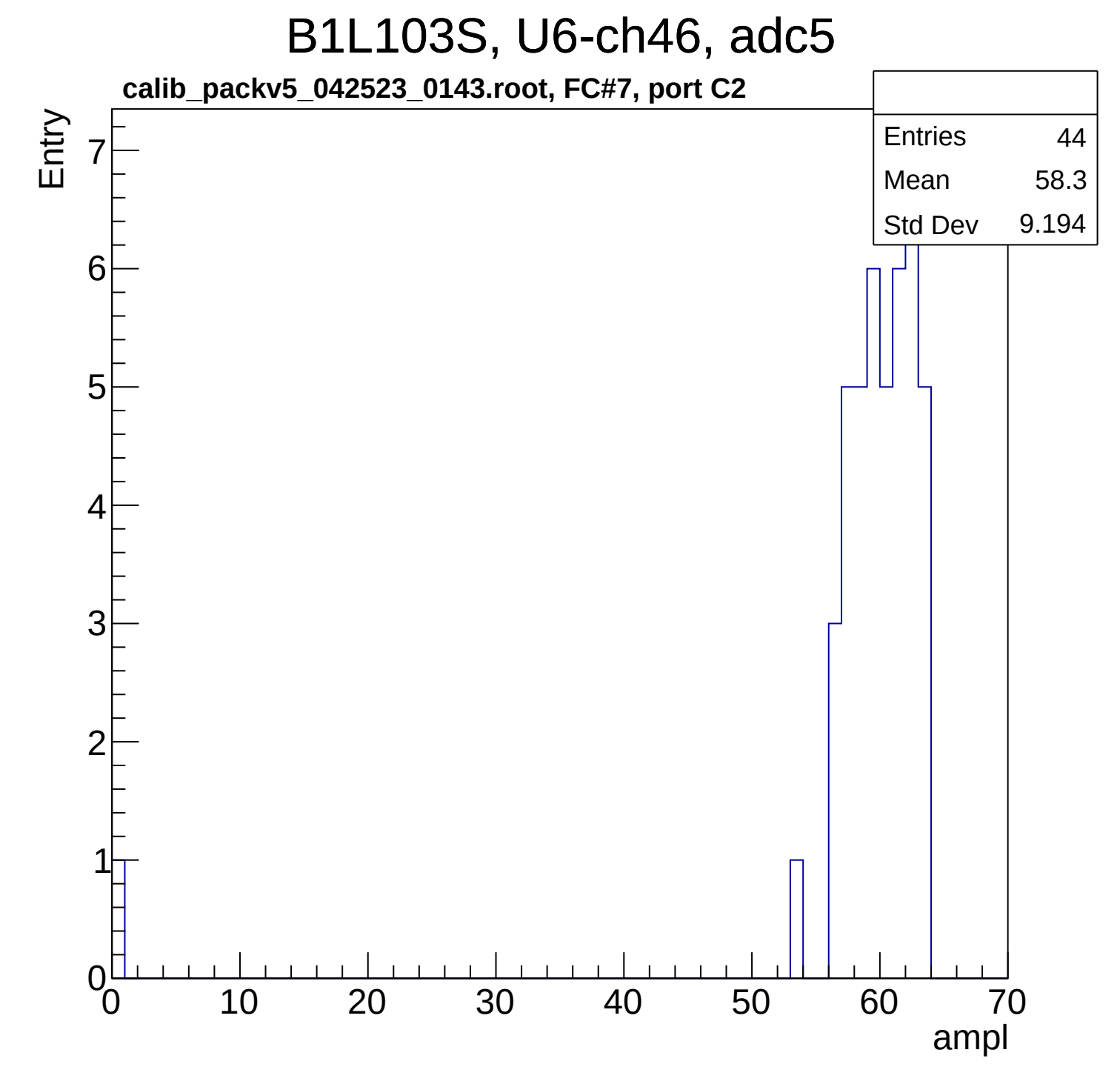
B1L103S, U6-ch46, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.3
Std Dev 9.194
Entry
ampl
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3
4
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6
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0
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20
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40
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60
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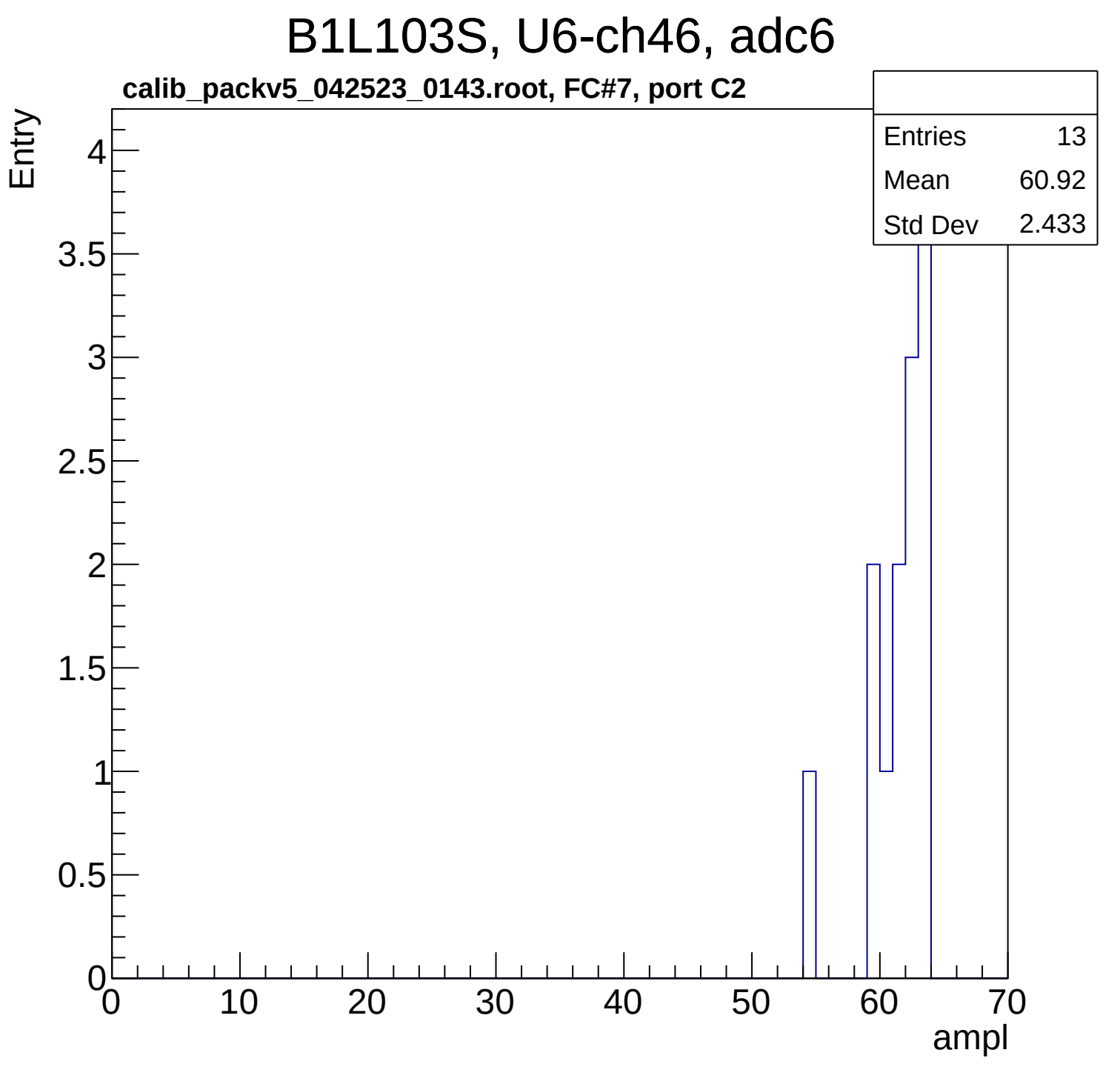
B1L103S, U6-ch46, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 60.92
Std Dev 2.433
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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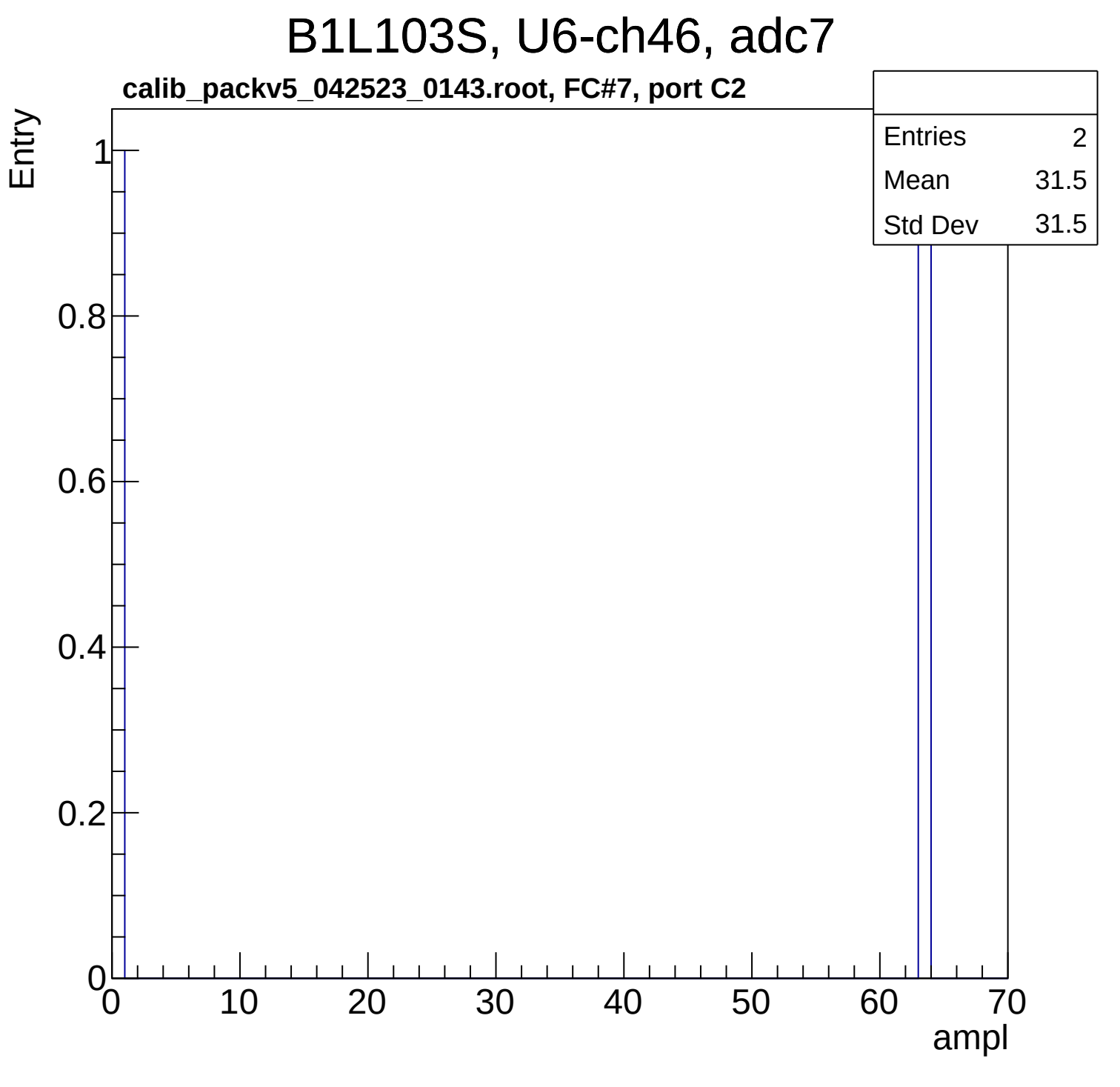
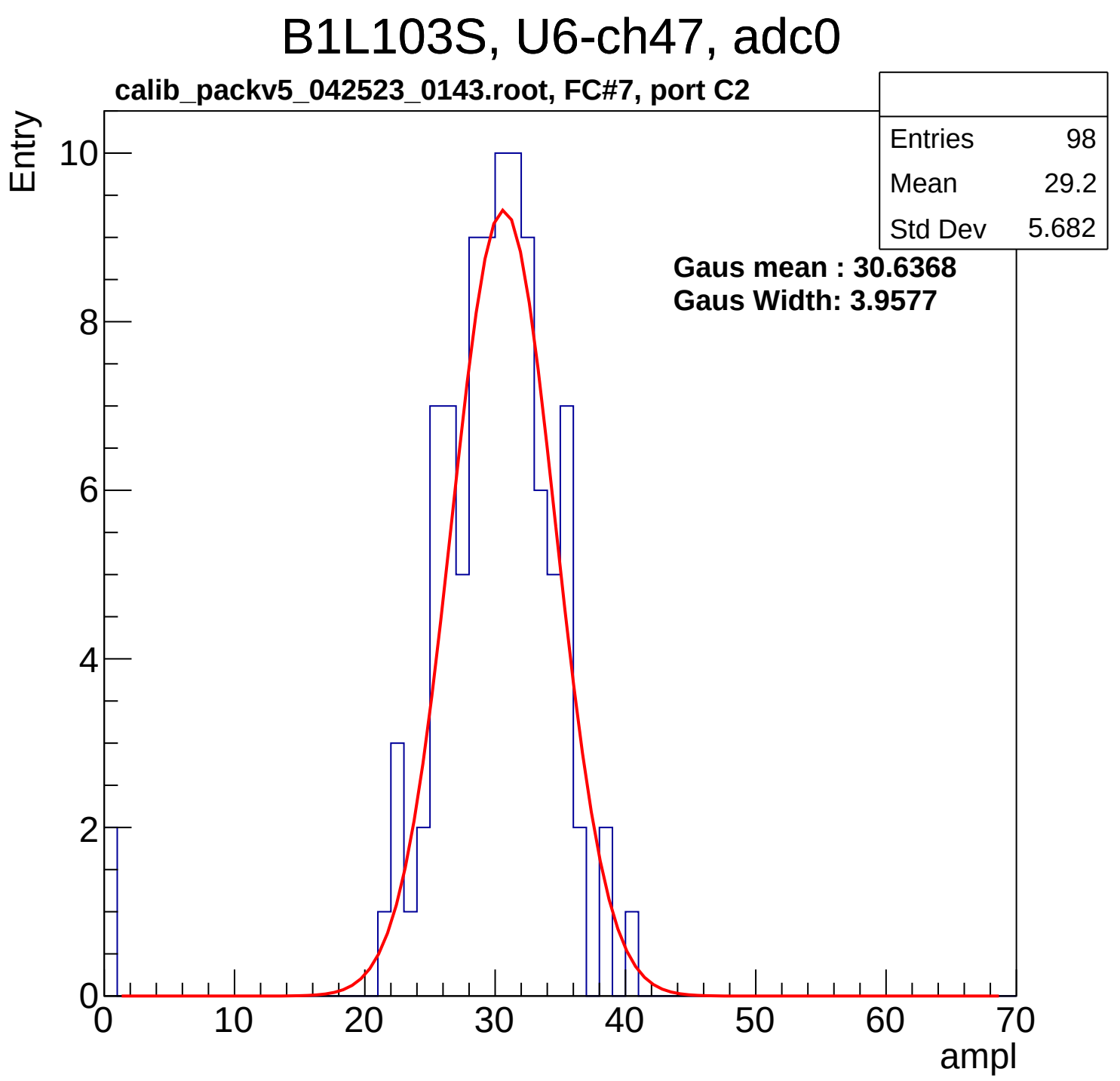
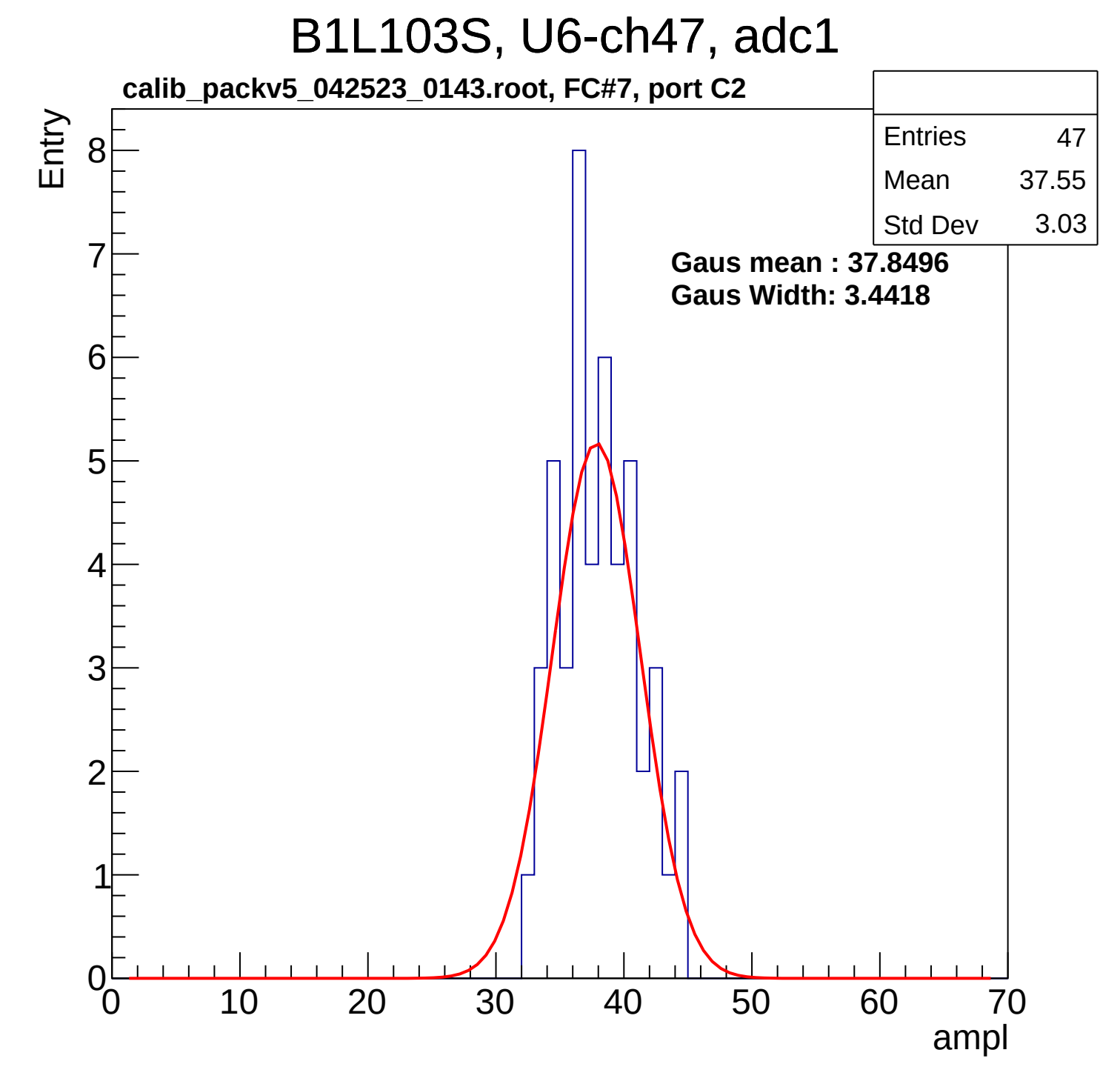

B1L103S, U6-ch46, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch47, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 98
Mean 29.2
Std Dev 5.682
Gaus mean : 30.6368
Gaus Width: 3.9577
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch47, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 37.55
Std Dev 3.03
Gaus mean : 37.8496
Gaus Width: 3.4418
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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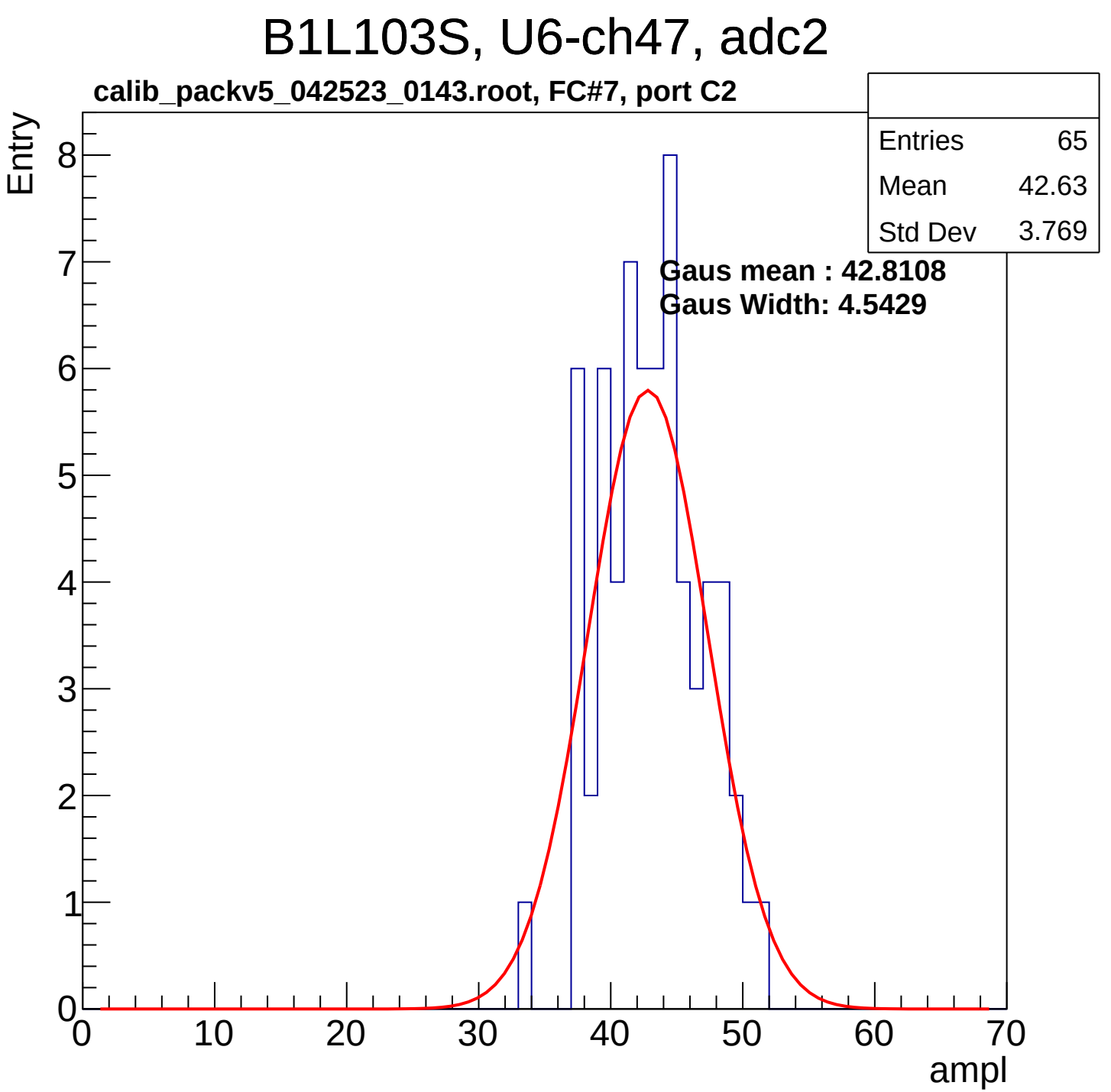
B1L103S, U6-ch47, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.63
Std Dev 3.769
Gaus mean : 42.8108
Gaus Width: 4.5429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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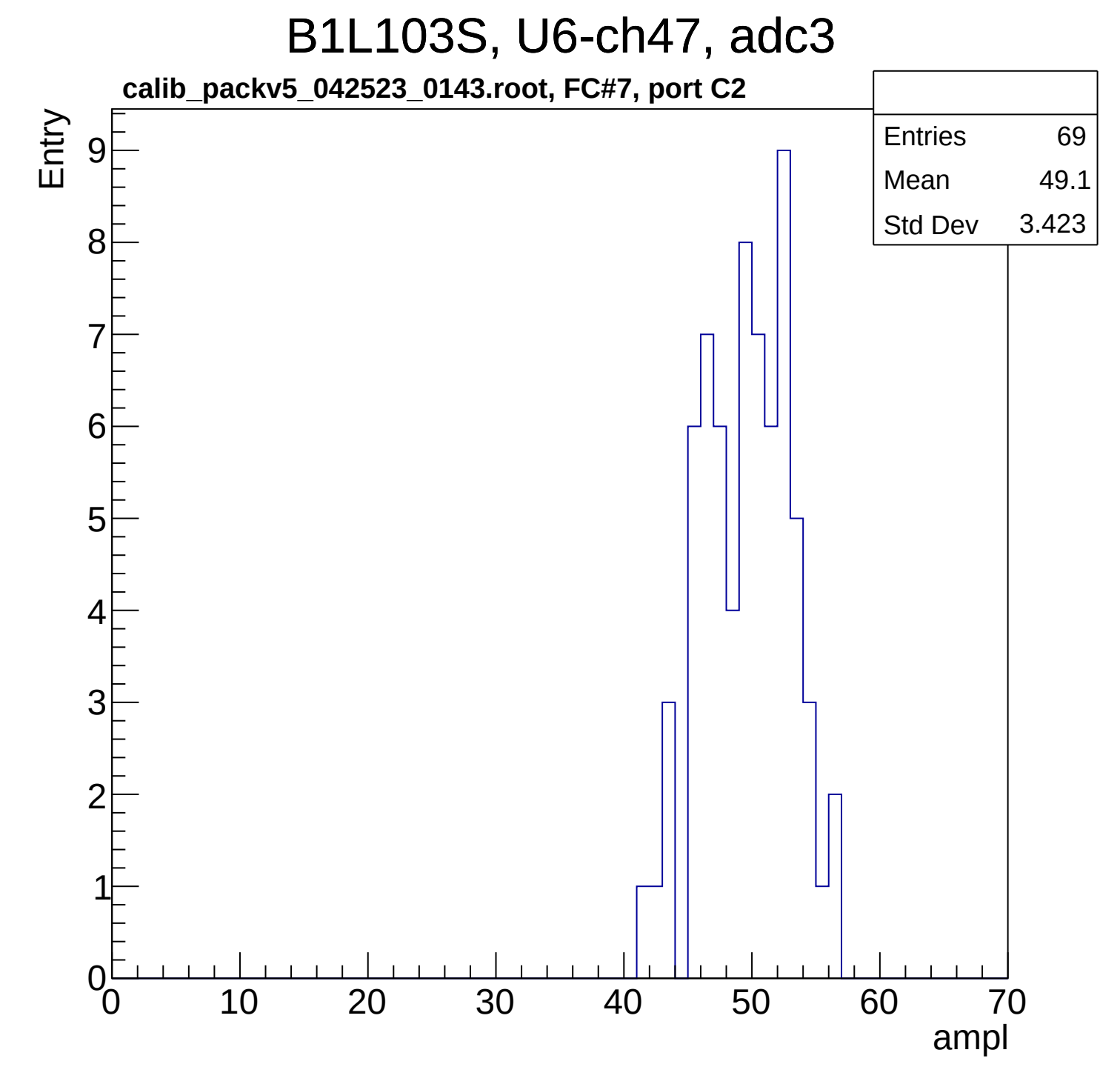
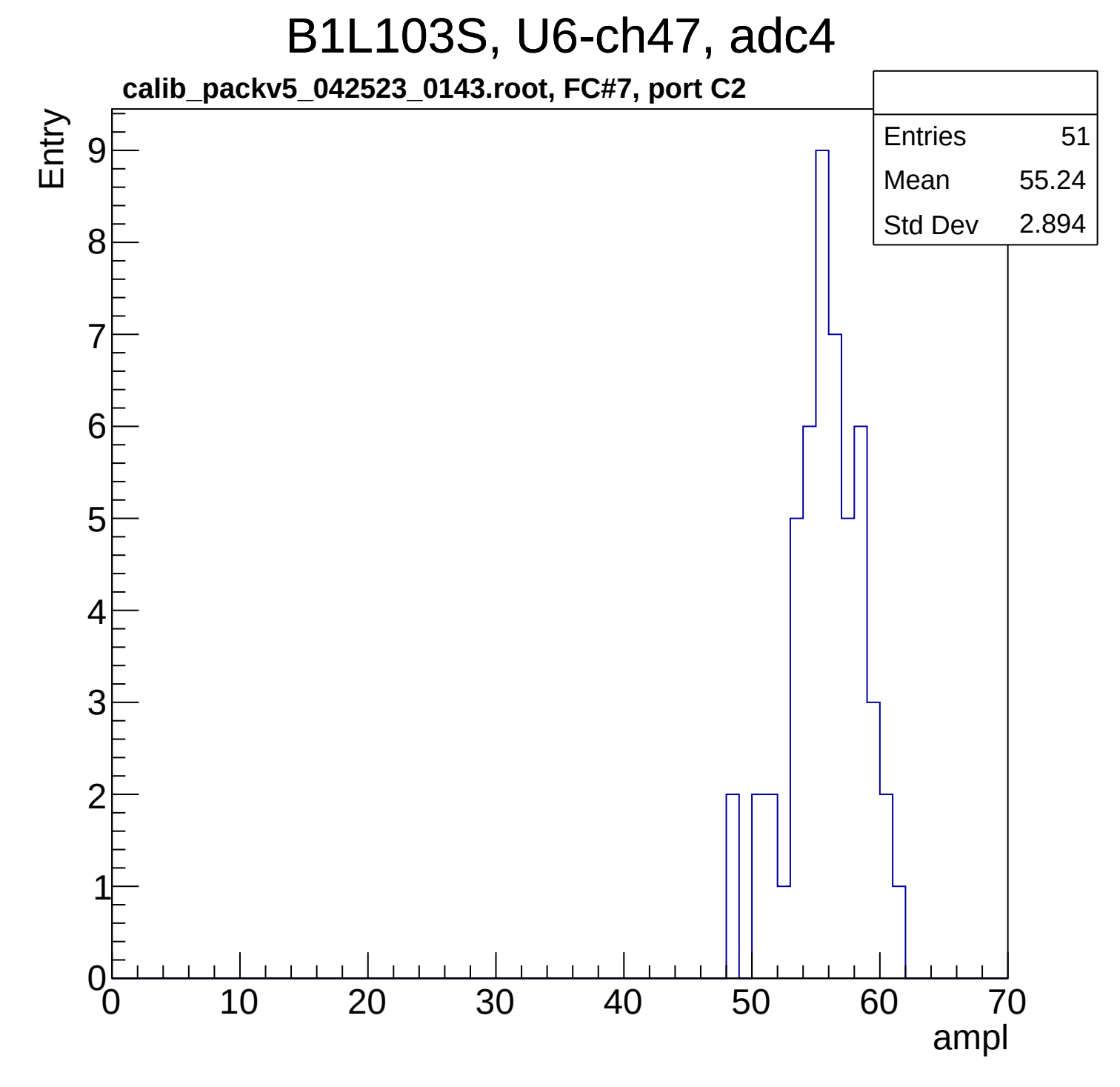

B1L103S, U6-ch47, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.1
Std Dev 3.423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch47, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.24
Std Dev 2.894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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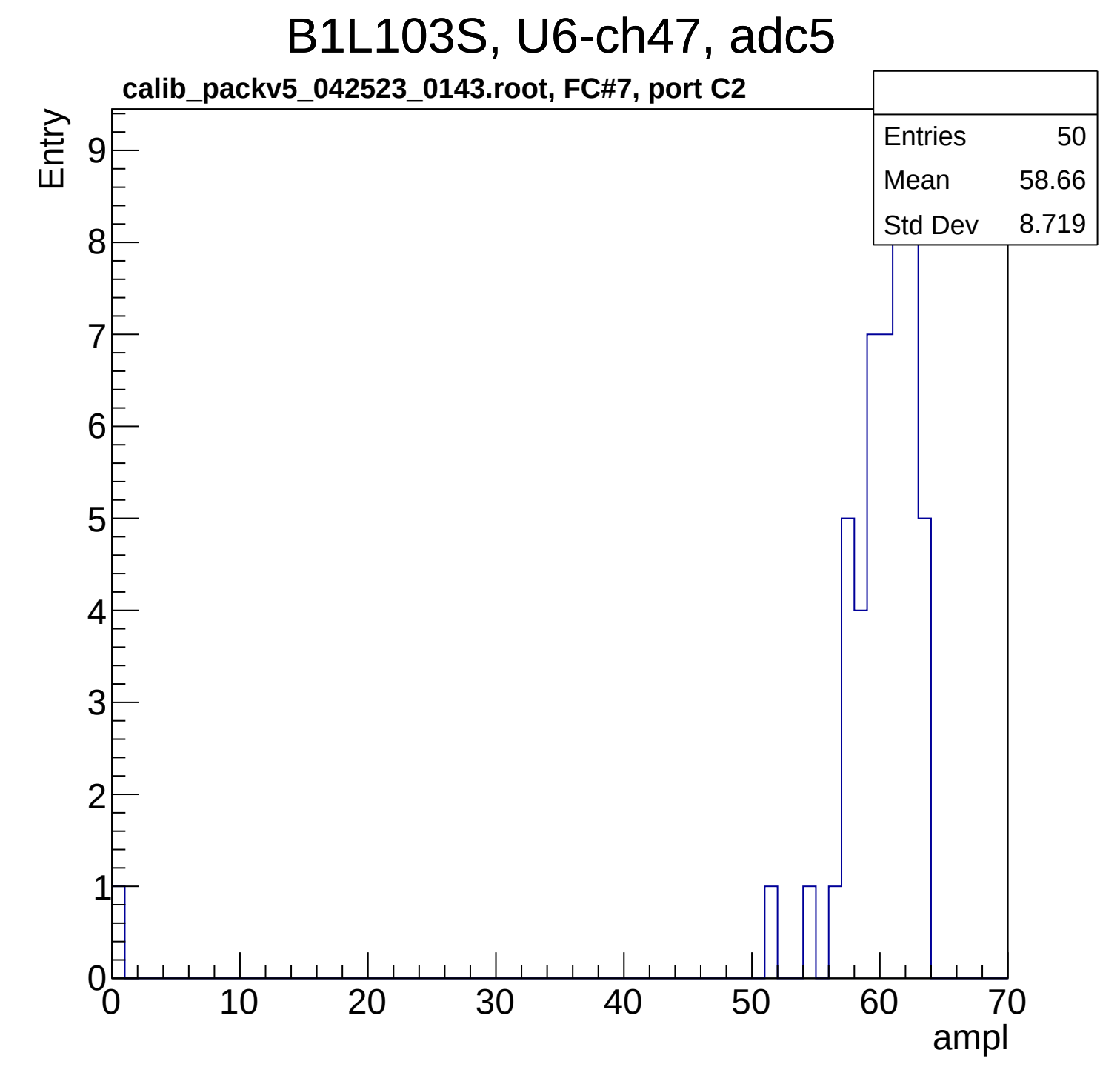
B1L103S, U6-ch47, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.66
Std Dev 8.719
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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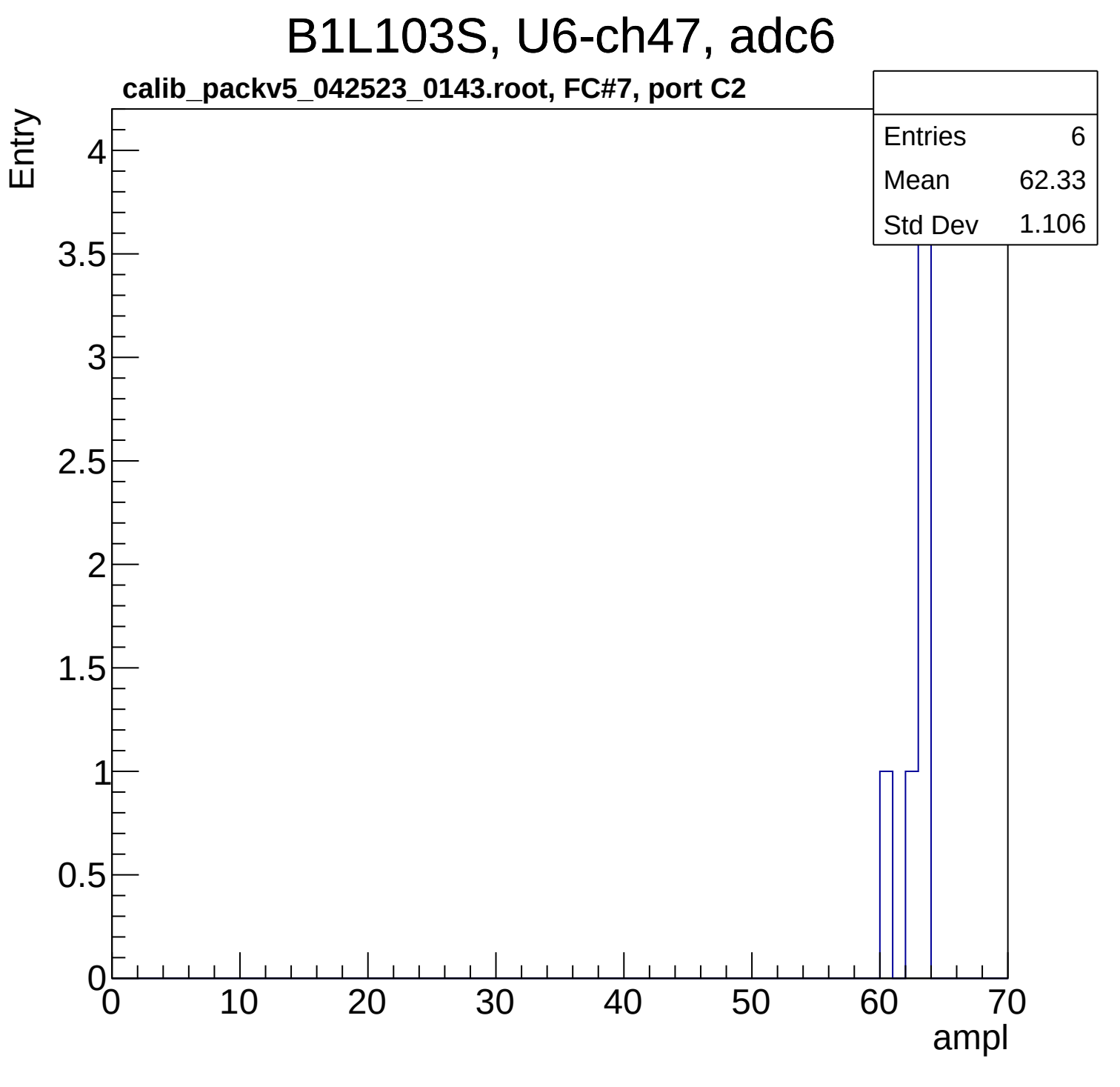
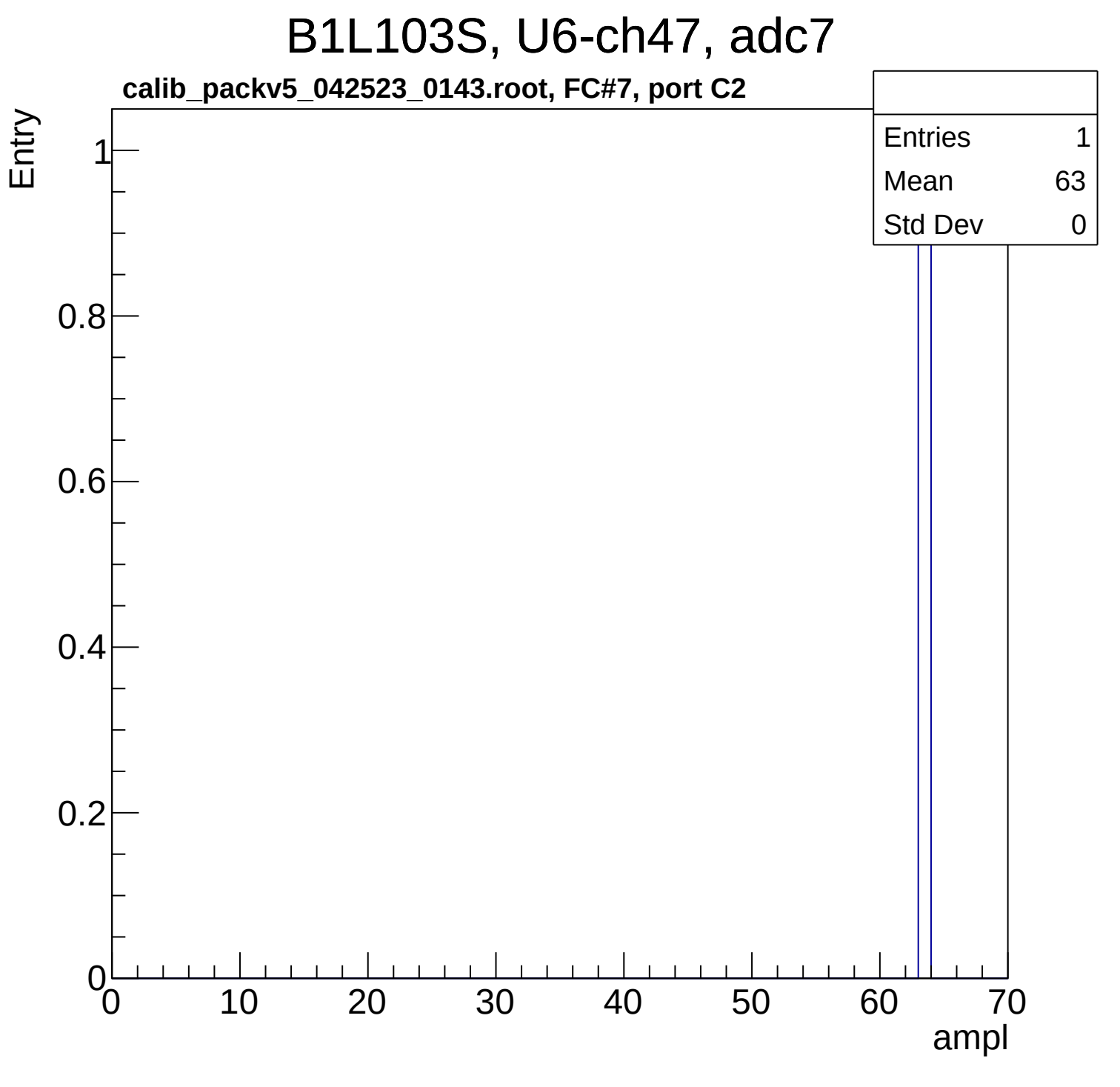
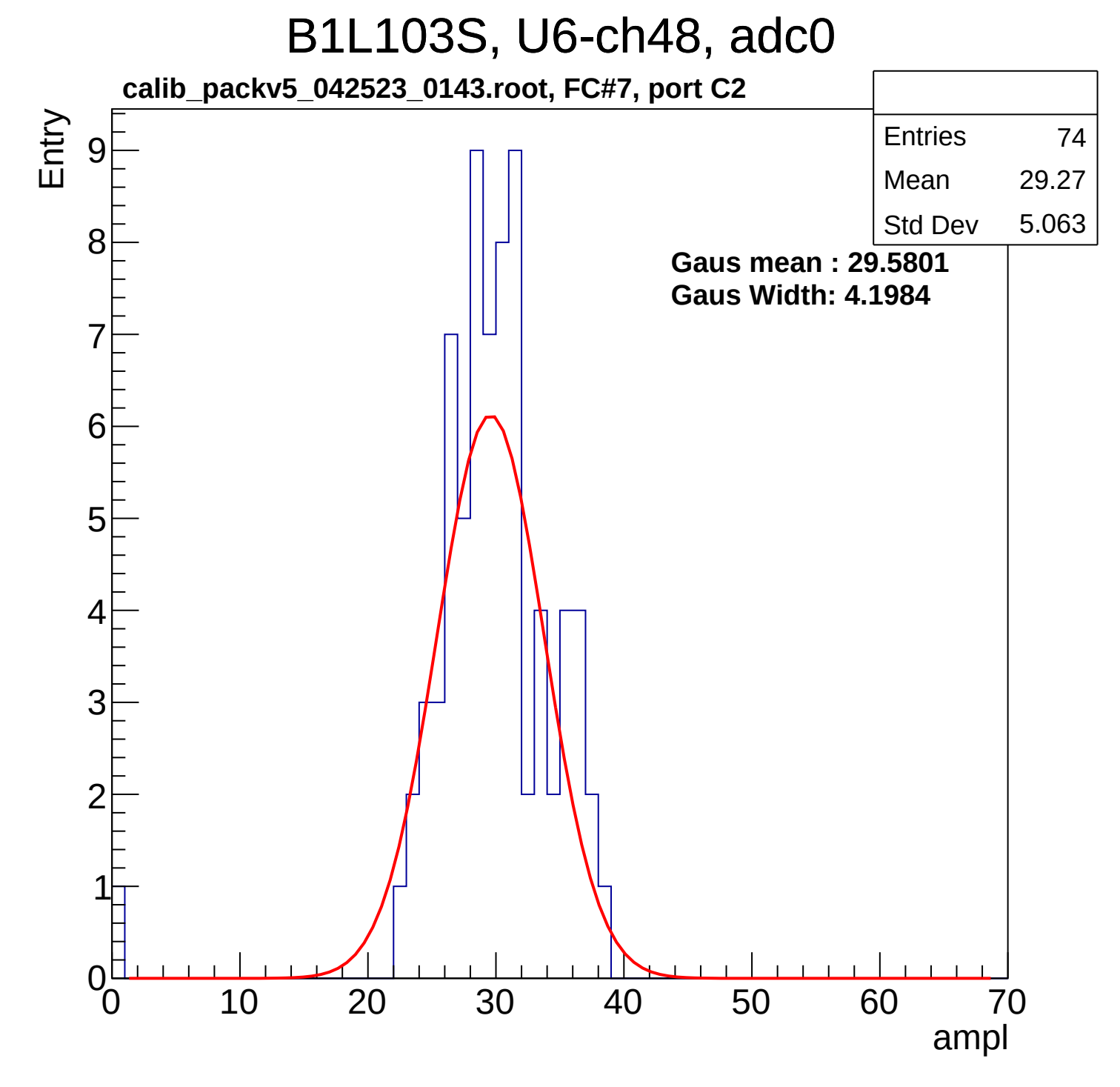
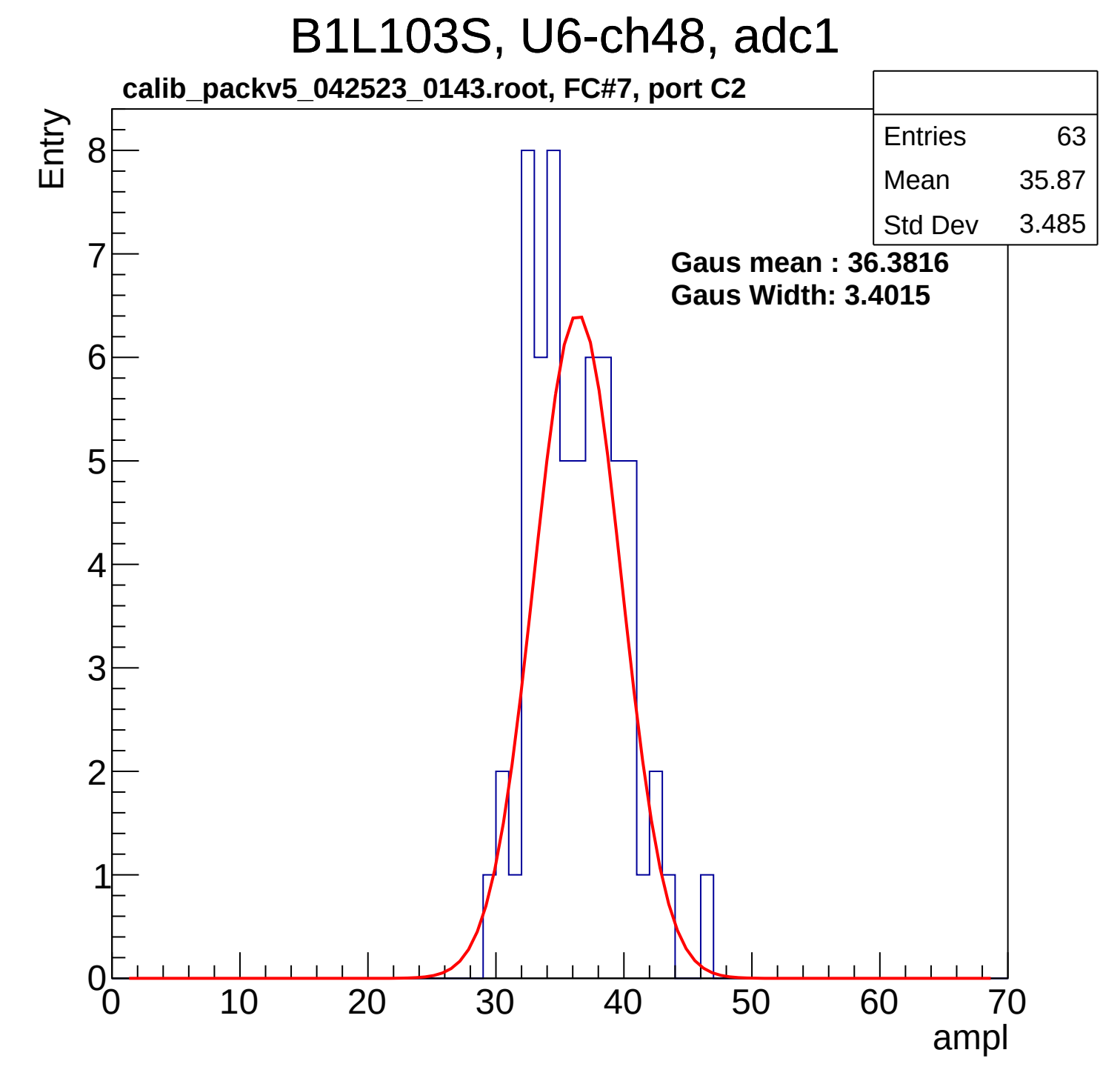

B1L103S, U6-ch47, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch47, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch48, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.27
Std Dev 5.063
Gaus mean : 29.5801
Gaus Width: 4.1984
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch48, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.87
Std Dev 3.485
Gaus mean : 36.3816
Gaus Width: 3.4015
Entry
ampl
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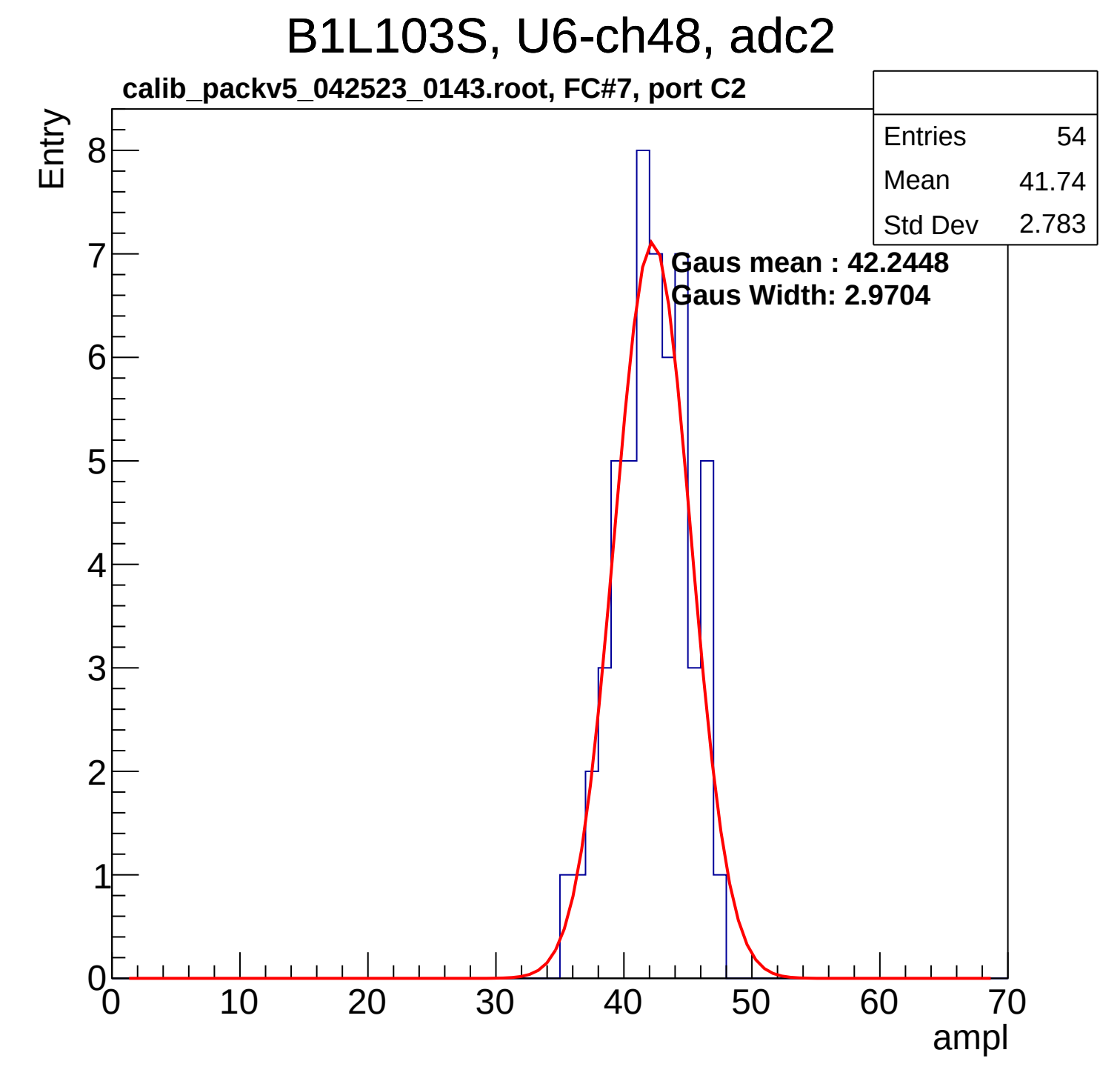
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch48, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 41.74
Std Dev 2.783
Gaus mean : 42.2448
Gaus Width: 2.9704
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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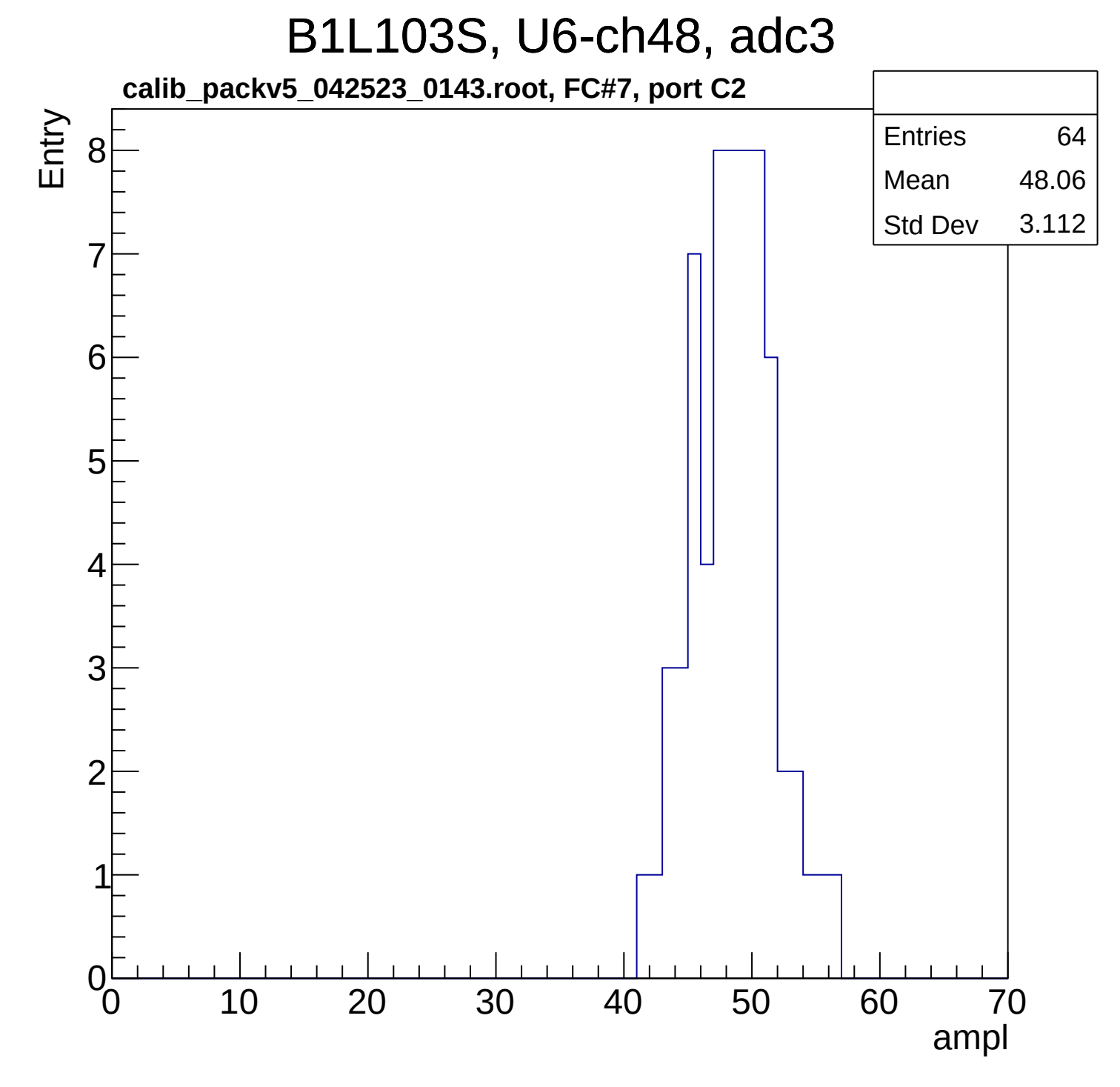
B1L103S, U6-ch48, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.06
Std Dev 3.112
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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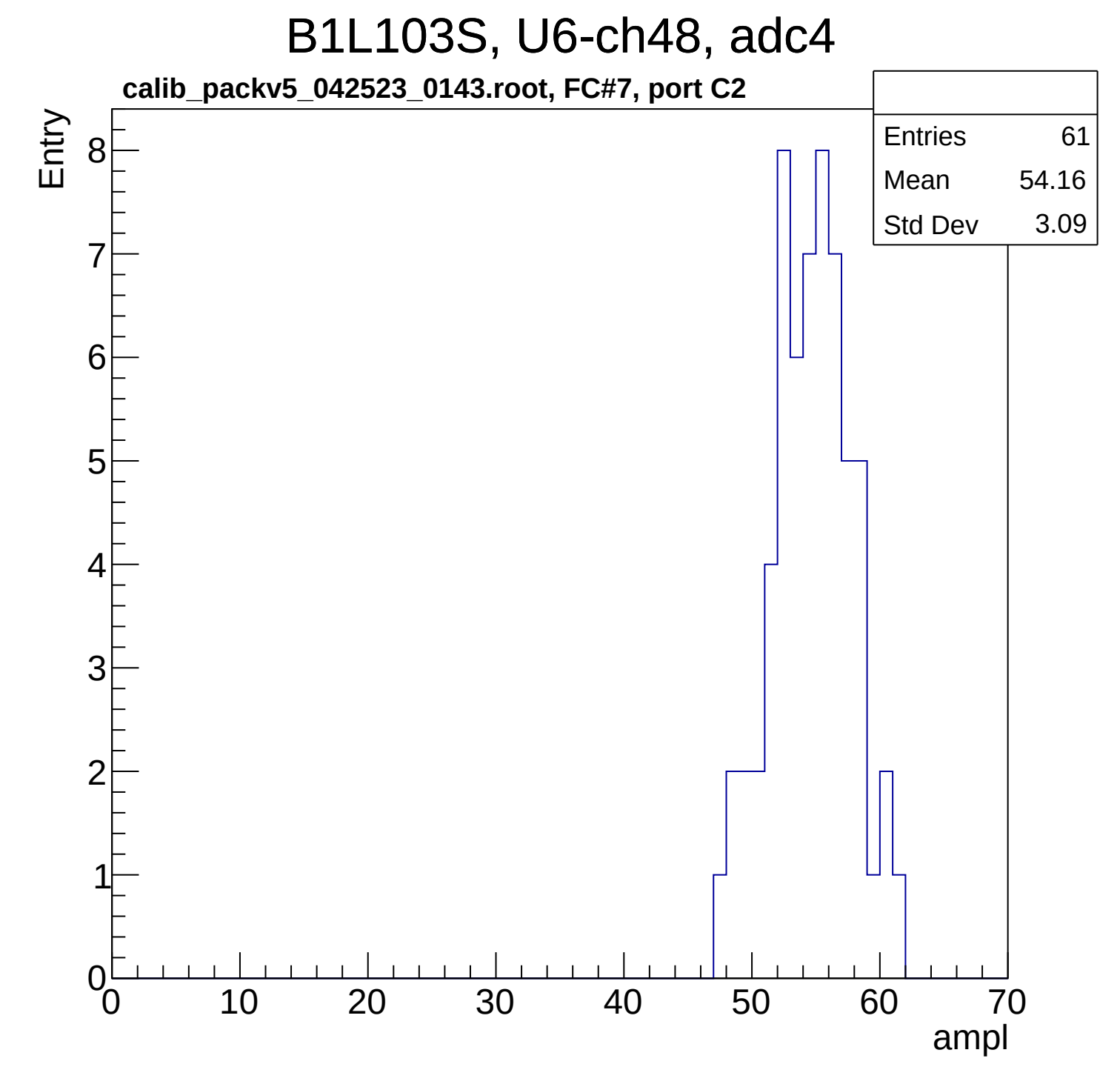
B1L103S, U6-ch48, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 54.16
Std Dev 3.09
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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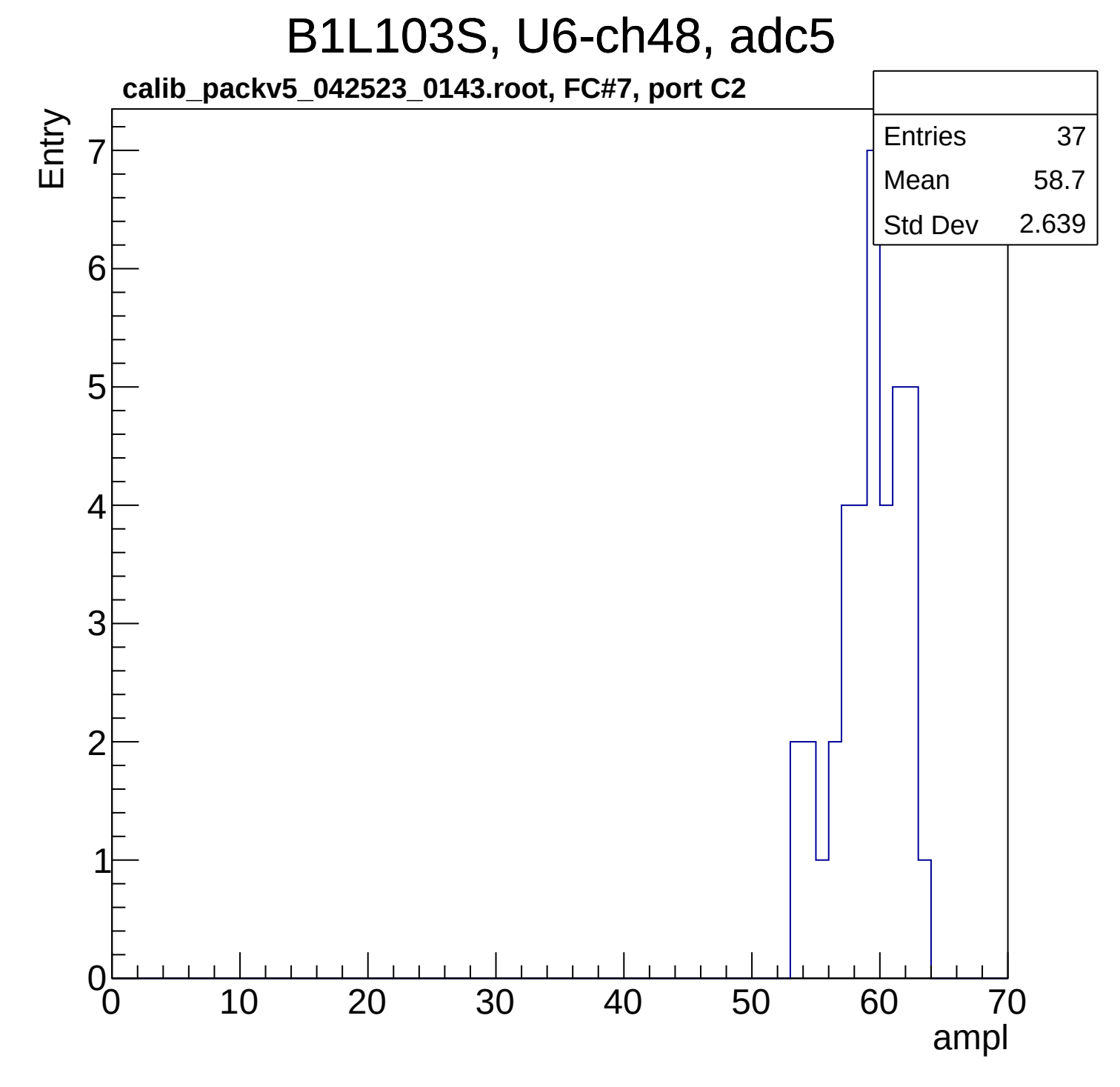
B1L103S, U6-ch48, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 58.7
Std Dev 2.639
Entry
ampl
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1
2
3
4
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6
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0
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60
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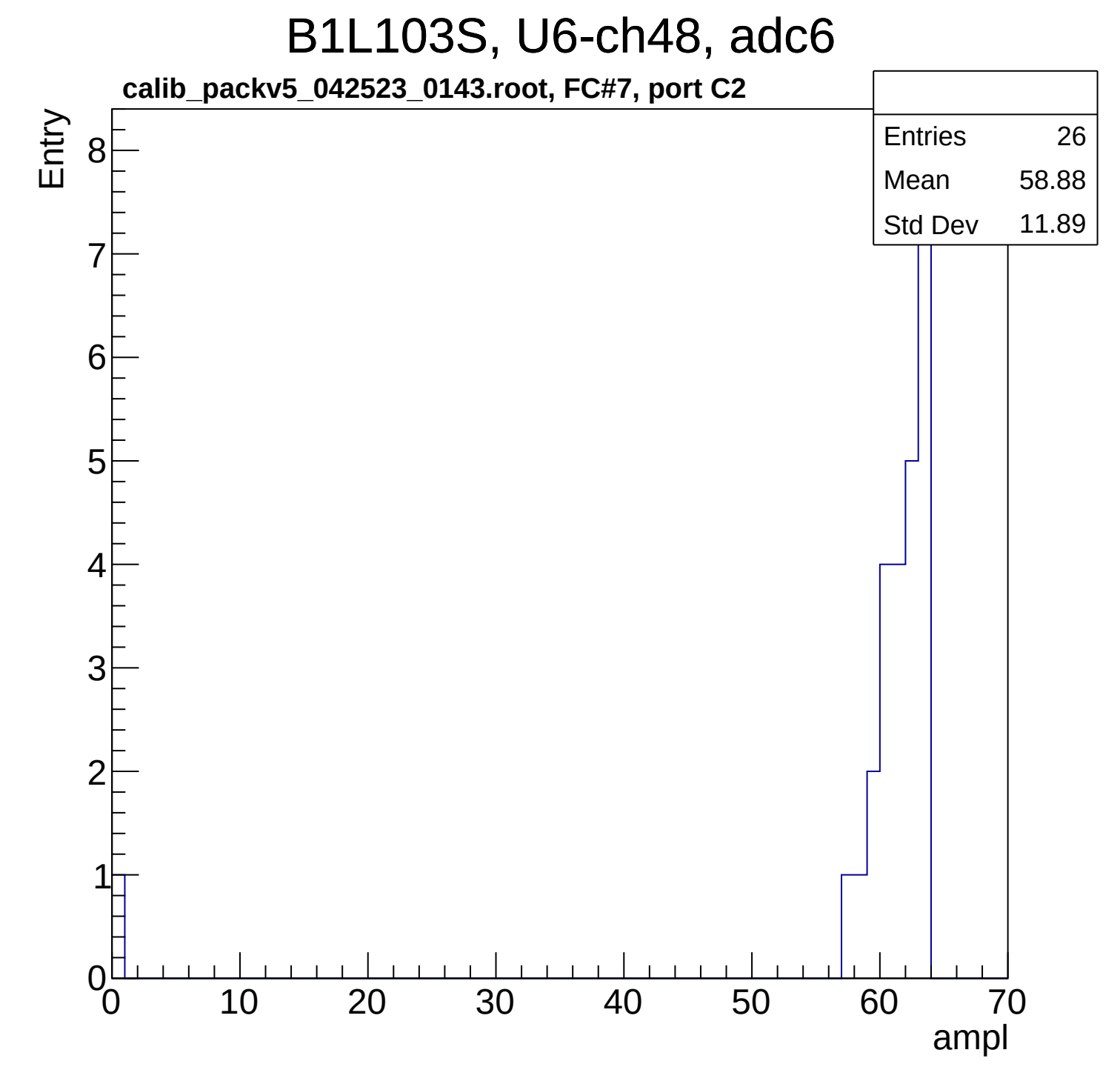
B1L103S, U6-ch48, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 58.88
Std Dev 11.89
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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50
60
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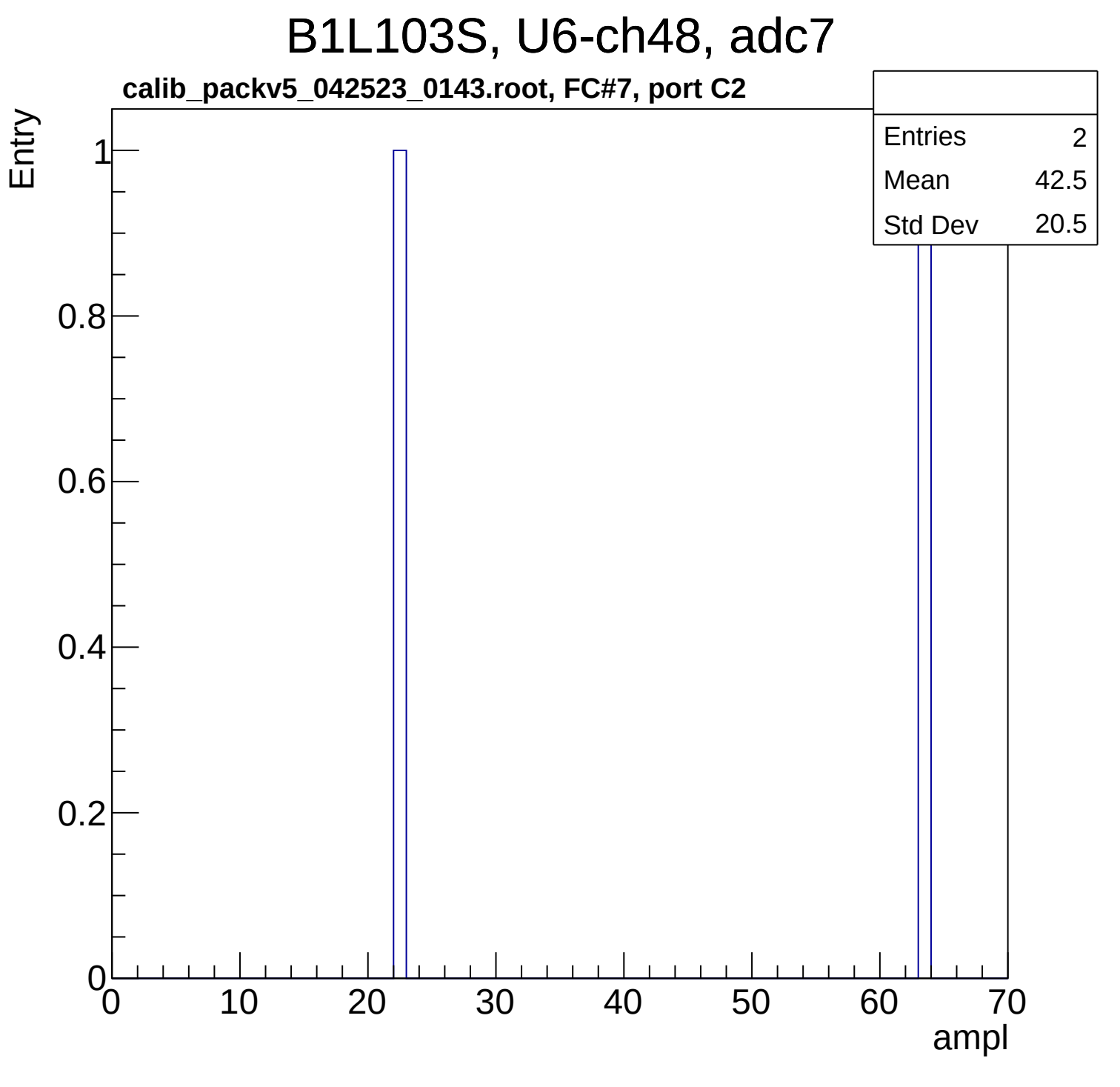
B1L103S, U6-ch48, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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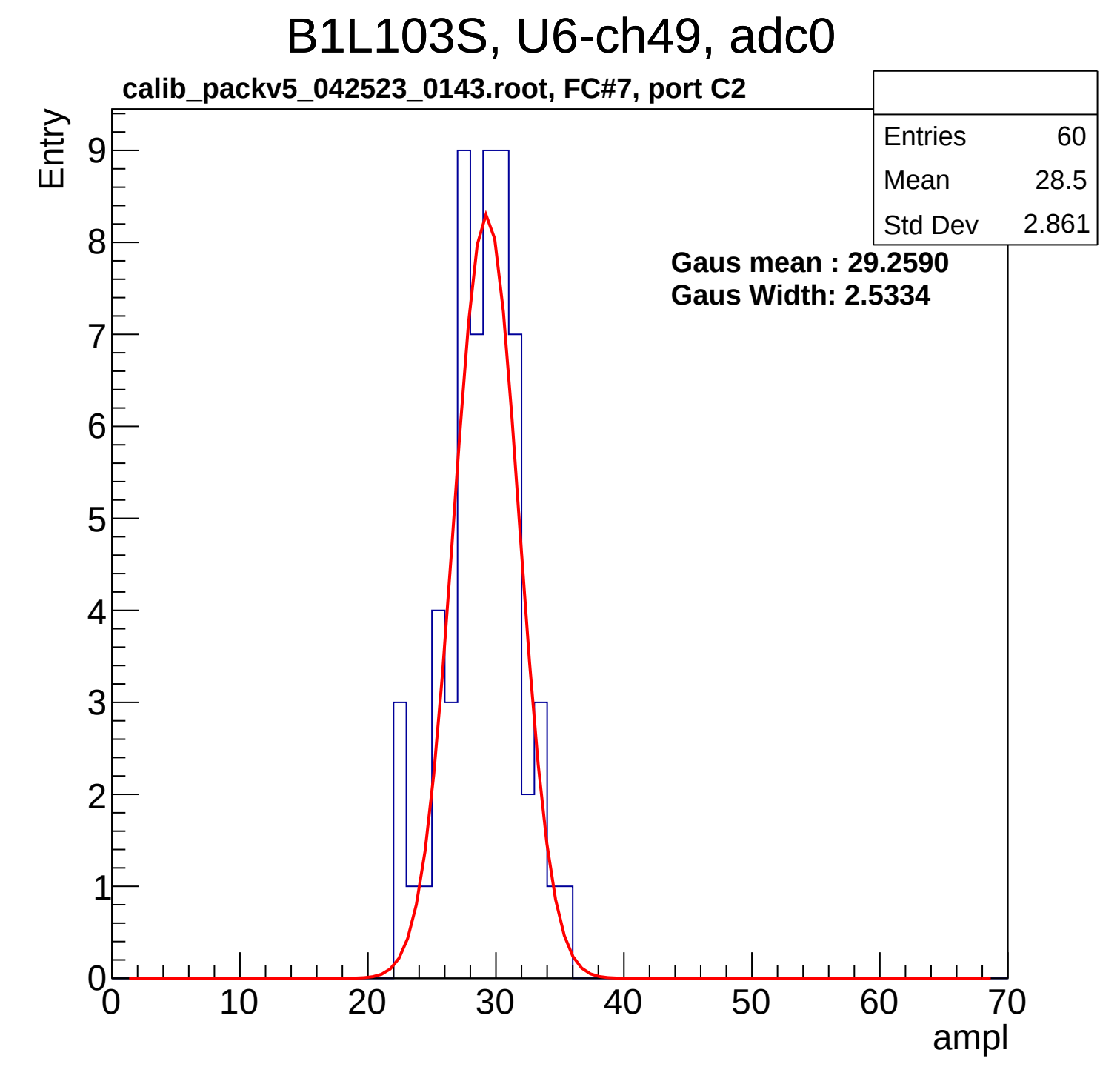
B1L103S, U6-ch49, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 28.5
Std Dev 2.861
Gaus mean : 29.2590
Gaus Width: 2.5334
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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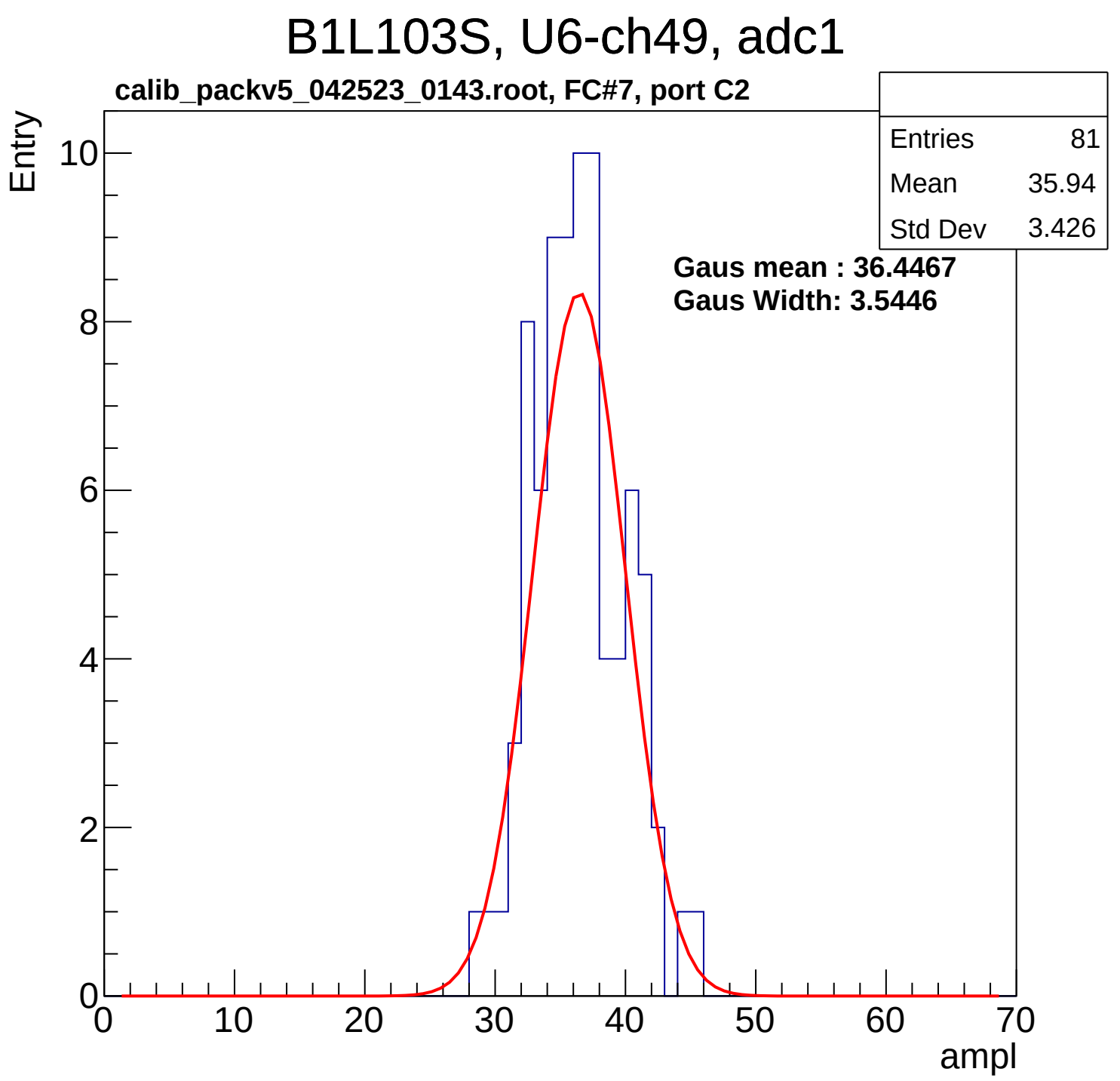
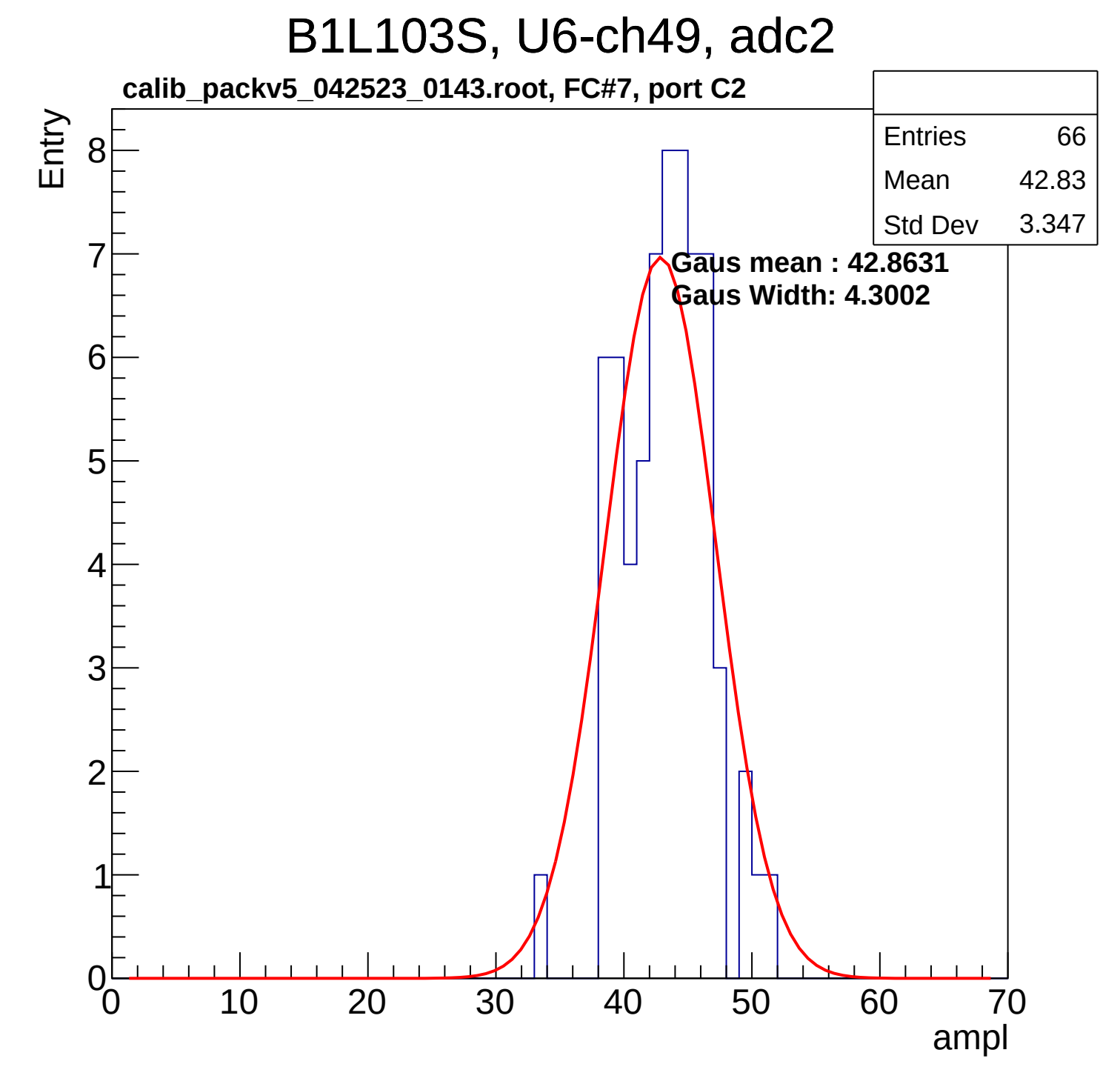

B1L103S, U6-ch49, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 35.94
Std Dev 3.426
Gaus mean : 36.4467
Gaus Width: 3.5446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch49, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.83
Std Dev 3.347
Gaus mean : 42.8631
Gaus Width: 4.3002
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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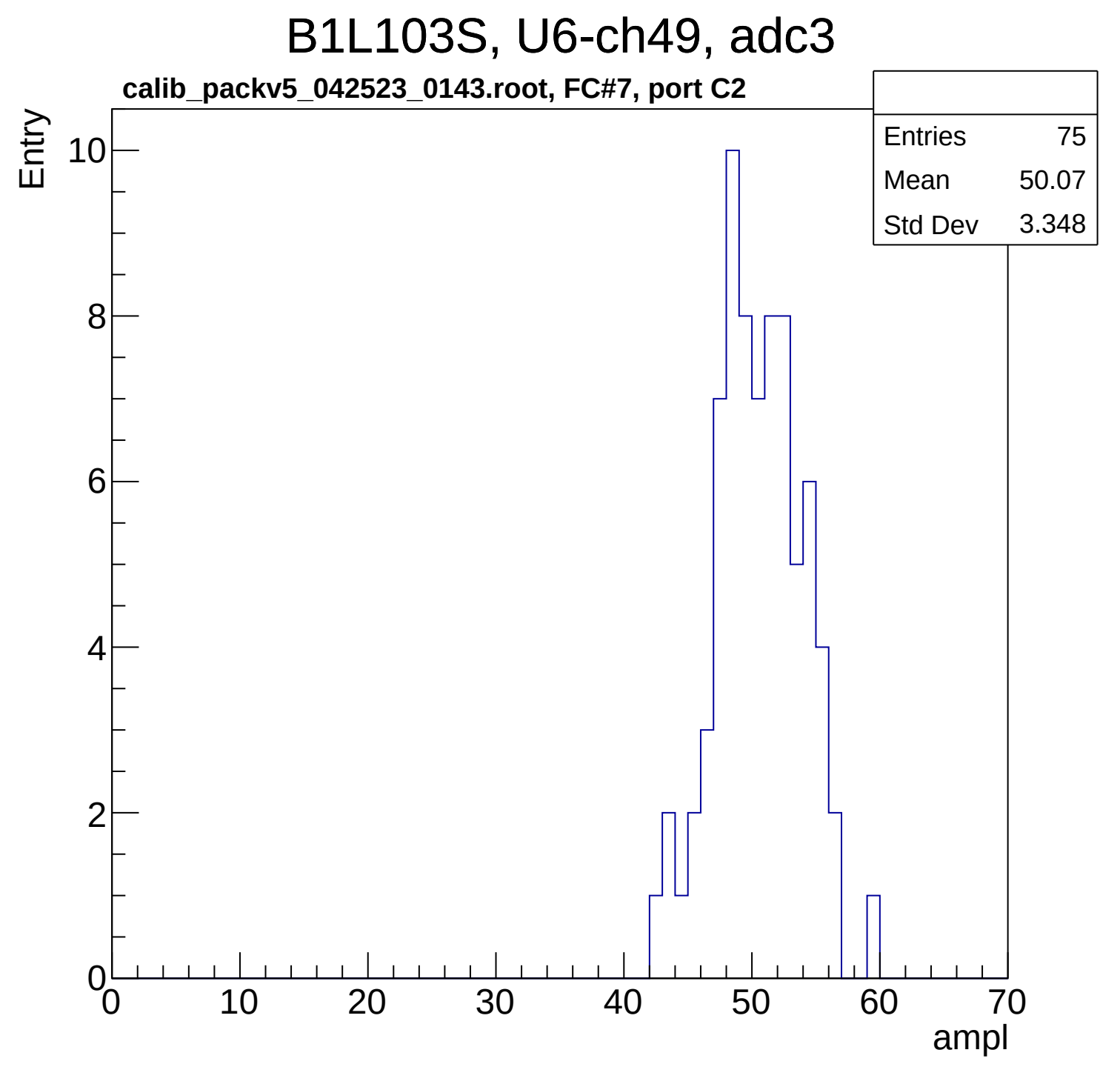
B1L103S, U6-ch49, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 50.07
Std Dev 3.348
Entry
ampl
0
2
4
6
8
10
0
10
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40
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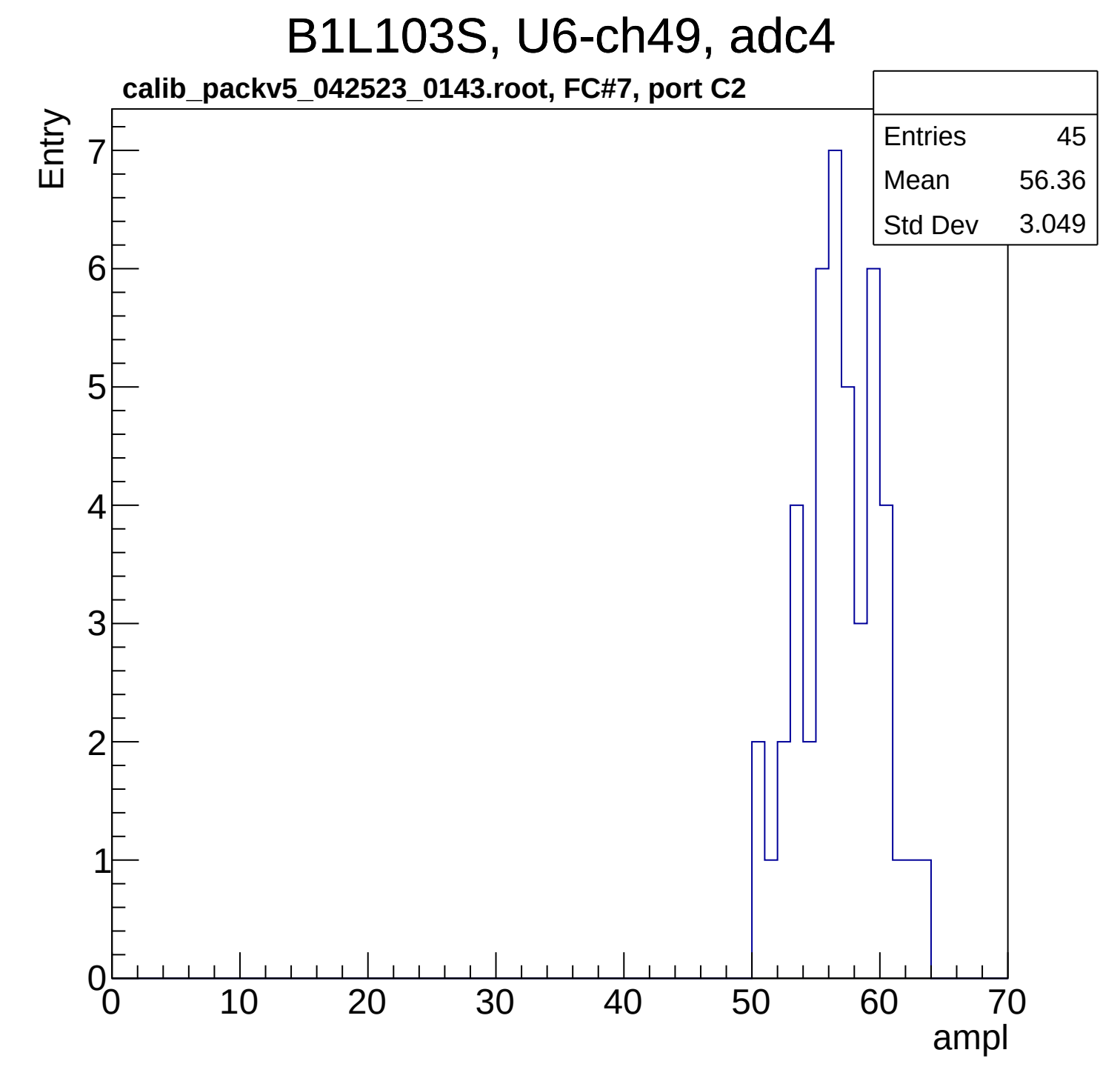
60
70


B1L103S, U6-ch49, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 56.36
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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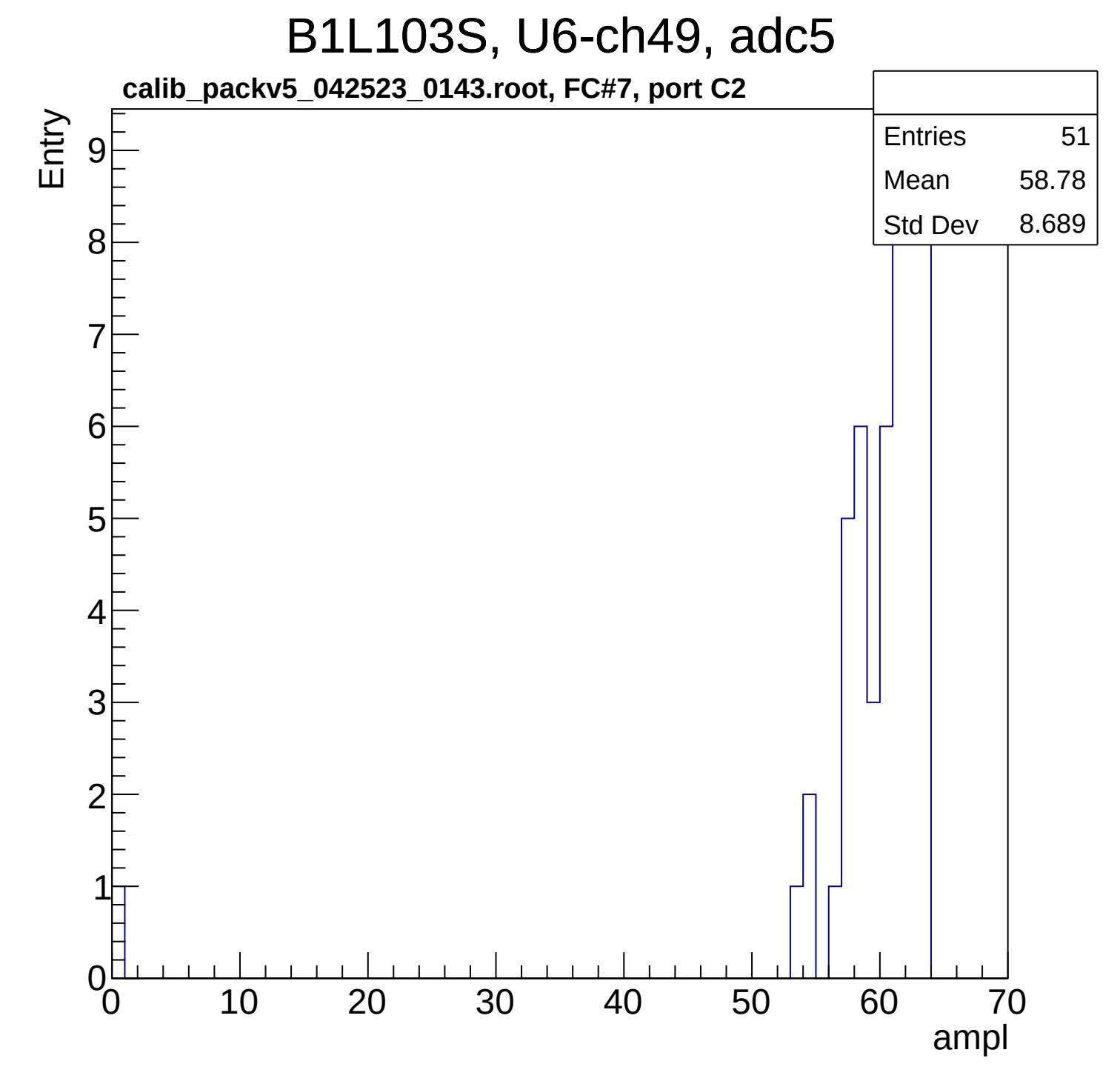
B1L103S, U6-ch49, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.78
Std Dev 8.689
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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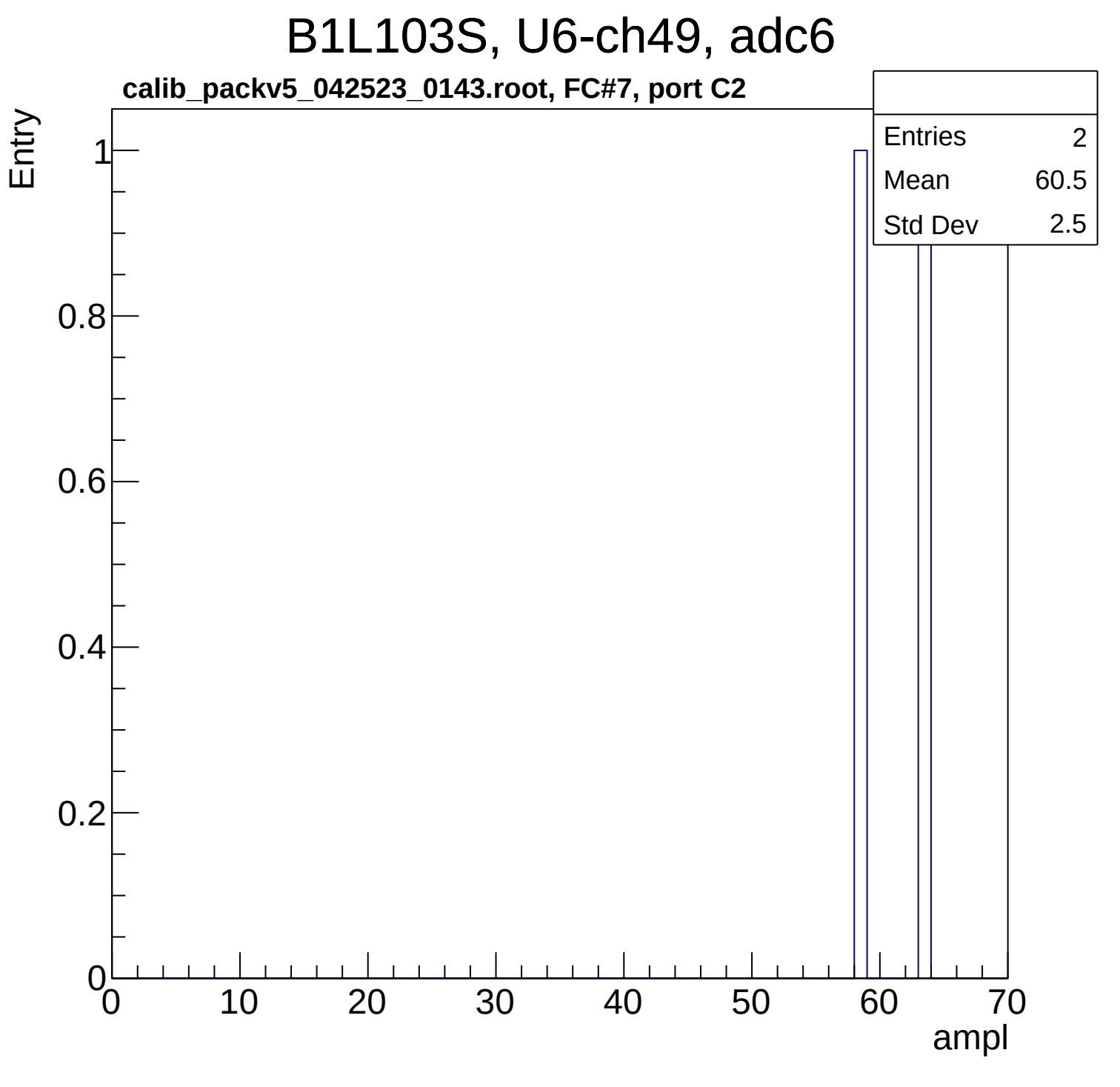
B1L103S, U6-ch49, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
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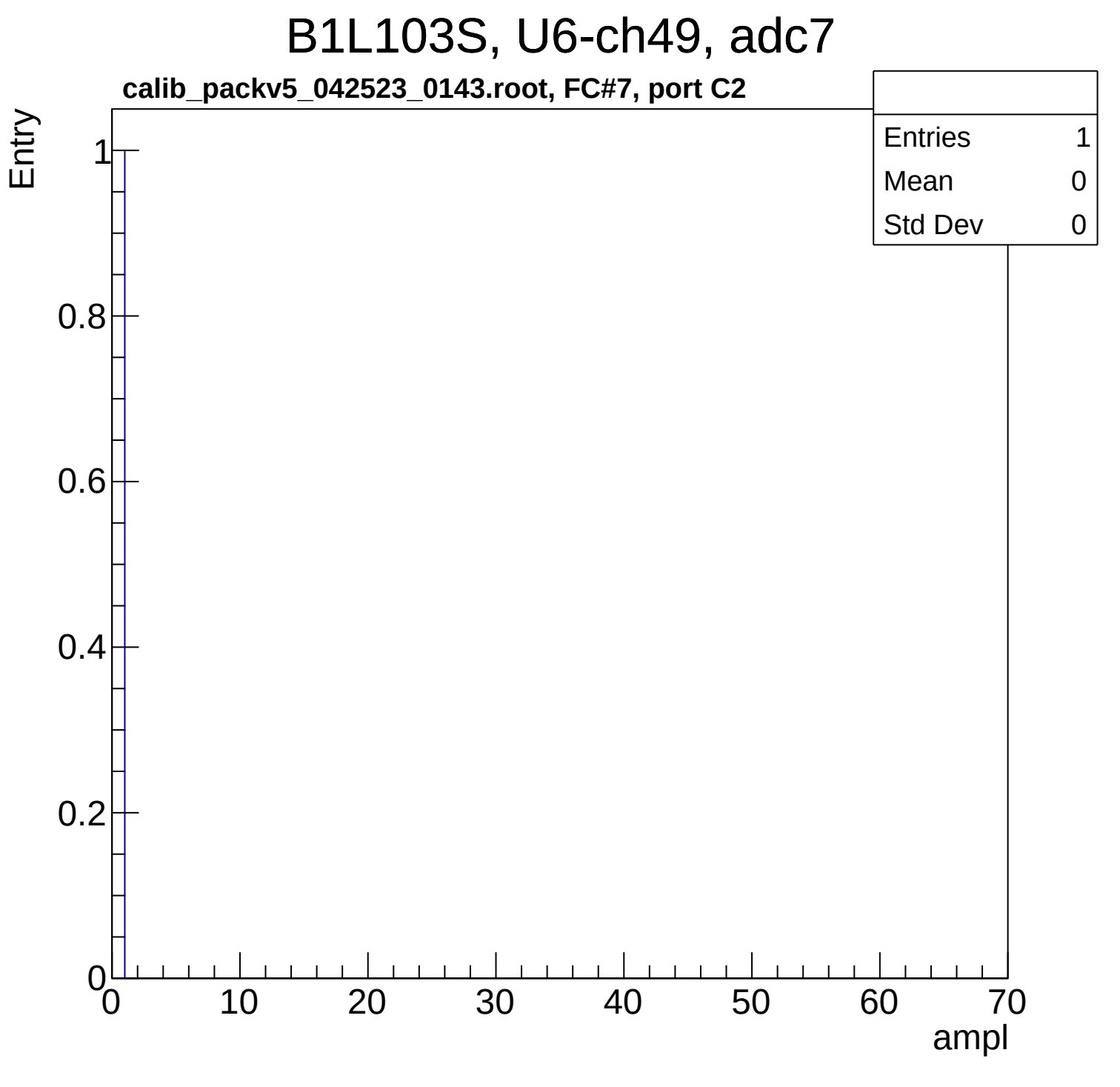
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch49, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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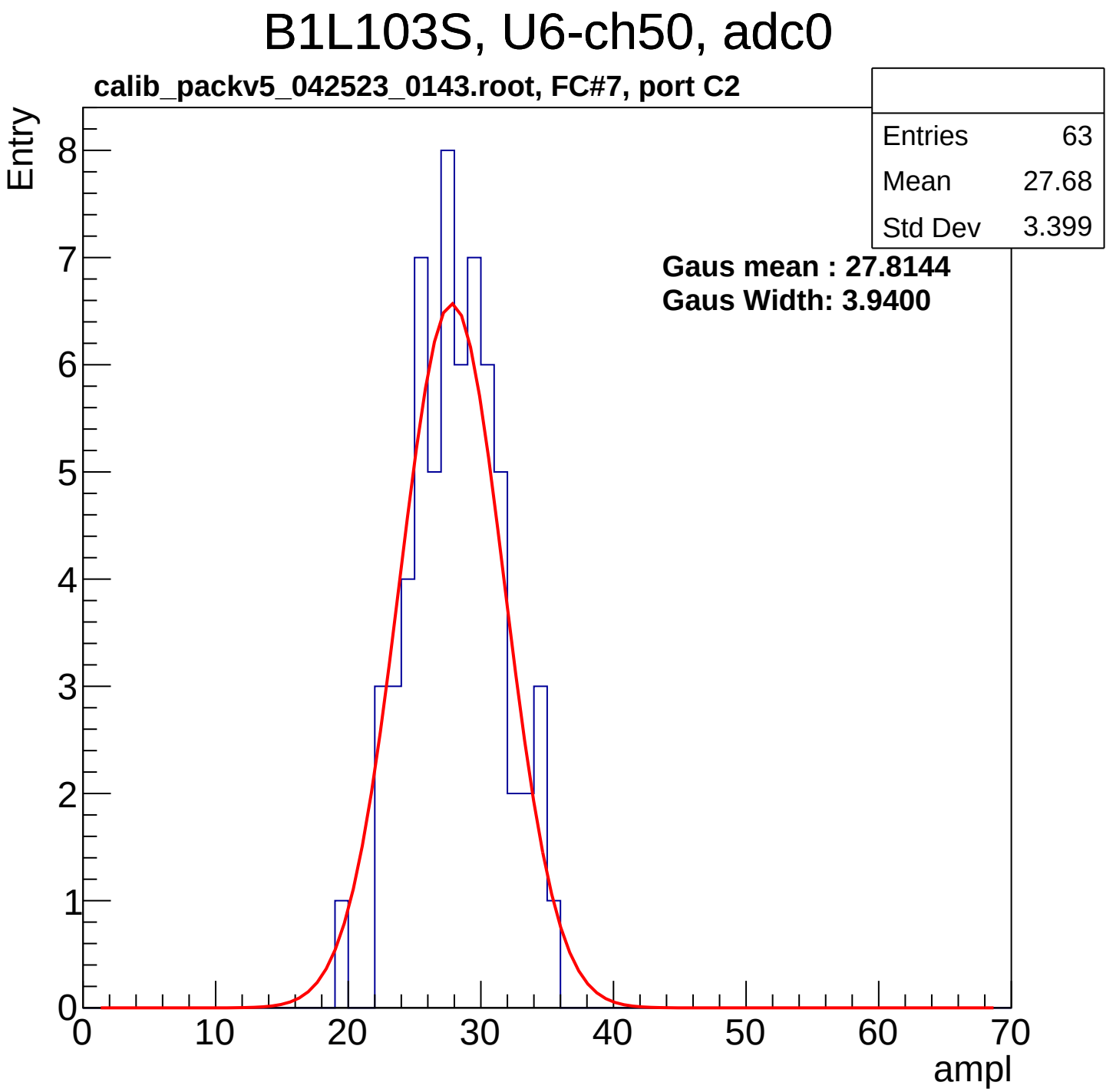
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch50, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 27.68
Std Dev 3.399
Gaus mean : 27.8144
Gaus Width: 3.9400
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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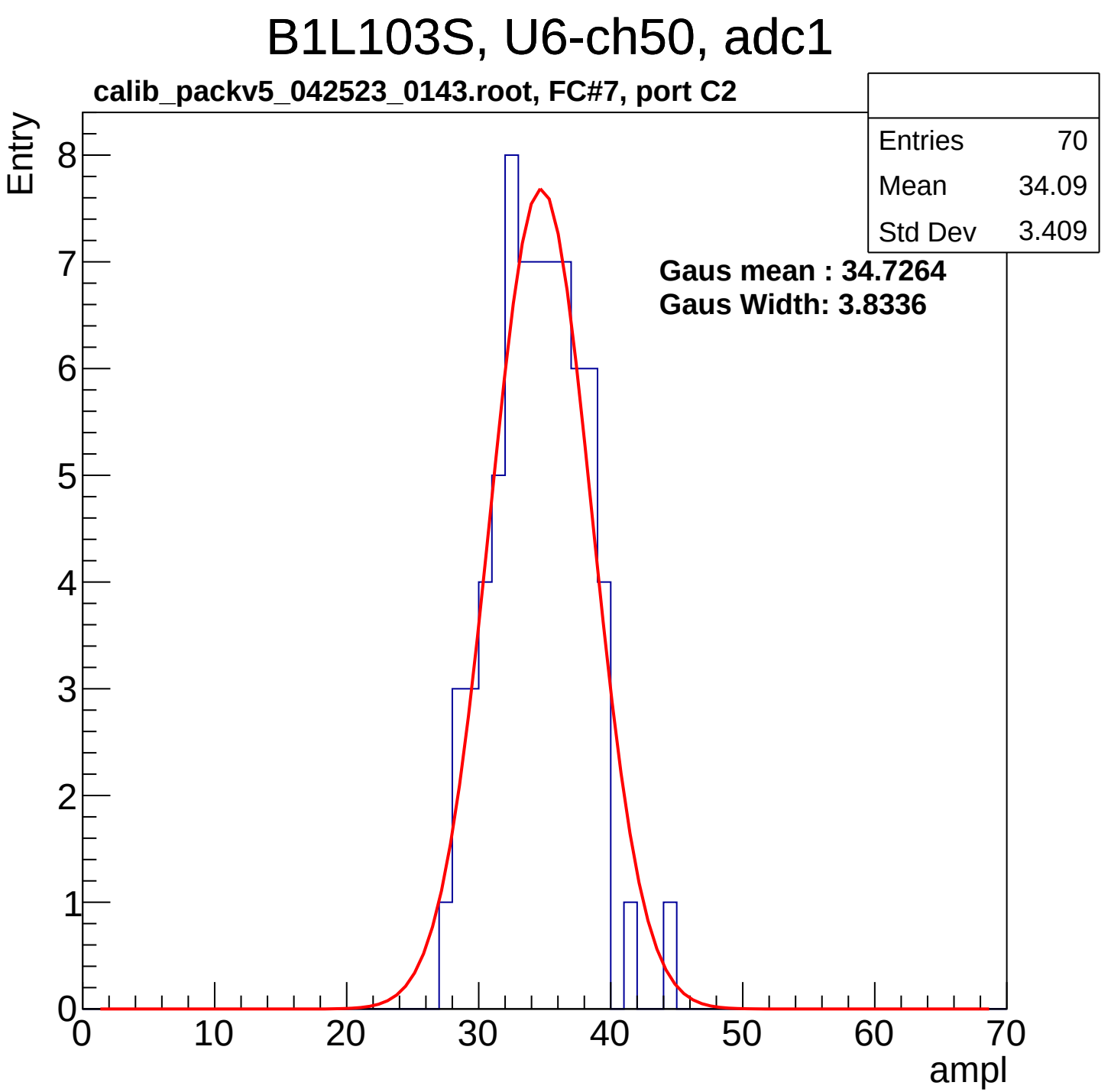
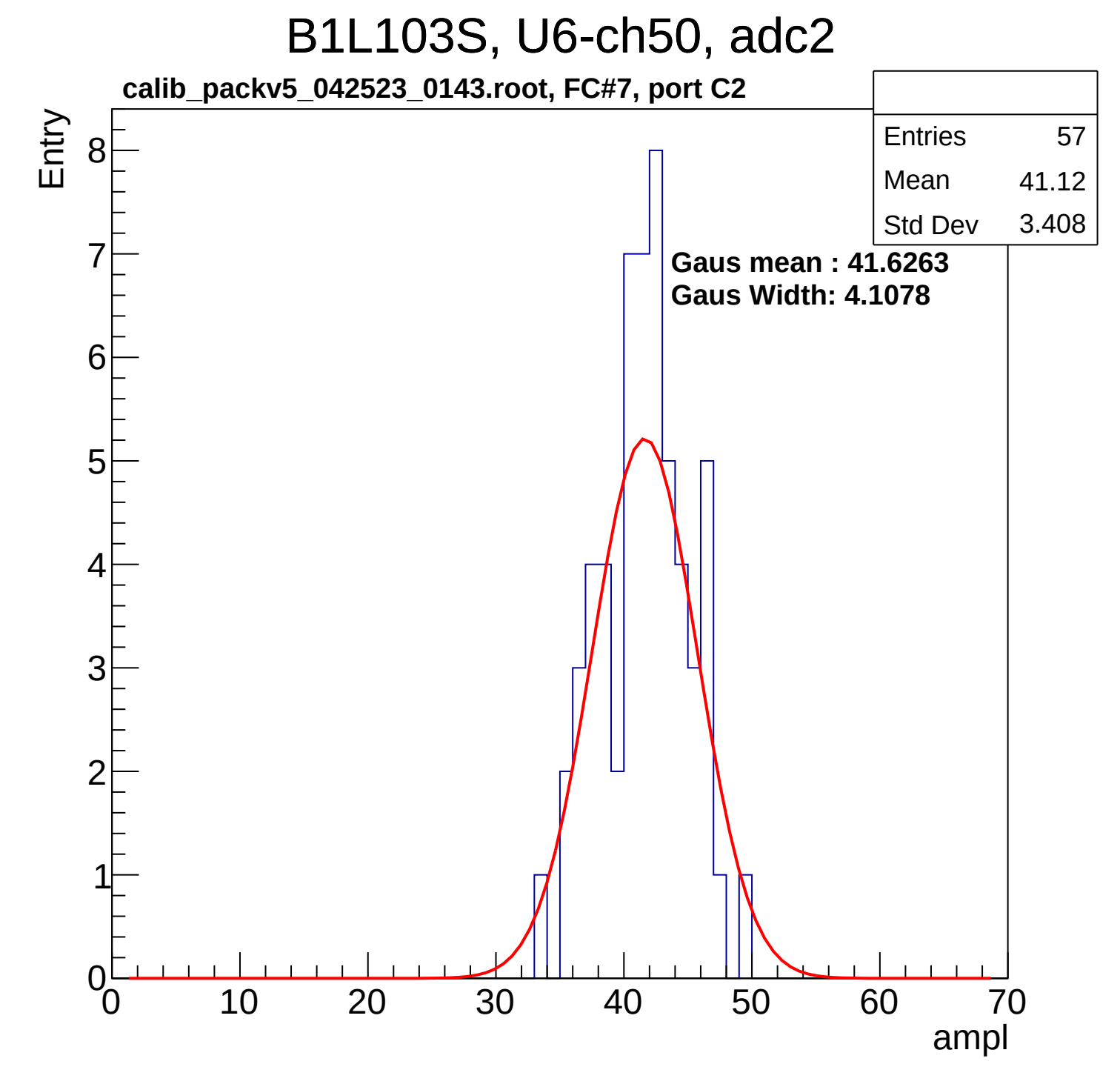

B1L103S, U6-ch50, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 34.09
Std Dev 3.409
Gaus mean : 34.7264
Gaus Width: 3.8336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch50, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 41.12
Std Dev 3.408
Gaus mean : 41.6263
Gaus Width: 4.1078
Entry
ampl
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1
2
3
4
5
6
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8
0
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40
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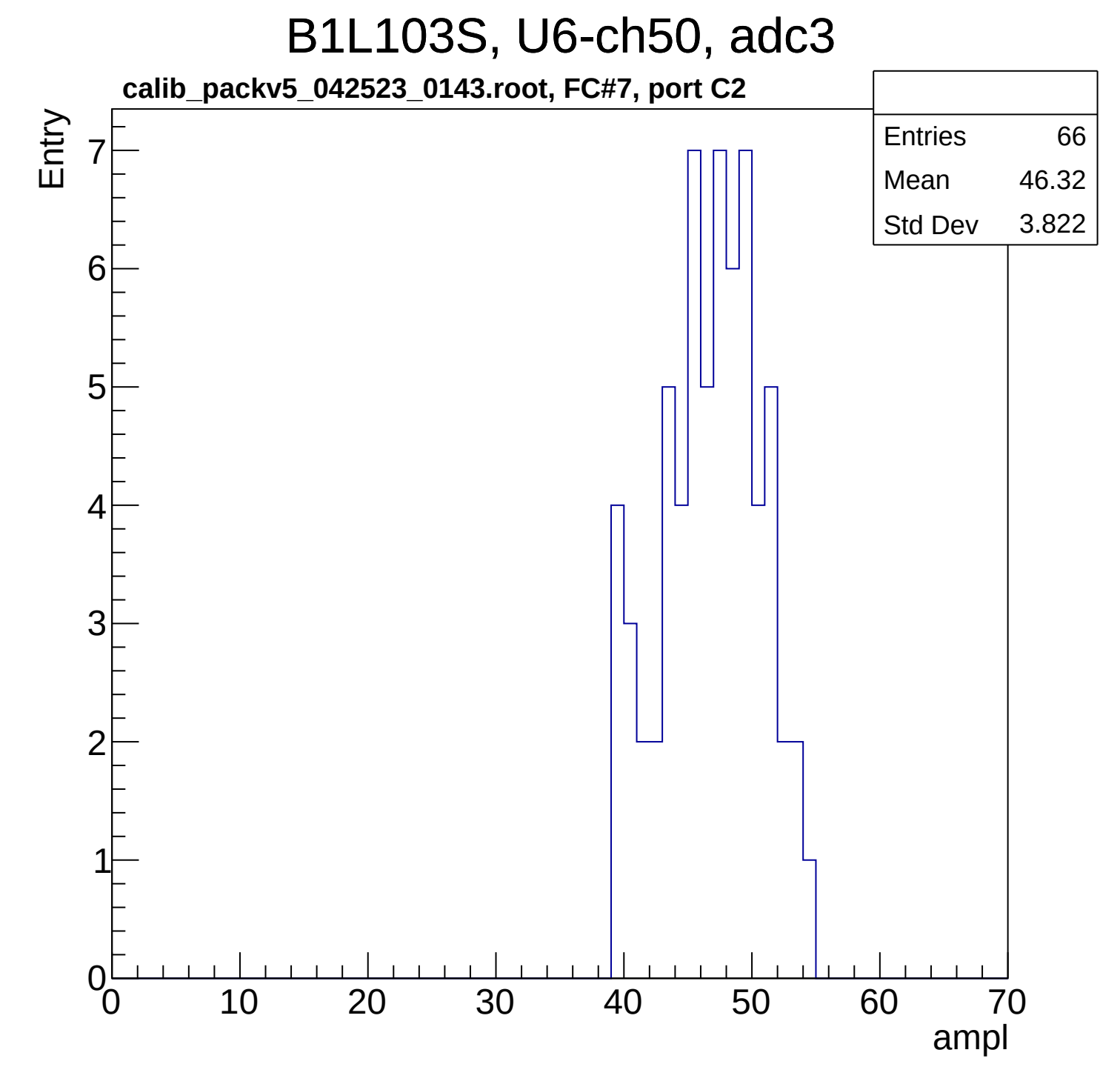
B1L103S, U6-ch50, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 46.32
Std Dev 3.822
Entry
ampl
0
1
2
3
4
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6
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0
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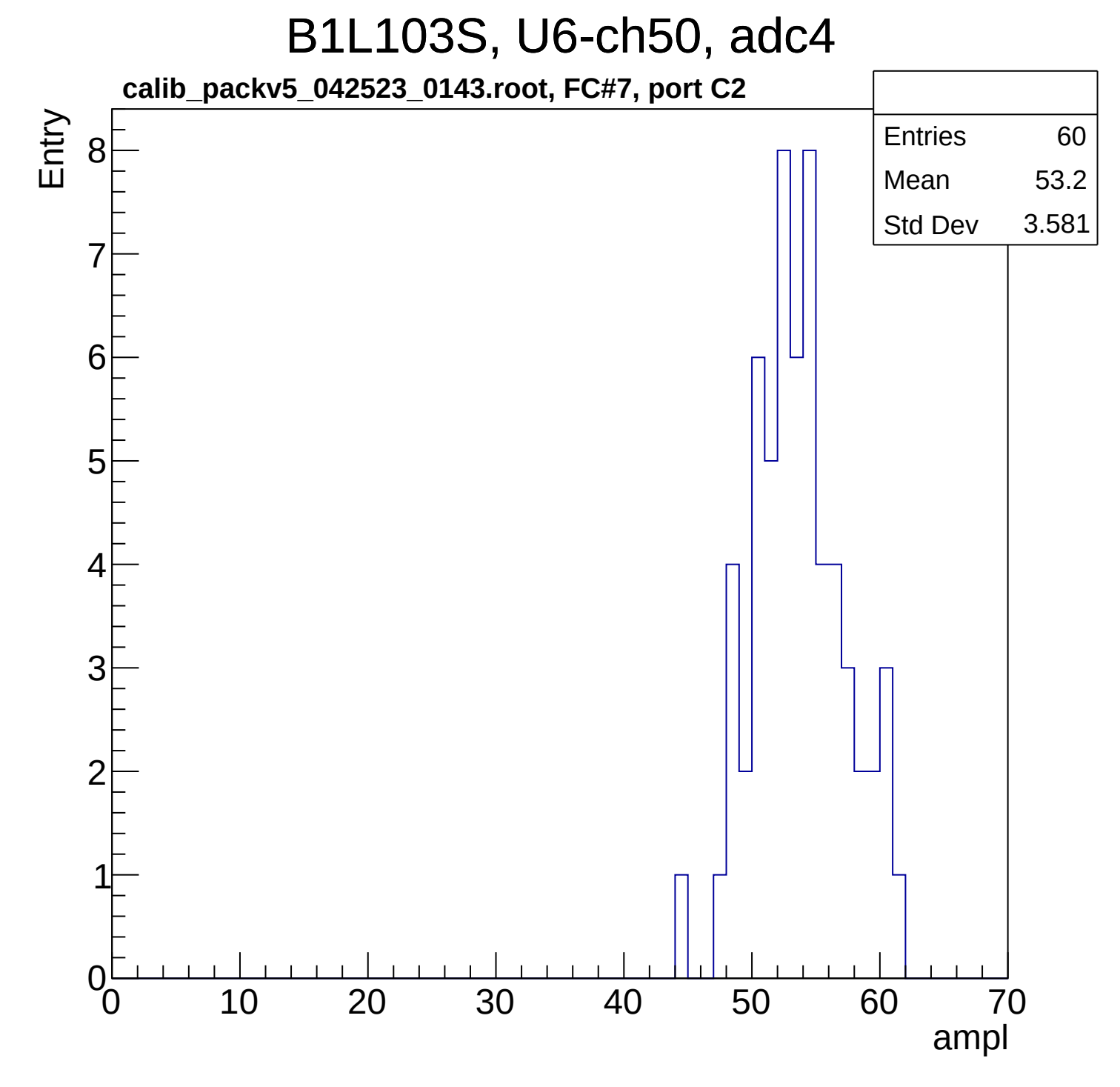
B1L103S, U6-ch50, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 53.2
Std Dev 3.581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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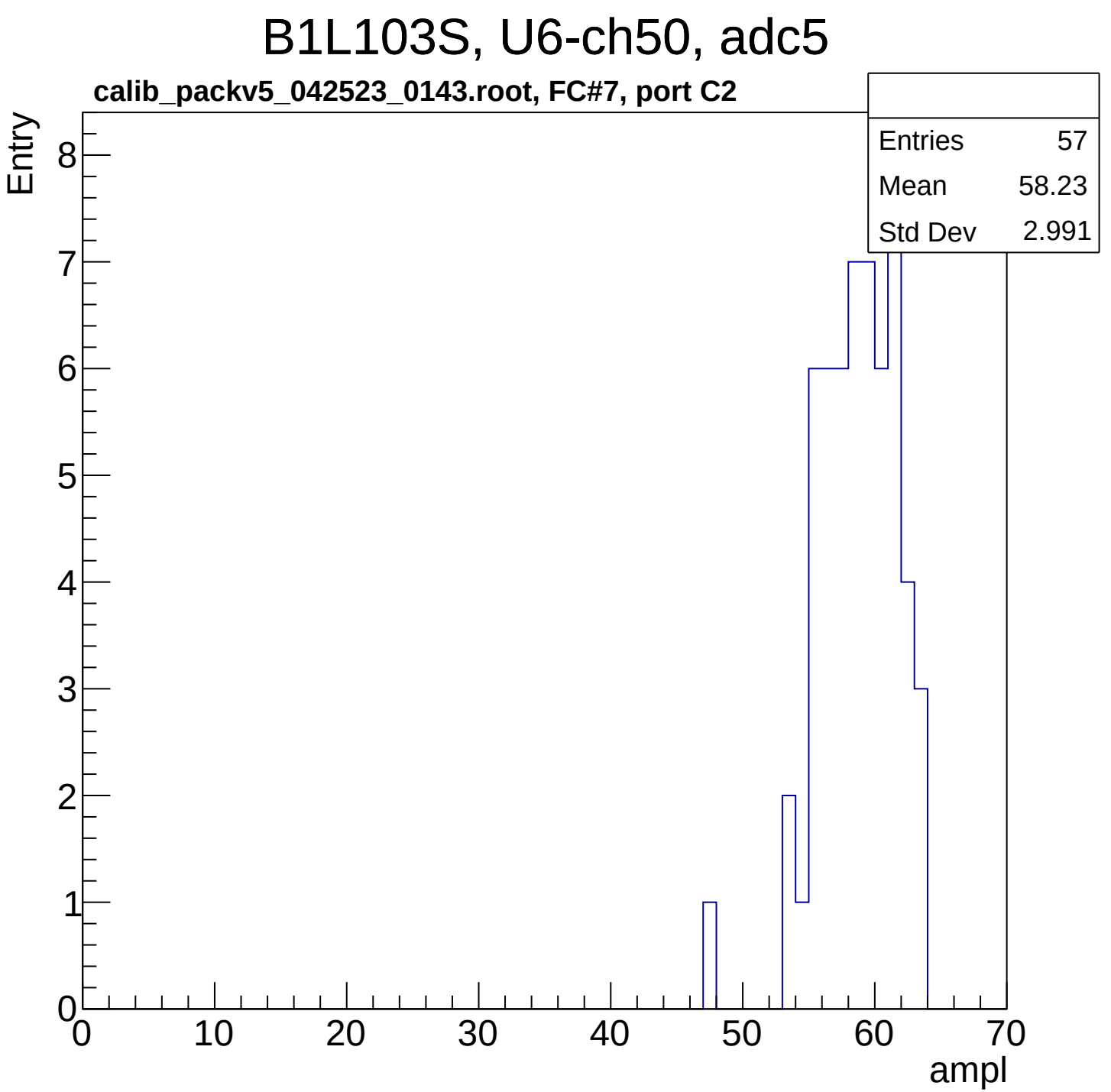
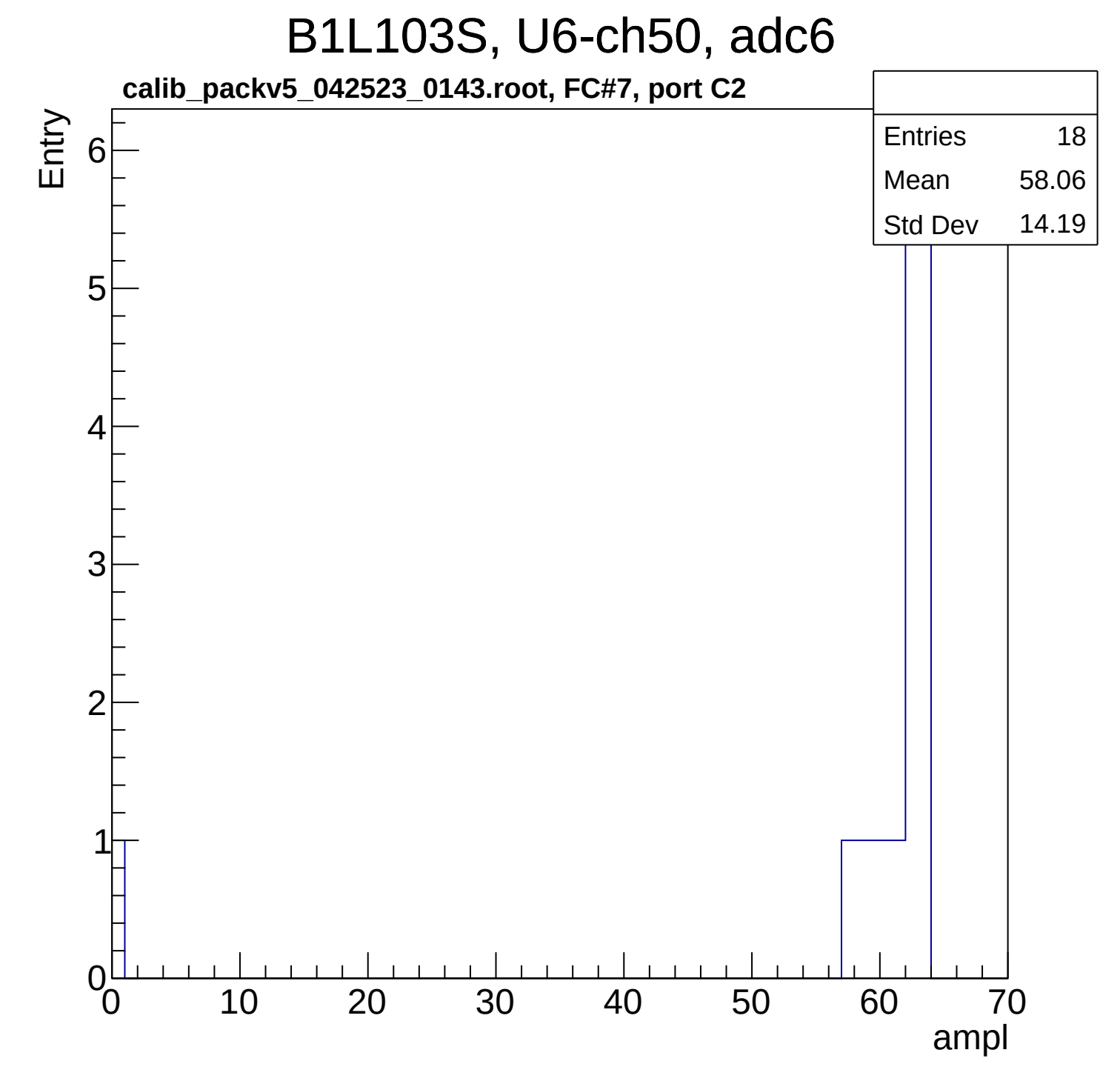
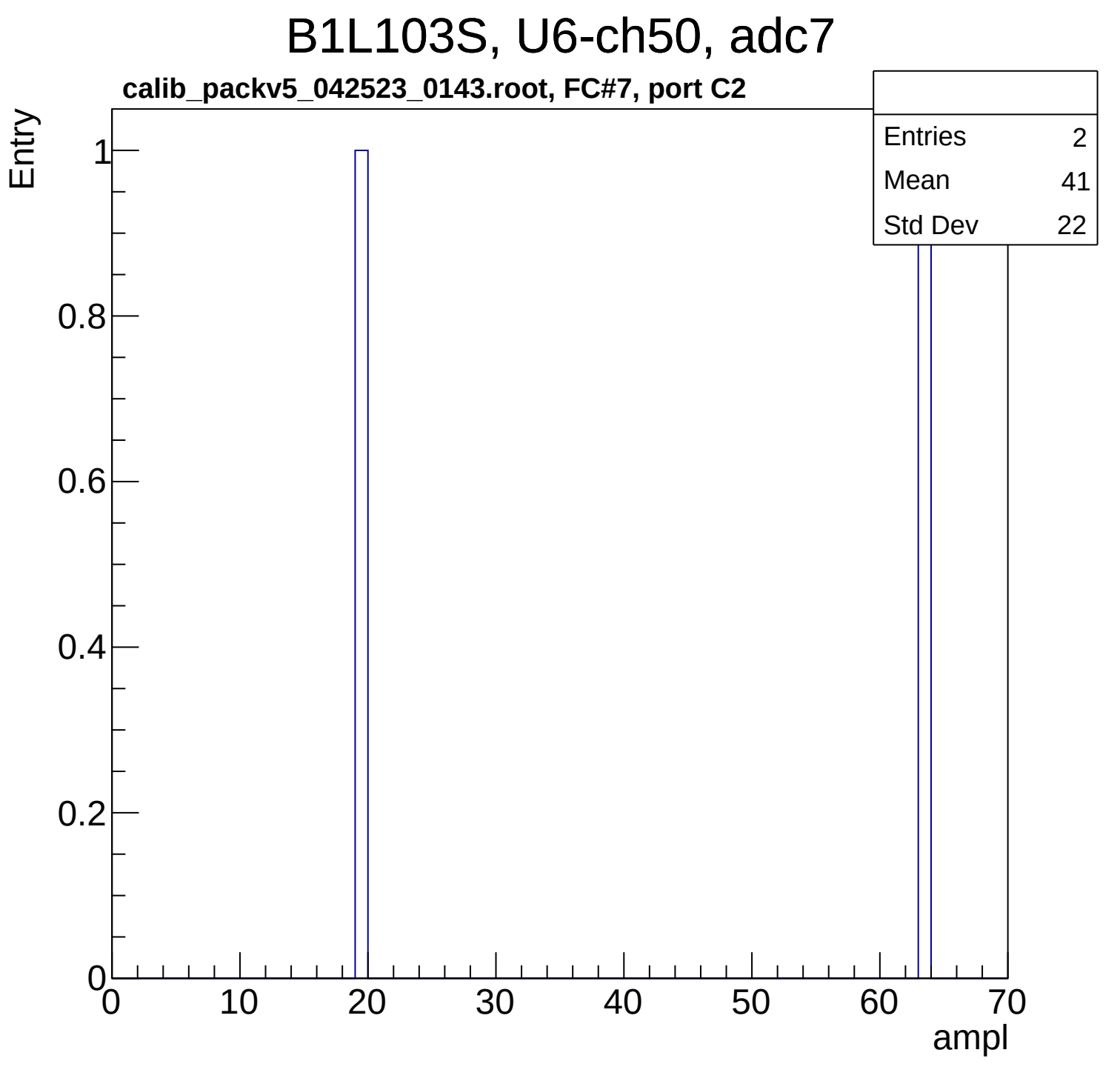
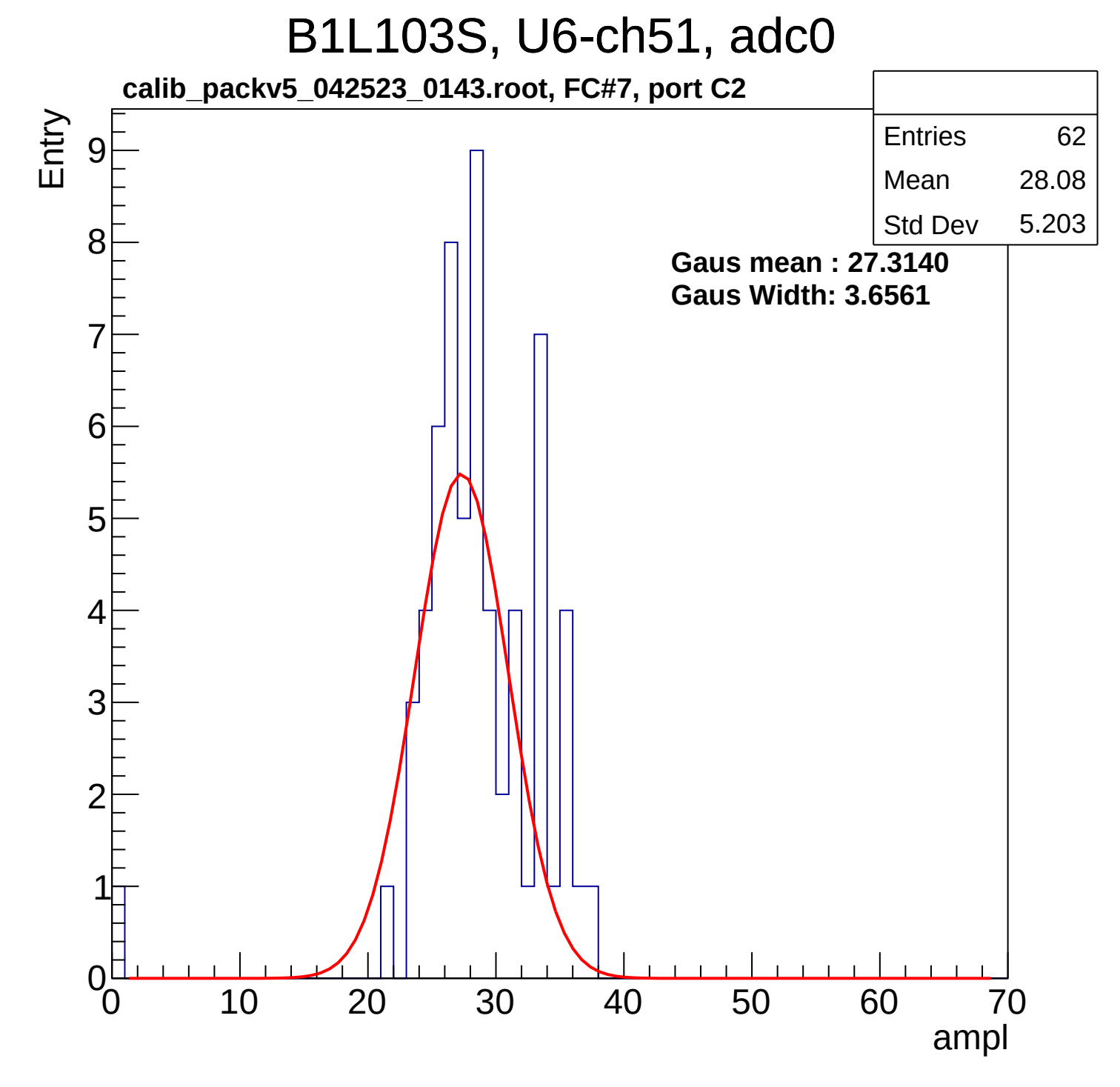

B1L103S, U6-ch50, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 58.23
Std Dev 2.991
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch50, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 58.06
Std Dev 14.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch50, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 41
Std Dev 22
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch51, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28.08
Std Dev 5.203
Gaus mean : 27.3140
Gaus Width: 3.6561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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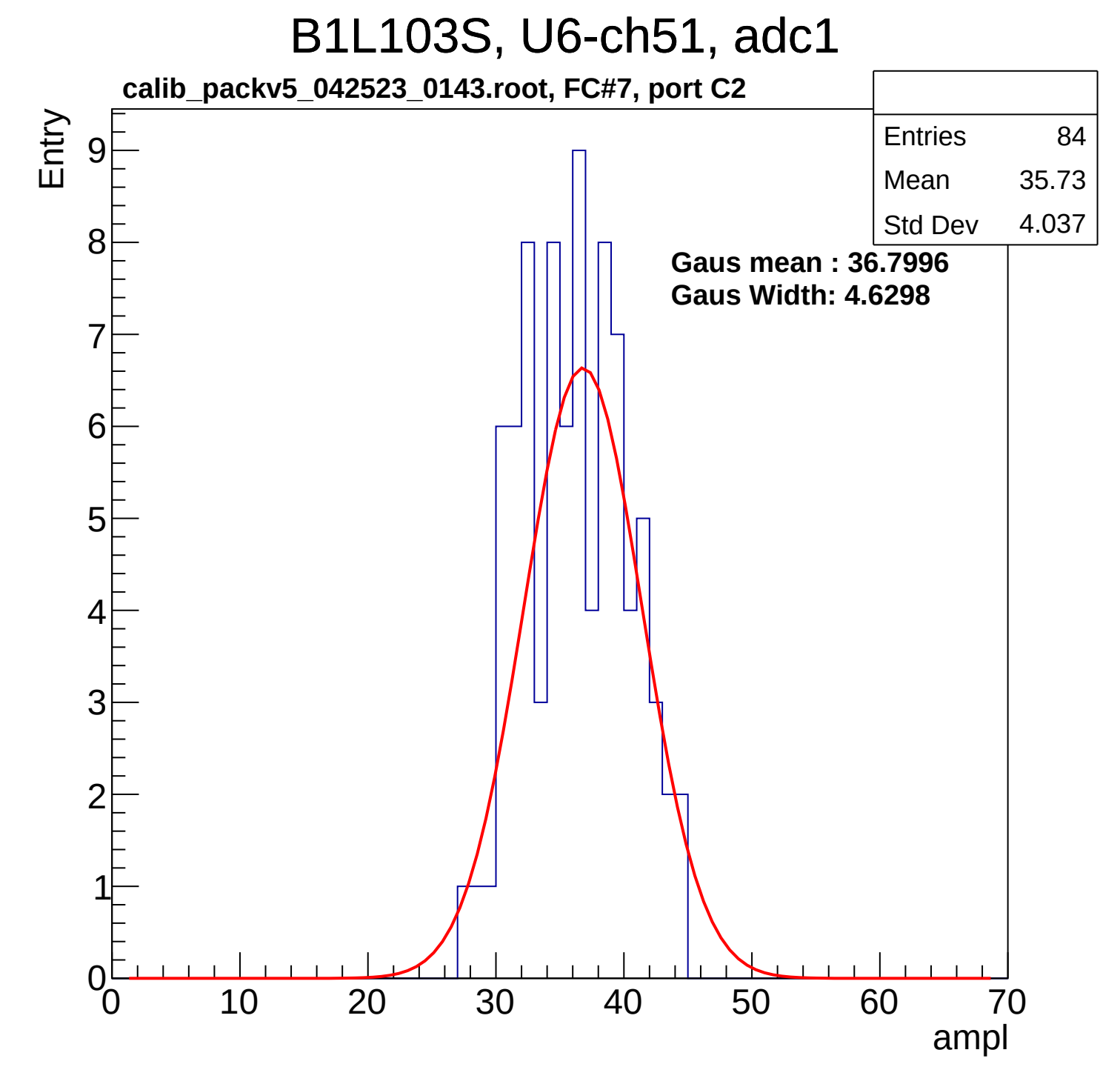
B1L103S, U6-ch51, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 35.73
Std Dev 4.037
Gaus mean : 36.7996
Gaus Width: 4.6298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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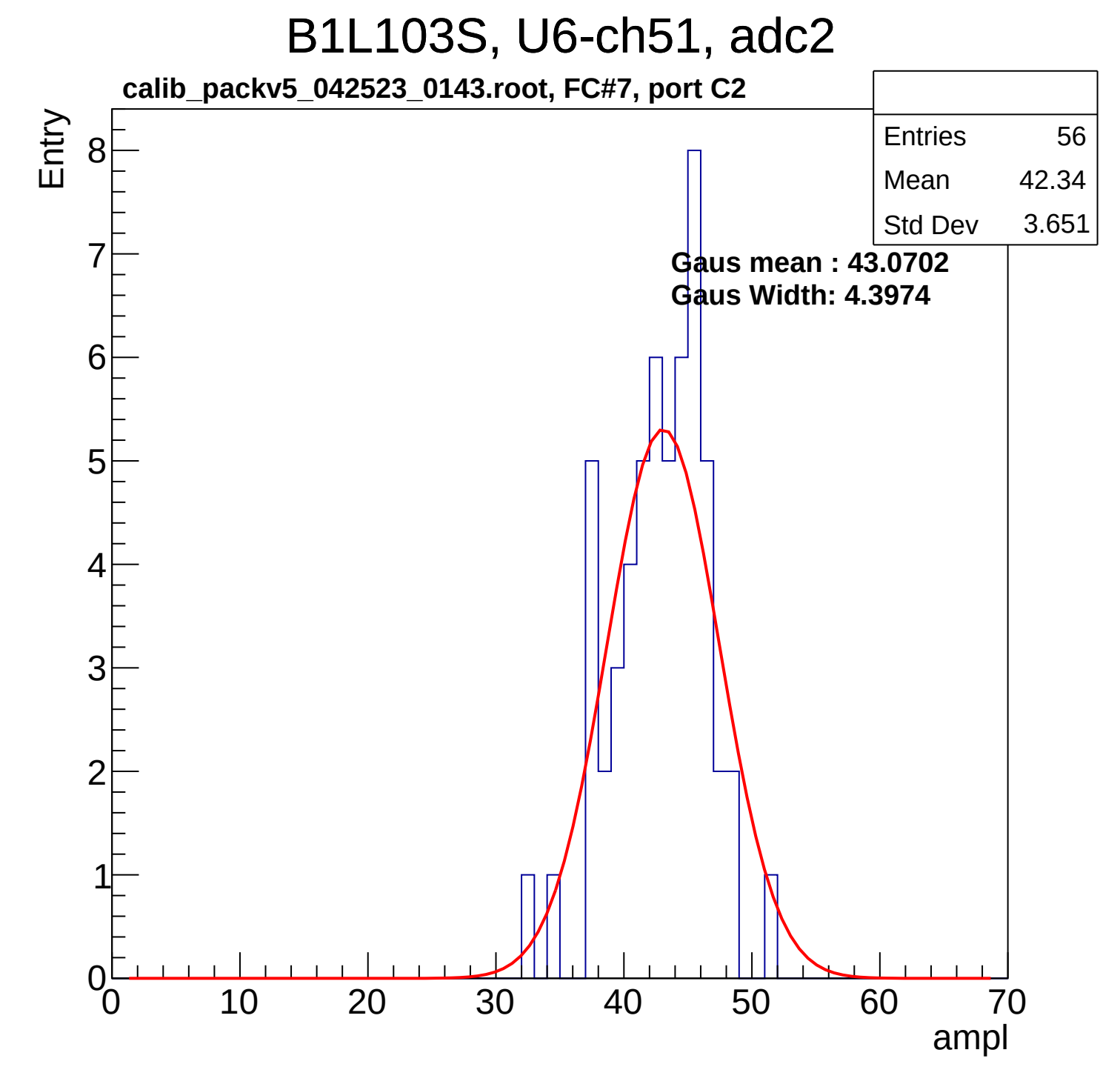
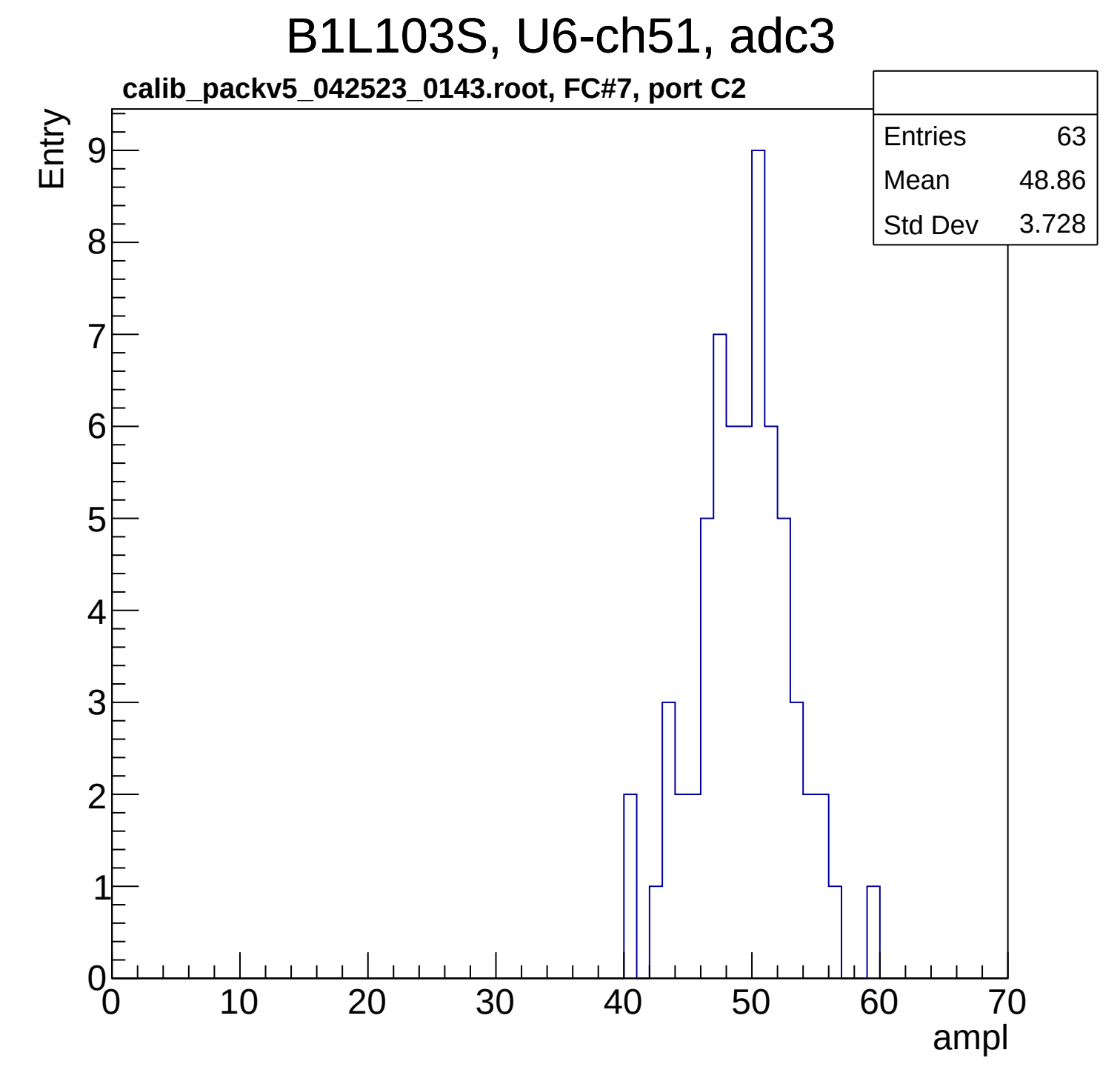

B1L103S, U6-ch51, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 42.34
Std Dev 3.651
Gaus mean : 43.0702
Gaus Width: 4.3974
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch51, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.86
Std Dev 3.728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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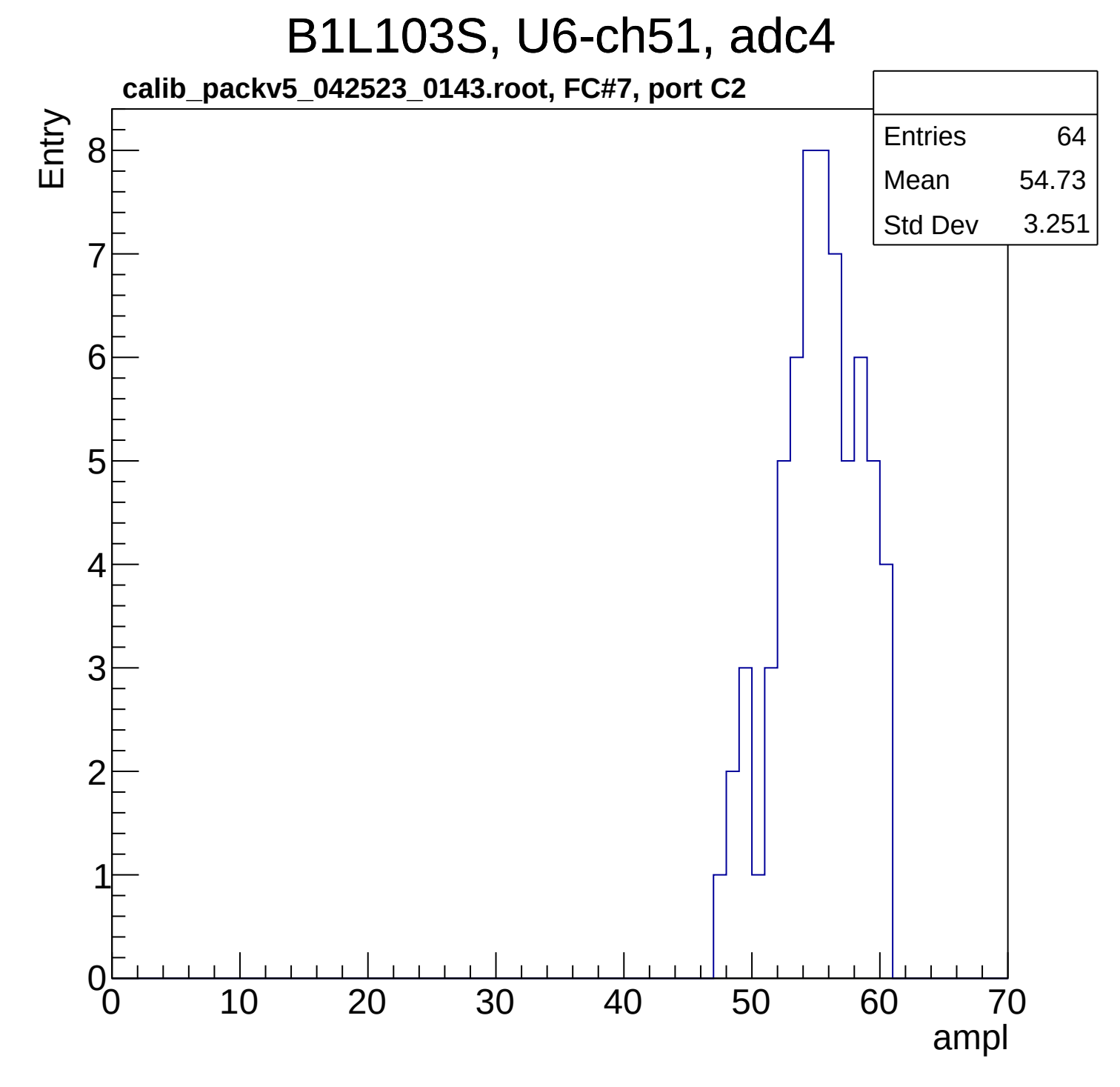
B1L103S, U6-ch51, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 54.73
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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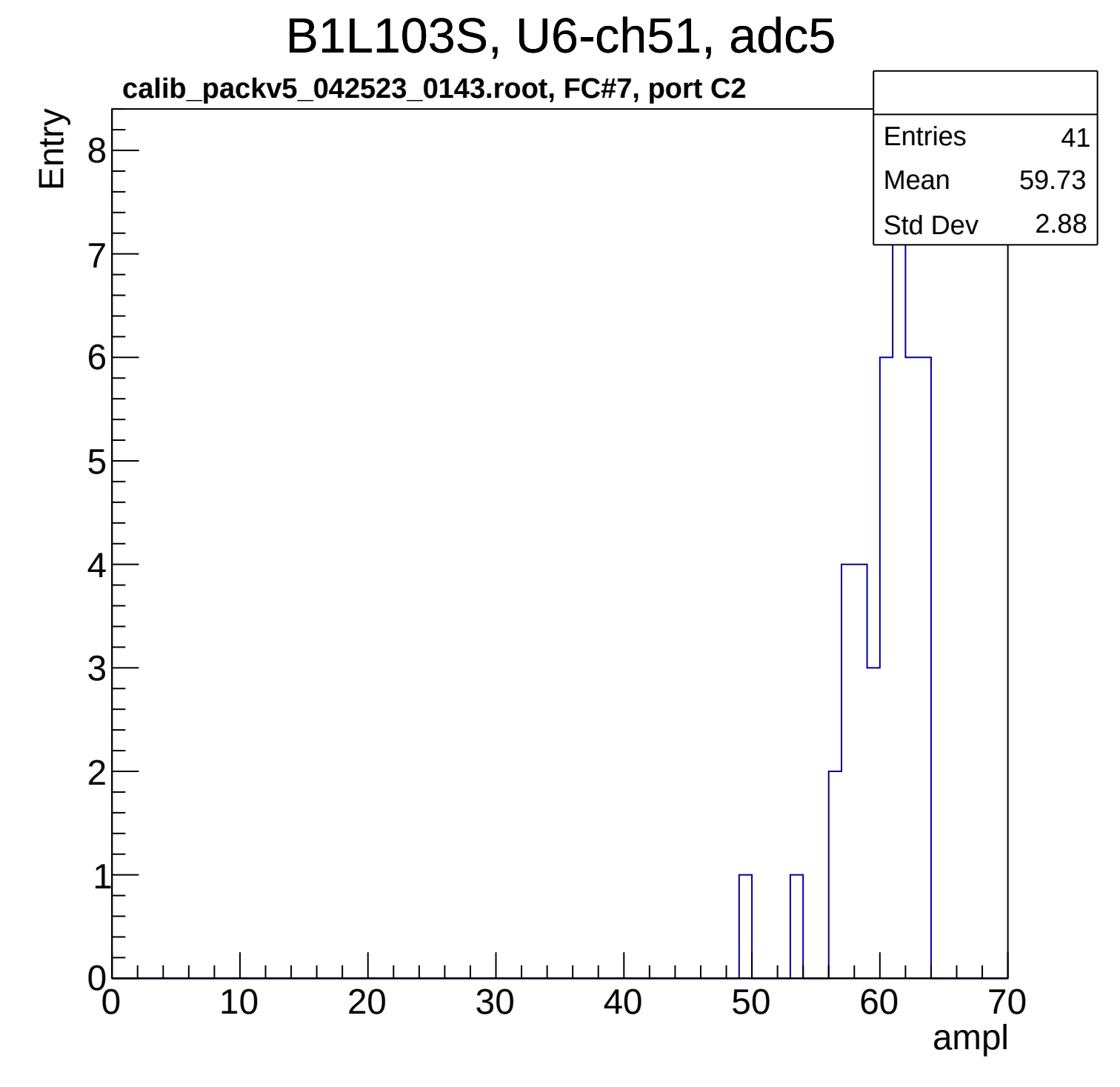
B1L103S, U6-ch51, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.73
Std Dev 2.88
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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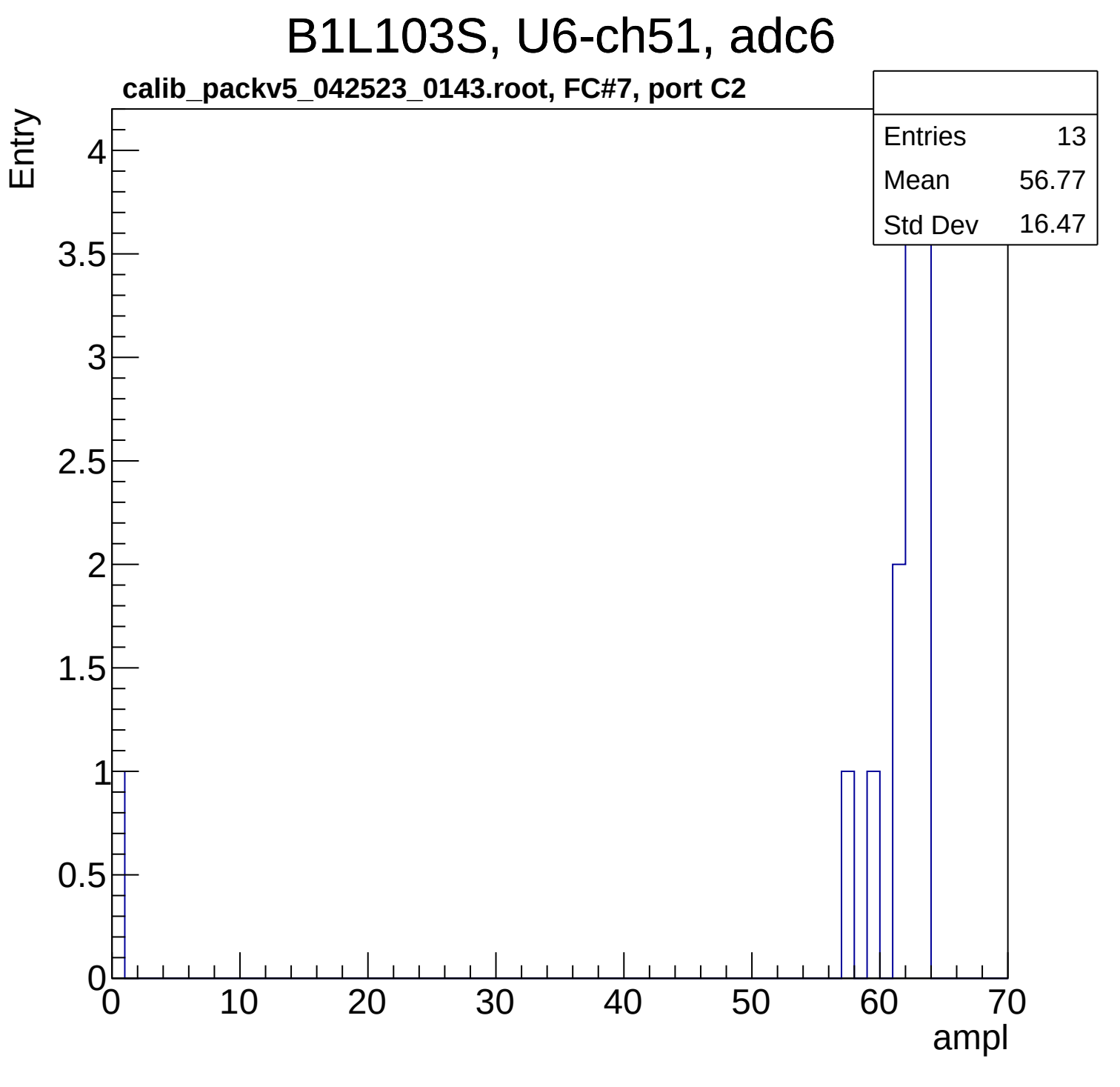
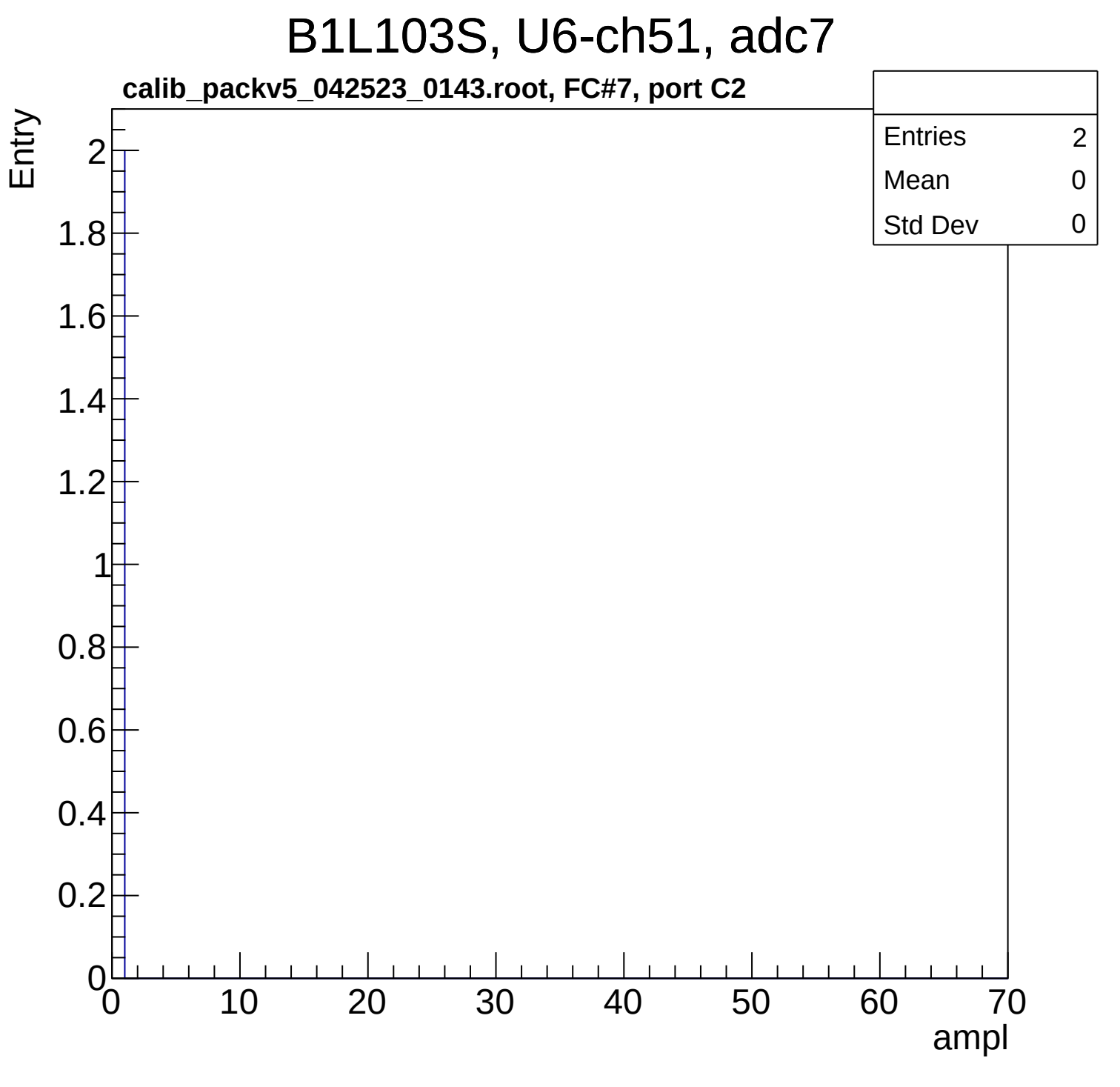
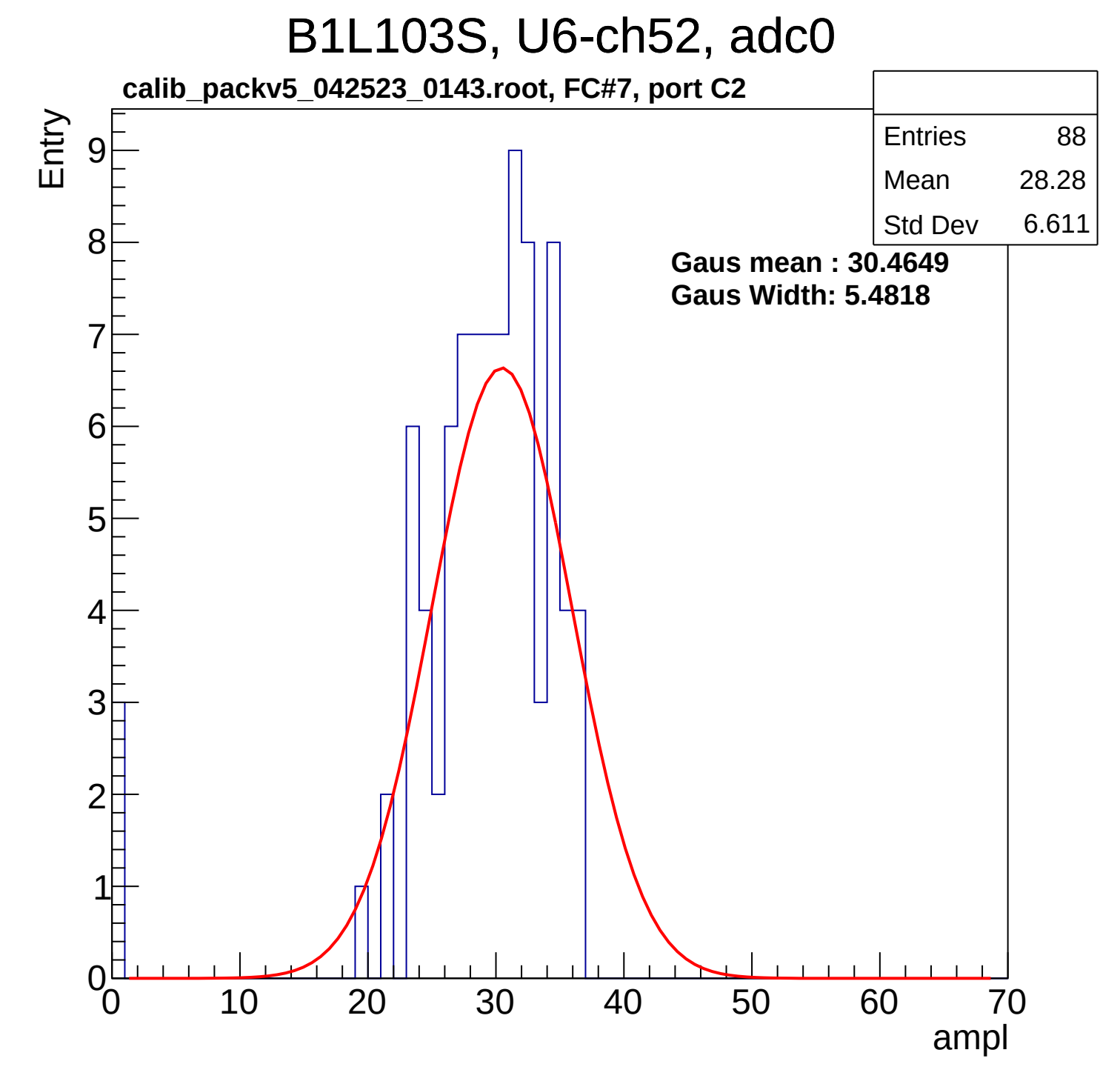

B1L103S, U6-ch51, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.77
Std Dev 16.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch51, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch52, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 28.28
Std Dev 6.611
Gaus mean : 30.4649
Gaus Width: 5.4818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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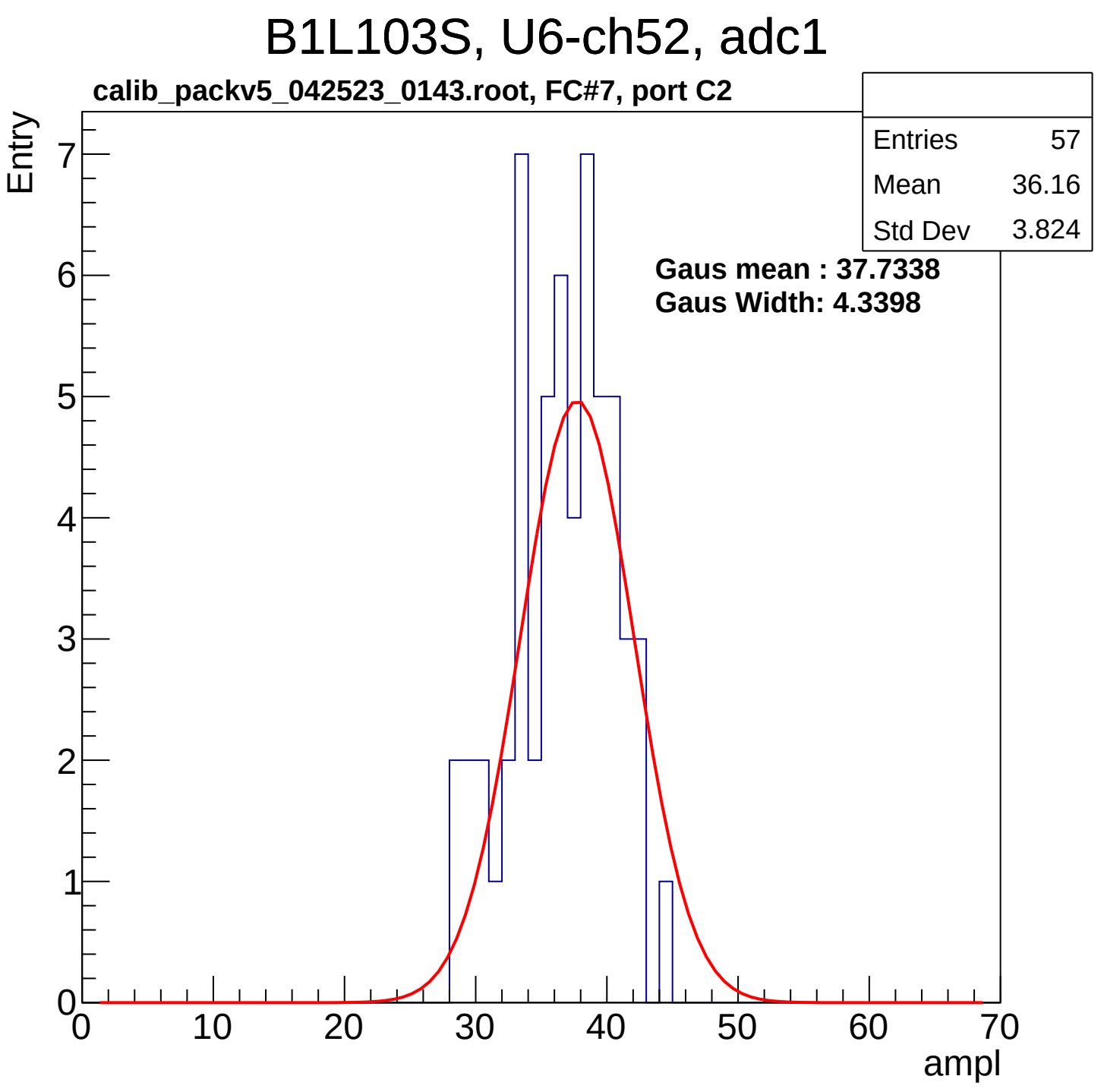
B1L103S, U6-ch52, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 36.16
Std Dev 3.824
Gaus mean : 37.7338
Gaus Width: 4.3398
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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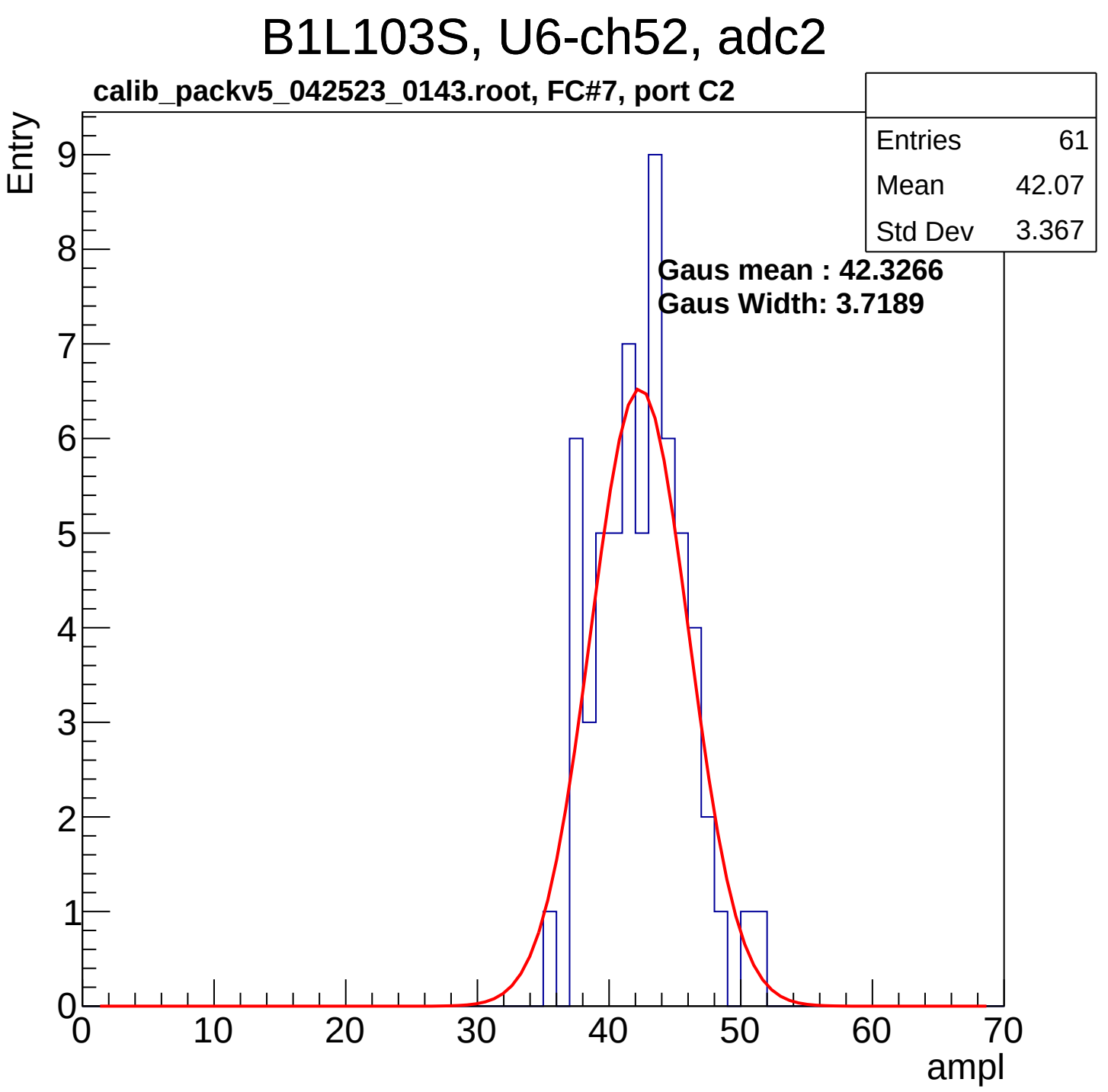
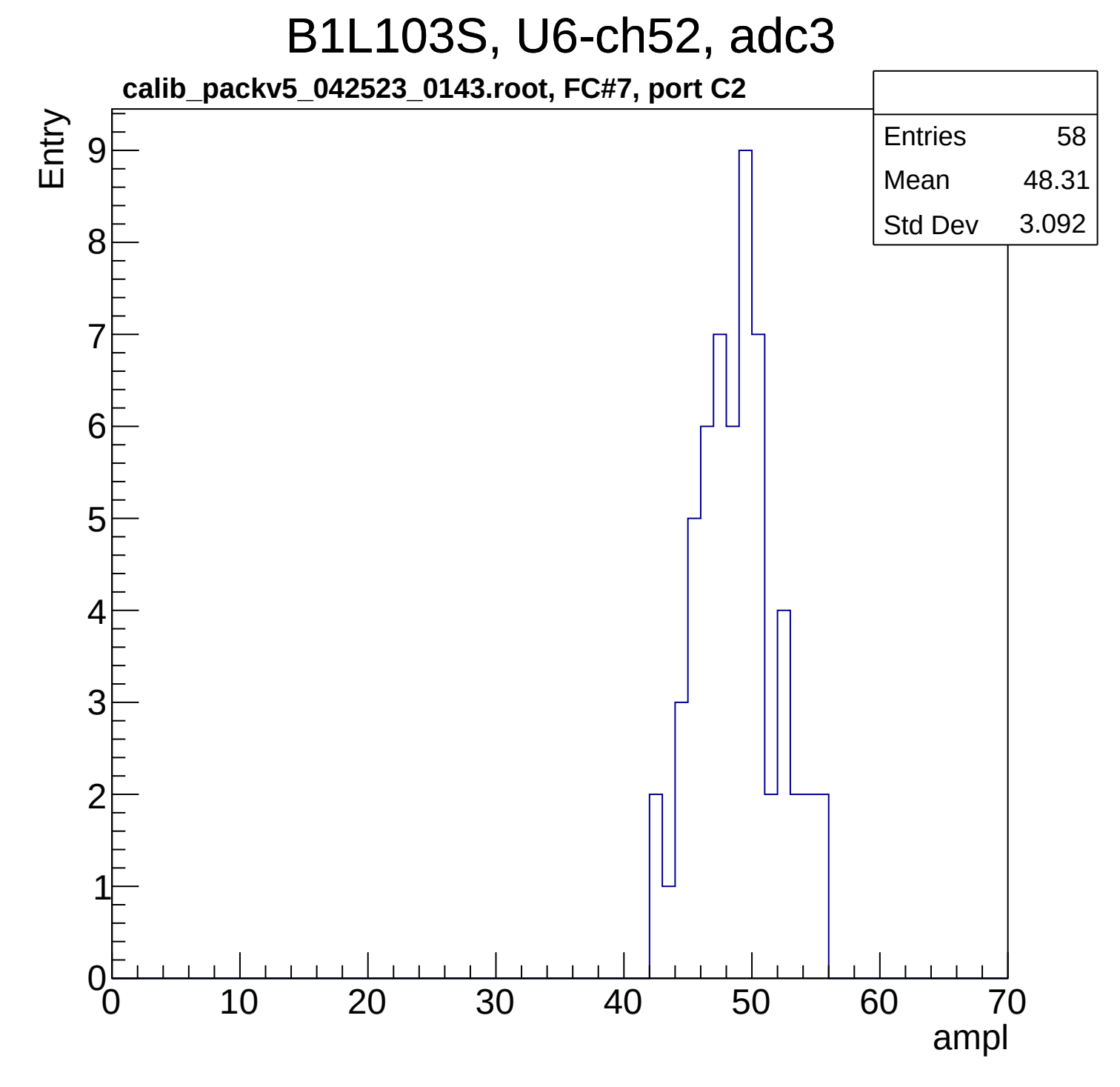

B1L103S, U6-ch52, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.07
Std Dev 3.367
Gaus mean : 42.3266
Gaus Width: 3.7189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch52, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 48.31
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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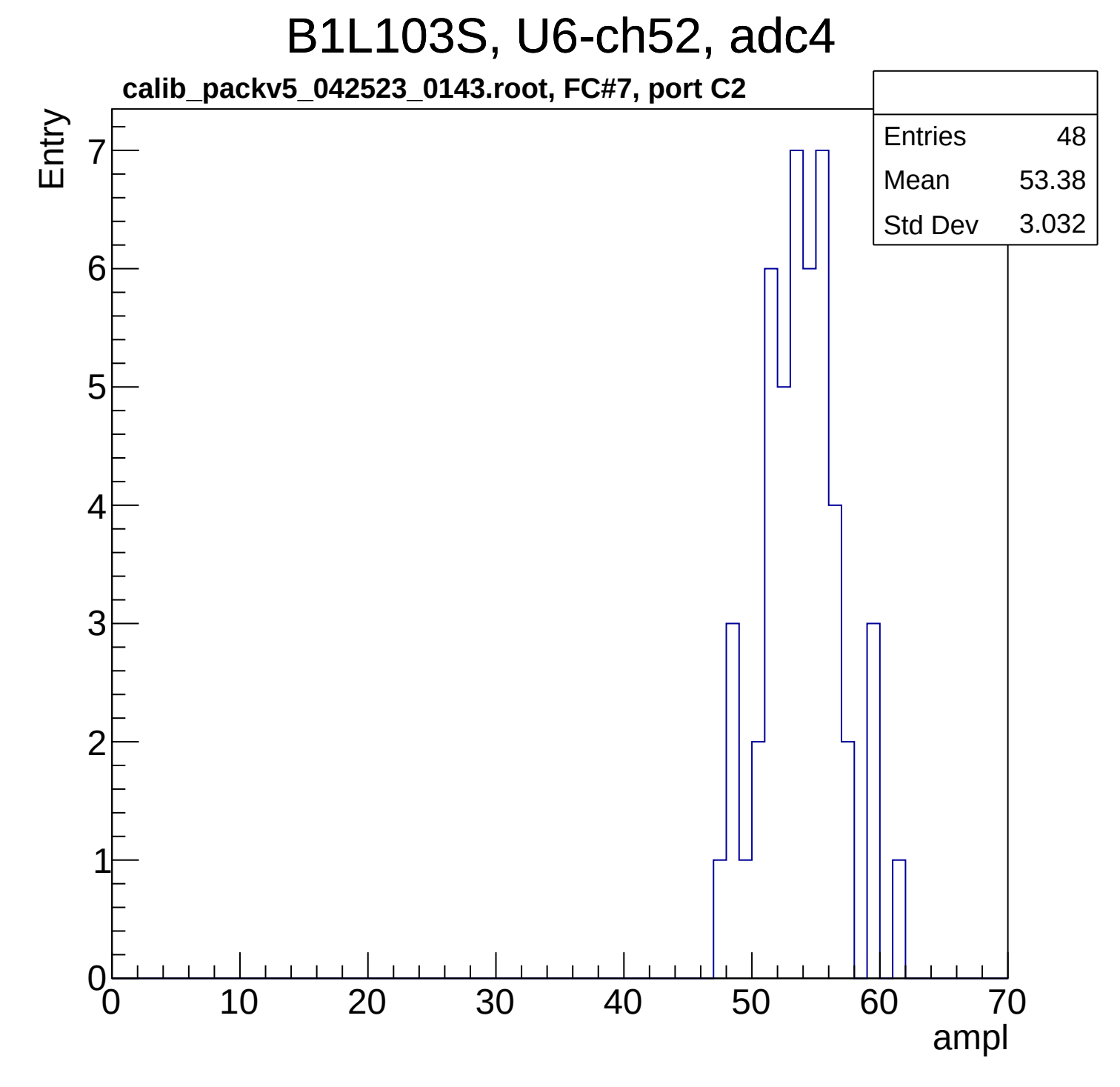
B1L103S, U6-ch52, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 53.38
Std Dev 3.032
Entry
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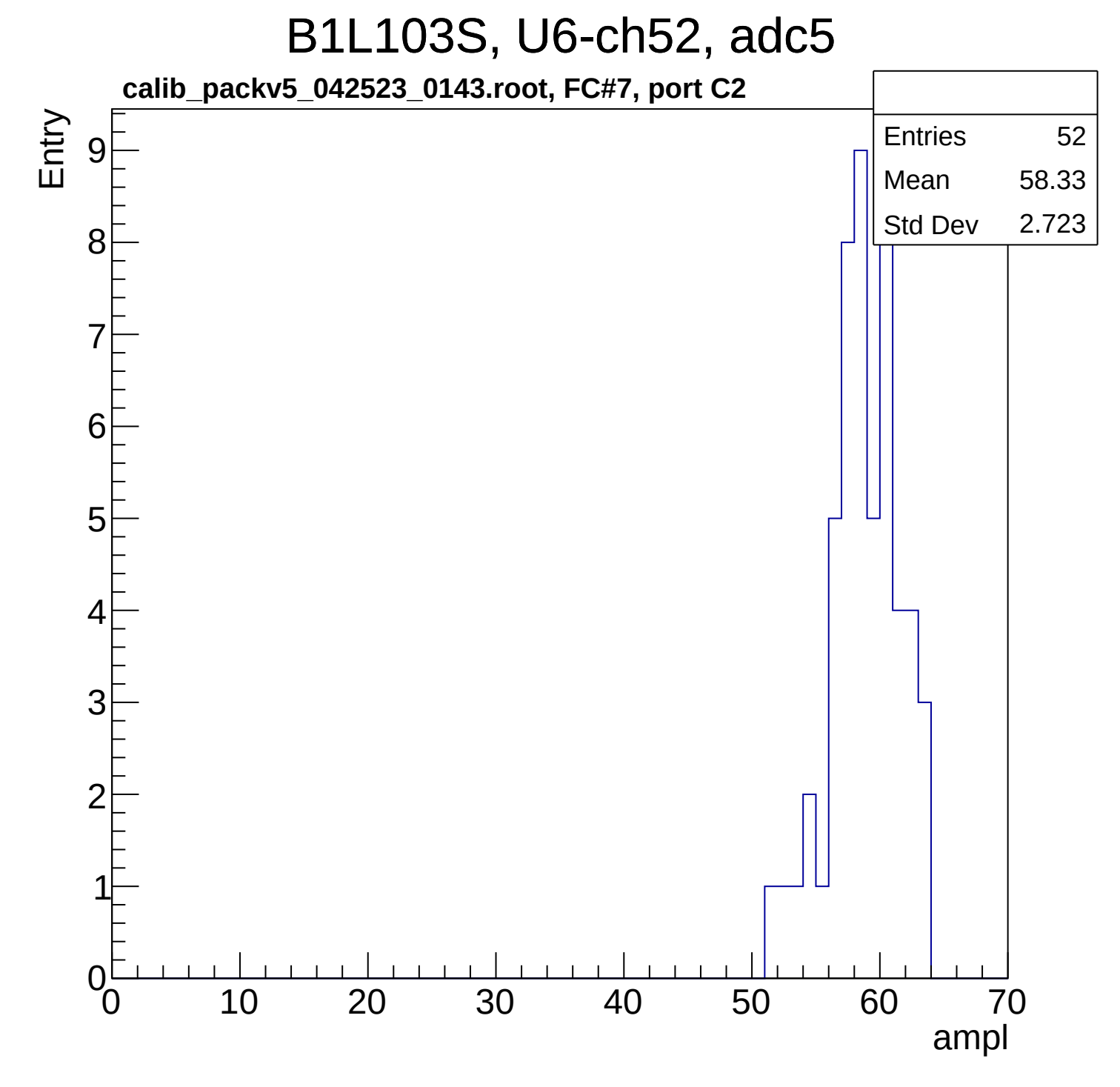
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch52, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.33
Std Dev 2.723
Entry
ampl
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4
5
6
7
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0
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20
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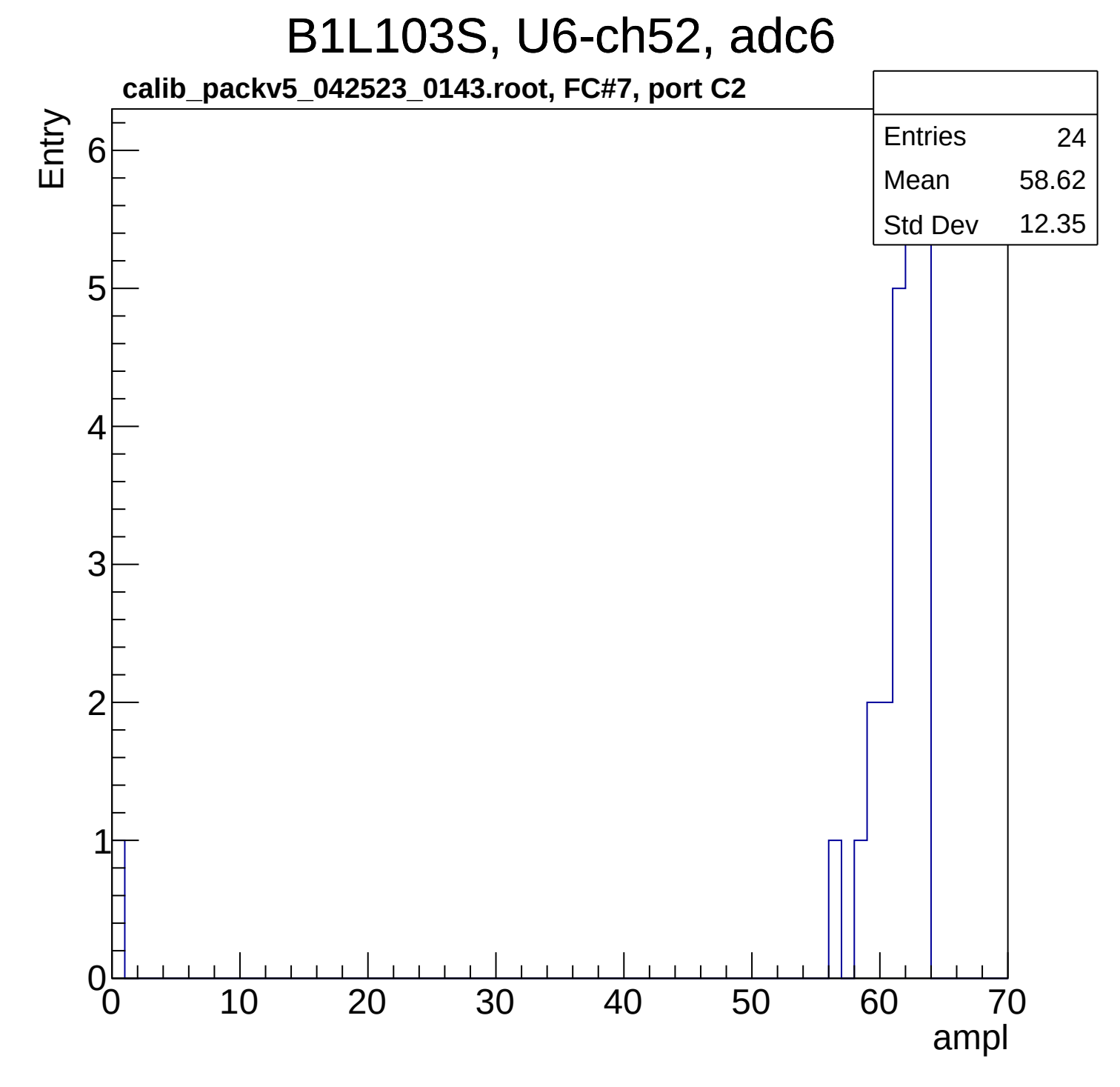
60
70


B1L103S, U6-ch52, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 58.62
Std Dev 12.35
Entry
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2
3
4
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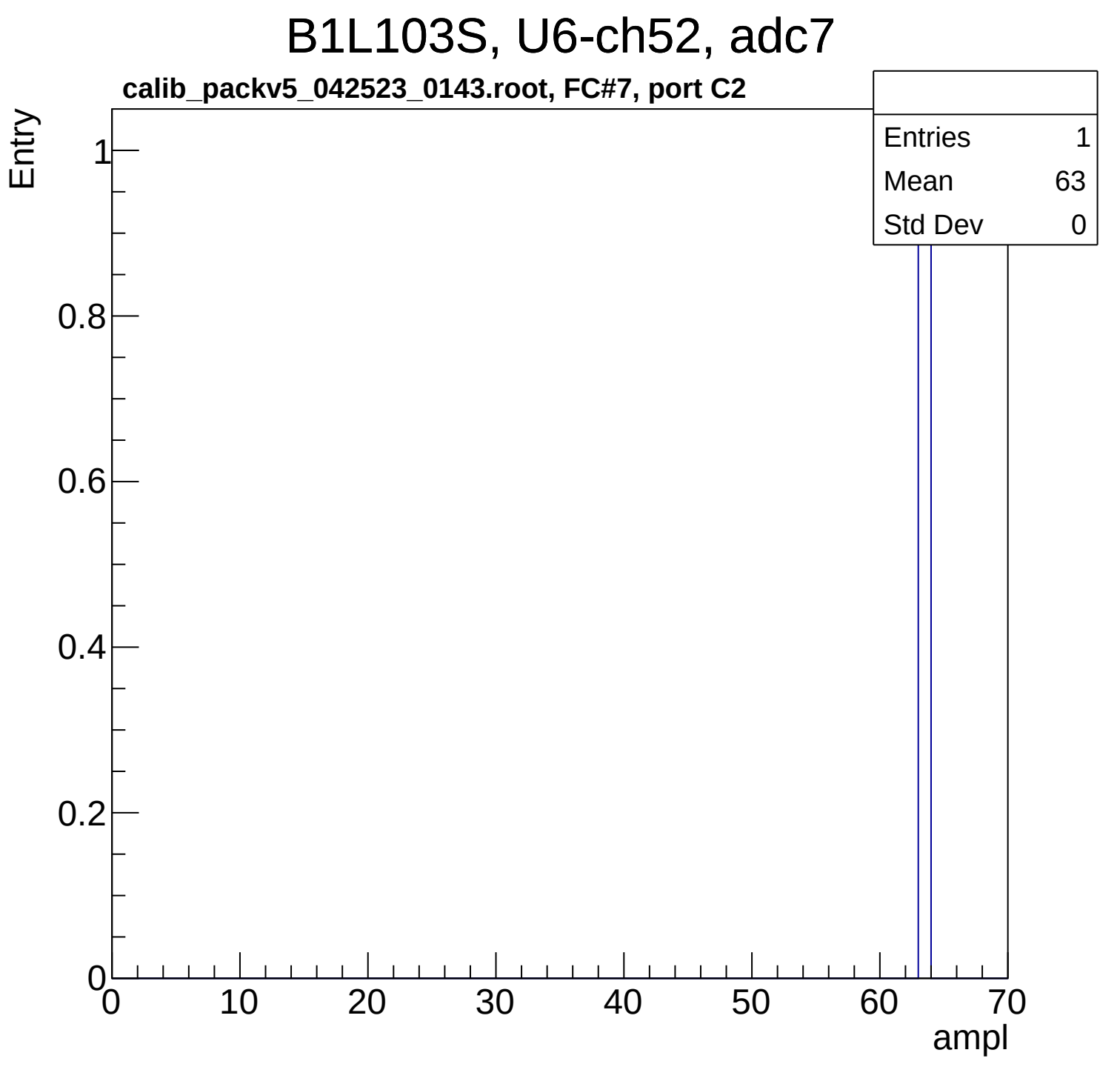
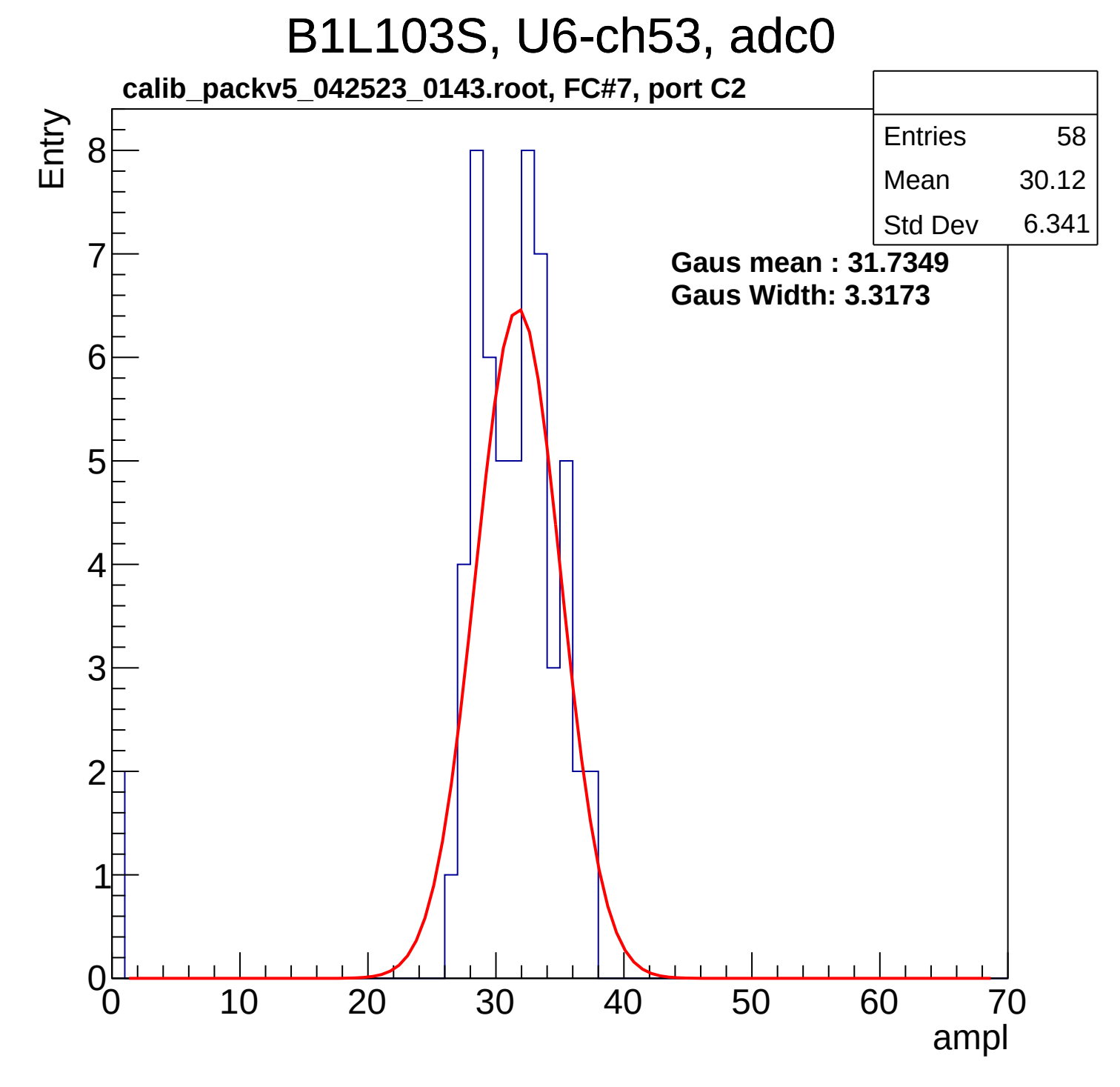
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch52, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch53, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 30.12
Std Dev 6.341
Gaus mean : 31.7349
Gaus Width: 3.3173
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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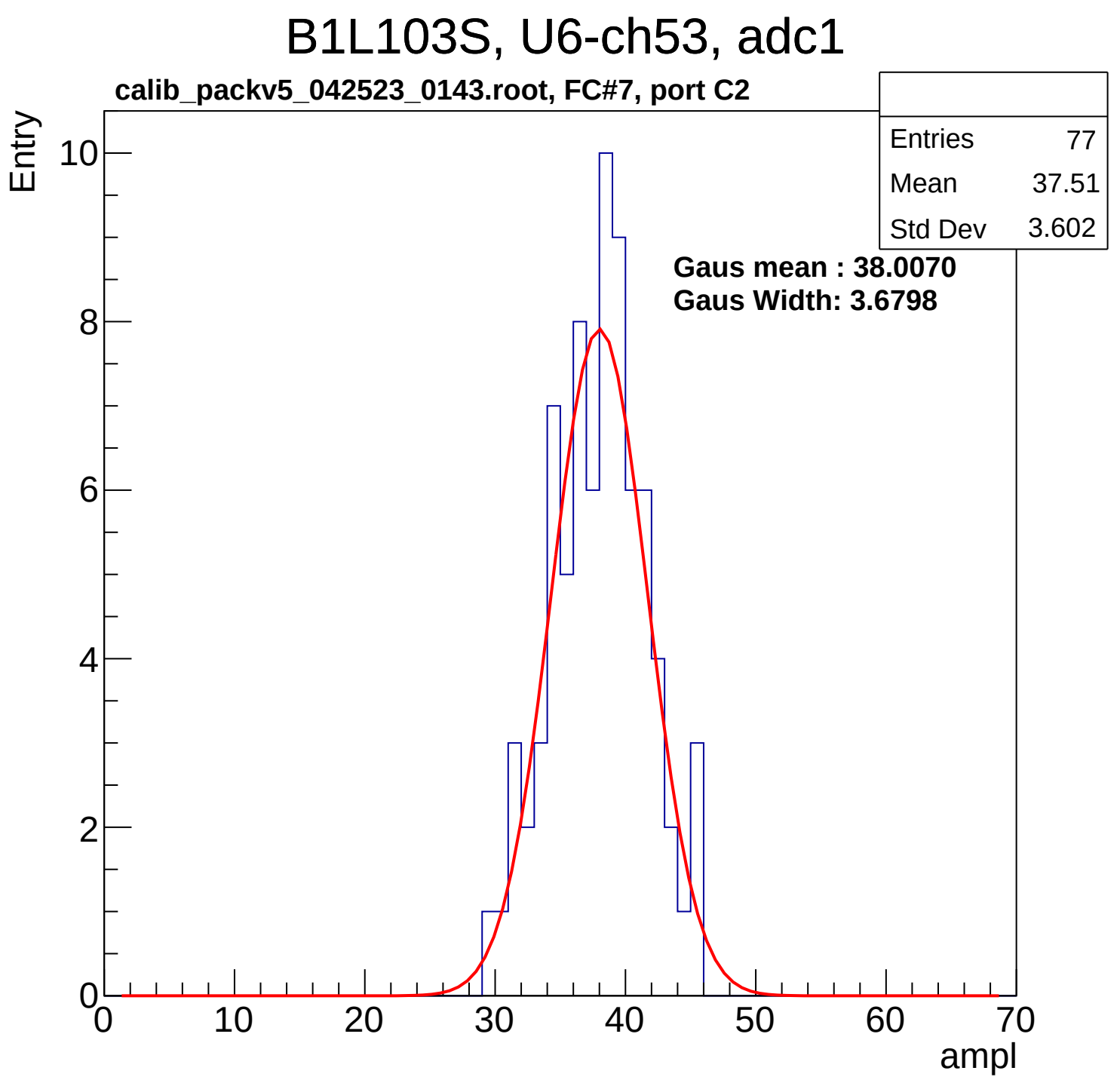
B1L103S, U6-ch53, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 37.51
Std Dev 3.602
Gaus mean : 38.0070
Gaus Width: 3.6798
Entry
ampl
0
2
4
6
8
10
0
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60
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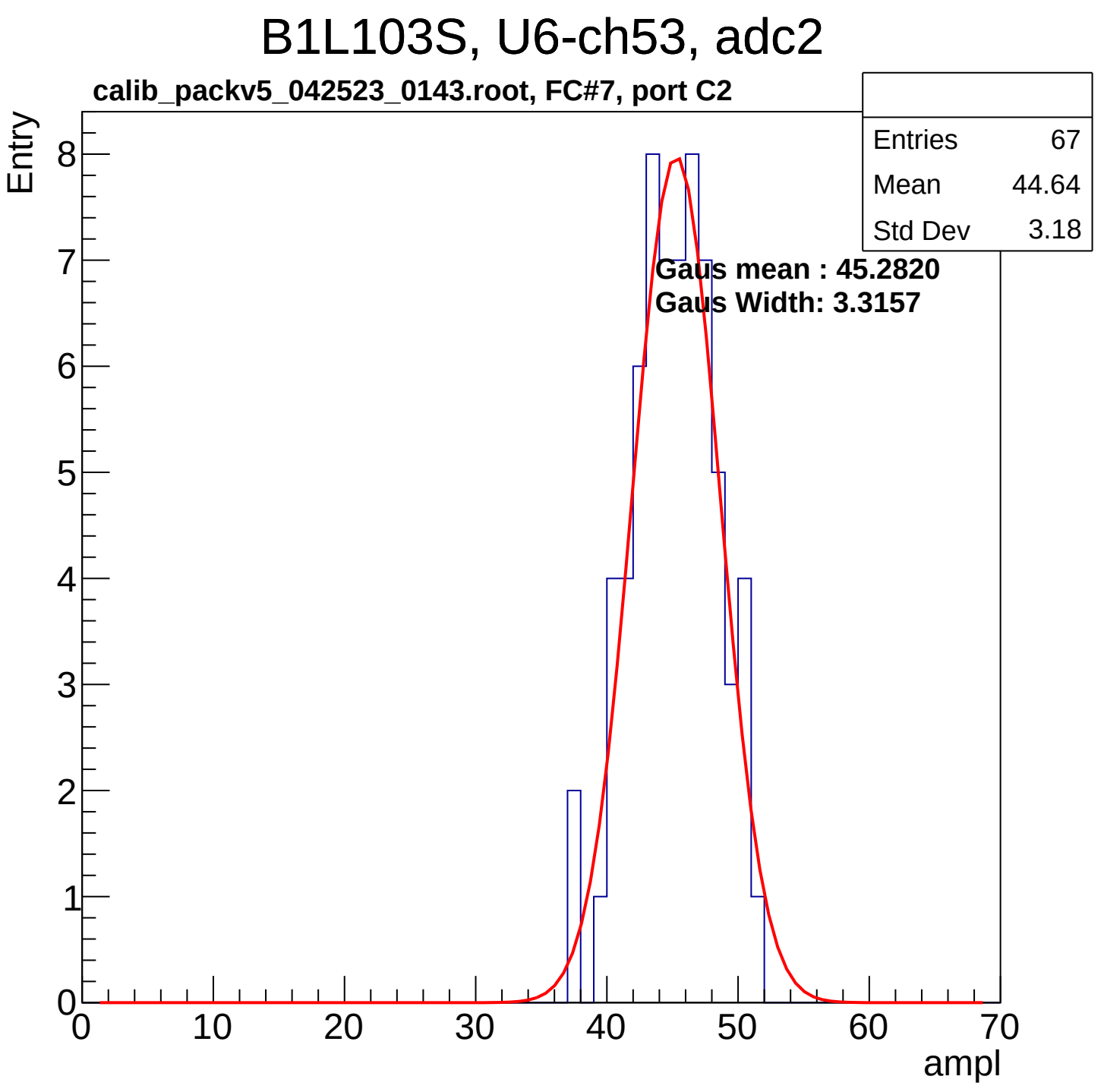
B1L103S, U6-ch53, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 44.64
Std Dev 3.18
Gaus mean : 45.2820
Gaus Width: 3.3157
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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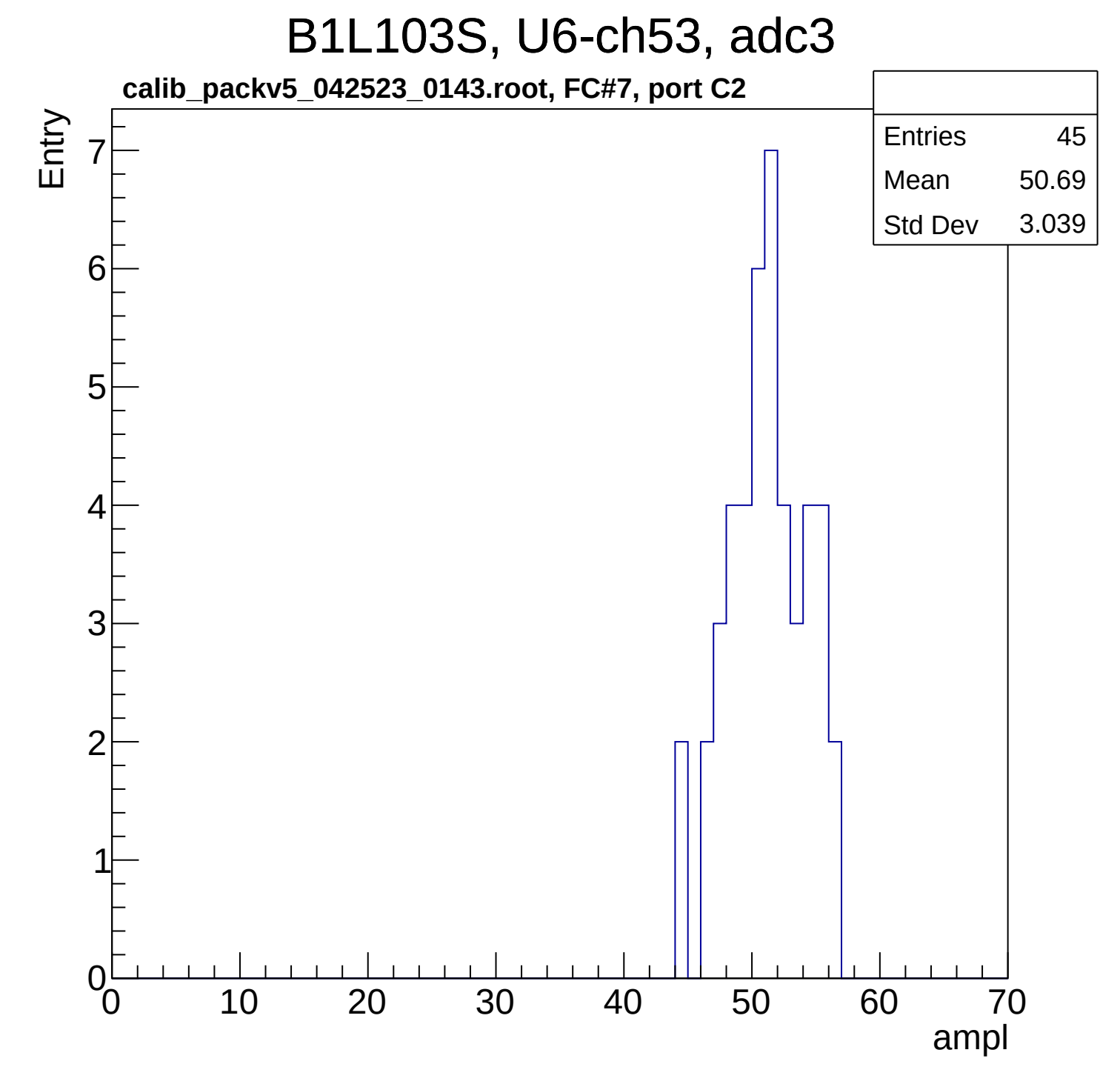
B1L103S, U6-ch53, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 50.69
Std Dev 3.039
Entry
ampl
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4
5
6
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0
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40
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60
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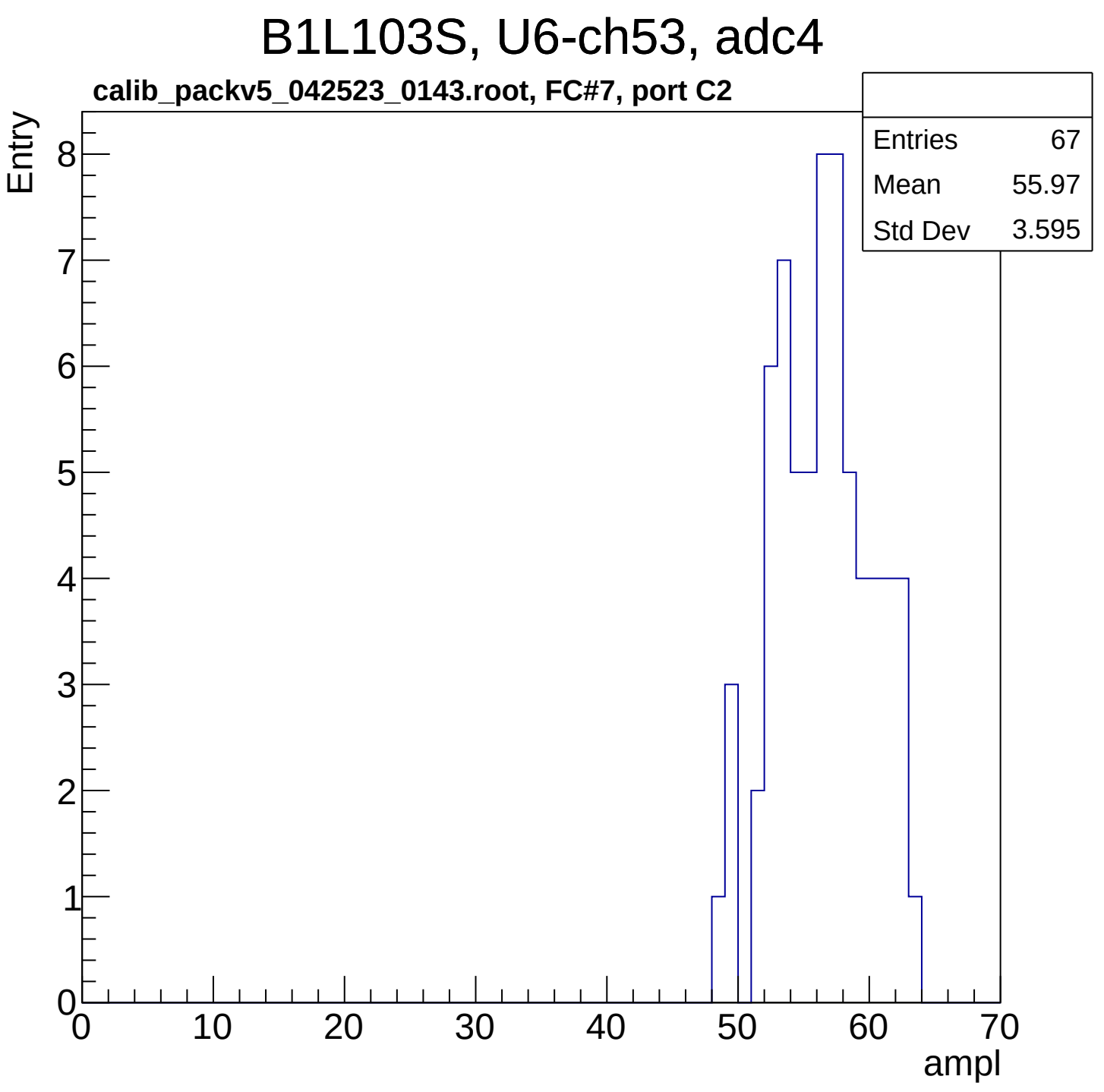
B1L103S, U6-ch53, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 55.97
Std Dev 3.595
Entry
ampl
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1
2
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4
5
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0
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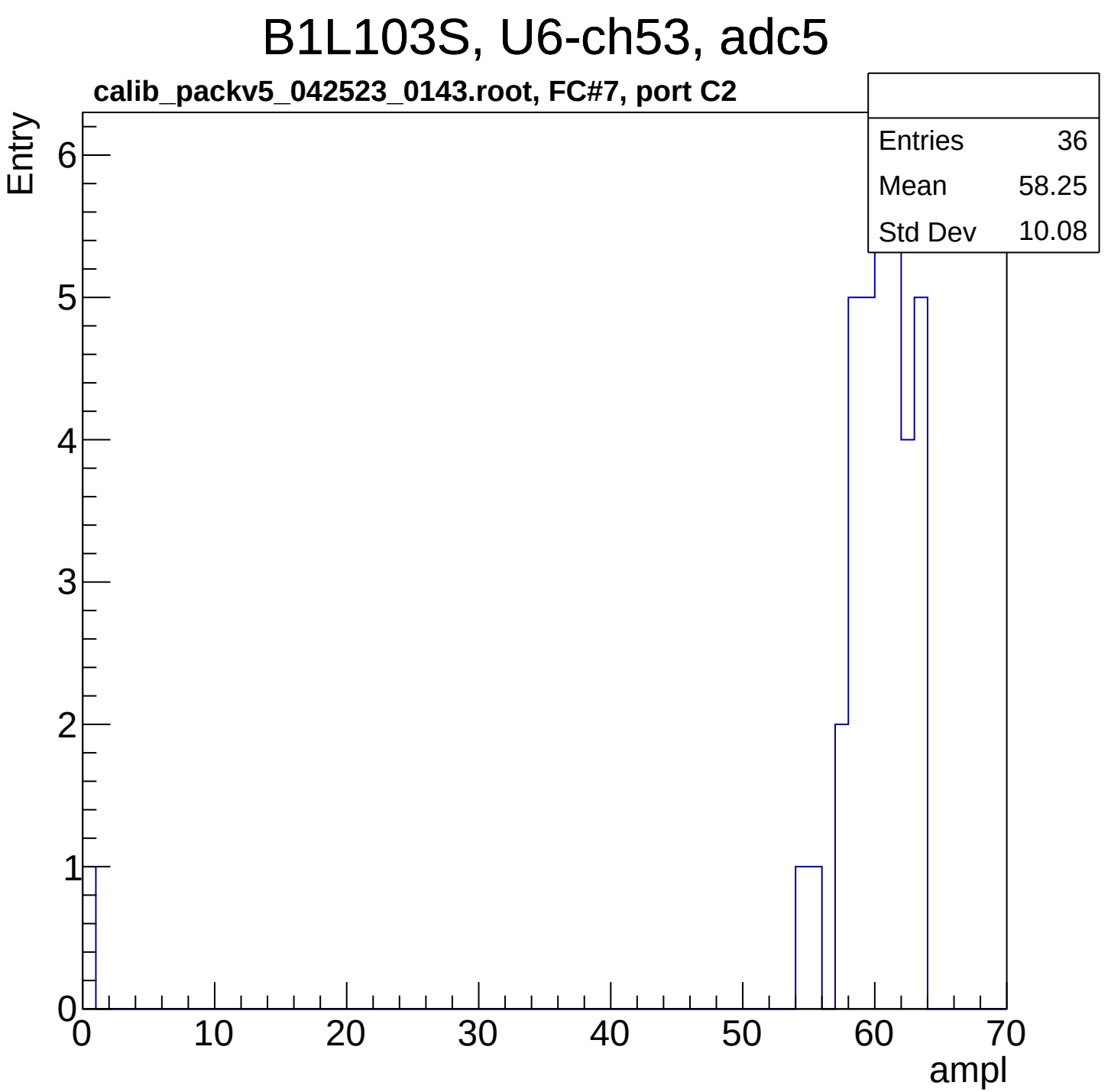
B1L103S, U6-ch53, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.25
Std Dev 10.08
Entry
0
1
2
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4
5
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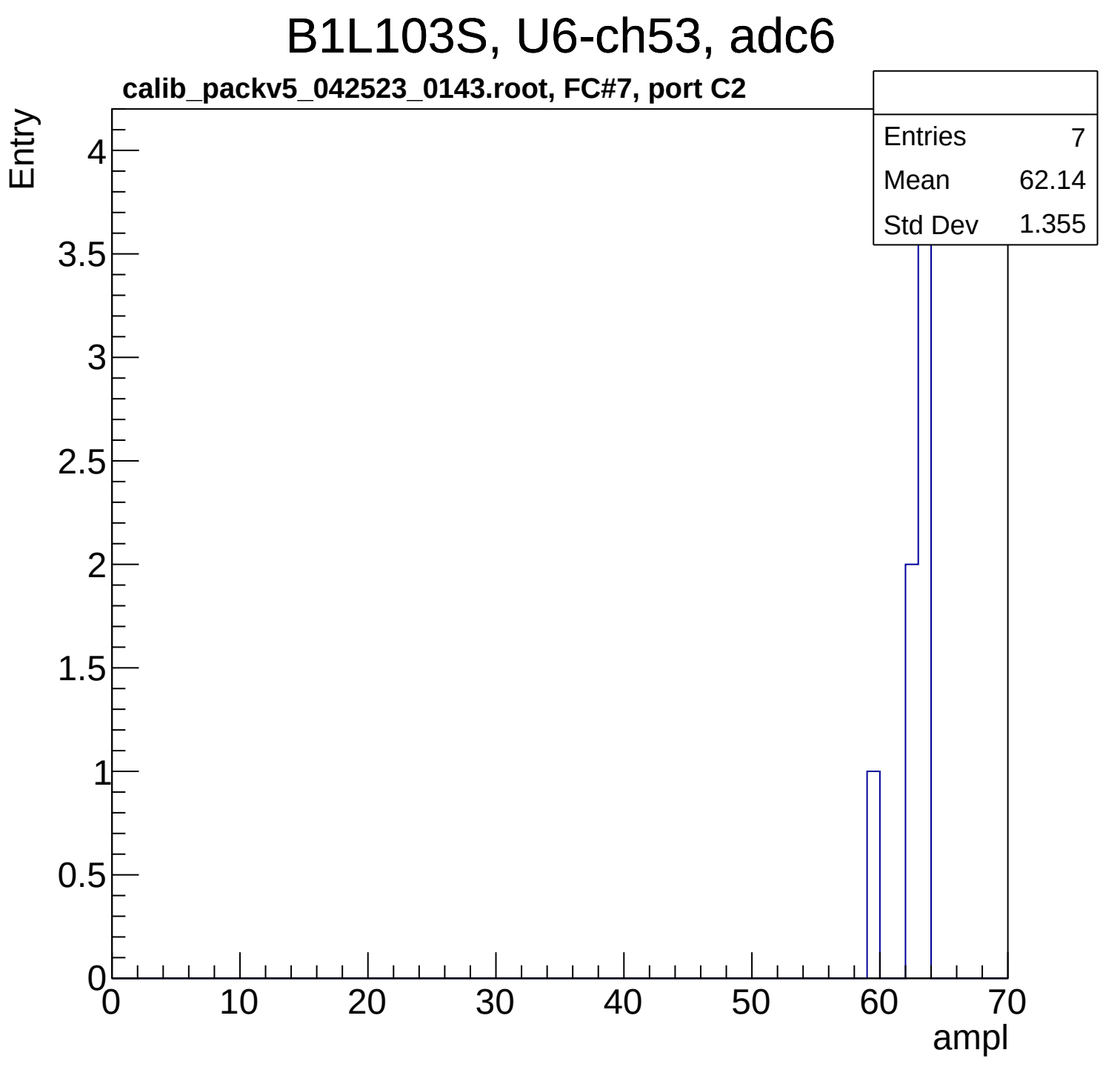
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch53, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.14
Std Dev 1.355
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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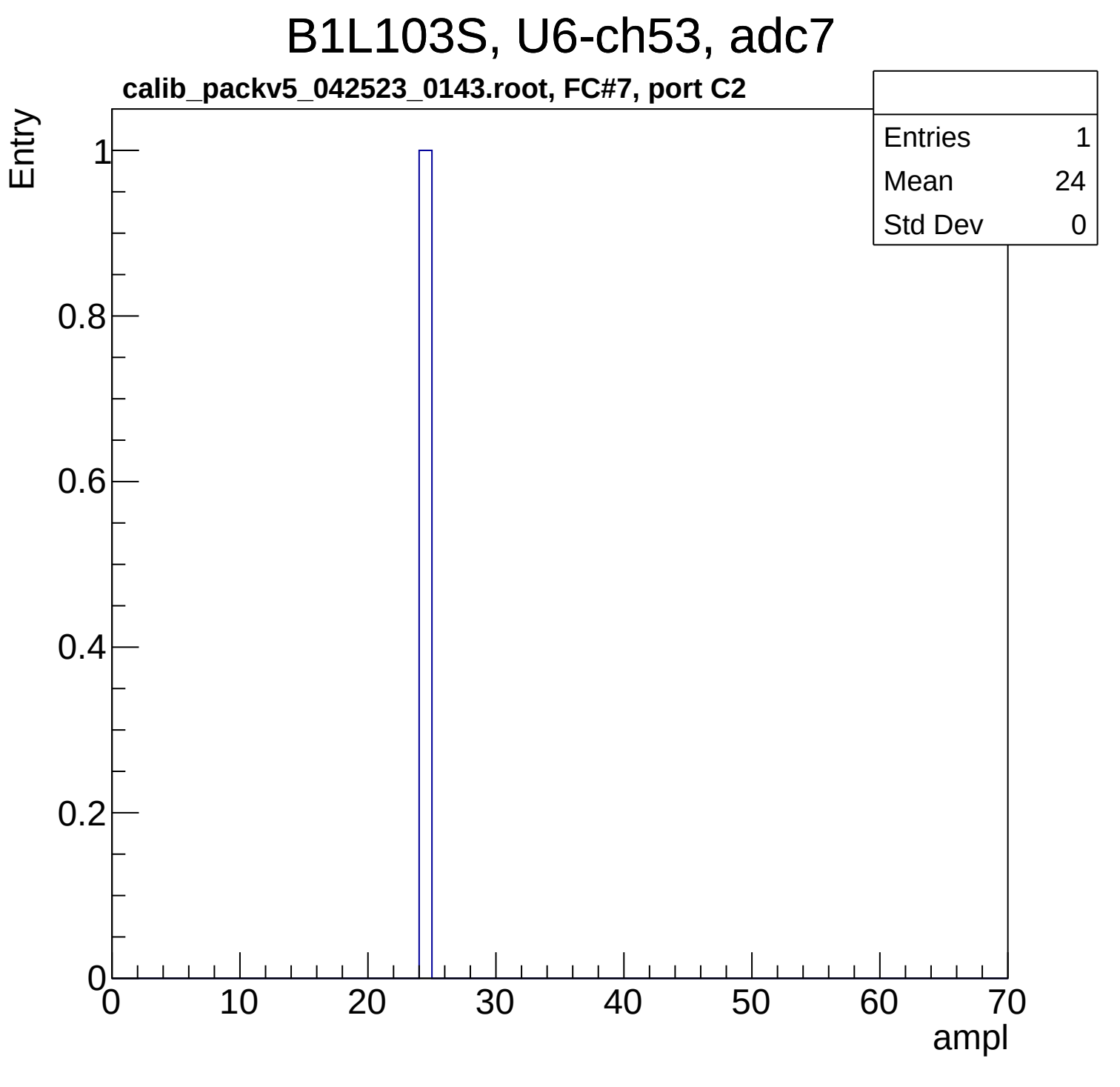
B1L103S, U6-ch53, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
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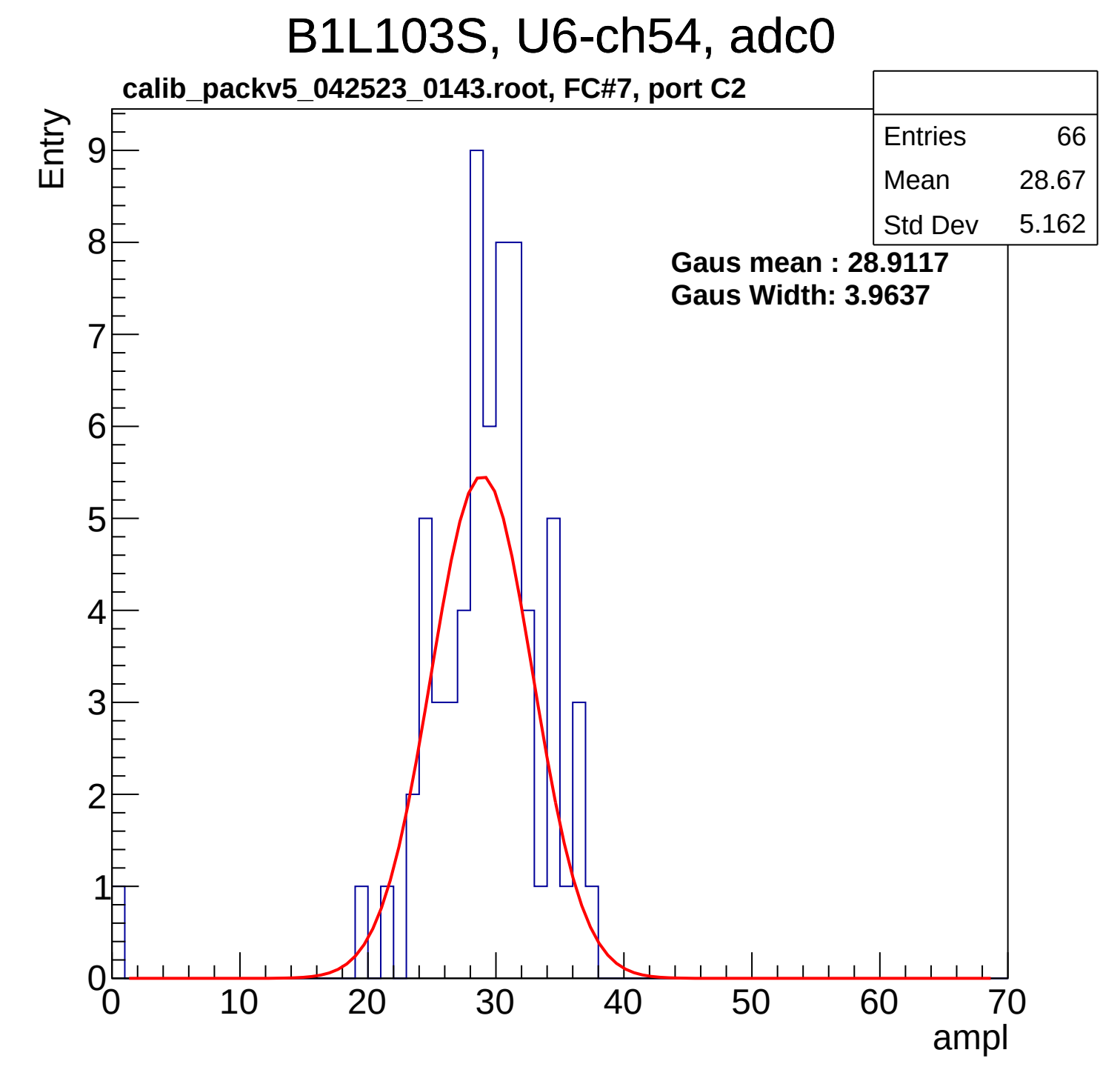
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch54, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 28.67
Std Dev 5.162
Gaus mean : 28.9117
Gaus Width: 3.9637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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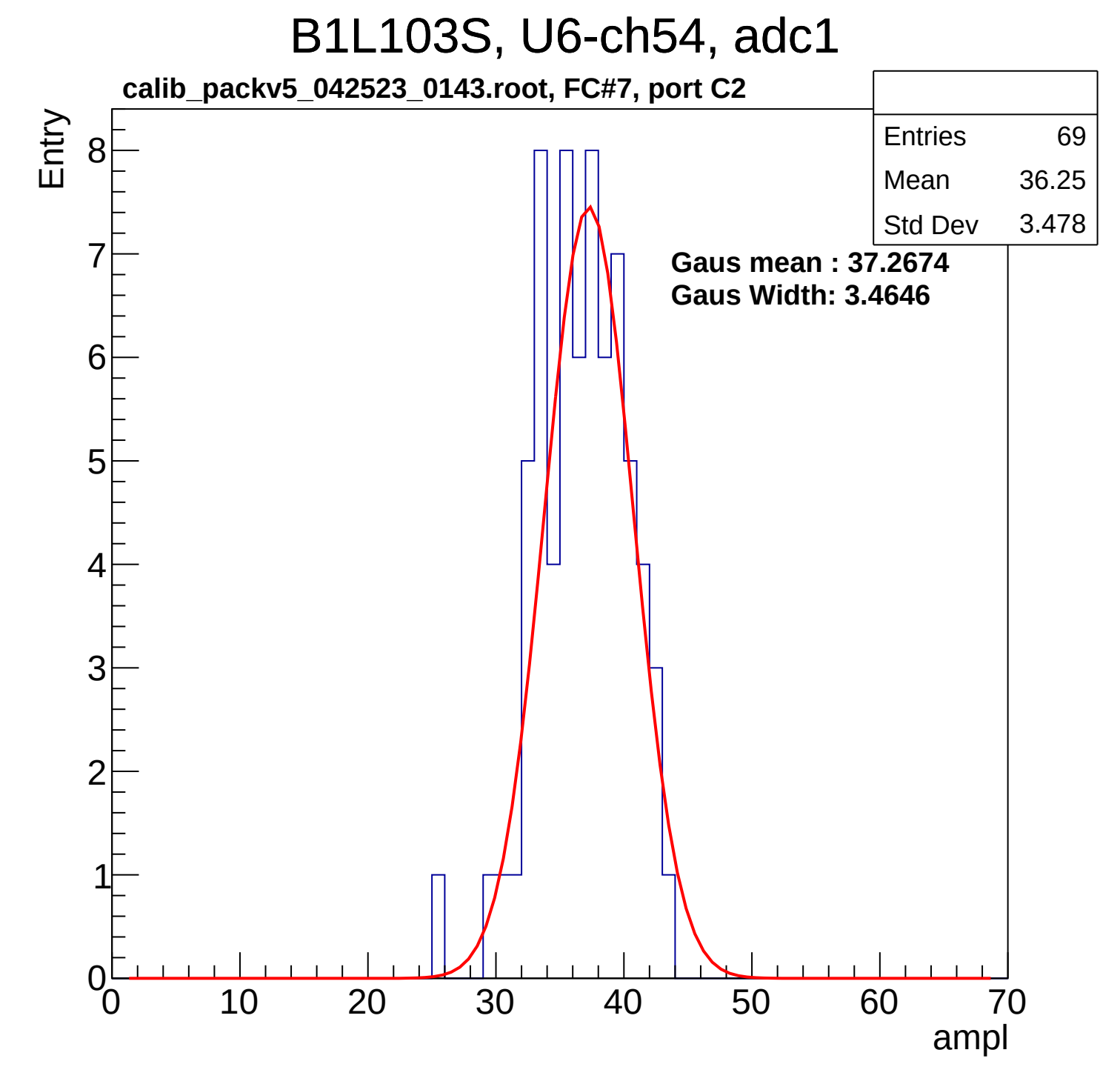
B1L103S, U6-ch54, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.25
Std Dev 3.478
Gaus mean : 37.2674
Gaus Width: 3.4646
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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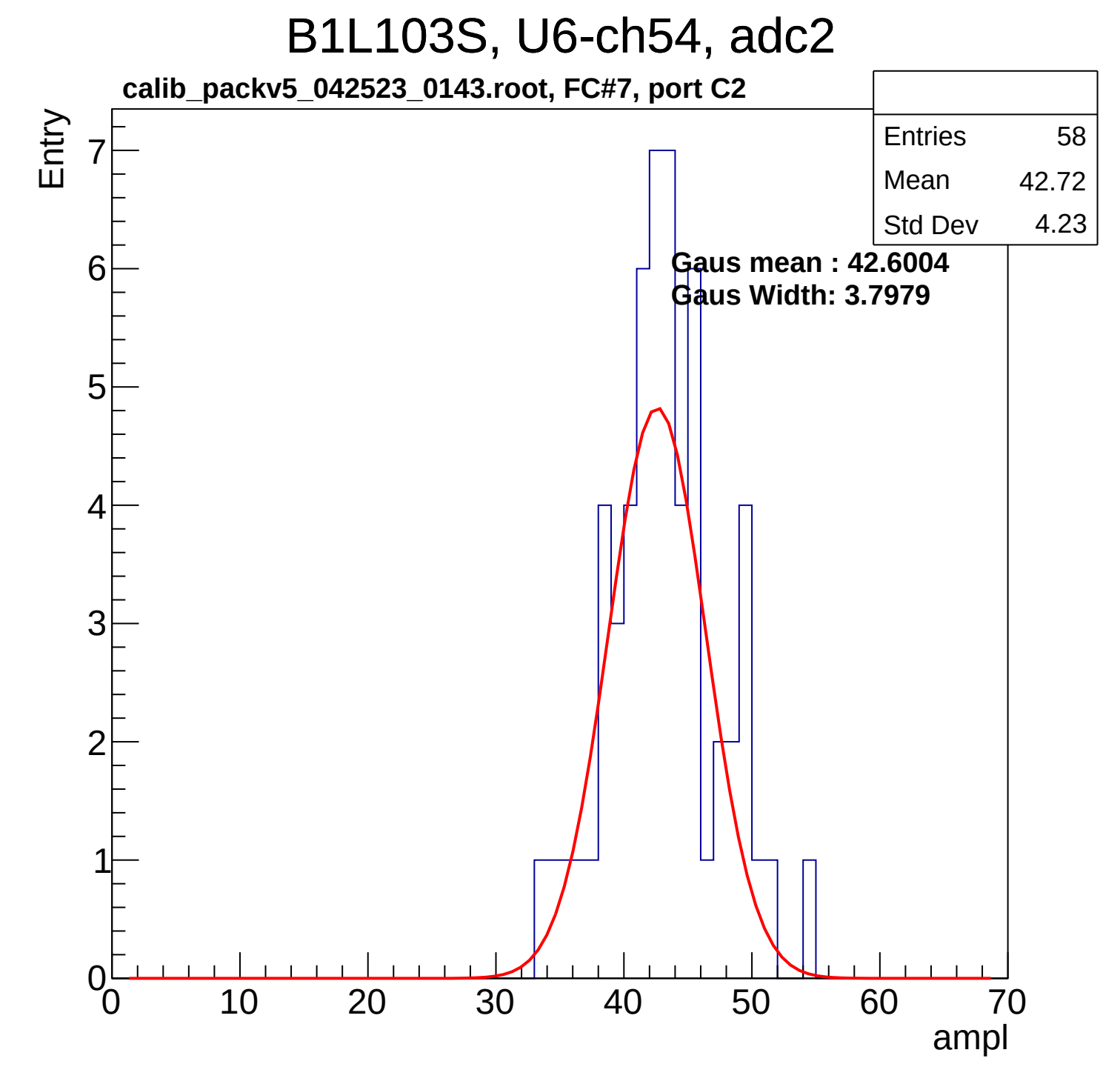
B1L103S, U6-ch54, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 42.72
Std Dev 4.23
Gaus mean : 42.6004
Gaus Width: 3.7979
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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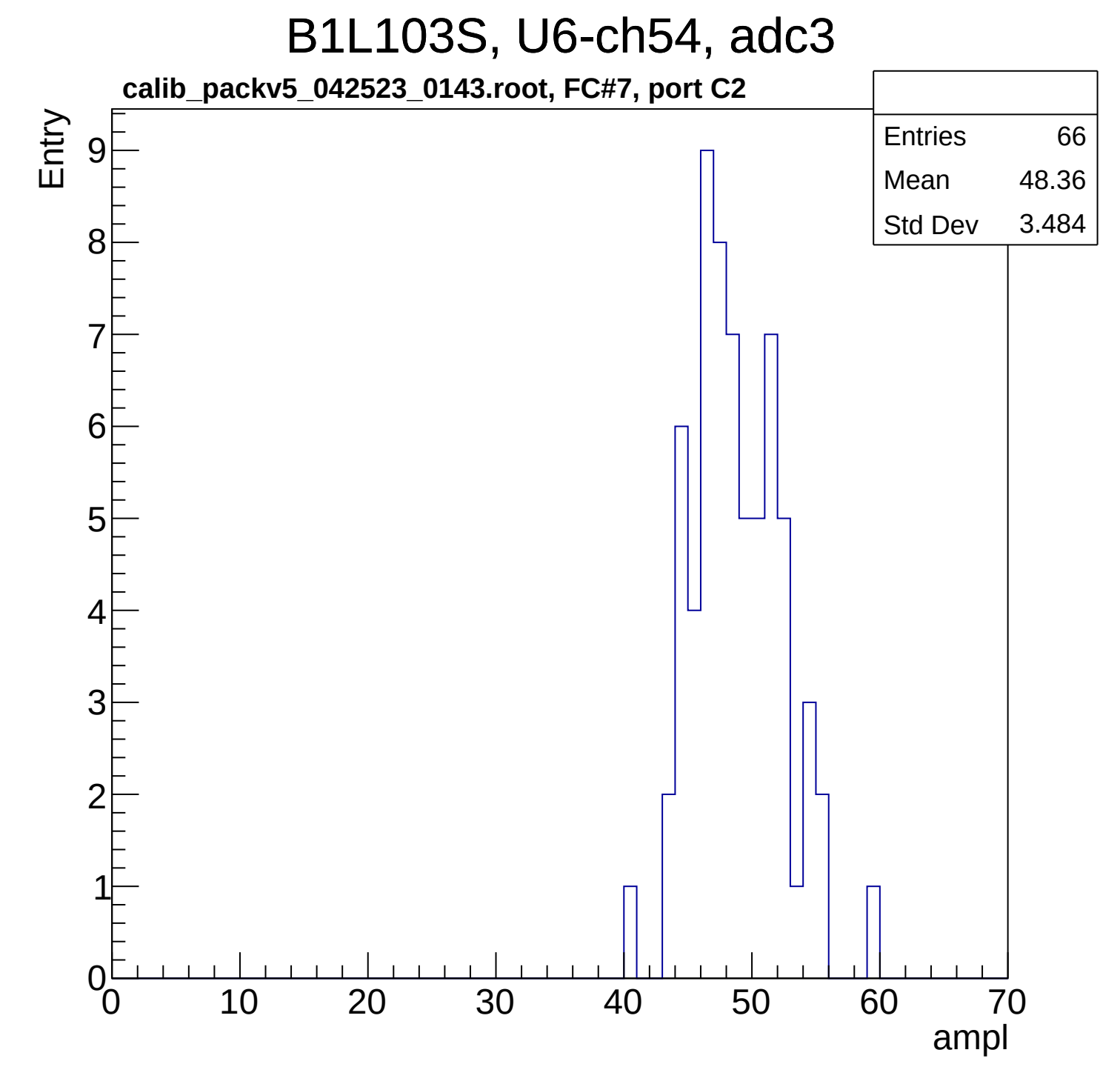
B1L103S, U6-ch54, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.36
Std Dev 3.484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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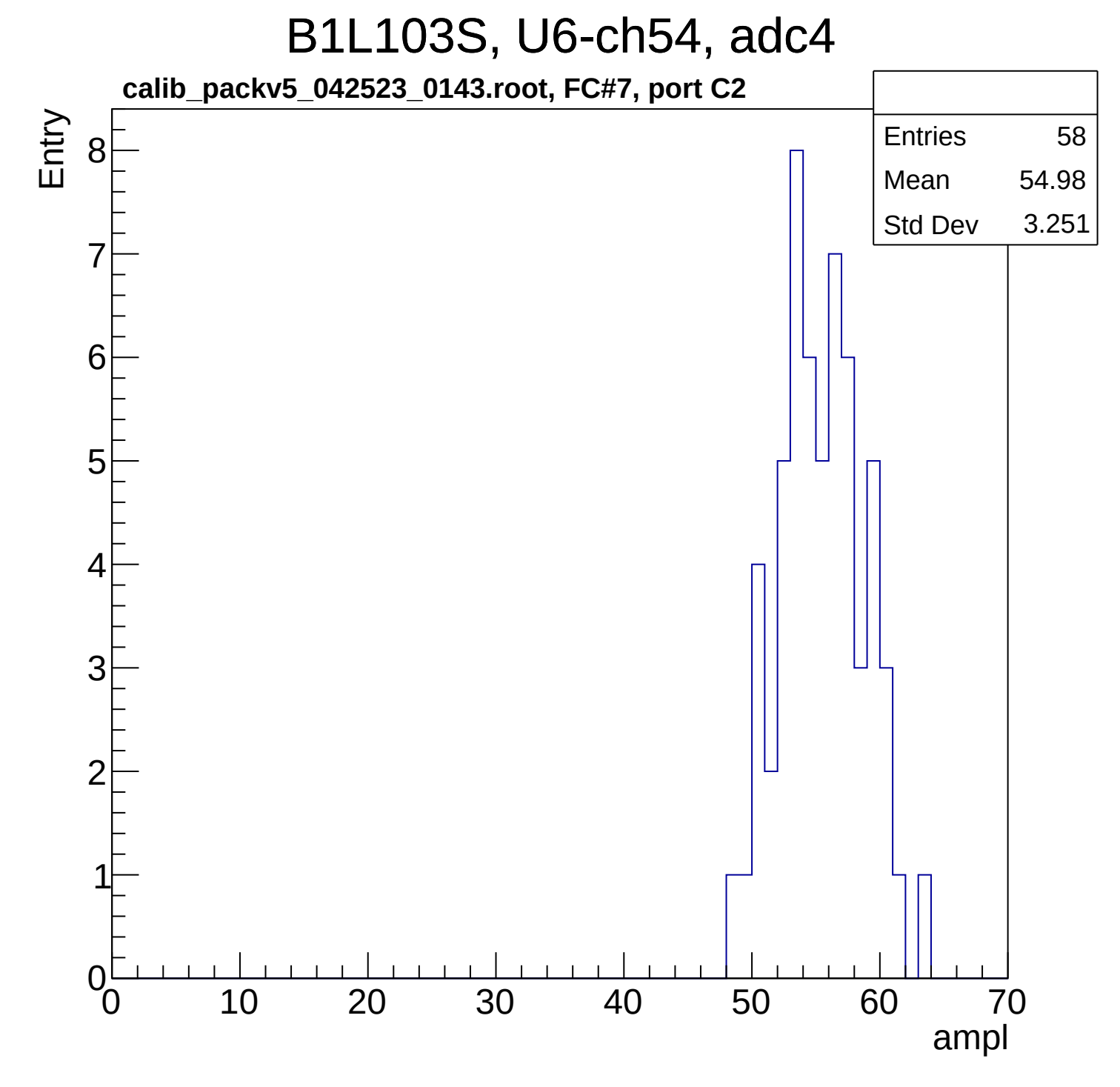
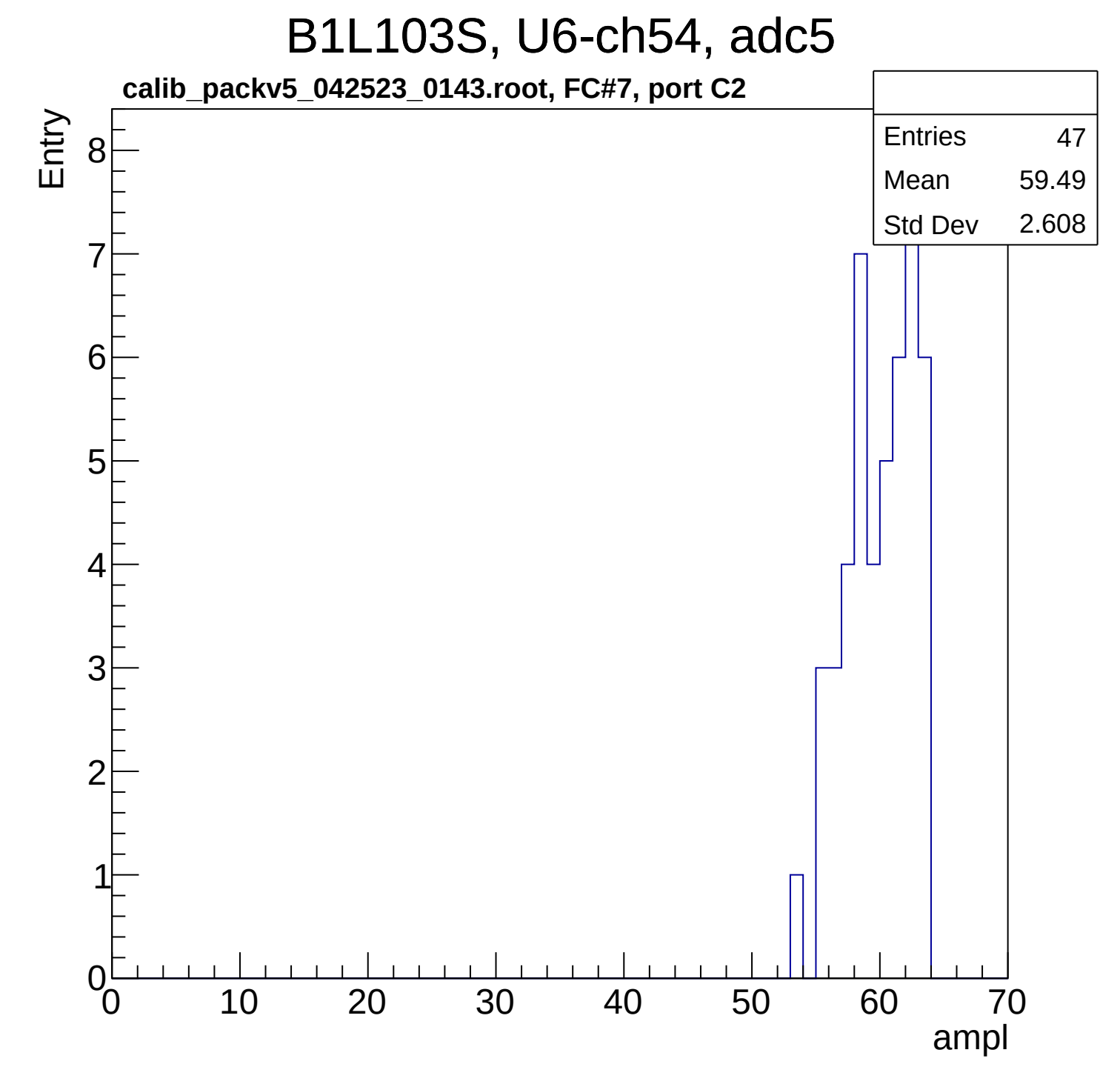

B1L103S, U6-ch54, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 54.98
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch54, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.49
Std Dev 2.608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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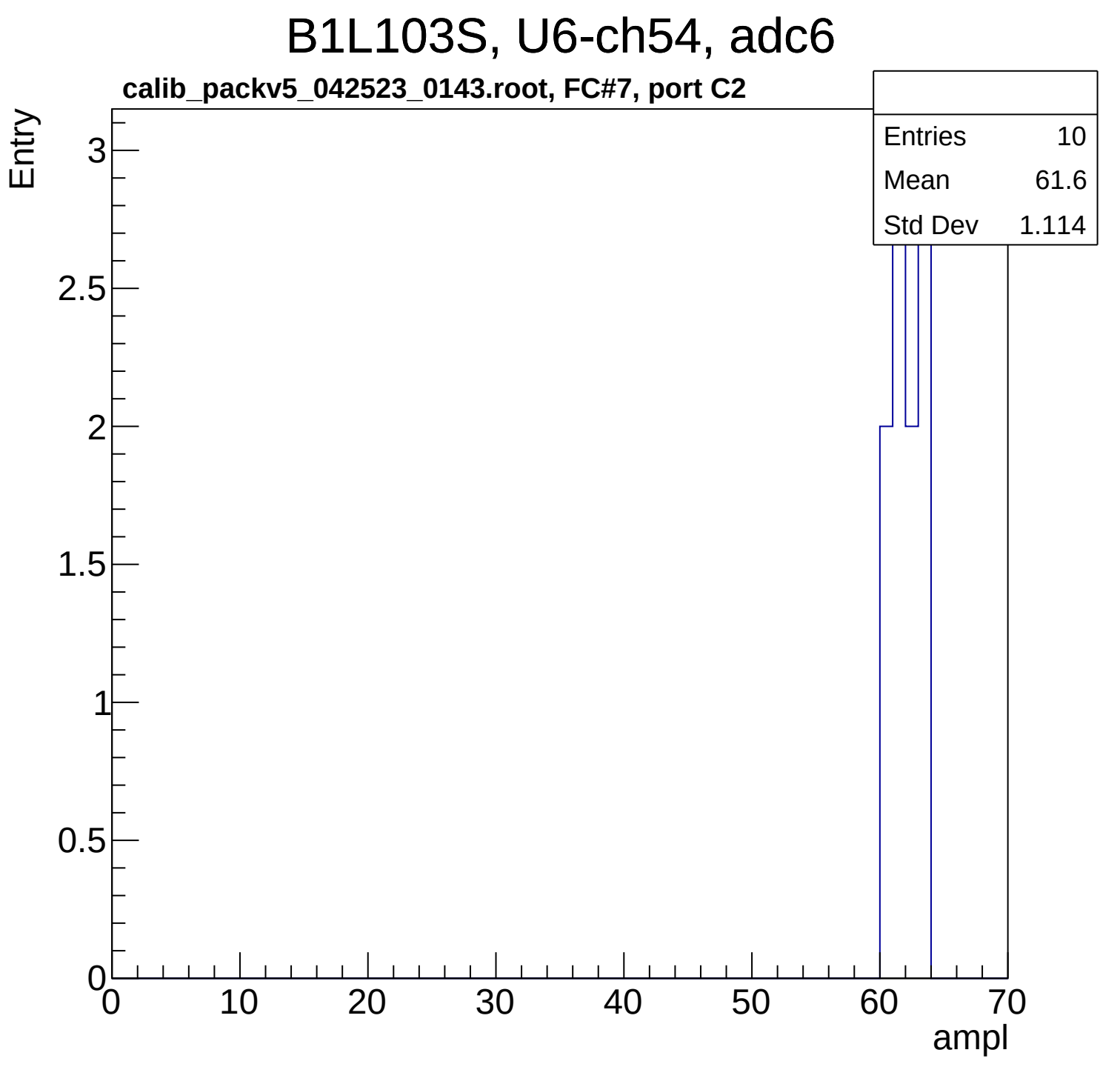
B1L103S, U6-ch54, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.6
Std Dev 1.114
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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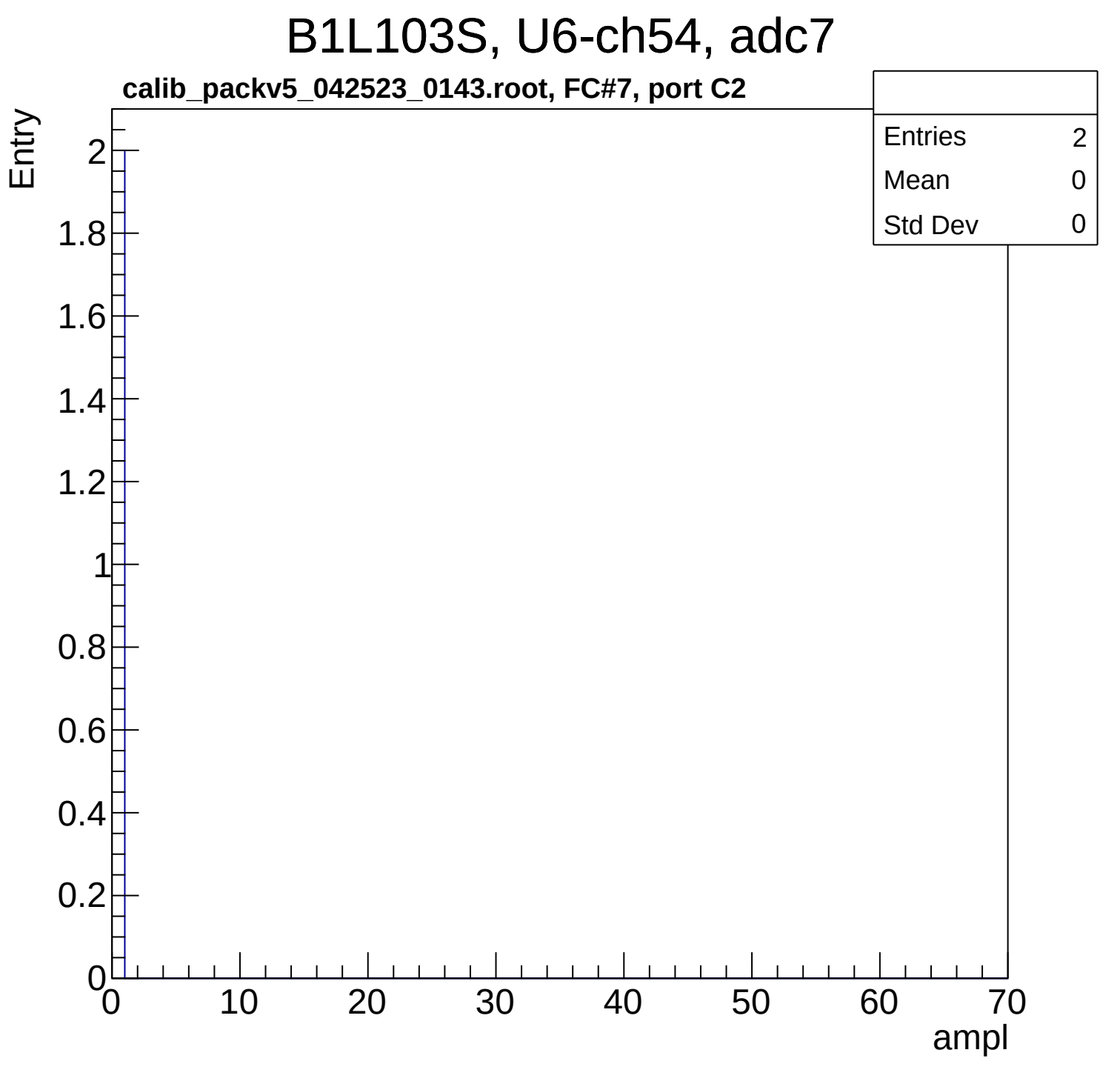
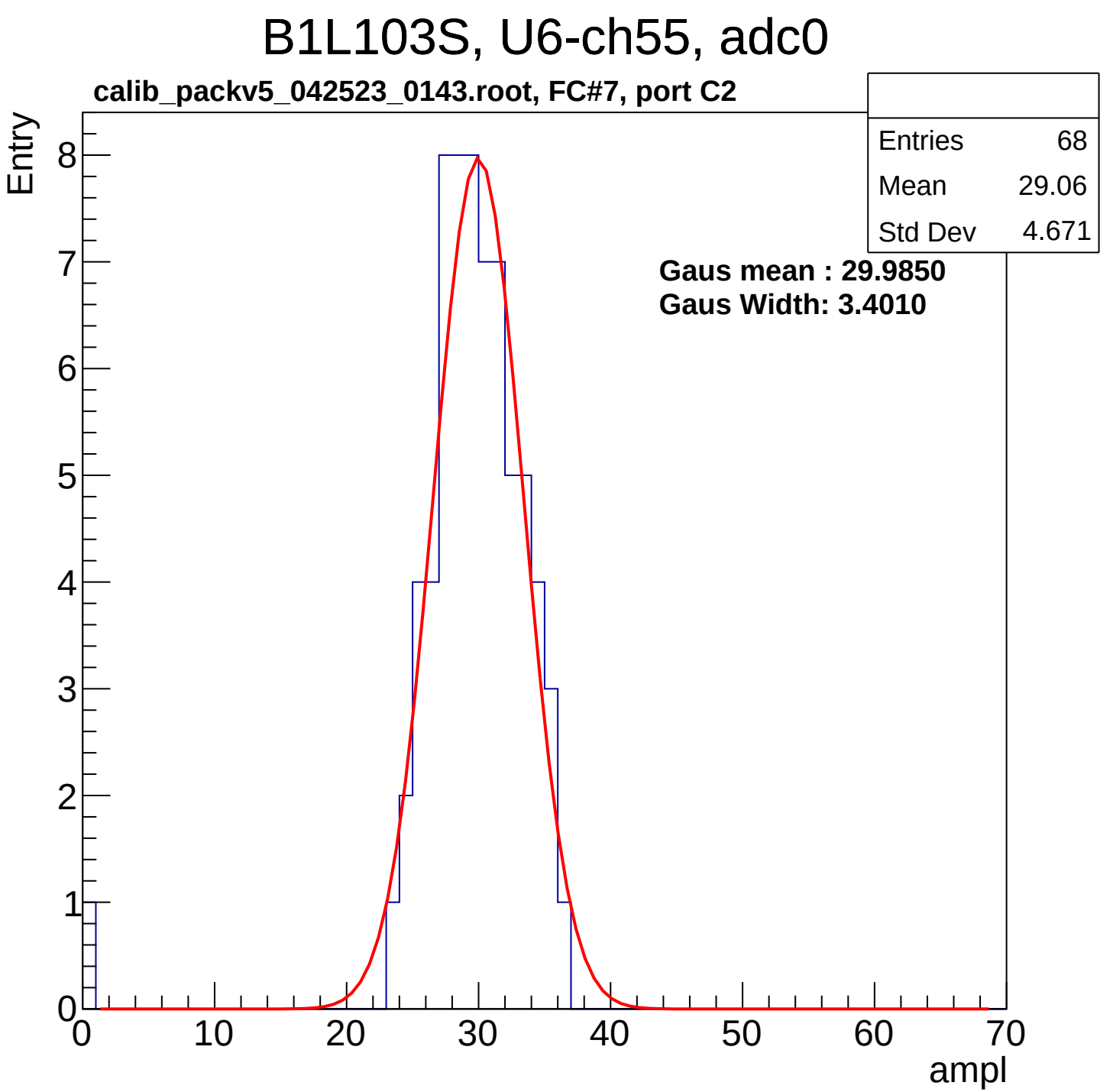

B1L103S, U6-ch54, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch55, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.06
Std Dev 4.671
Gaus mean : 29.9850
Gaus Width: 3.4010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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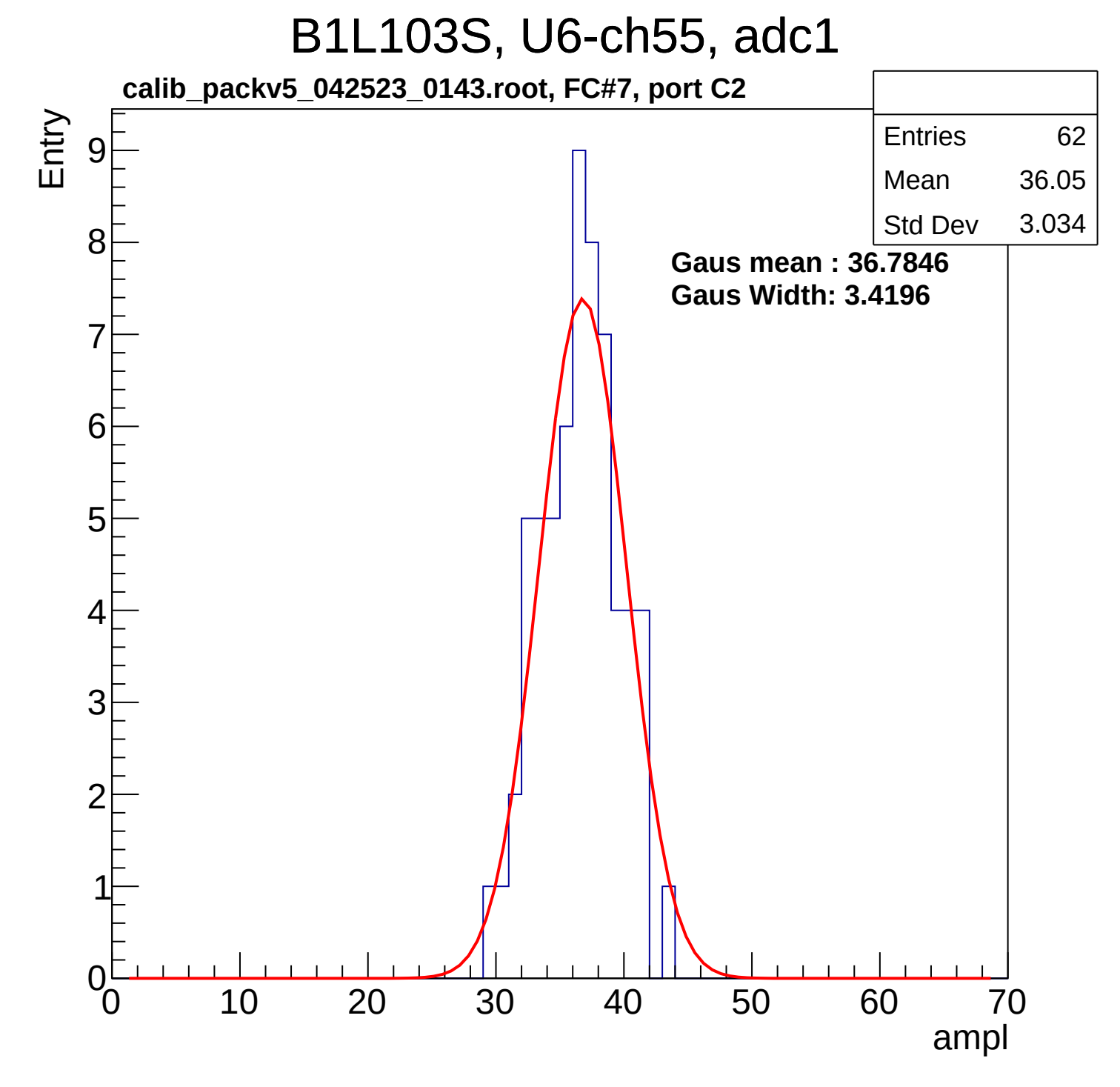
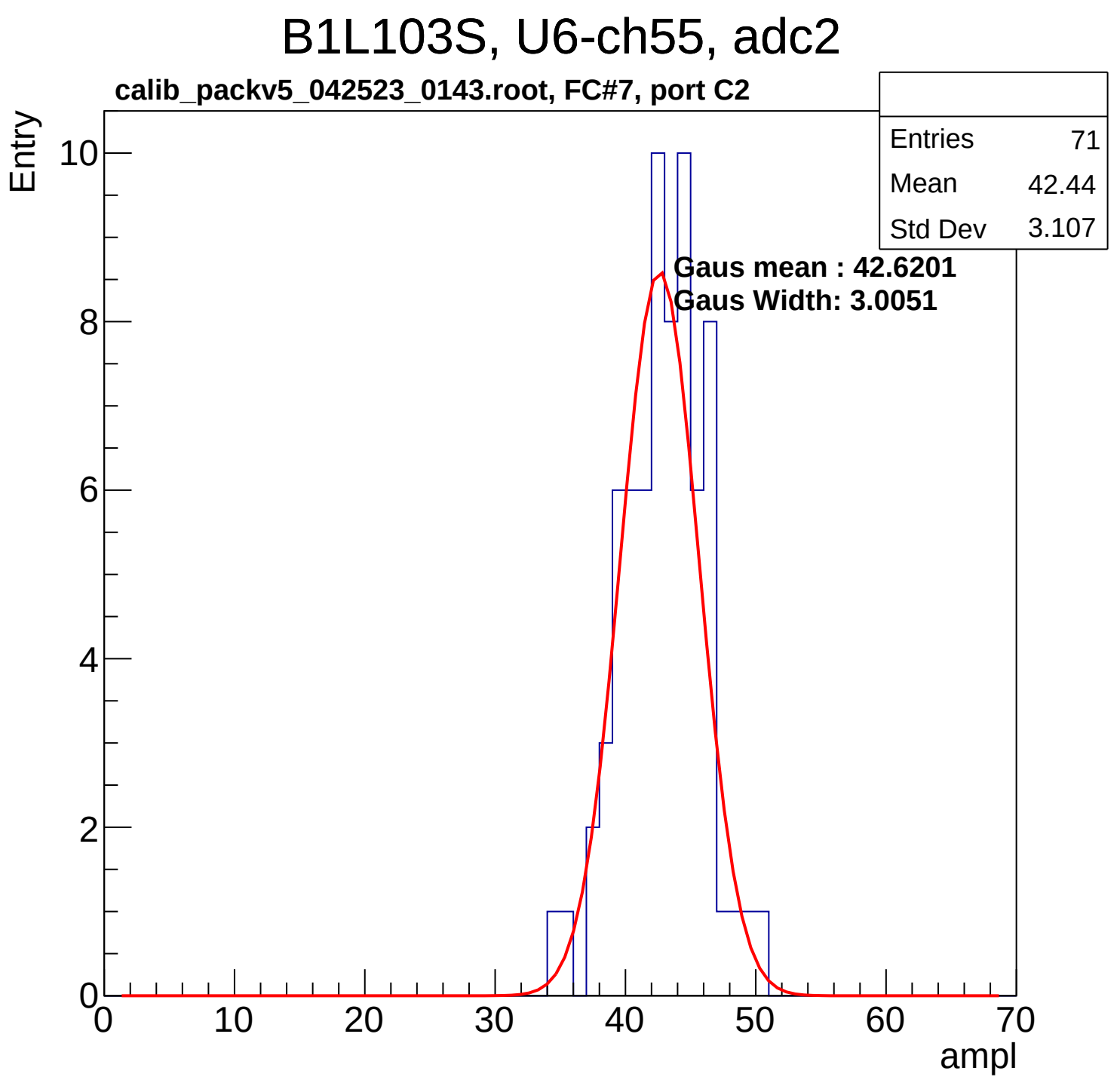
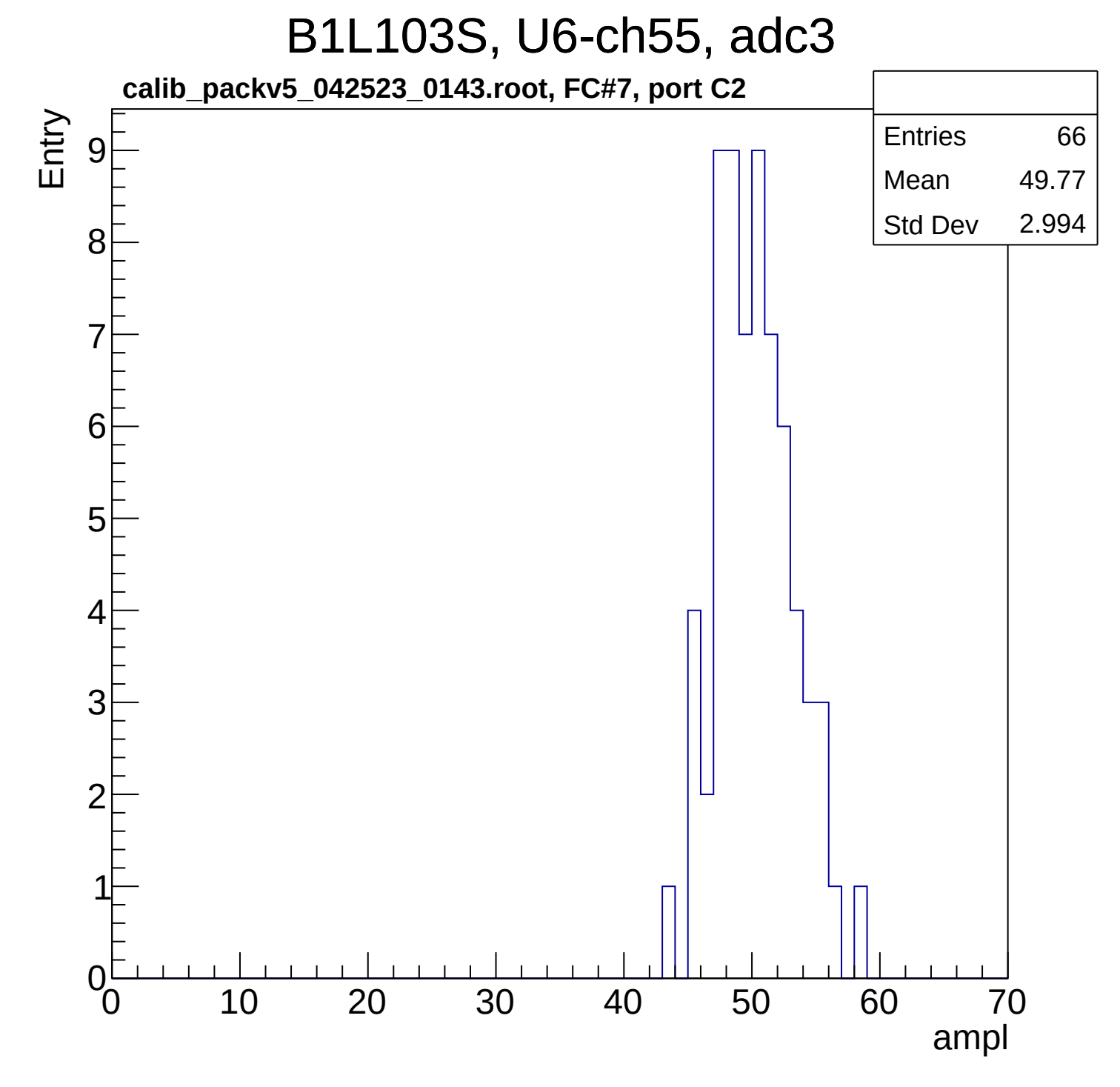

B1L103S, U6-ch55, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 36.05
Std Dev 3.034
Gaus mean : 36.7846
Gaus Width: 3.4196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch55, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.44
Std Dev 3.107
Gaus mean : 42.6201
Gaus Width: 3.0051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch55, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 49.77
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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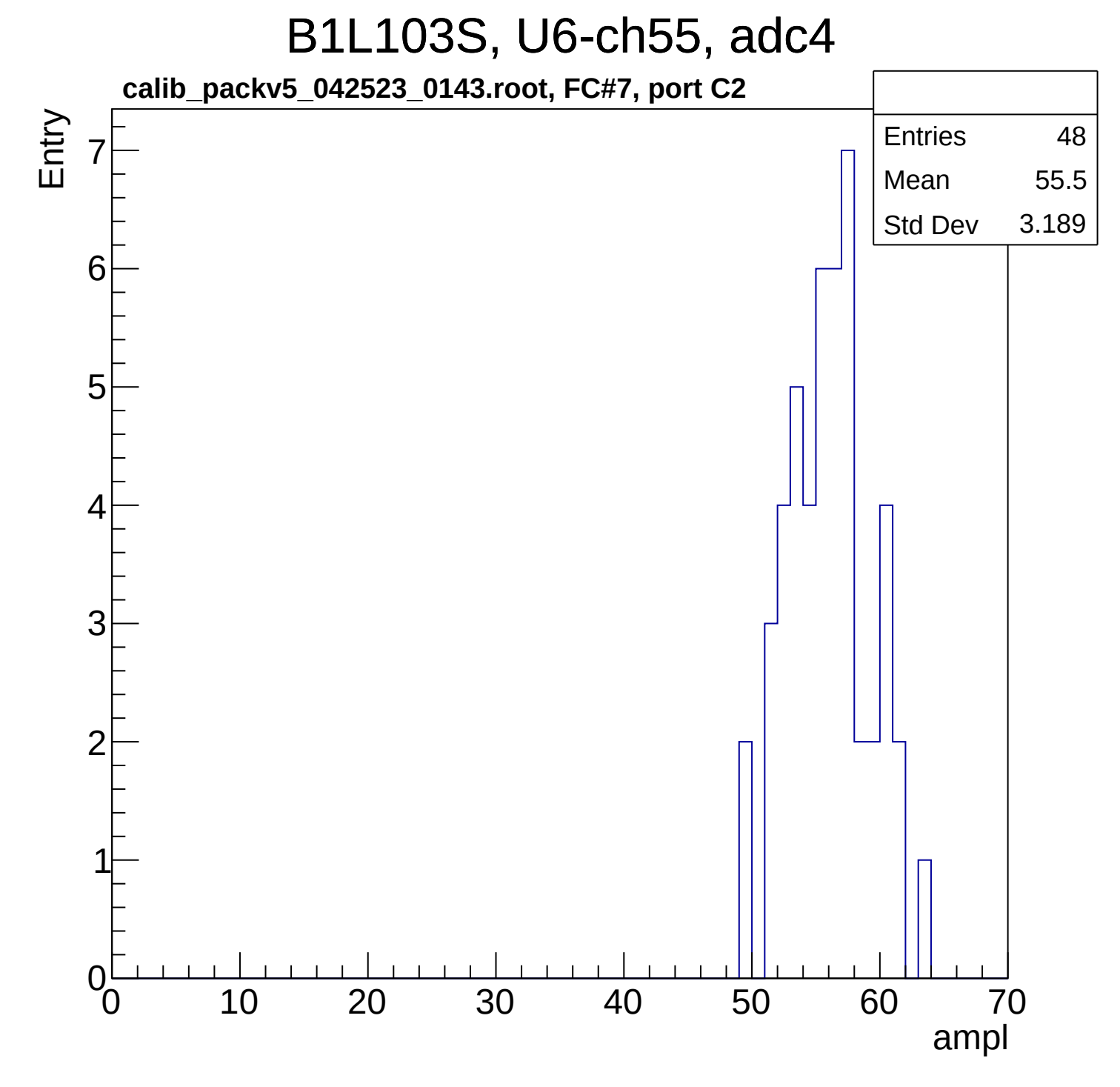
B1L103S, U6-ch55, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 55.5
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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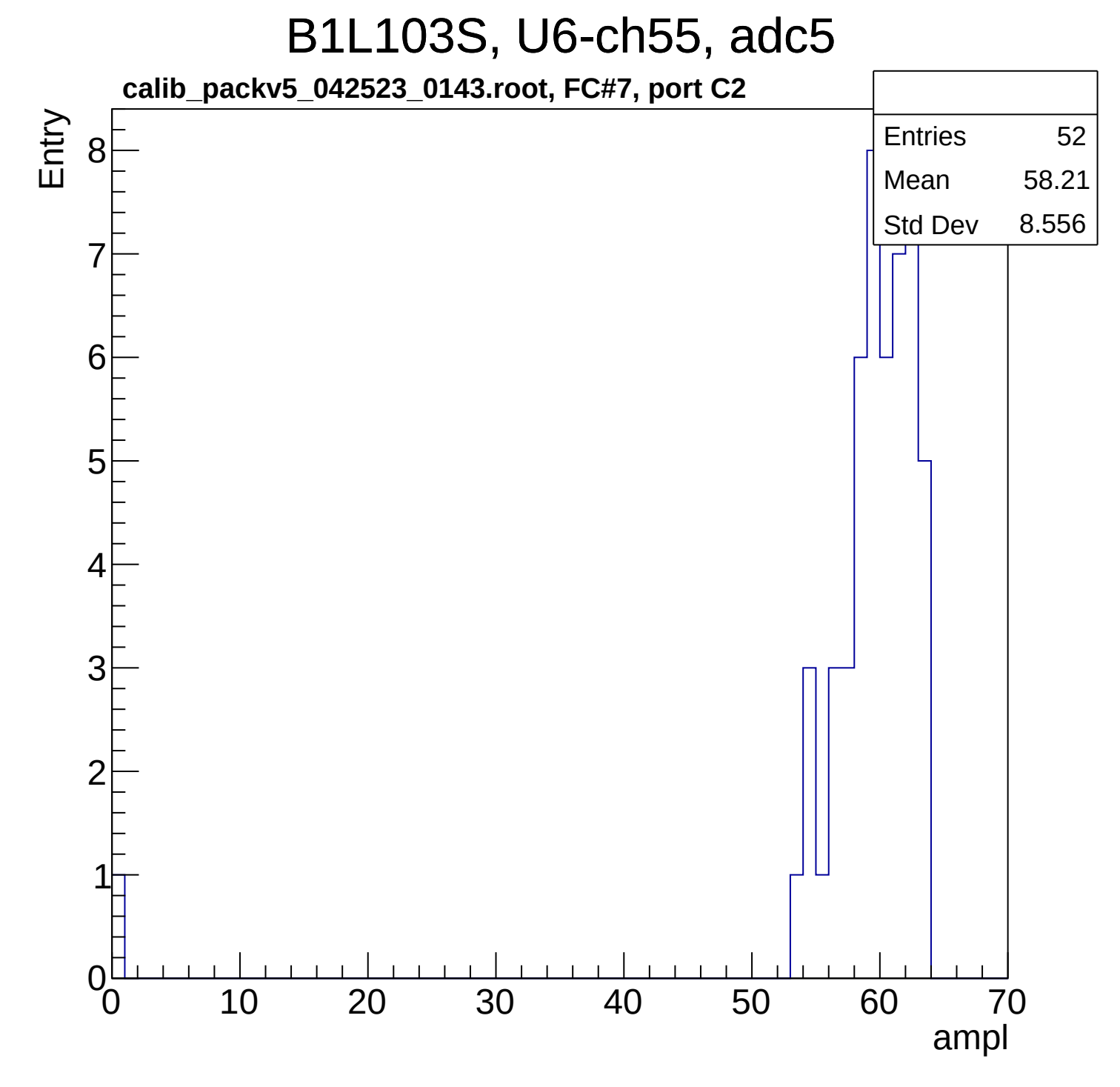
B1L103S, U6-ch55, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.21
Std Dev 8.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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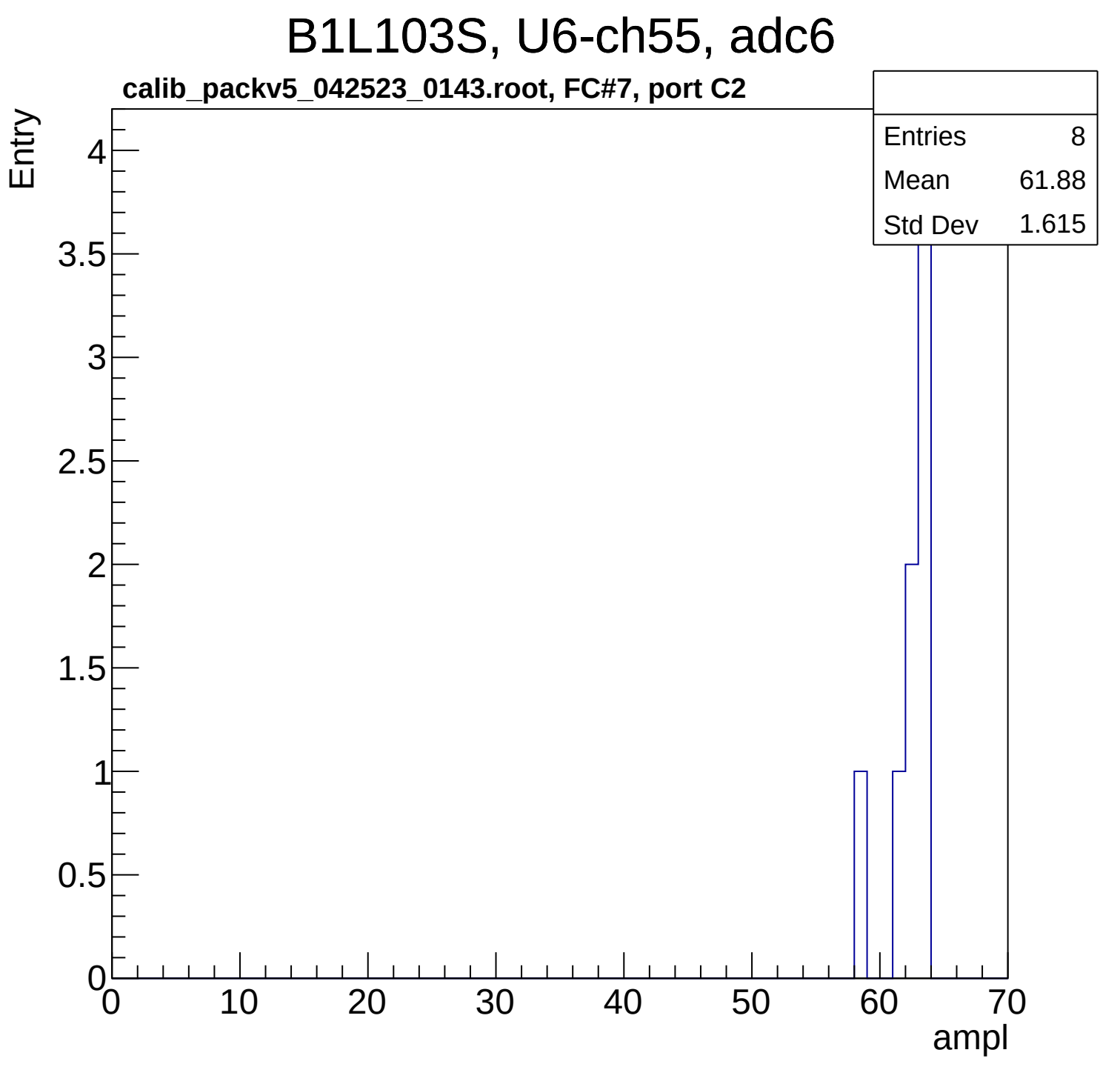
B1L103S, U6-ch55, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.88
Std Dev 1.615
Entry
ampl
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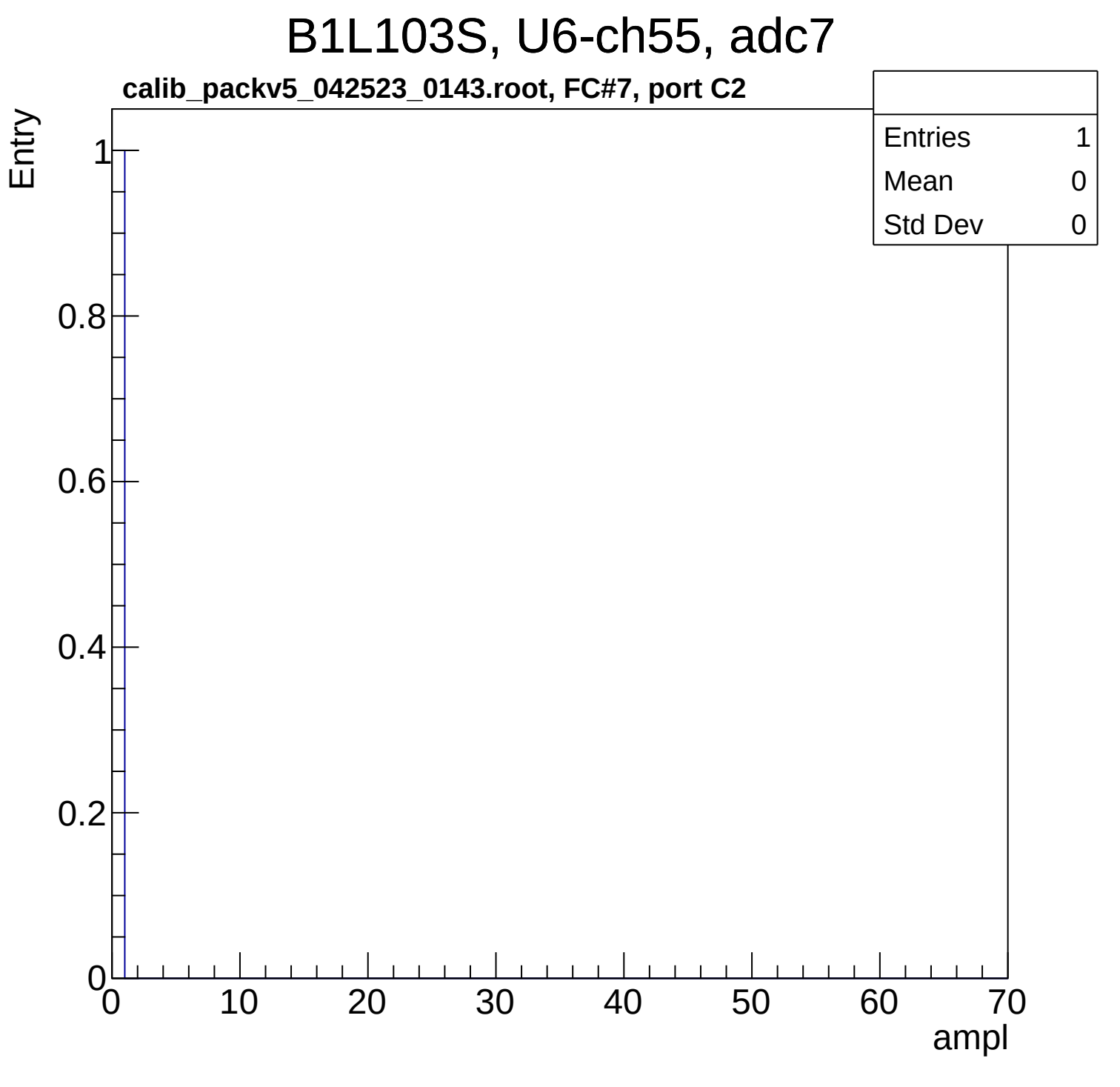
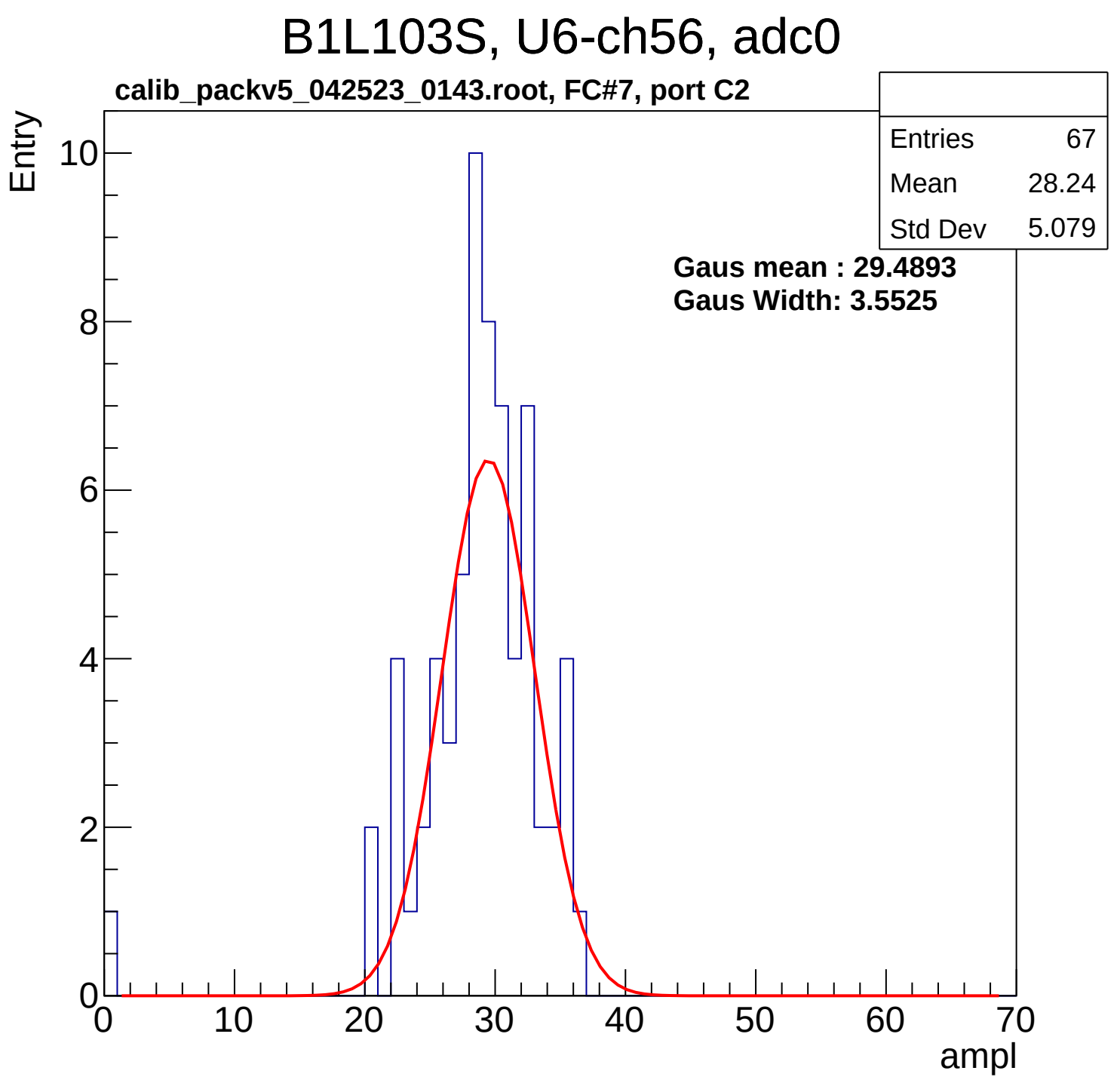
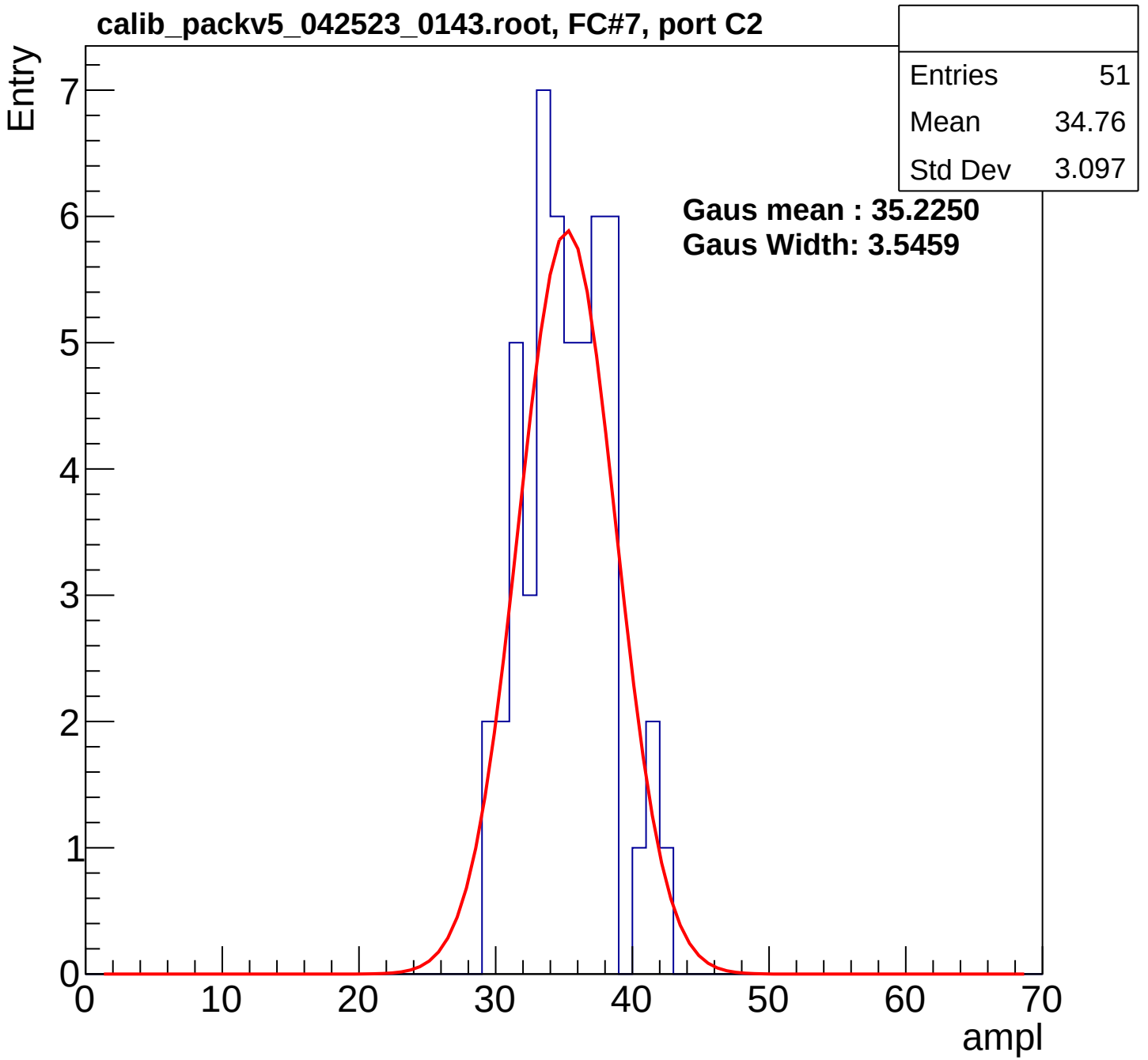
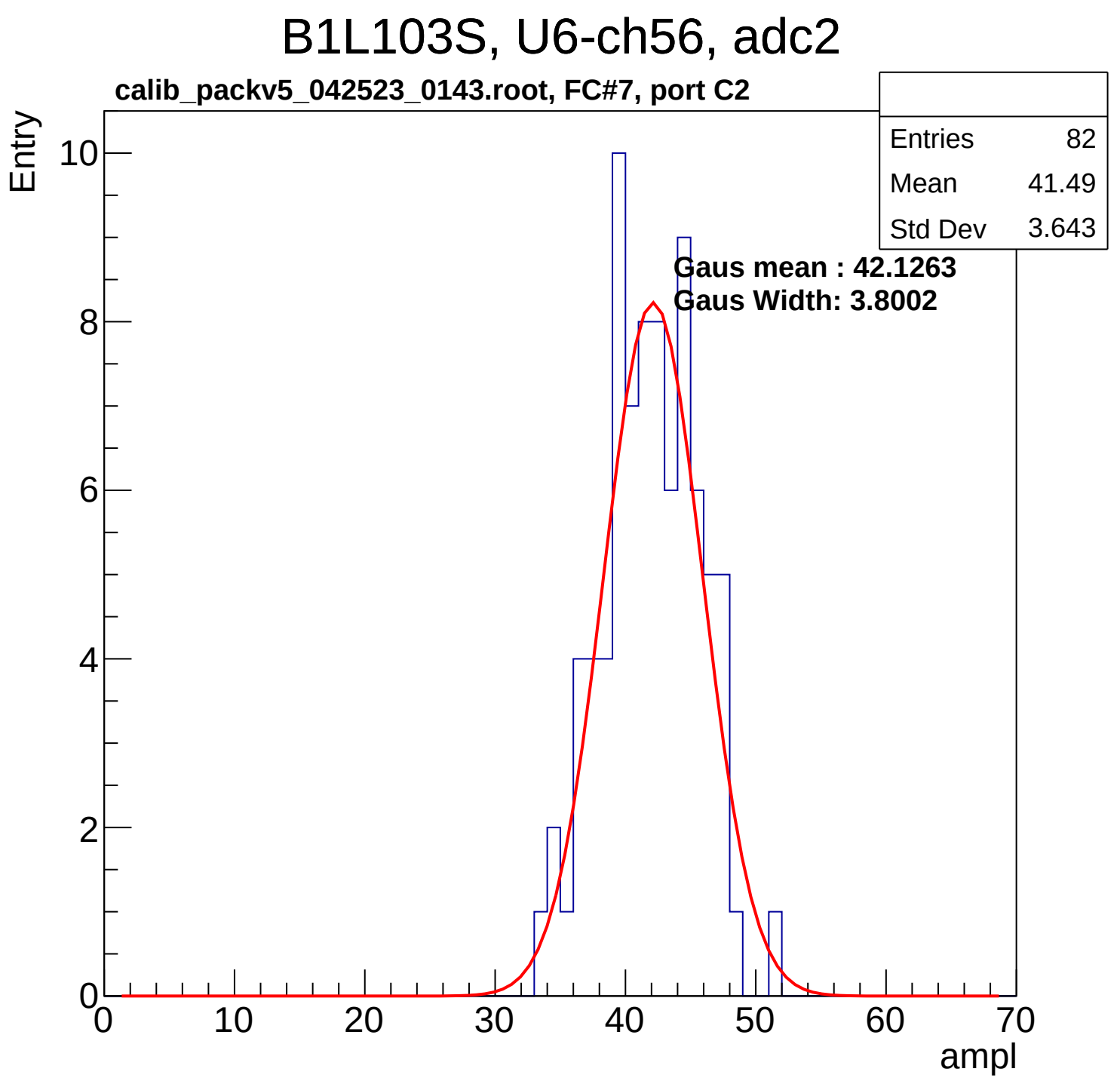
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch55, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch56, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.24
Std Dev 5.079
Gaus mean : 29.4893
Gaus Width: 3.5525
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch56, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 34.76
Std Dev 3.097
Gaus mean : 35.2250
Gaus Width: 3.5459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch56, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 41.49
Std Dev 3.643
Gaus mean : 42.1263
Gaus Width: 3.8002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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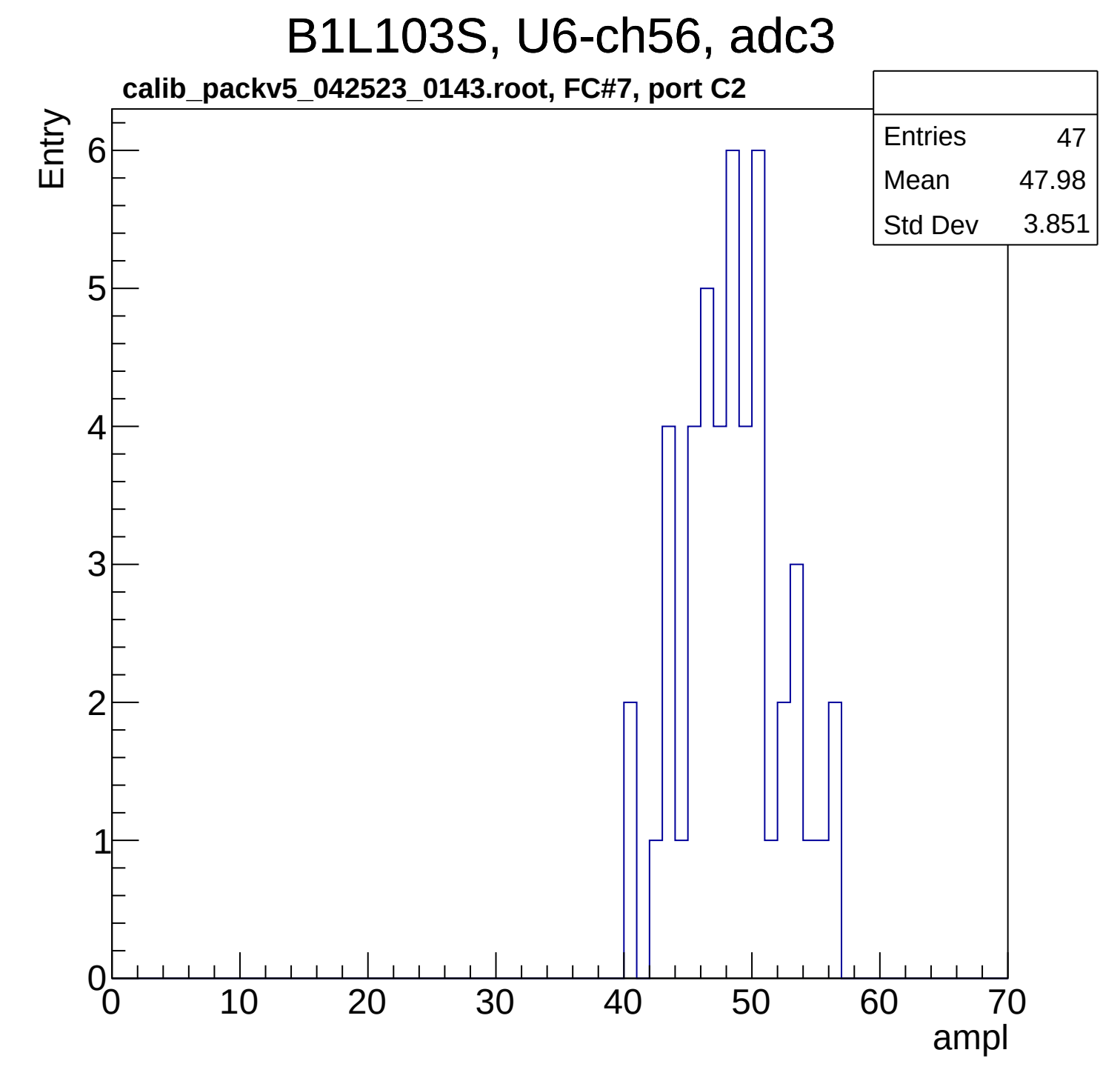
B1L103S, U6-ch56, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 47.98
Std Dev 3.851
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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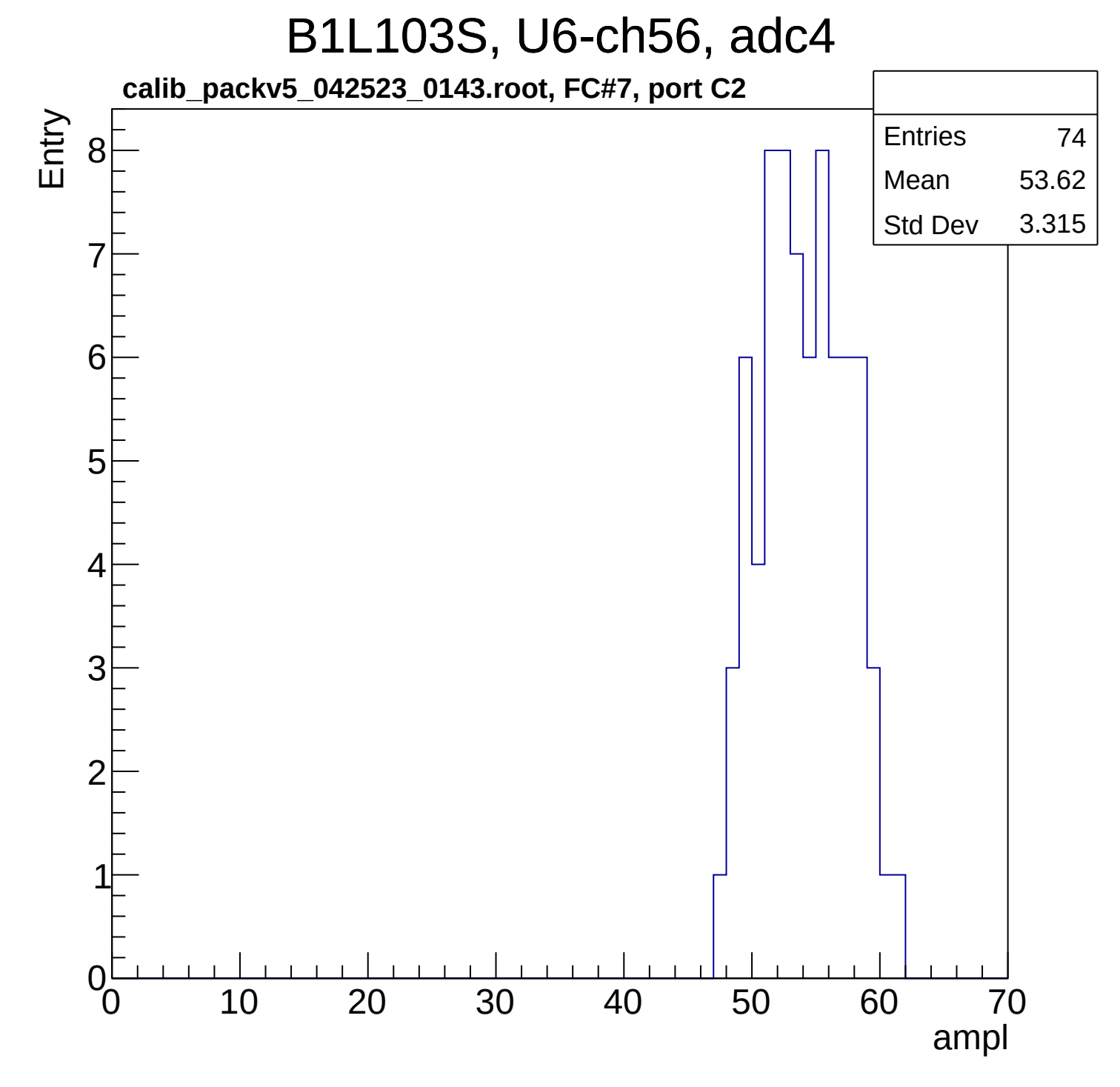
B1L103S, U6-ch56, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 53.62
Std Dev 3.315
Entry
ampl
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2
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4
5
6
7
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0
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20
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60
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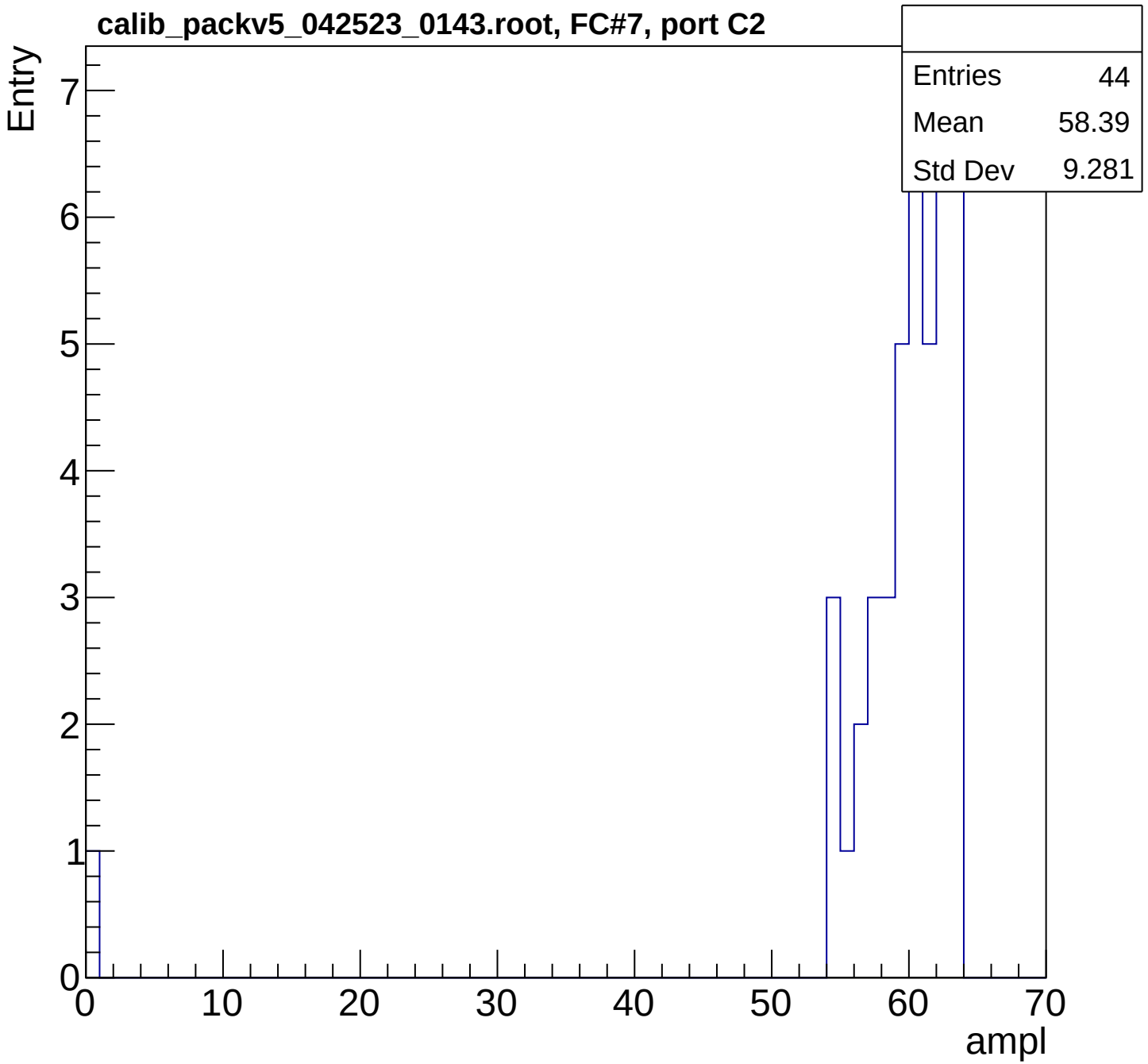
B1L103S, U6-ch56, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.39
Std Dev 9.281
Entry
ampl
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1
2
3
4
5
6
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0
10
20
30
40
50
60
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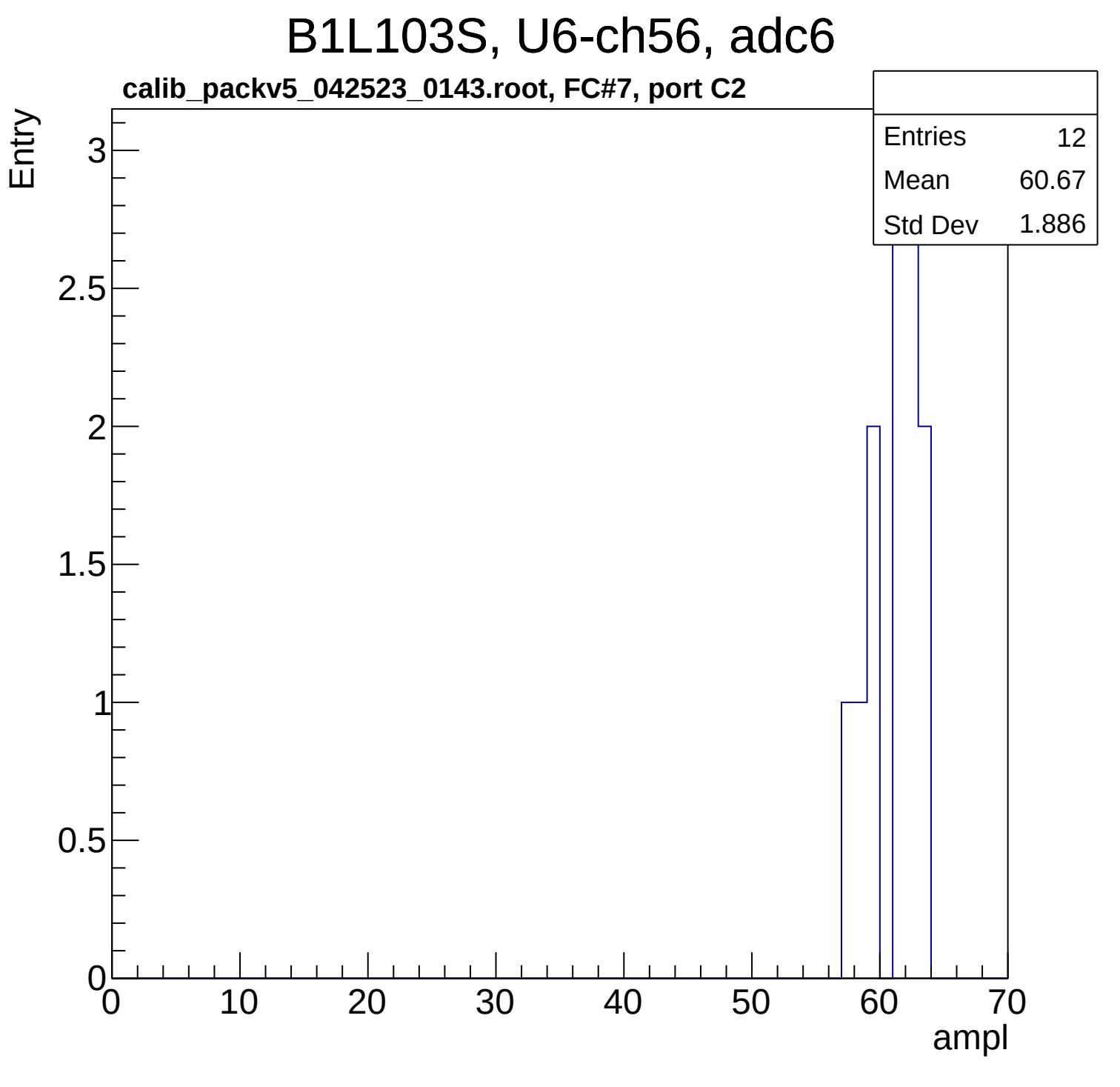
B1L103S, U6-ch56, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 60.67
Std Dev 1.886
Entry
ampl
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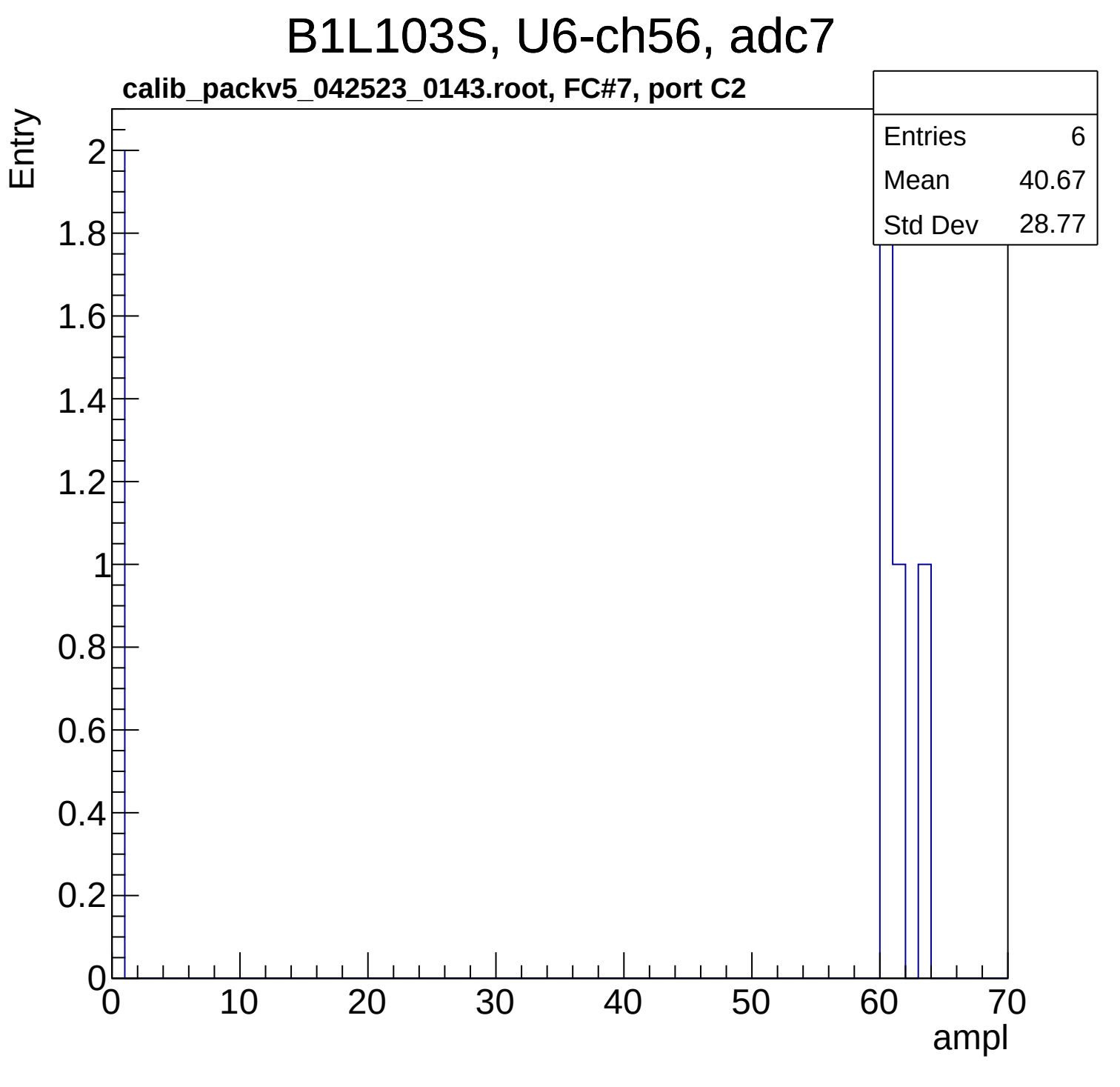
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch56, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 40.67
Std Dev 28.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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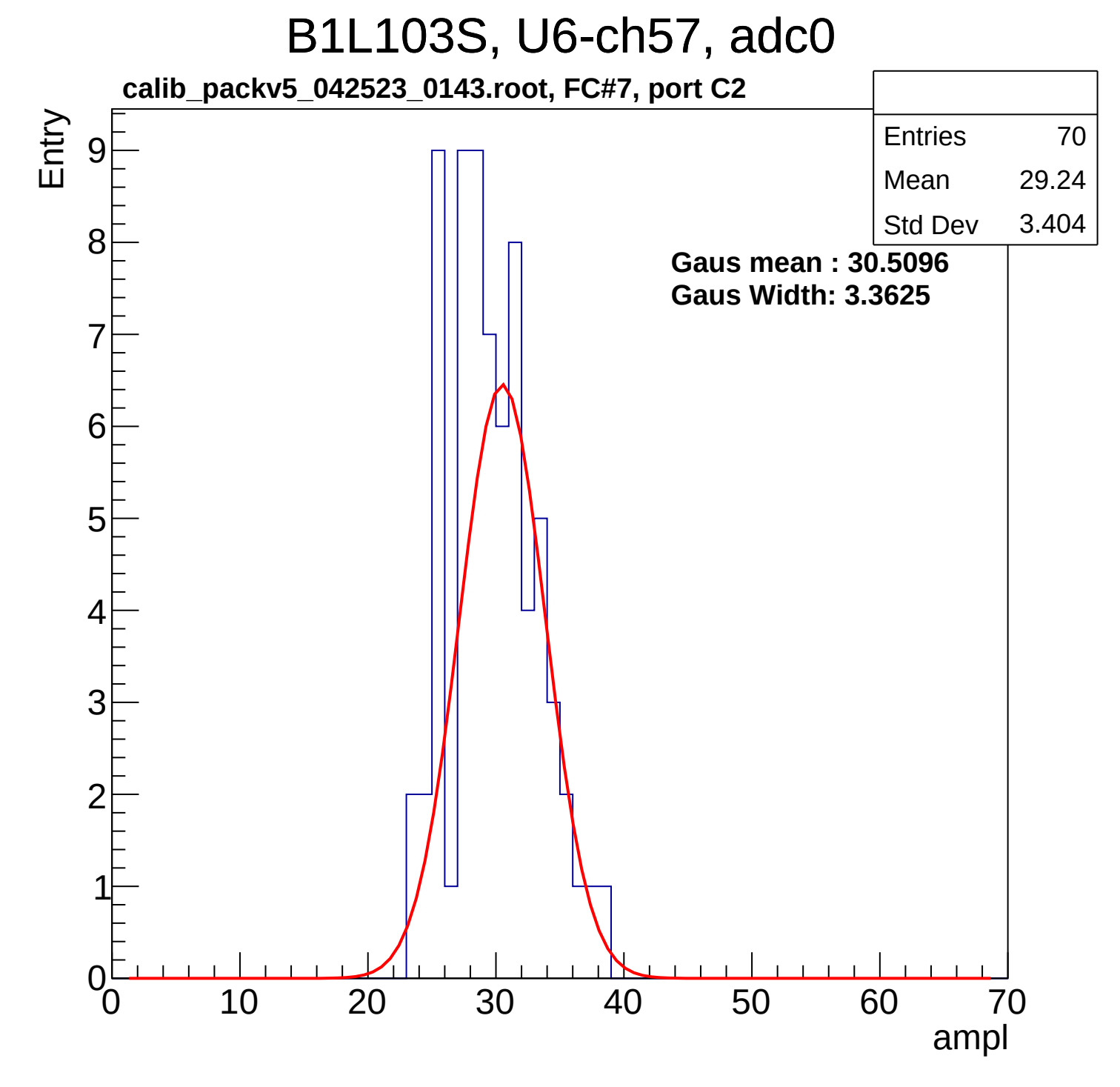
B1L103S, U6-ch57, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.24
Std Dev 3.404
Gaus mean : 30.5096
Gaus Width: 3.3625
Entry
ampl
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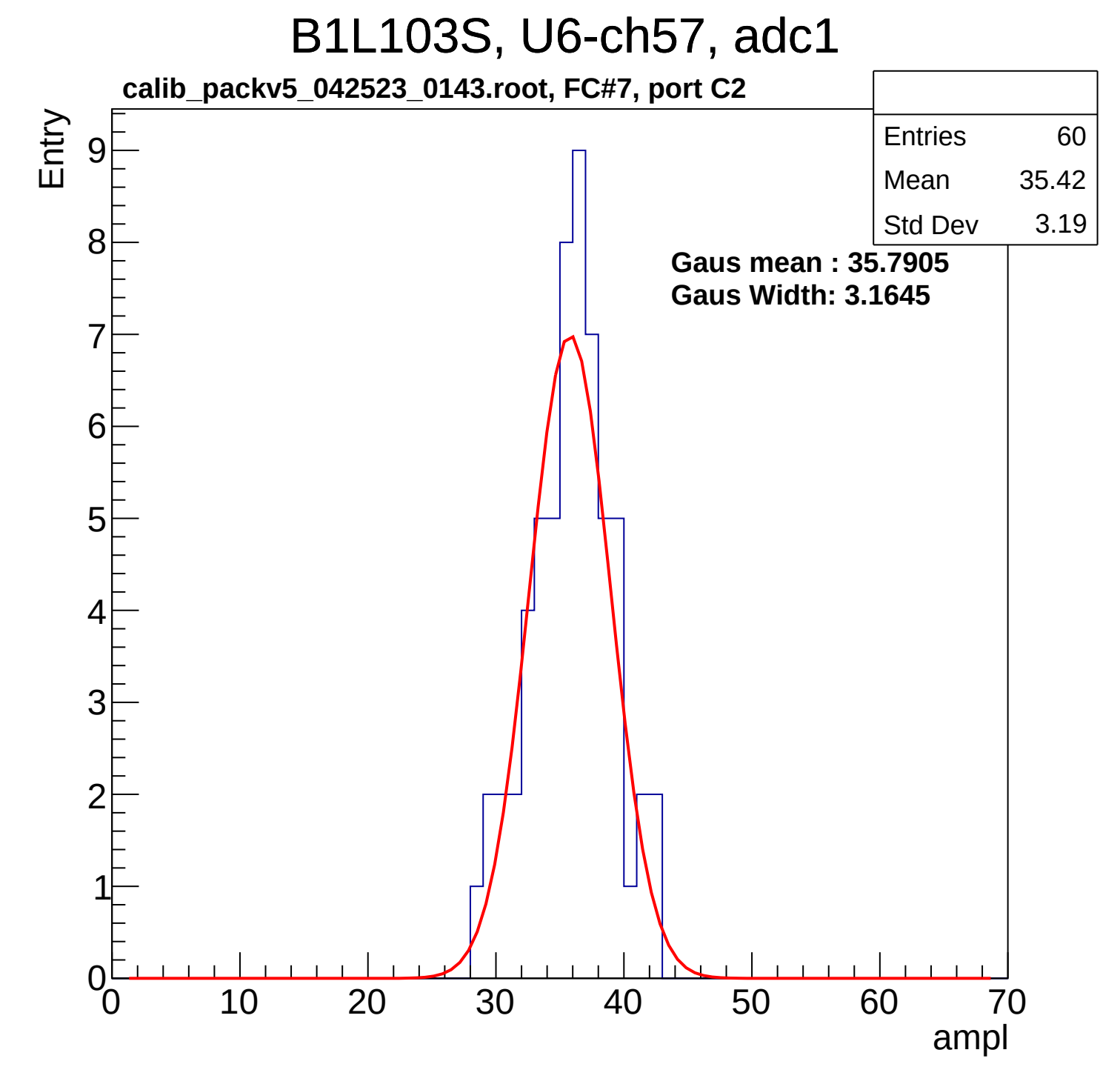
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch57, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 35.42
Std Dev 3.19
Gaus mean : 35.7905
Gaus Width: 3.1645
Entry
ampl
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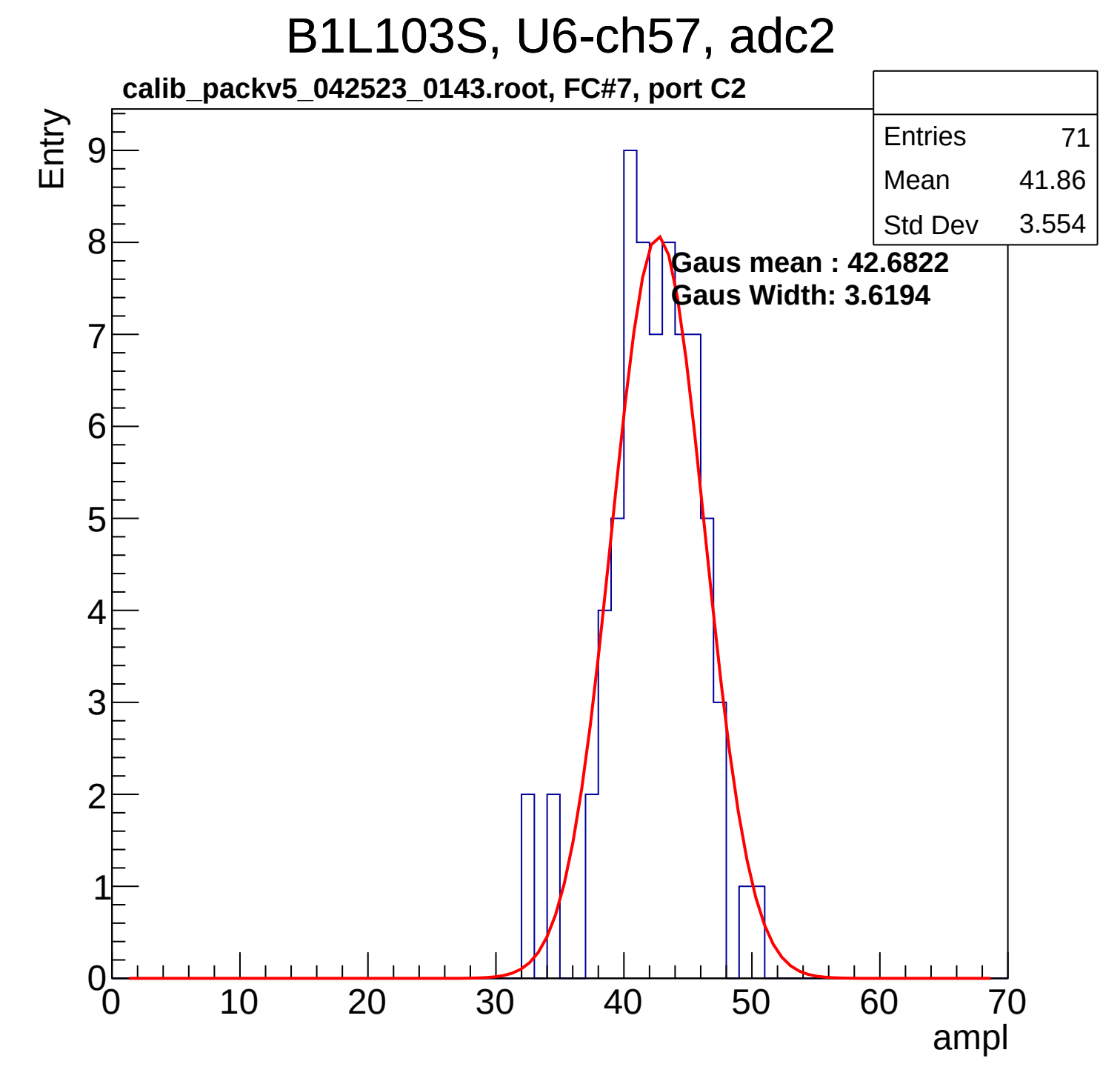
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch57, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 41.86
Std Dev 3.554
Gaus mean : 42.6822
Gaus Width: 3.6194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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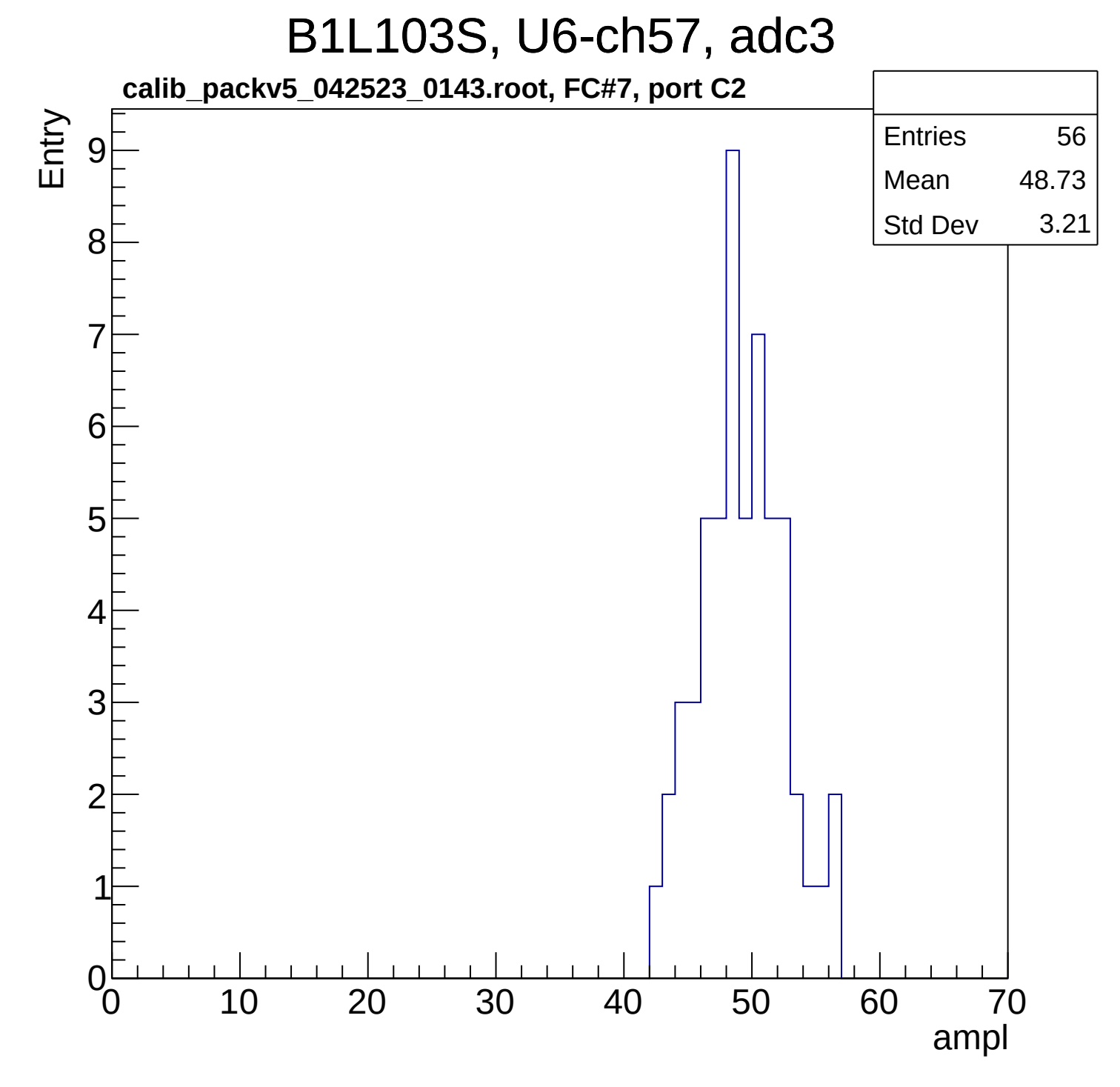
B1L103S, U6-ch57, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 48.73
Std Dev 3.21
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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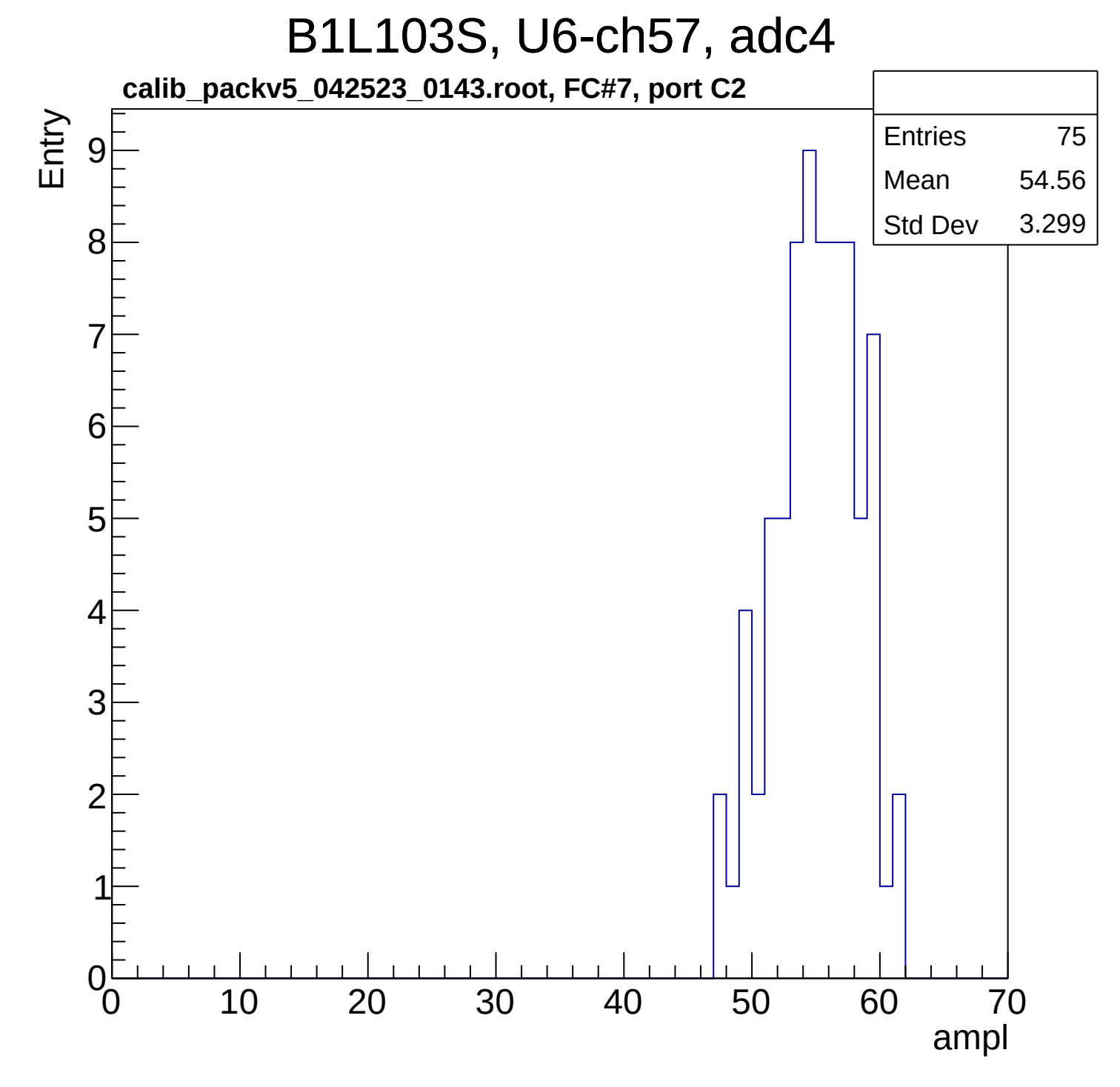
B1L103S, U6-ch57, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 54.56
Std Dev 3.299
Entry
ampl
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3
4
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7
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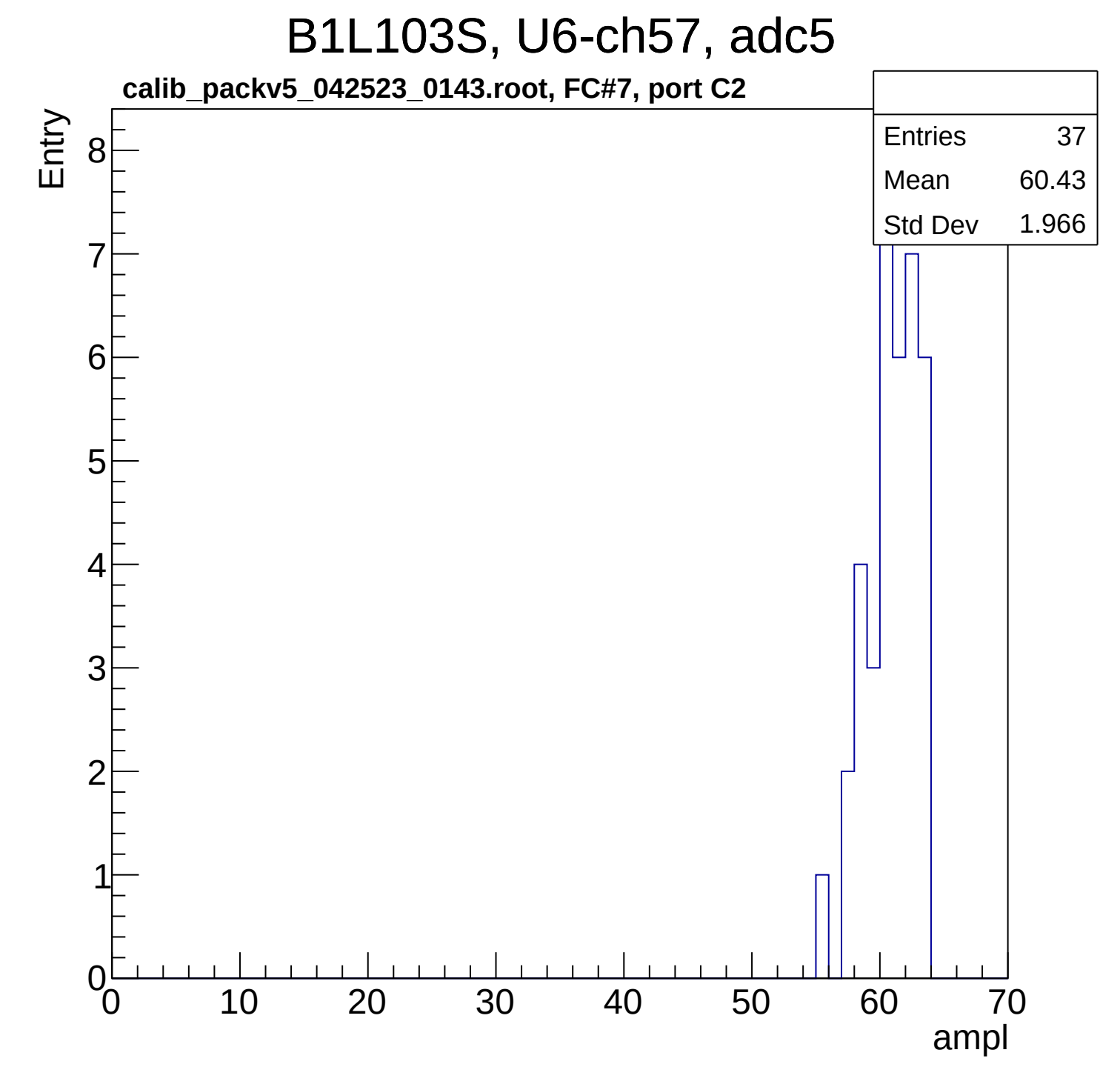
B1L103S, U6-ch57, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.43
Std Dev 1.966
Entry
ampl
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1
2
3
4
5
6
7
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60
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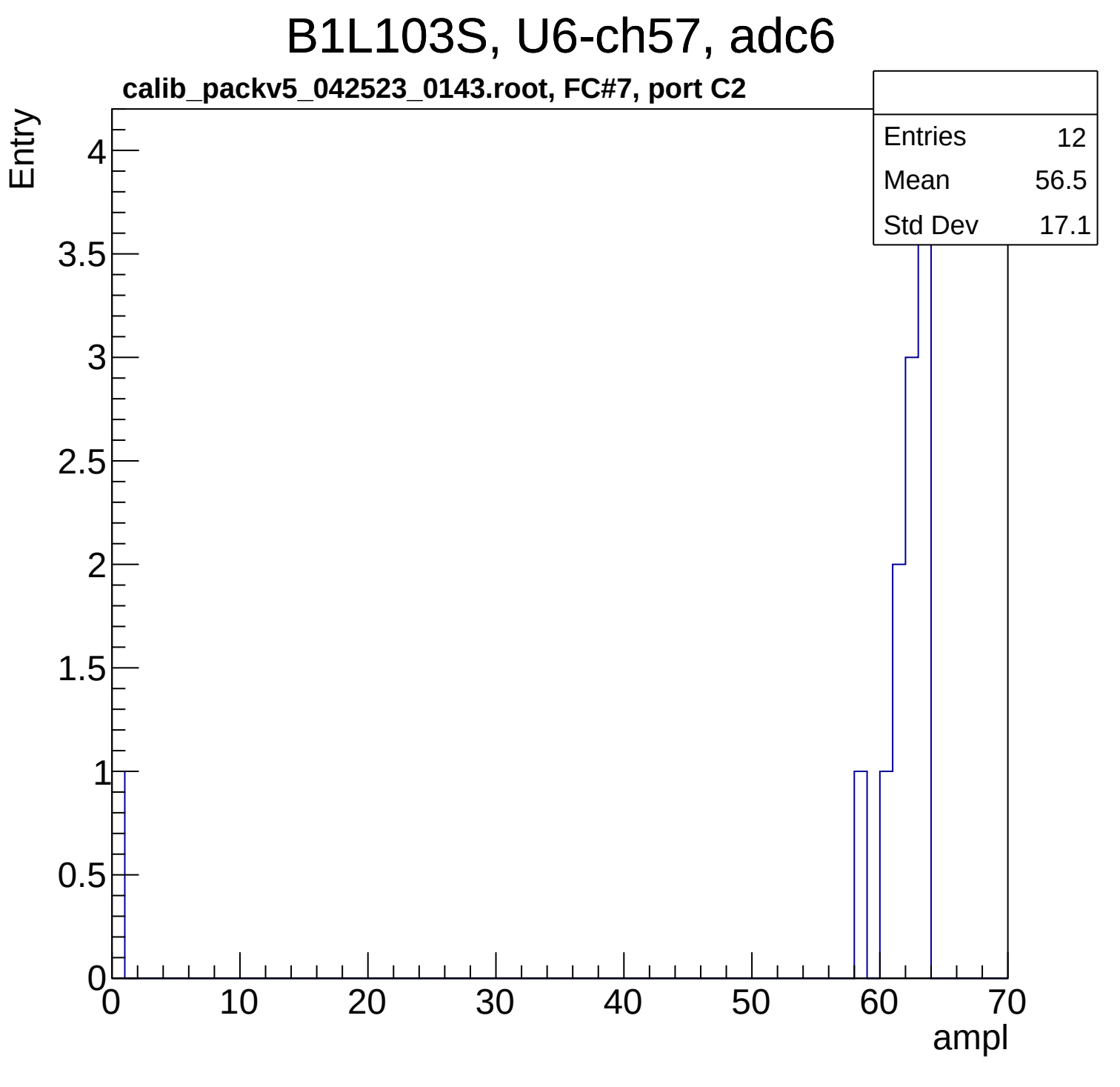
B1L103S, U6-ch57, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.5
Std Dev 17.1
Entry
ampl
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0.5
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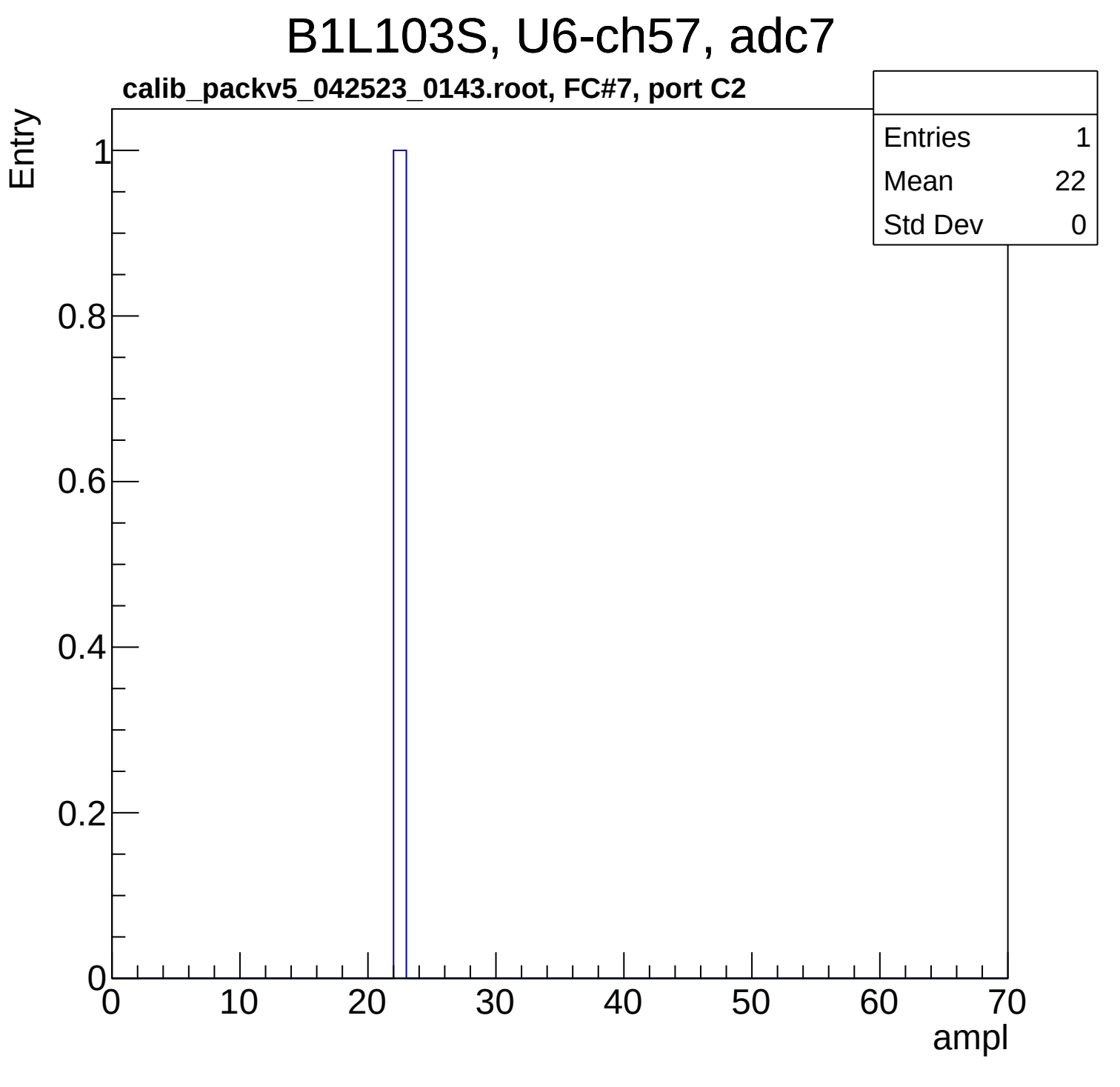
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch57, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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50
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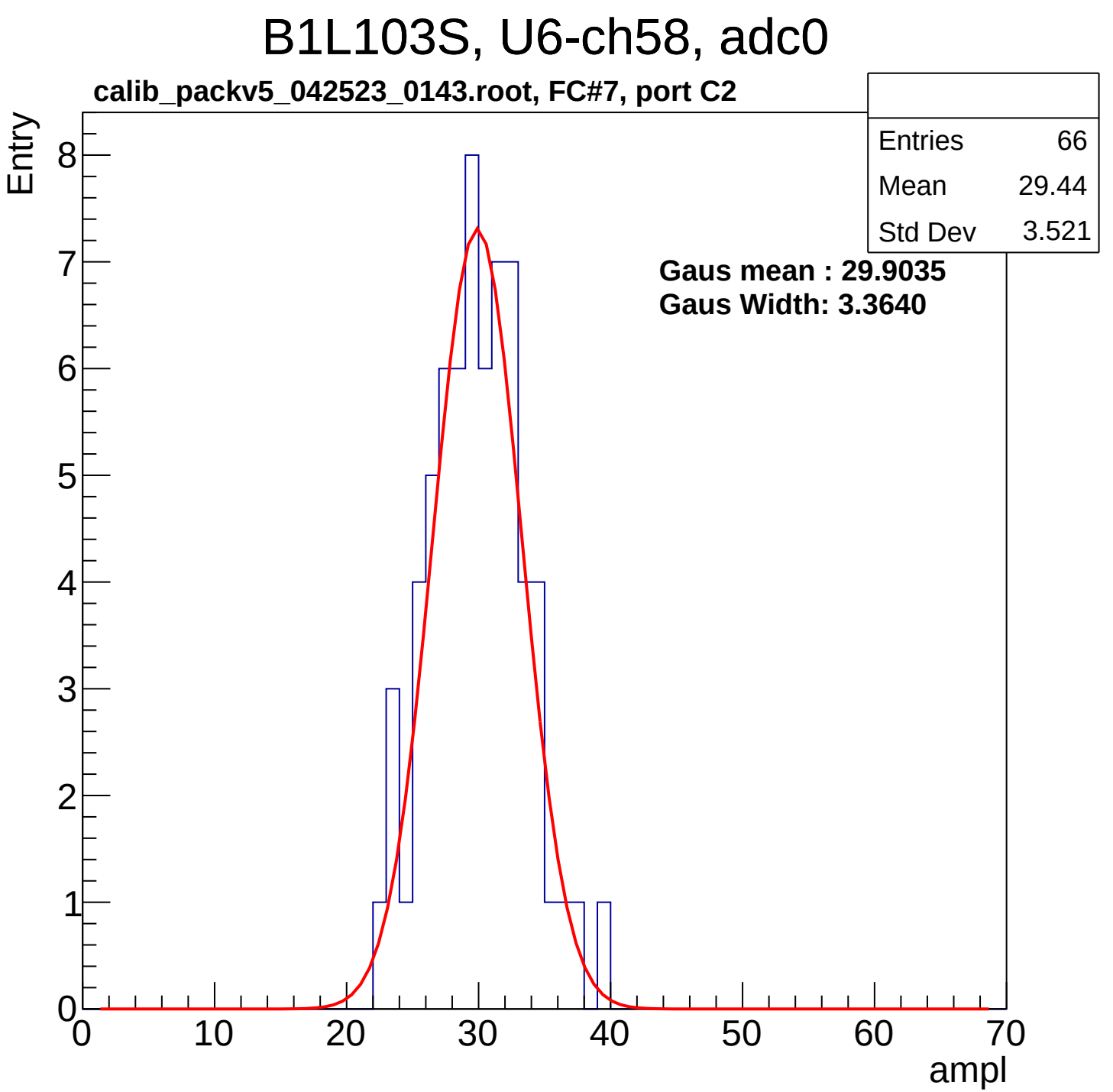
70
ampl


B1L103S, U6-ch58, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.44
Std Dev 3.521
Gaus mean : 29.9035
Gaus Width: 3.3640
Entry
ampl
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1
2
3
4
5
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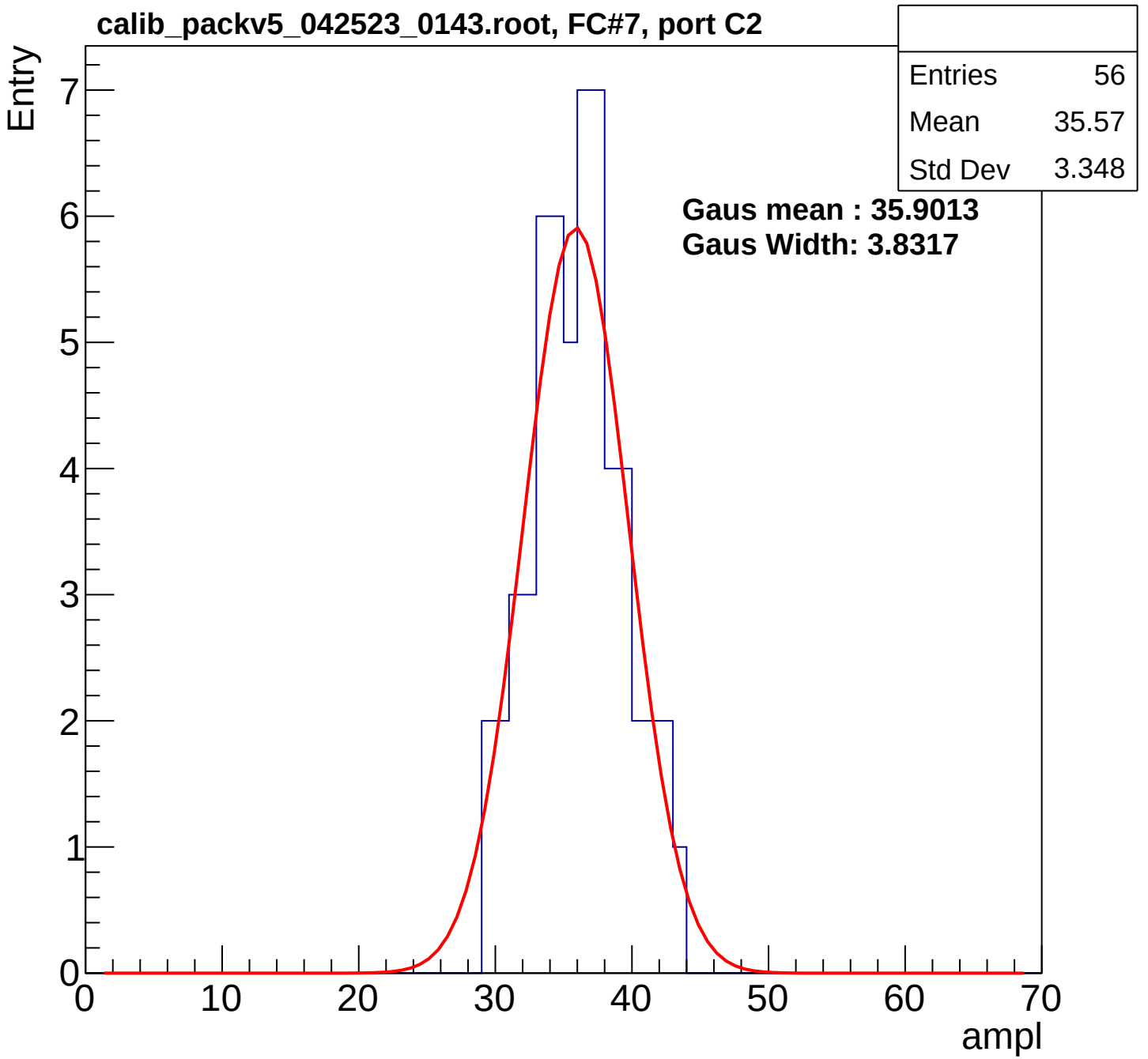
B1L103S, U6-ch58, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 35.57
Std Dev 3.348
Gaus mean : 35.9013
Gaus Width: 3.8317
Entry
ampl
0
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4
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0
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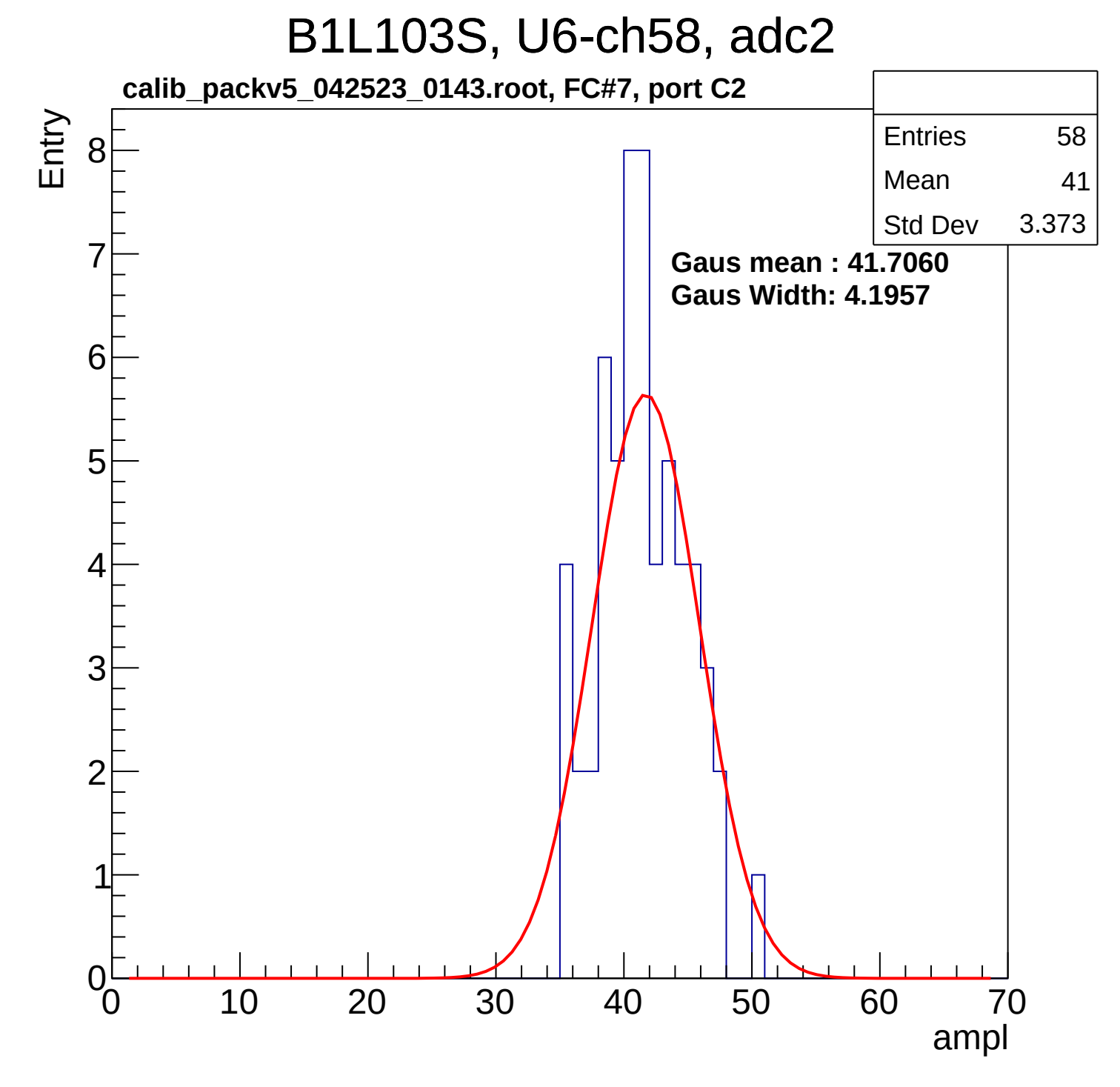
B1L103S, U6-ch58, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 41
Std Dev 3.373
Gaus mean : 41.7060
Gaus Width: 4.1957
Entry
ampl
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1
2
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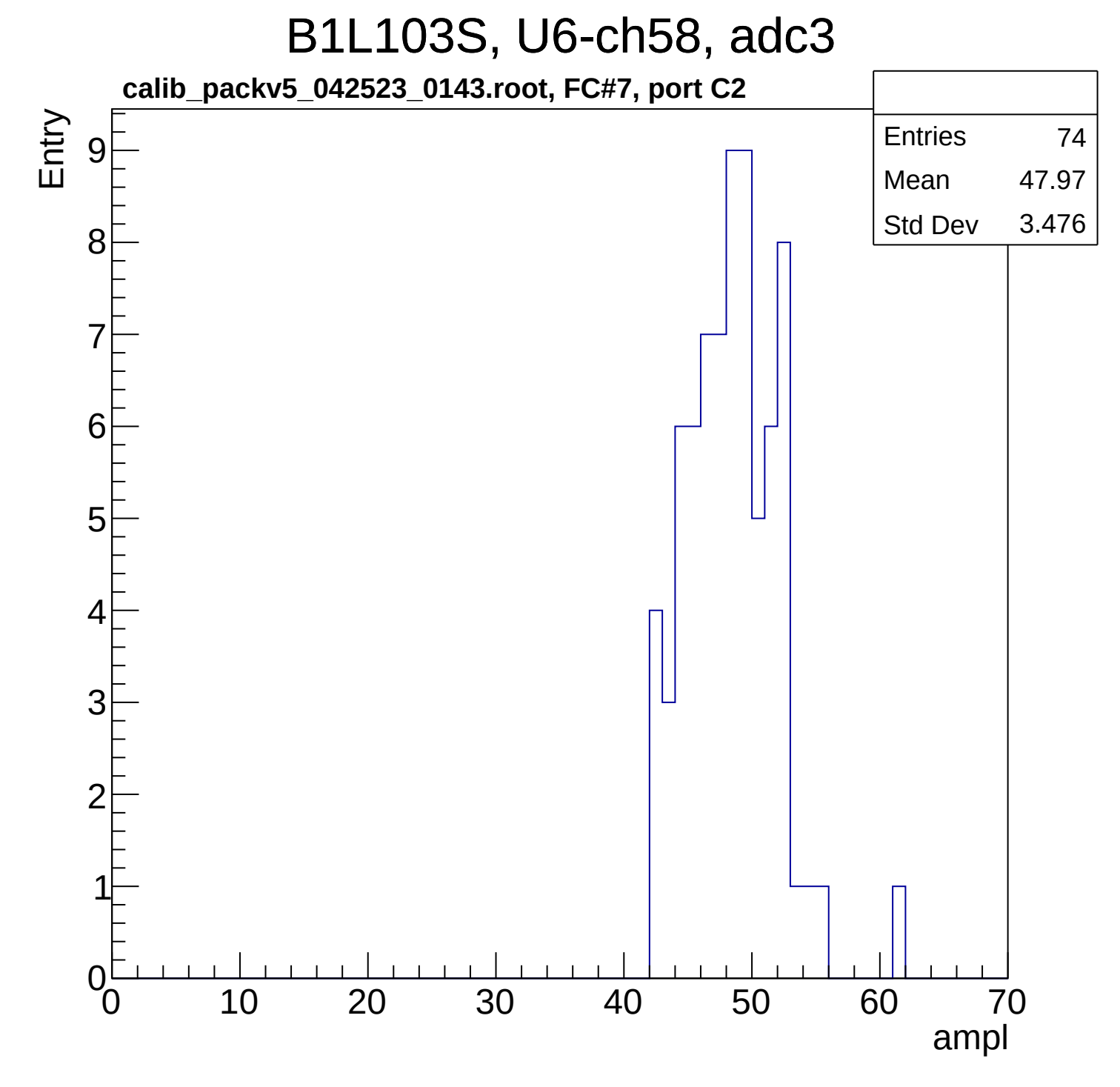
B1L103S, U6-ch58, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 47.97
Std Dev 3.476
Entry
ampl
0
1
2
3
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0
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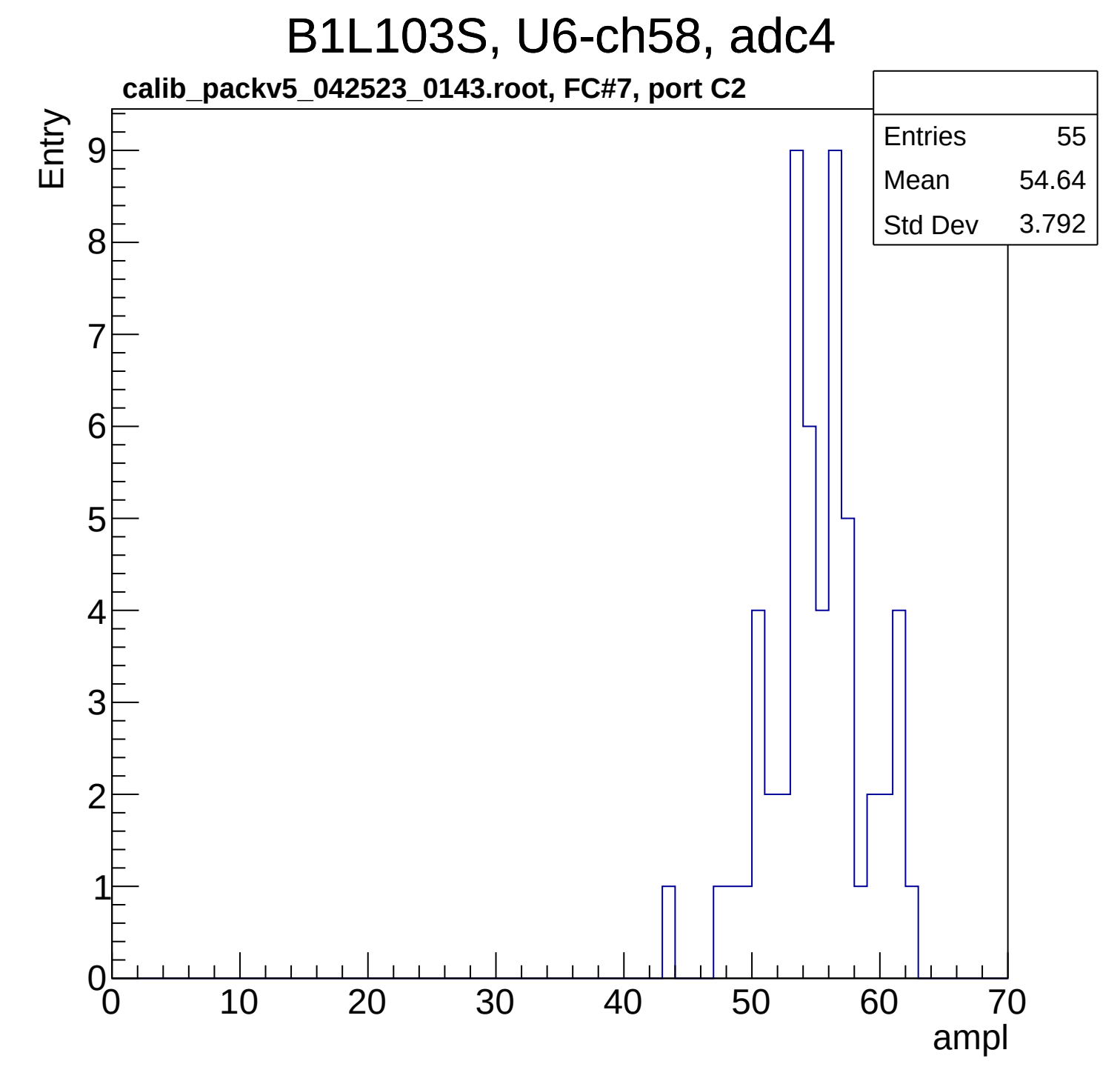
B1L103S, U6-ch58, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.64
Std Dev 3.792
Entry
ampl
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4
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0
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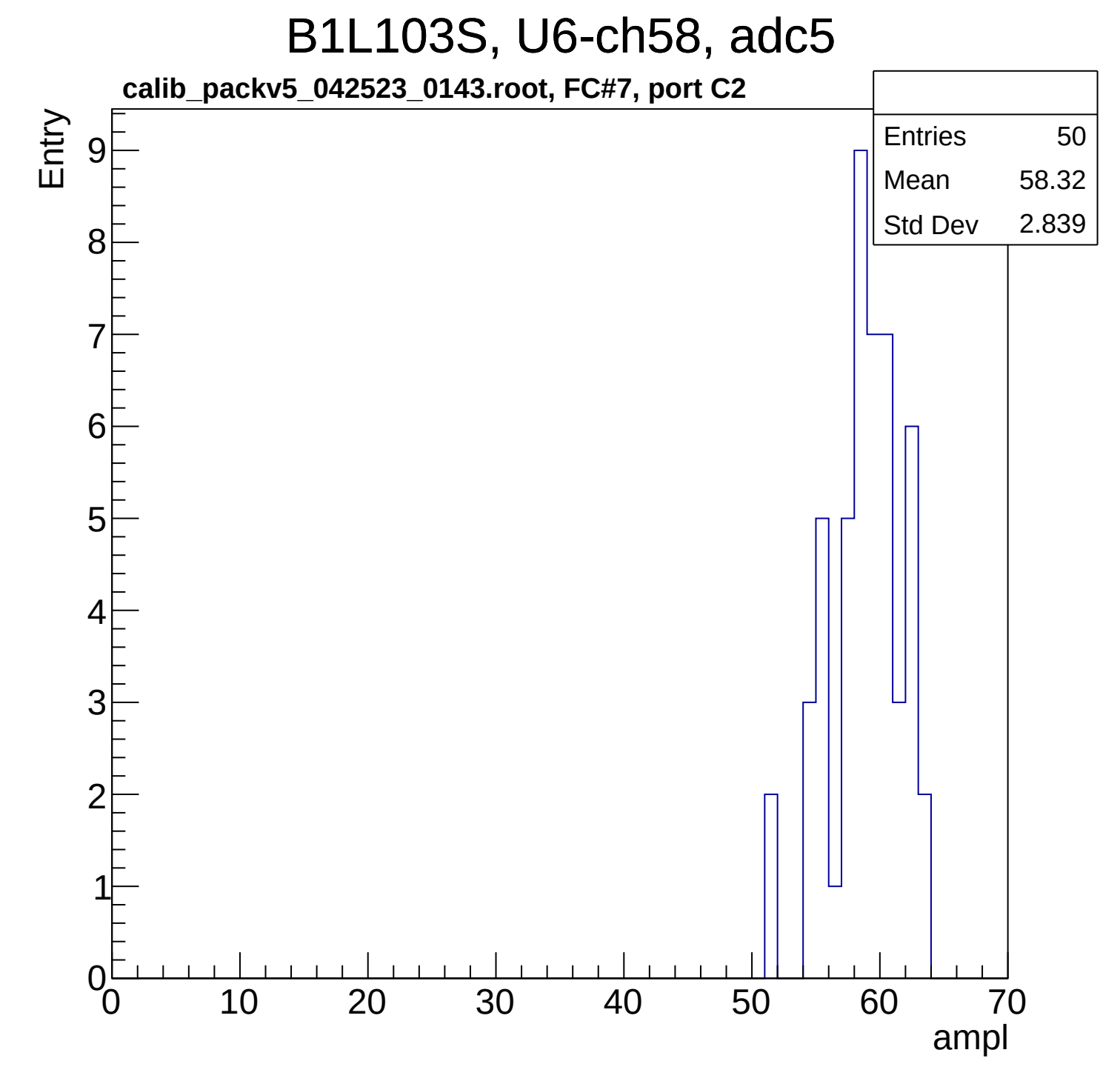
B1L103S, U6-ch58, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.32
Std Dev 2.839
Entry
ampl
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1
2
3
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6
7
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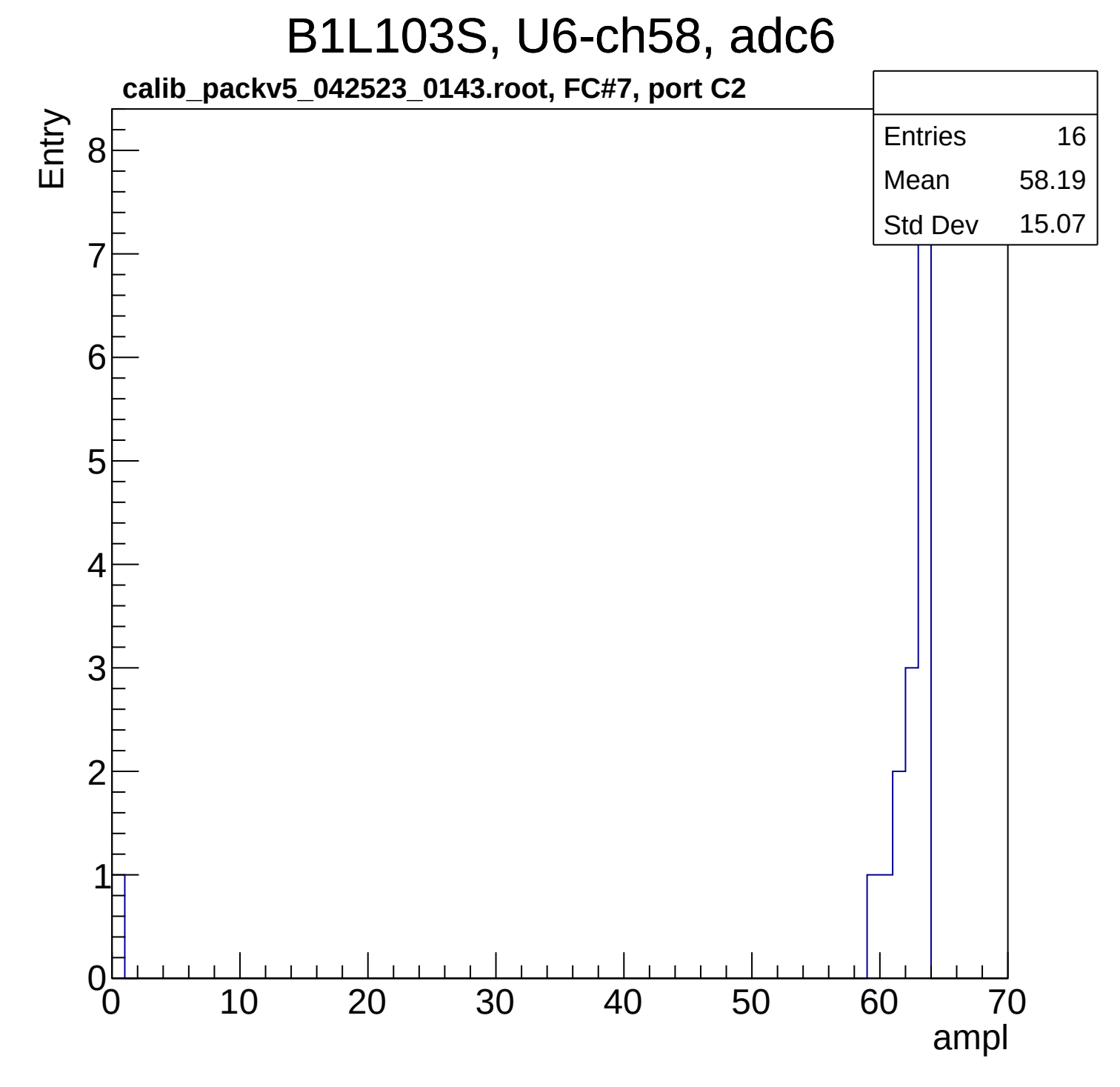
B1L103S, U6-ch58, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 58.19
Std Dev 15.07
Entry
ampl
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1
2
3
4
5
6
7
8
0
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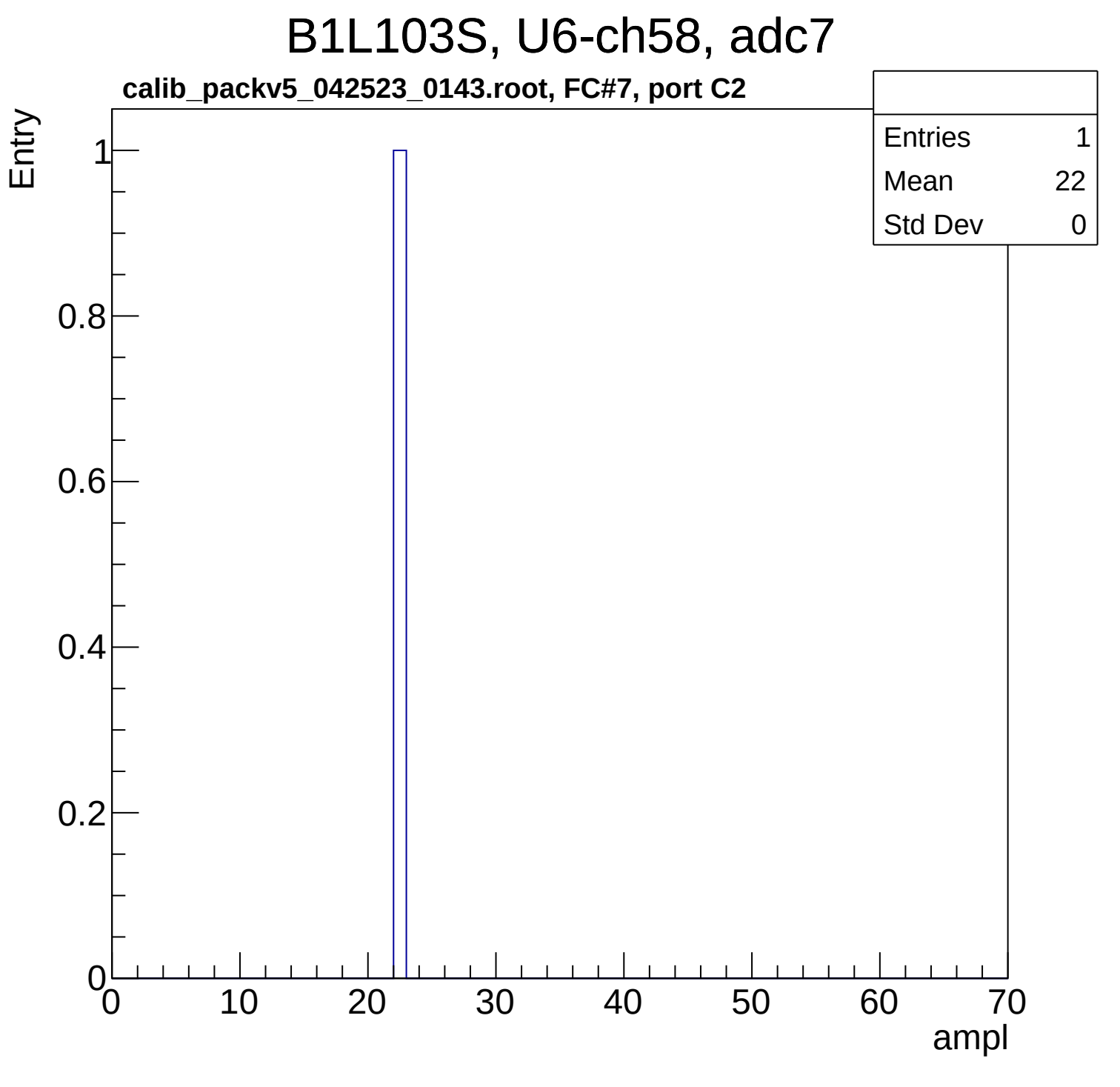
B1L103S, U6-ch58, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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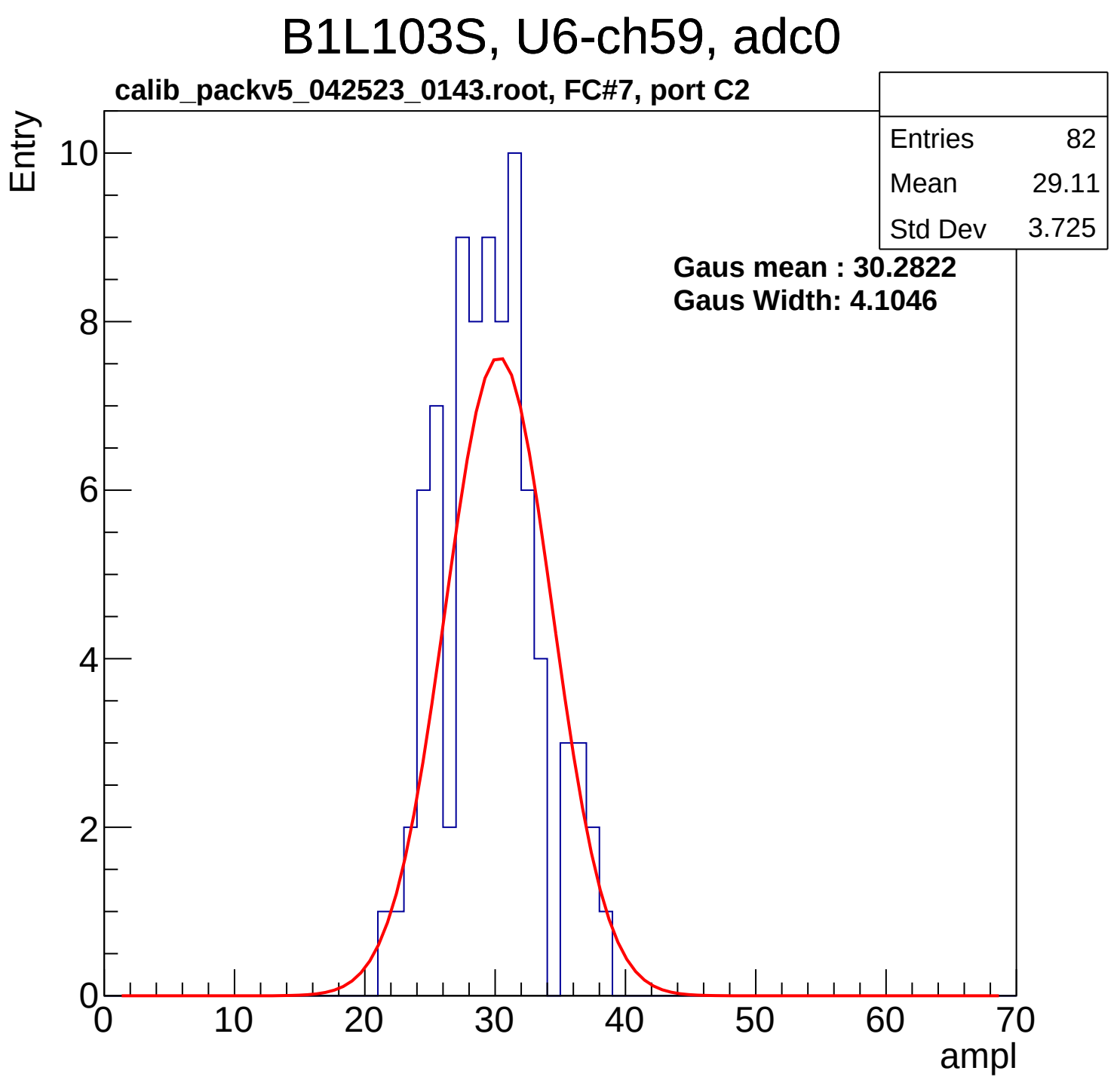
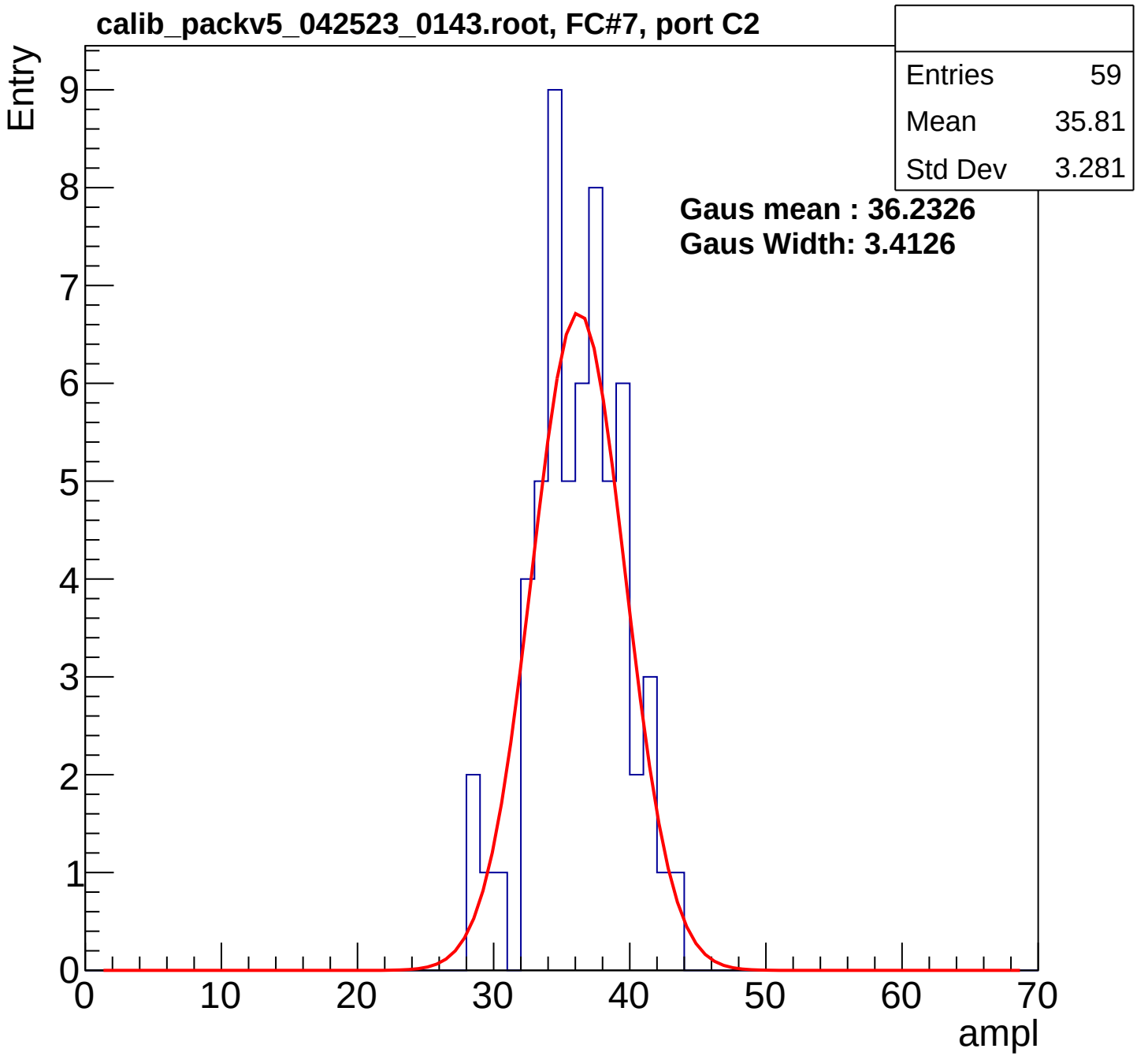
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch59, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 29.11
Std Dev 3.725
Gaus mean : 30.2822
Gaus Width: 4.1046
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch59, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 35.81
Std Dev 3.281
Gaus mean : 36.2326
Gaus Width: 3.4126
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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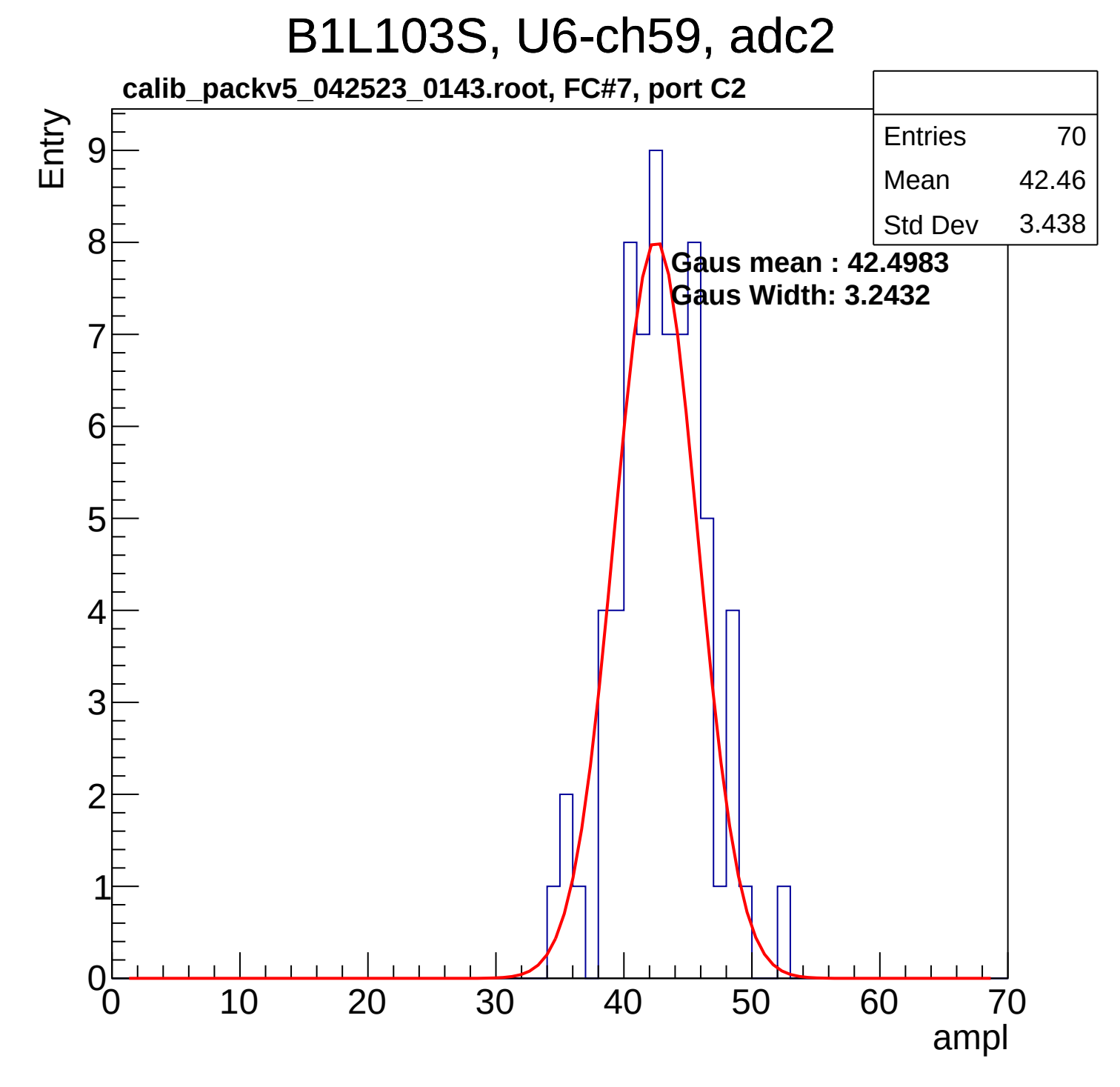
B1L103S, U6-ch59, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.46
Std Dev 3.438
Gaus mean : 42.4983
Gaus Width: 3.2432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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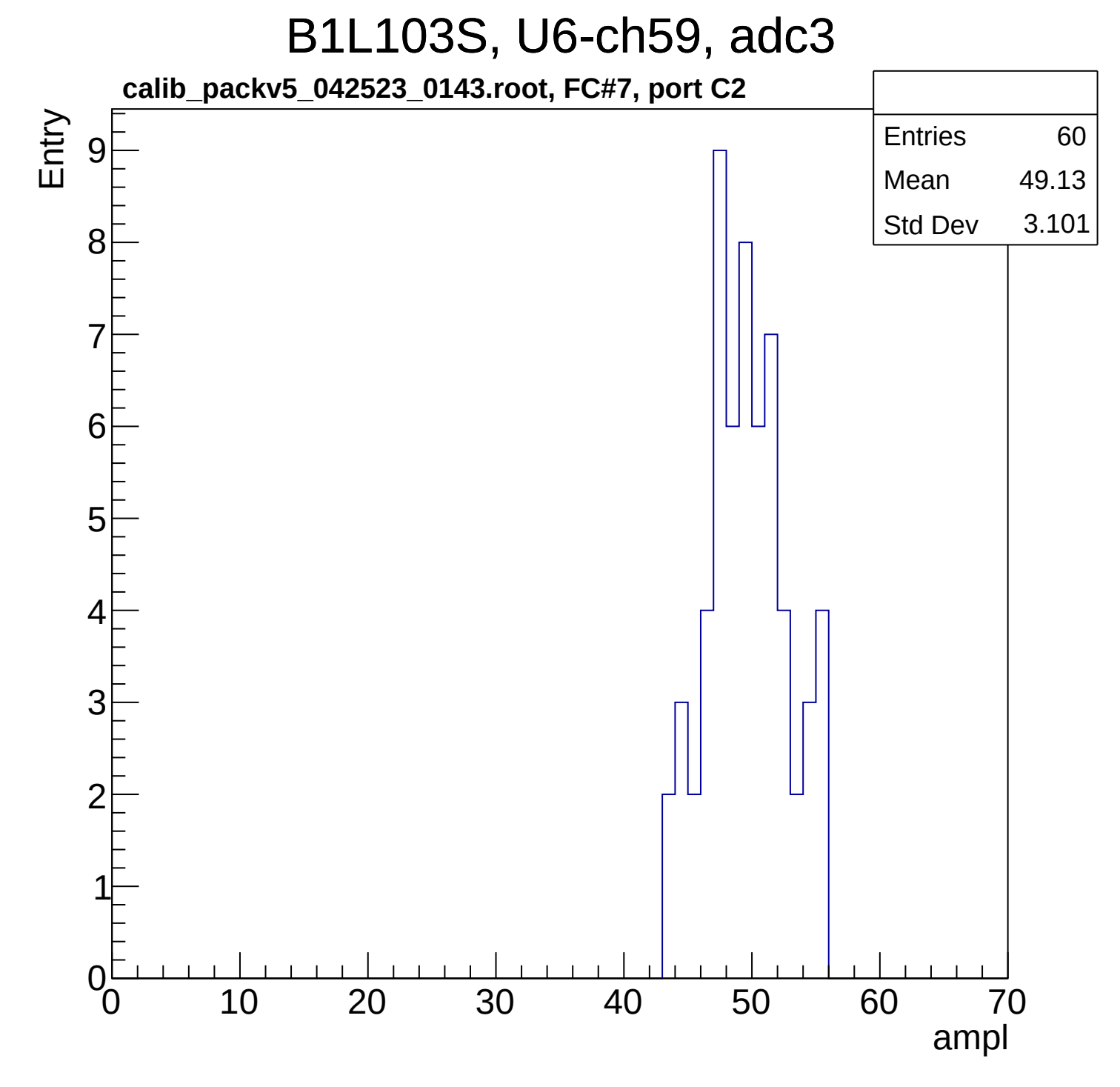
B1L103S, U6-ch59, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.13
Std Dev 3.101
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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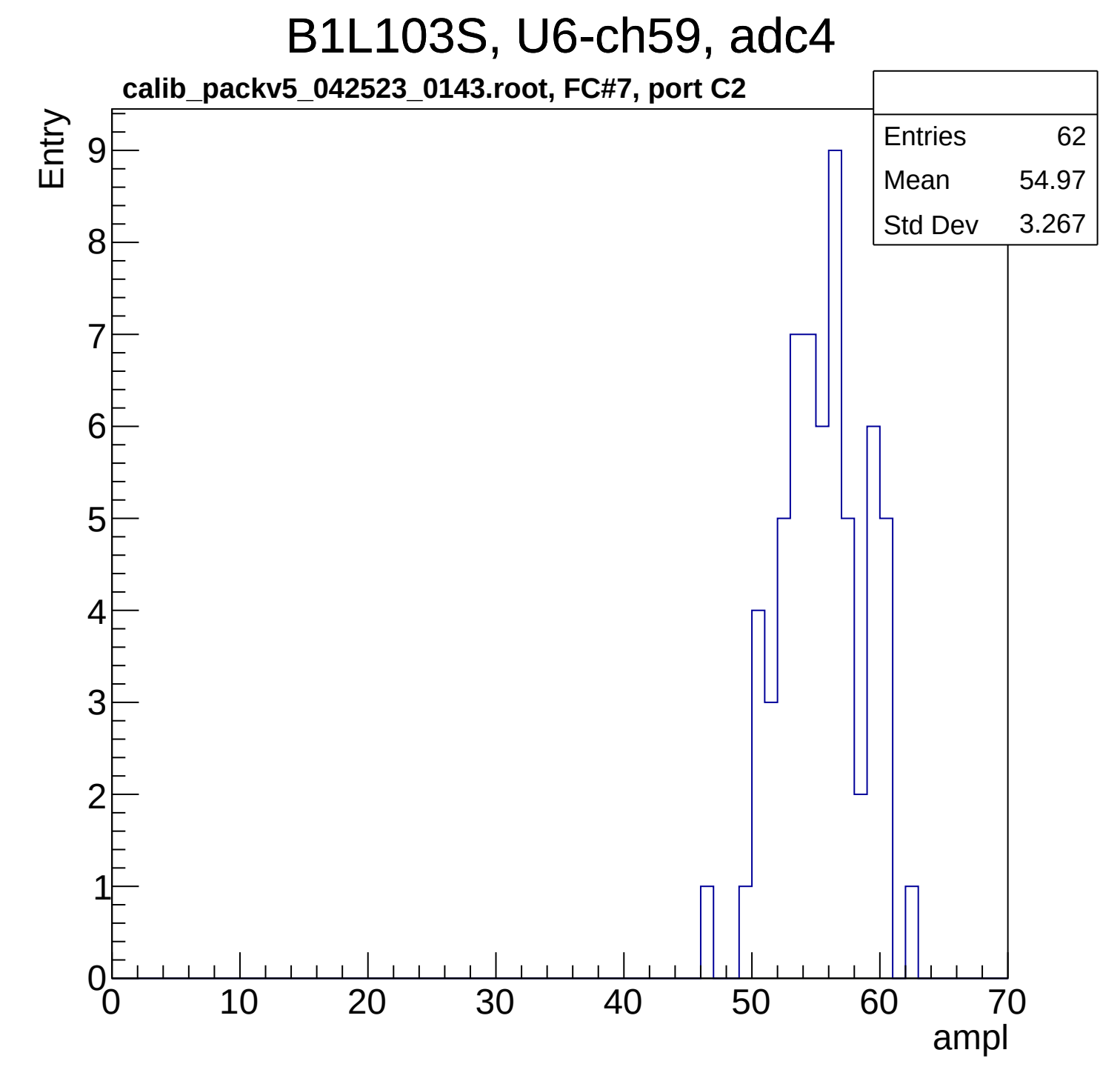
B1L103S, U6-ch59, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 54.97
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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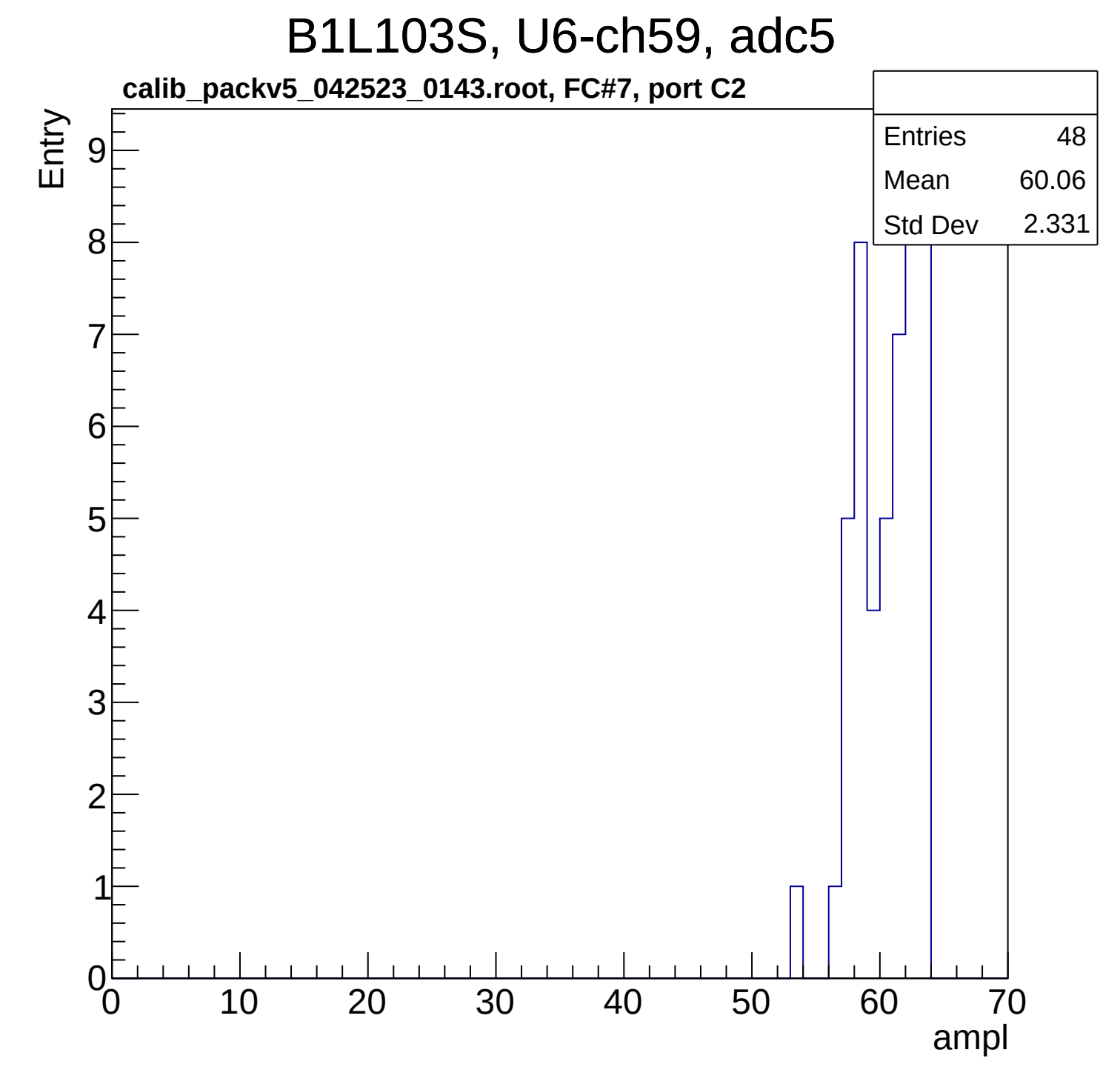
B1L103S, U6-ch59, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 60.06
Std Dev 2.331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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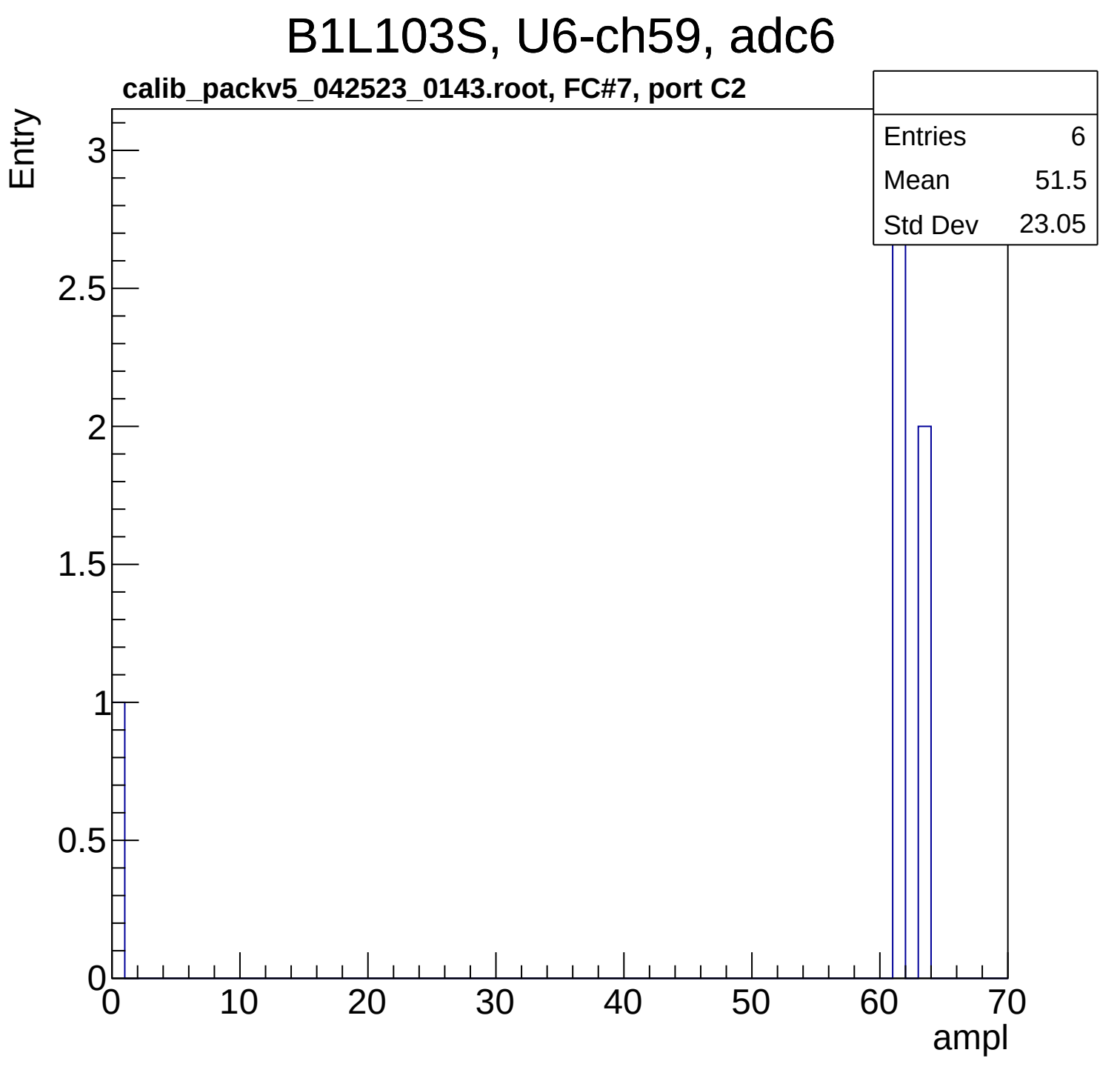
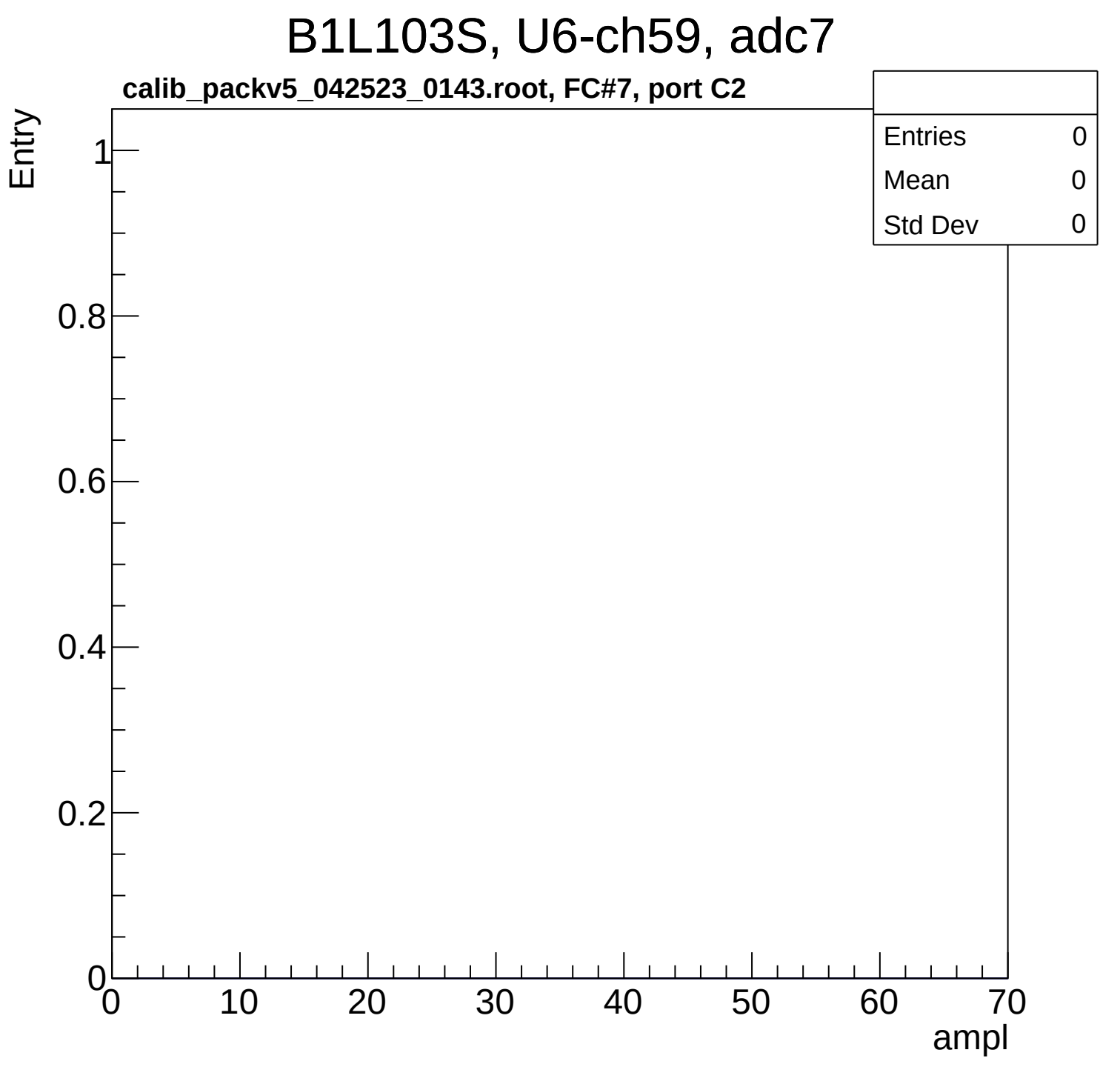
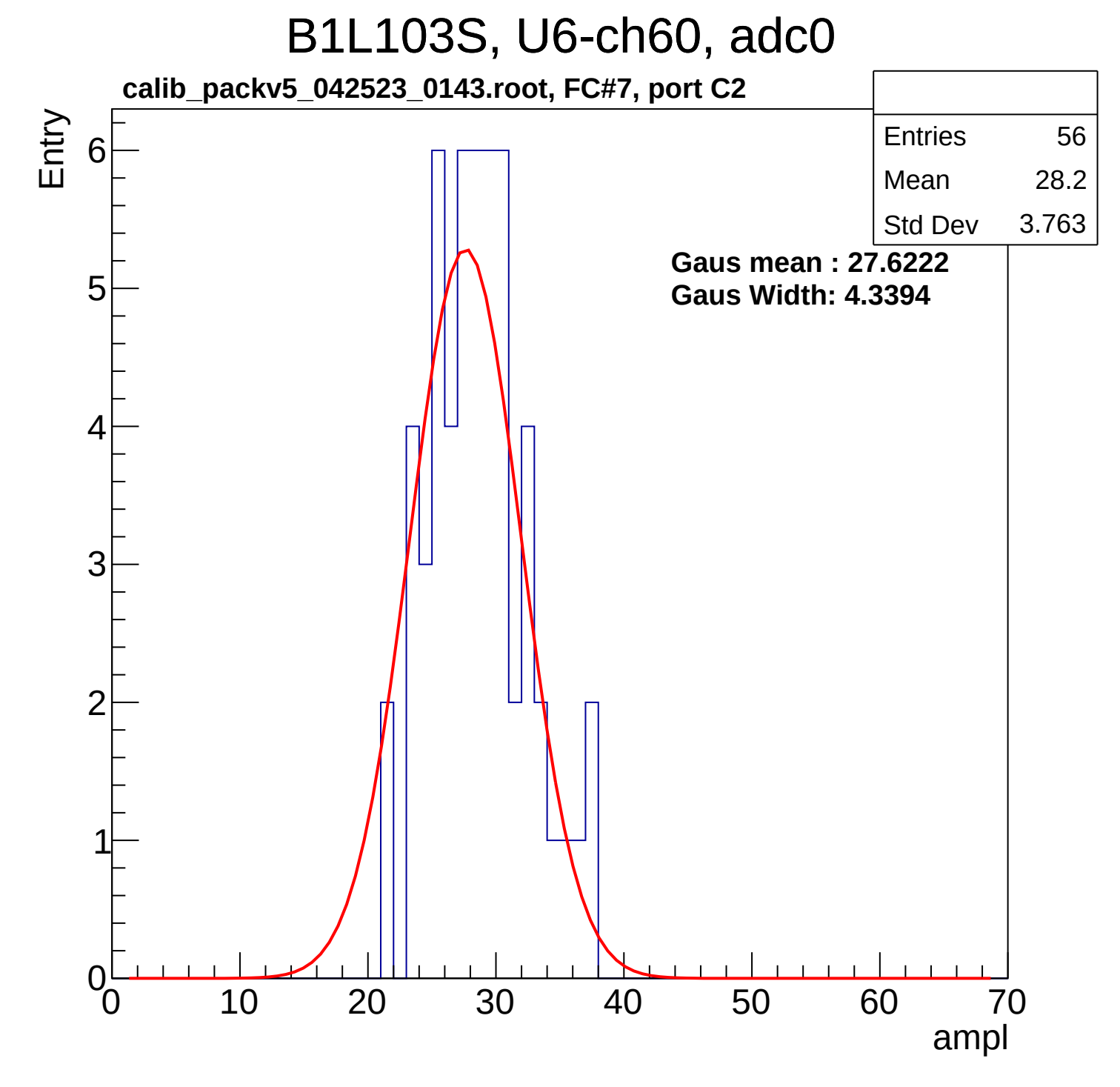

B1L103S, U6-ch59, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.5
Std Dev 23.05
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch59, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch60, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 28.2
Std Dev 3.763
Gaus mean : 27.6222
Gaus Width: 4.3394
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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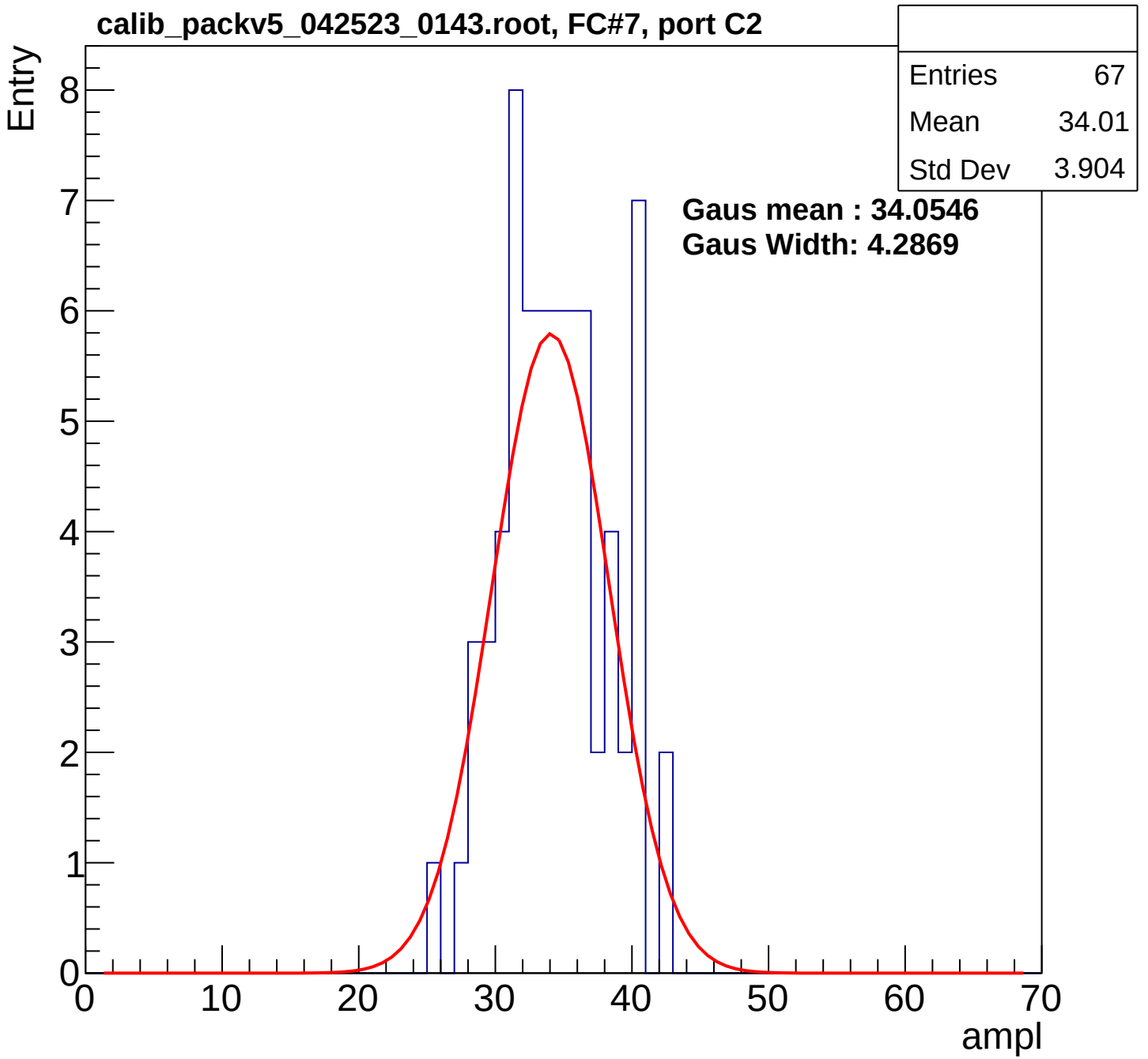
40
50
60
70


B1L103S, U6-ch60, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 34.01
Std Dev 3.904
Gaus mean : 34.0546
Gaus Width: 4.2869
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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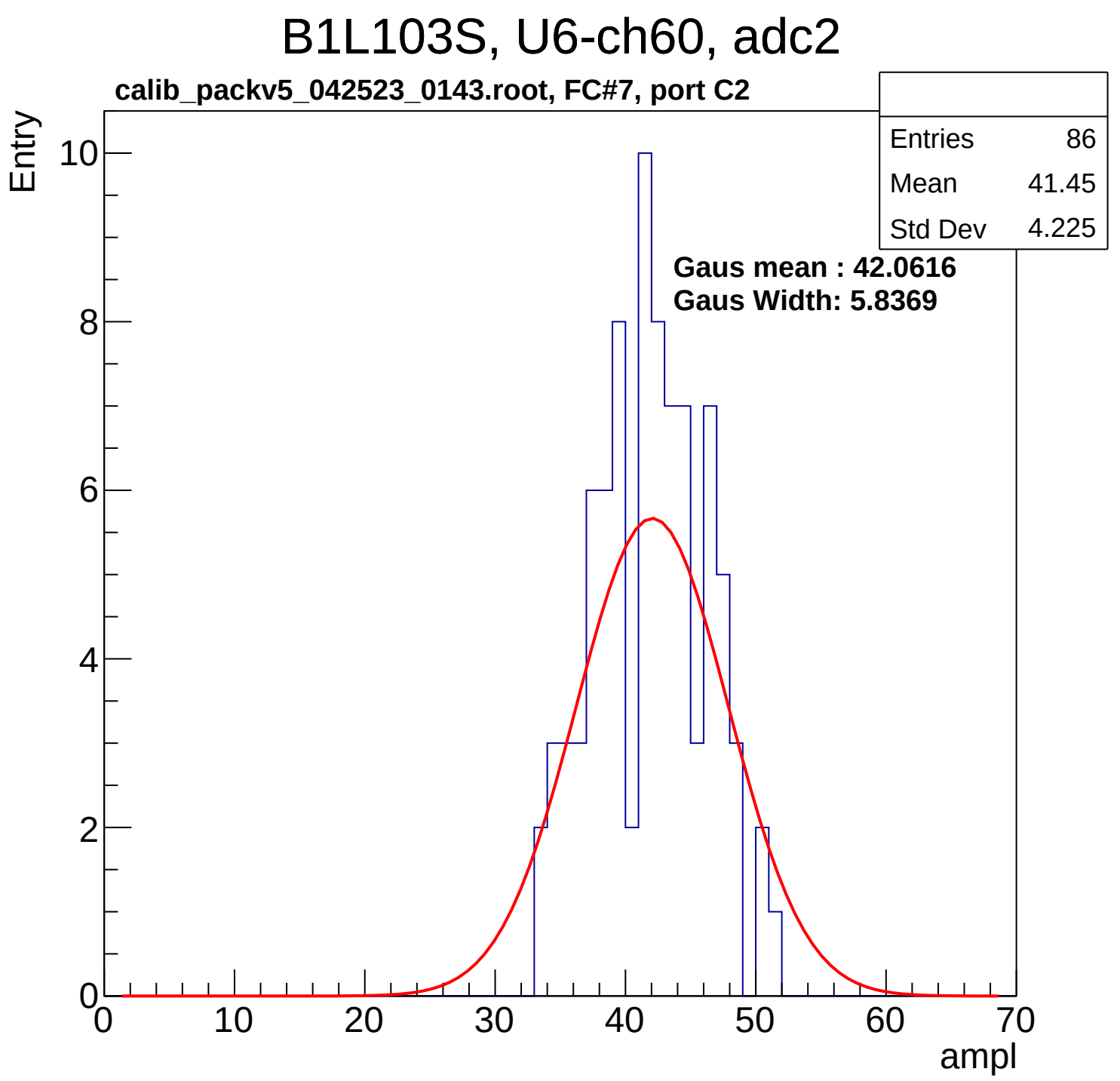
B1L103S, U6-ch60, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 41.45
Std Dev 4.225
Gaus mean : 42.0616
Gaus Width: 5.8369
Entry
ampl
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60
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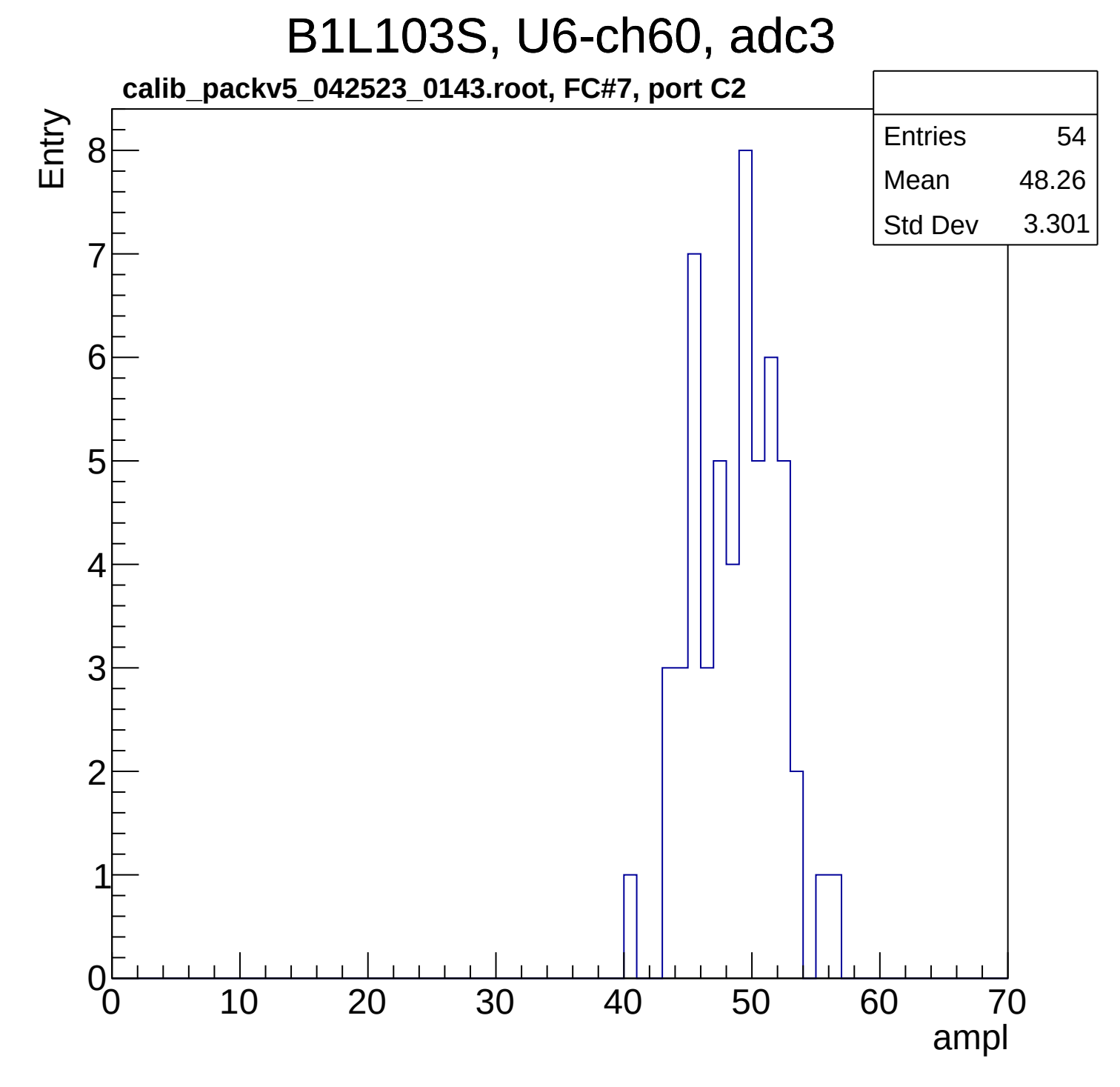
B1L103S, U6-ch60, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 48.26
Std Dev 3.301
Entry
ampl
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60
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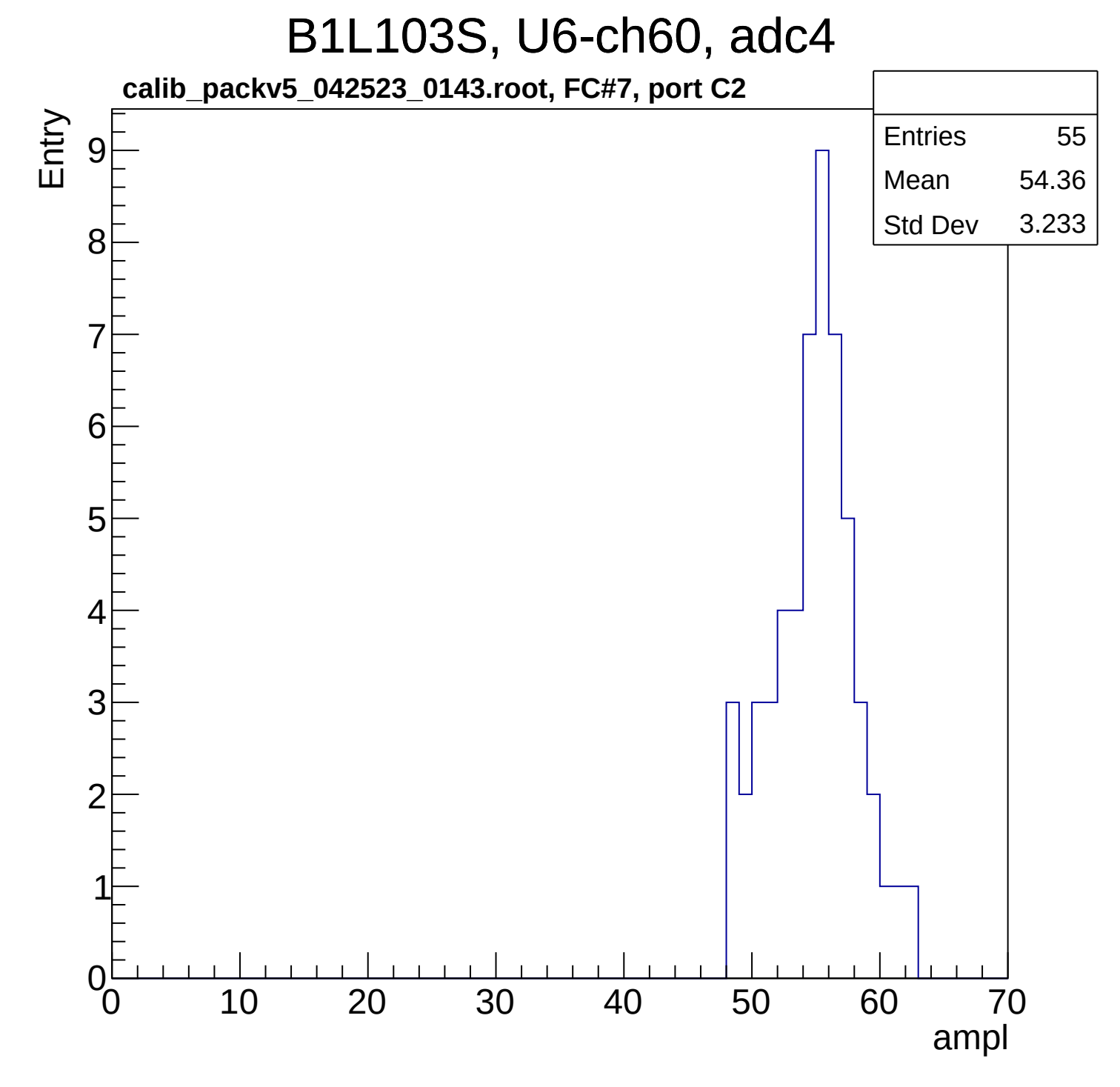
B1L103S, U6-ch60, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.36
Std Dev 3.233
Entry
ampl
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5
6
7
8
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0
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60
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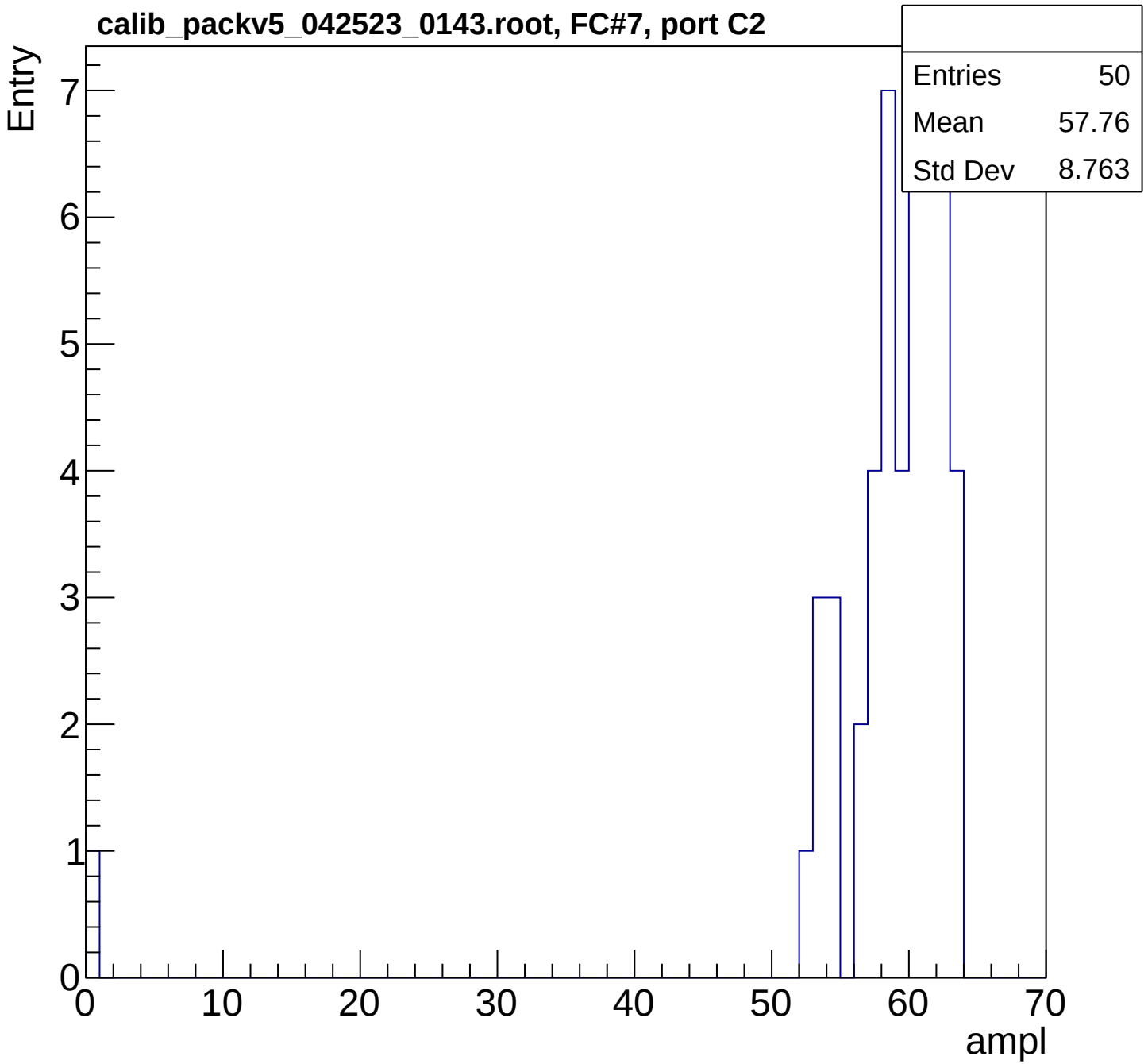
B1L103S, U6-ch60, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 57.76
Std Dev 8.763
Entry
ampl
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4
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0
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60
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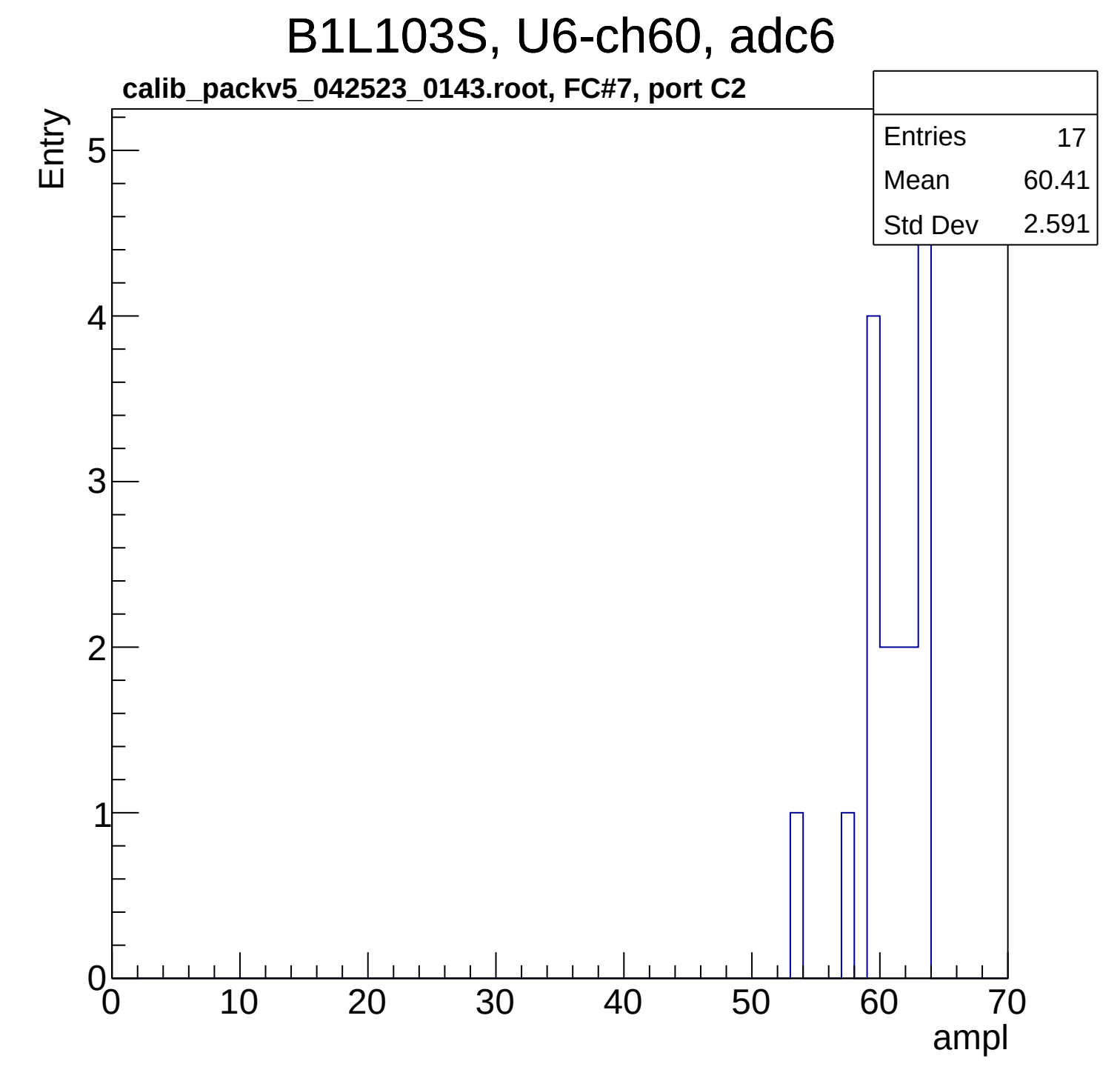
B1L103S, U6-ch60, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 60.41
Std Dev 2.591
Entry
ampl
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60
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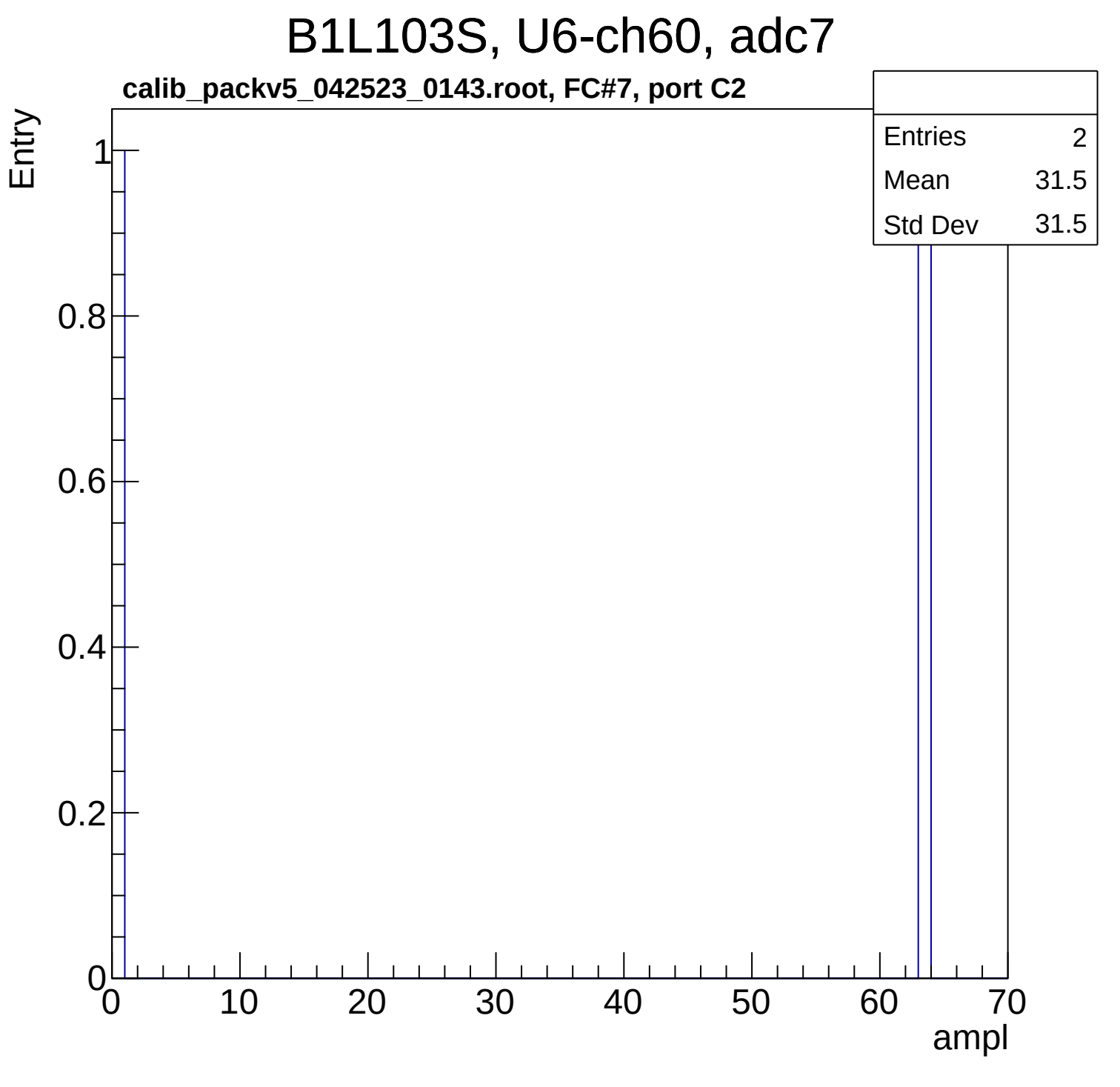
B1L103S, U6-ch60, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.4
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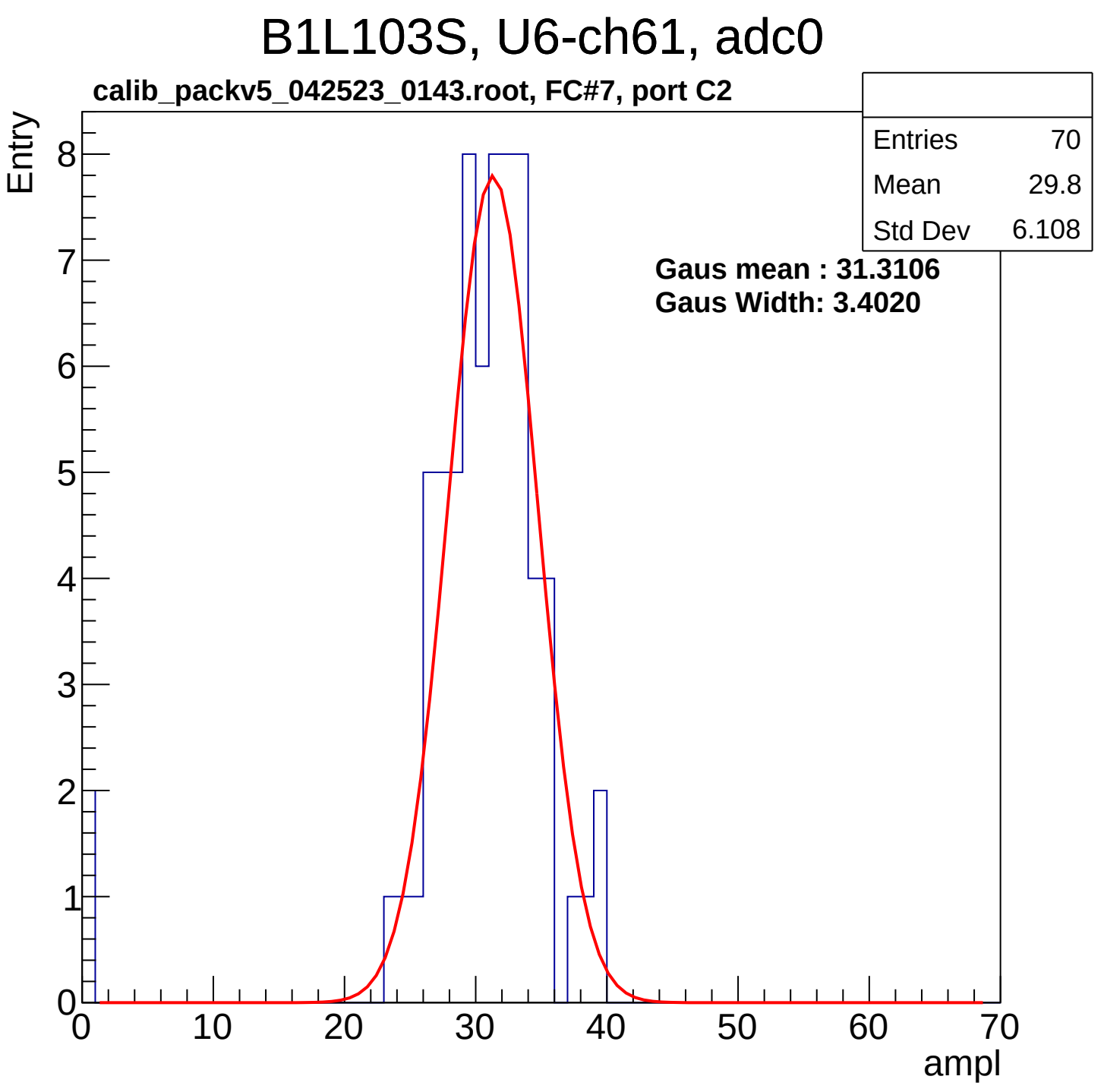
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch61, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.8
Std Dev 6.108
Gaus mean : 31.3106
Gaus Width: 3.4020
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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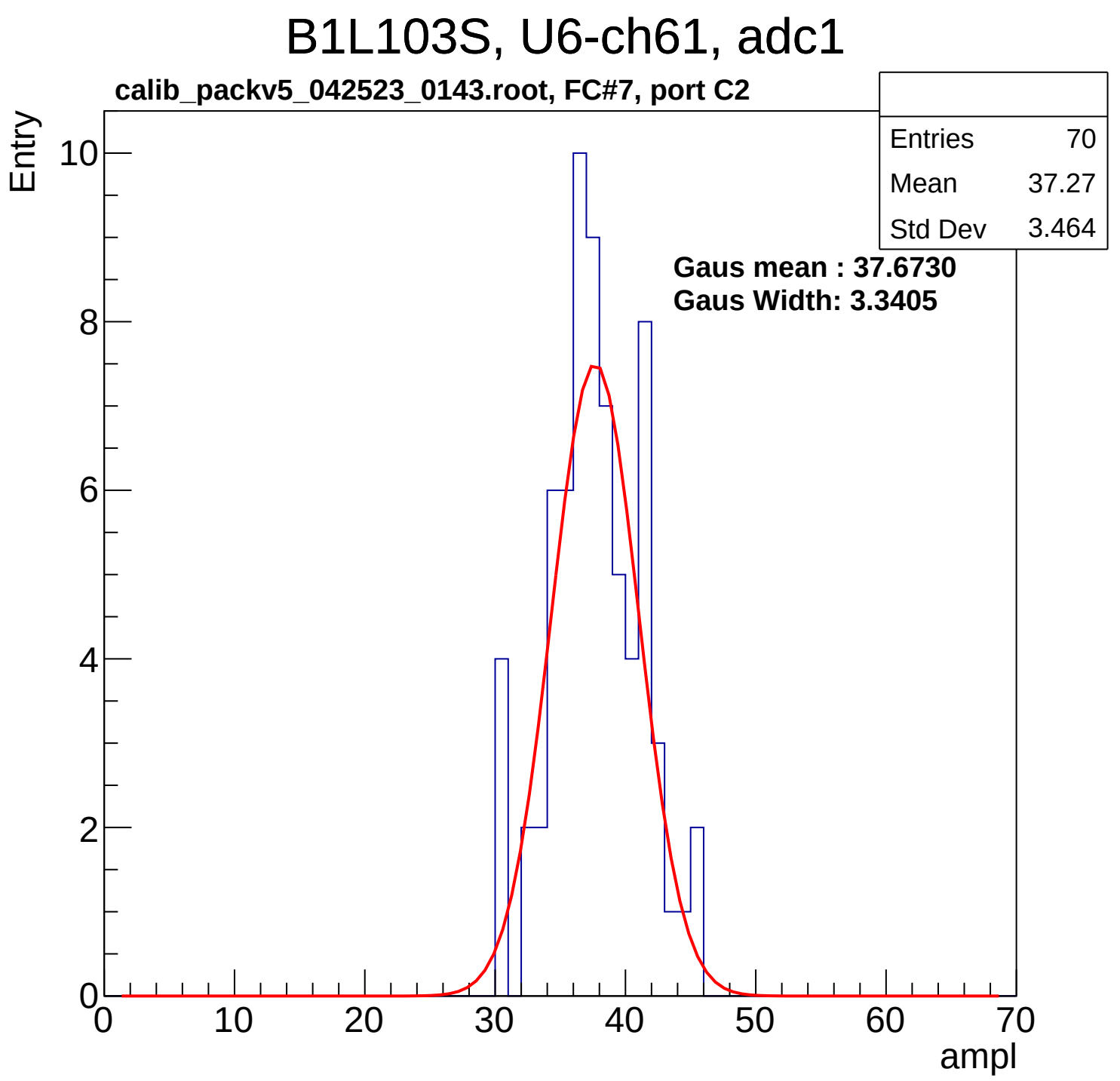
B1L103S, U6-ch61, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 37.27
Std Dev 3.464
Gaus mean : 37.6730
Gaus Width: 3.3405
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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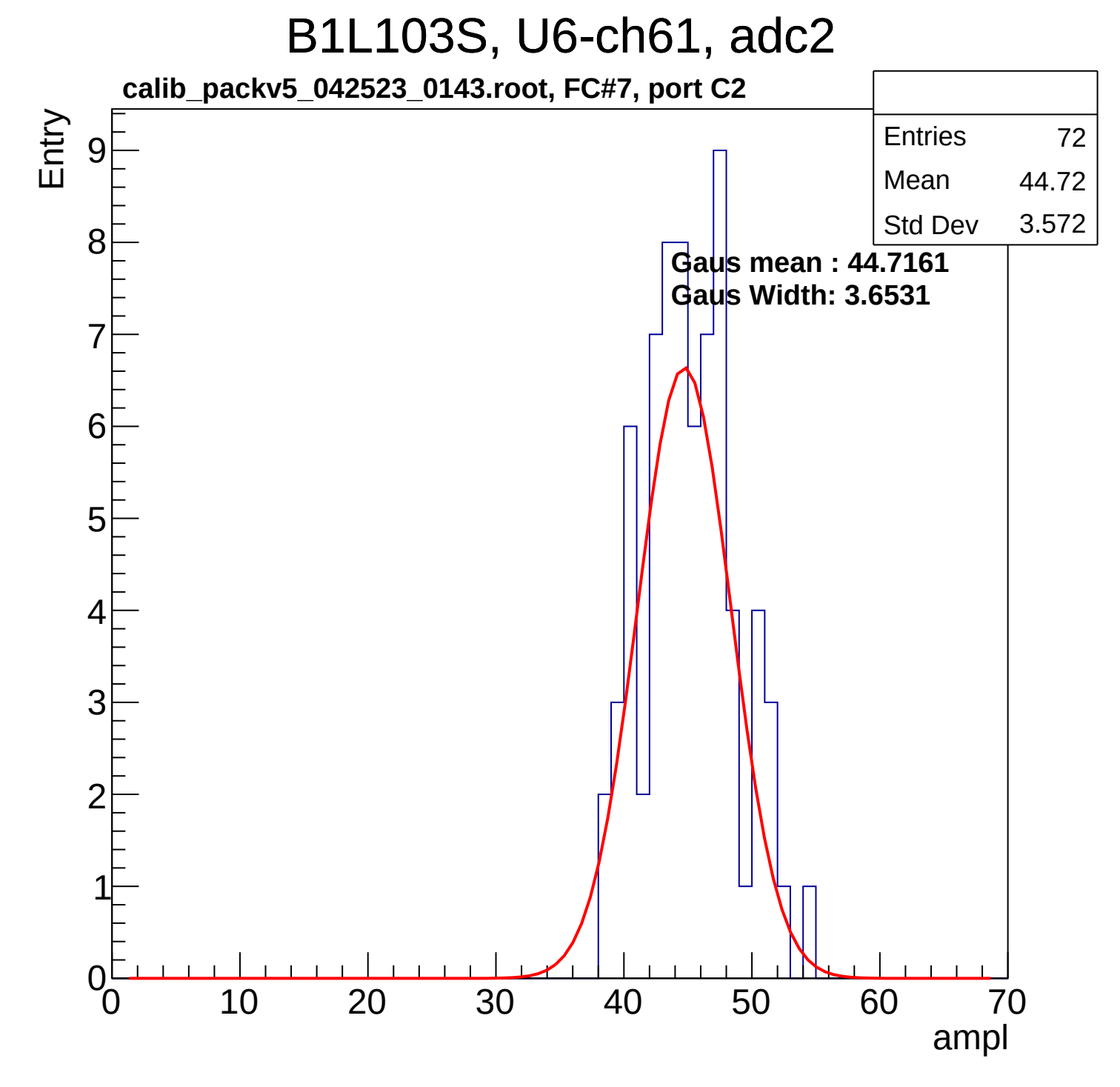
B1L103S, U6-ch61, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 44.72
Std Dev 3.572
Gaus mean : 44.7161
Gaus Width: 3.6531
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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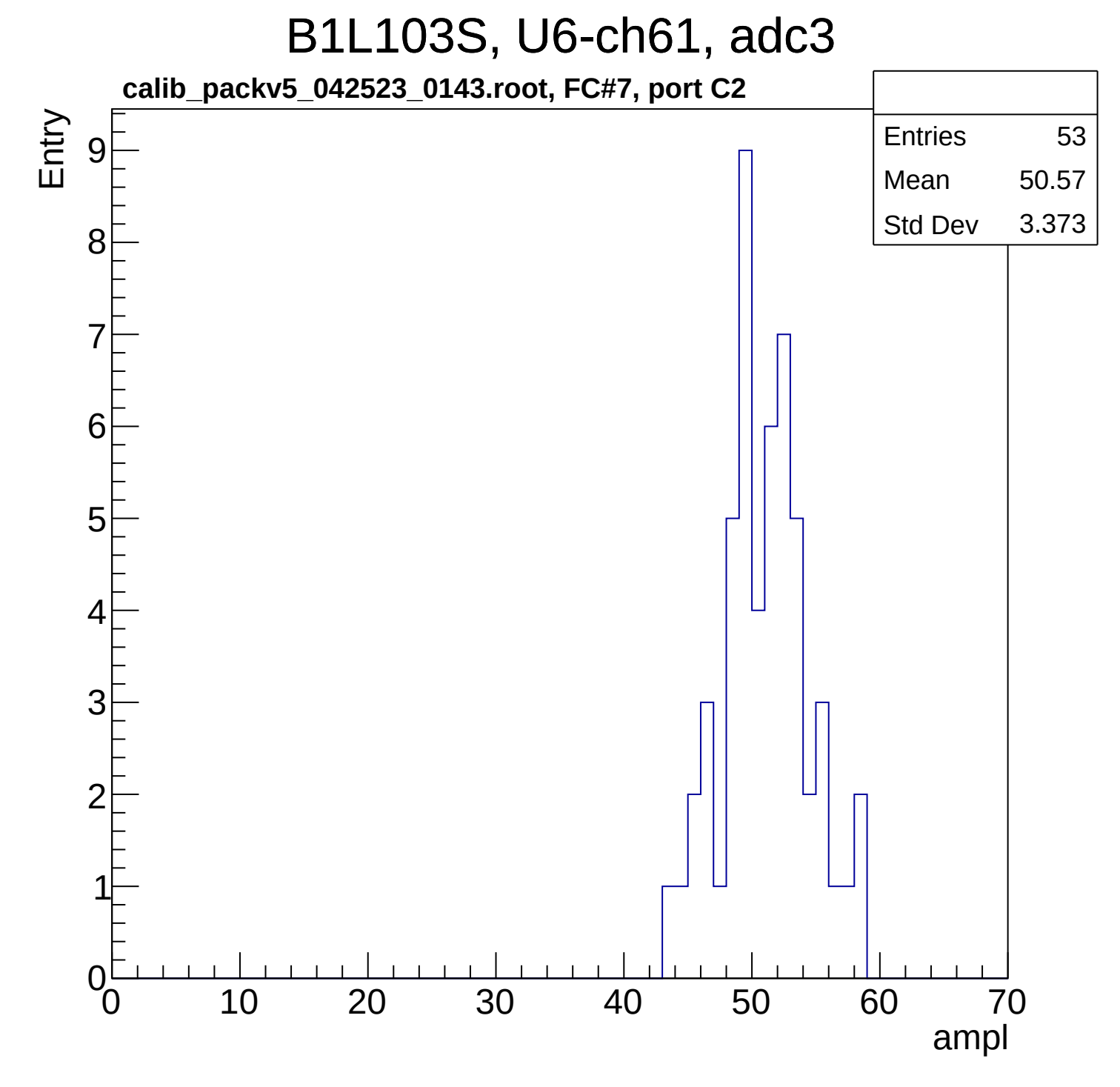
B1L103S, U6-ch61, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 50.57
Std Dev 3.373
Entry
ampl
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3
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6
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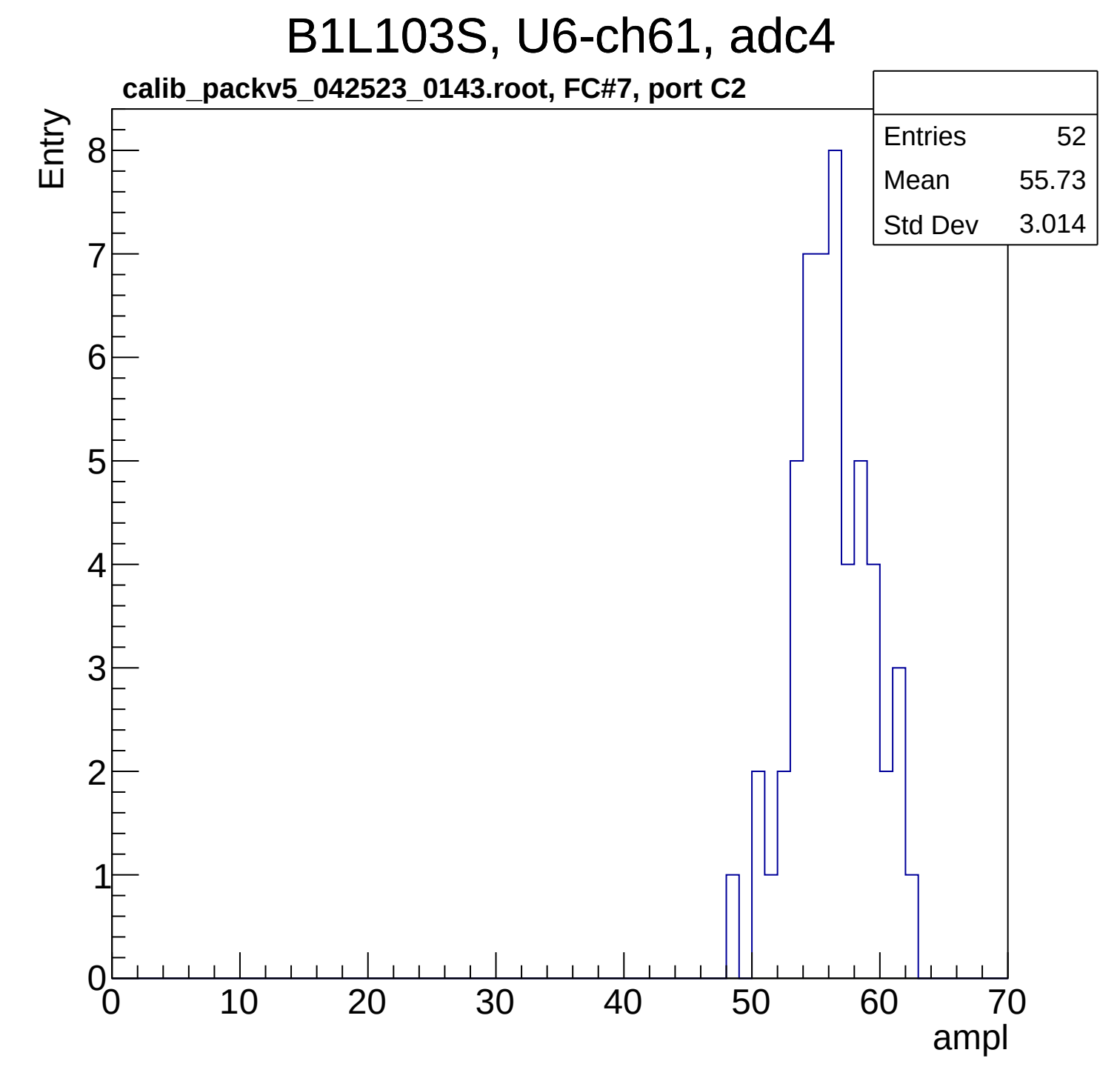
B1L103S, U6-ch61, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.73
Std Dev 3.014
Entry
ampl
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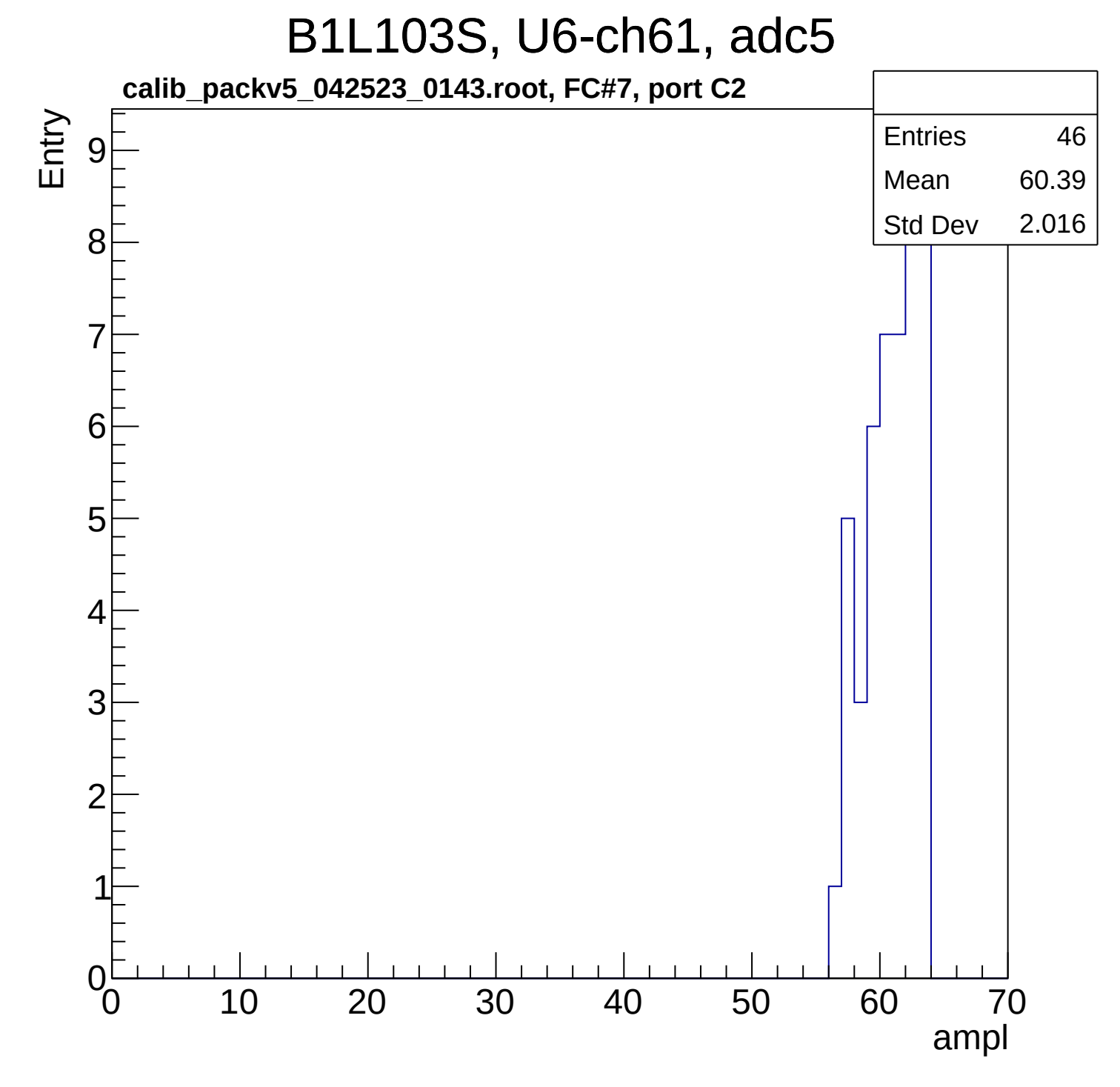
B1L103S, U6-ch61, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 60.39
Std Dev 2.016
Entry
ampl
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3
4
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0
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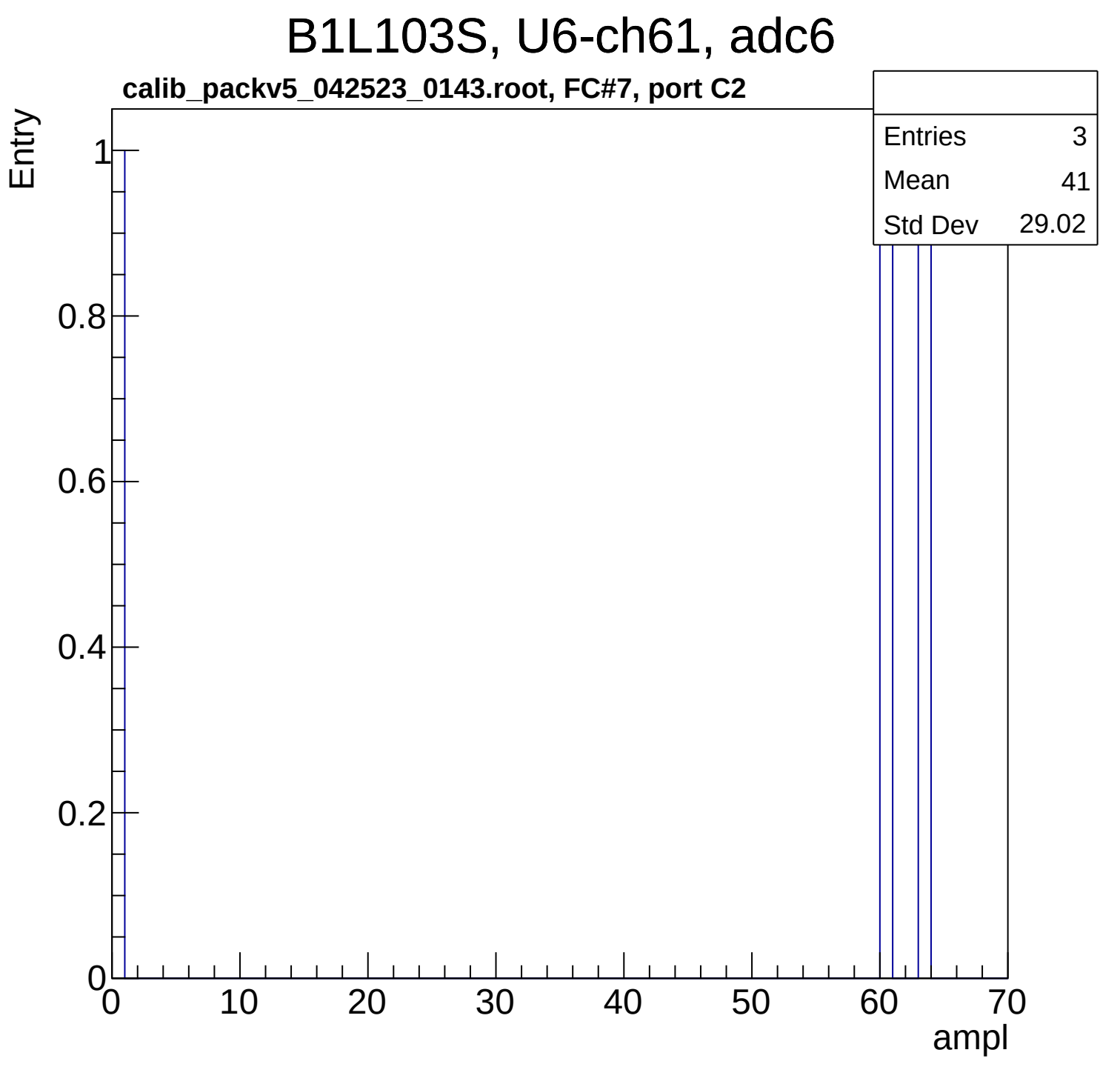
B1L103S, U6-ch61, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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50
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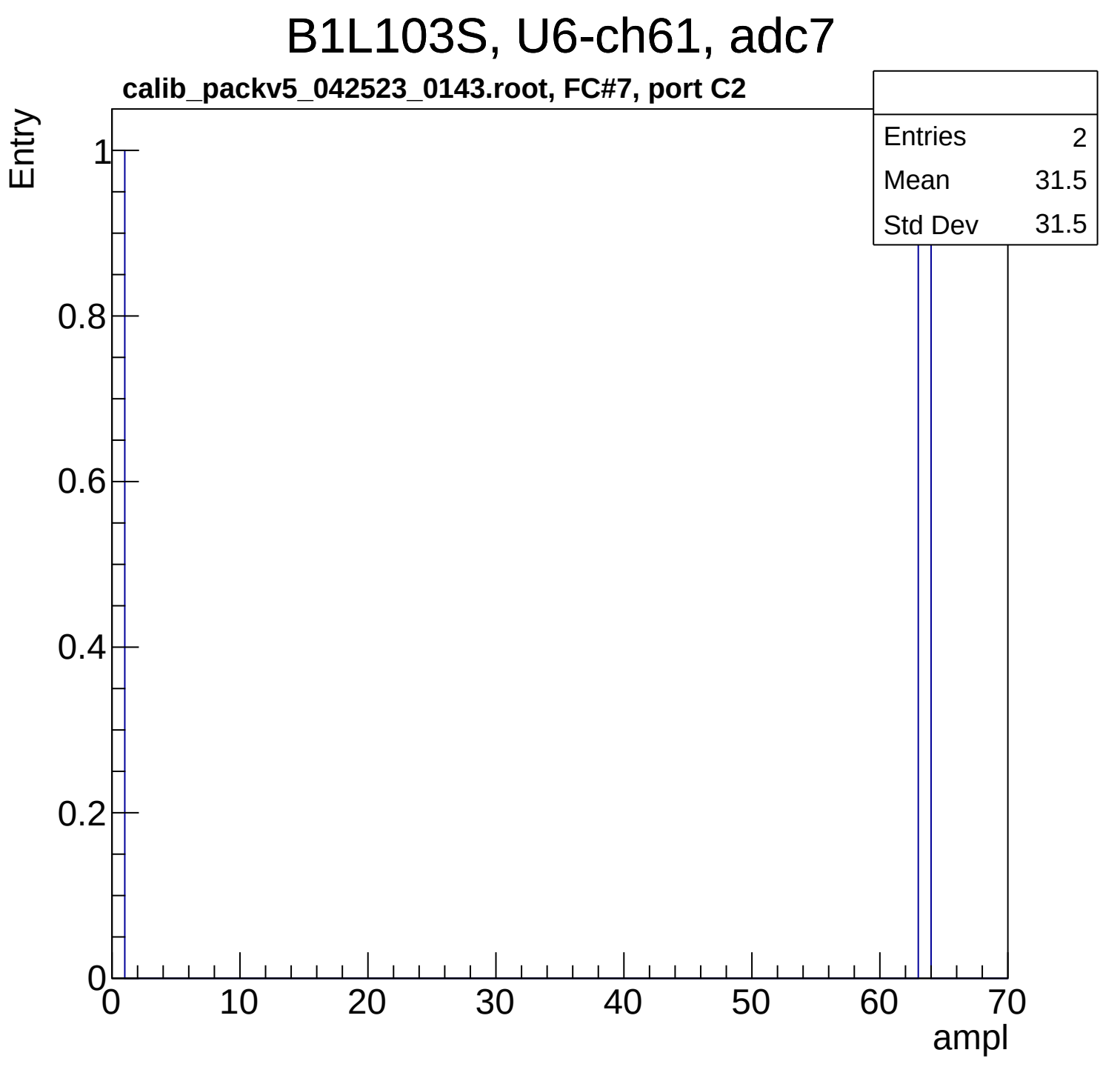
70
ampl


B1L103S, U6-ch61, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
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10
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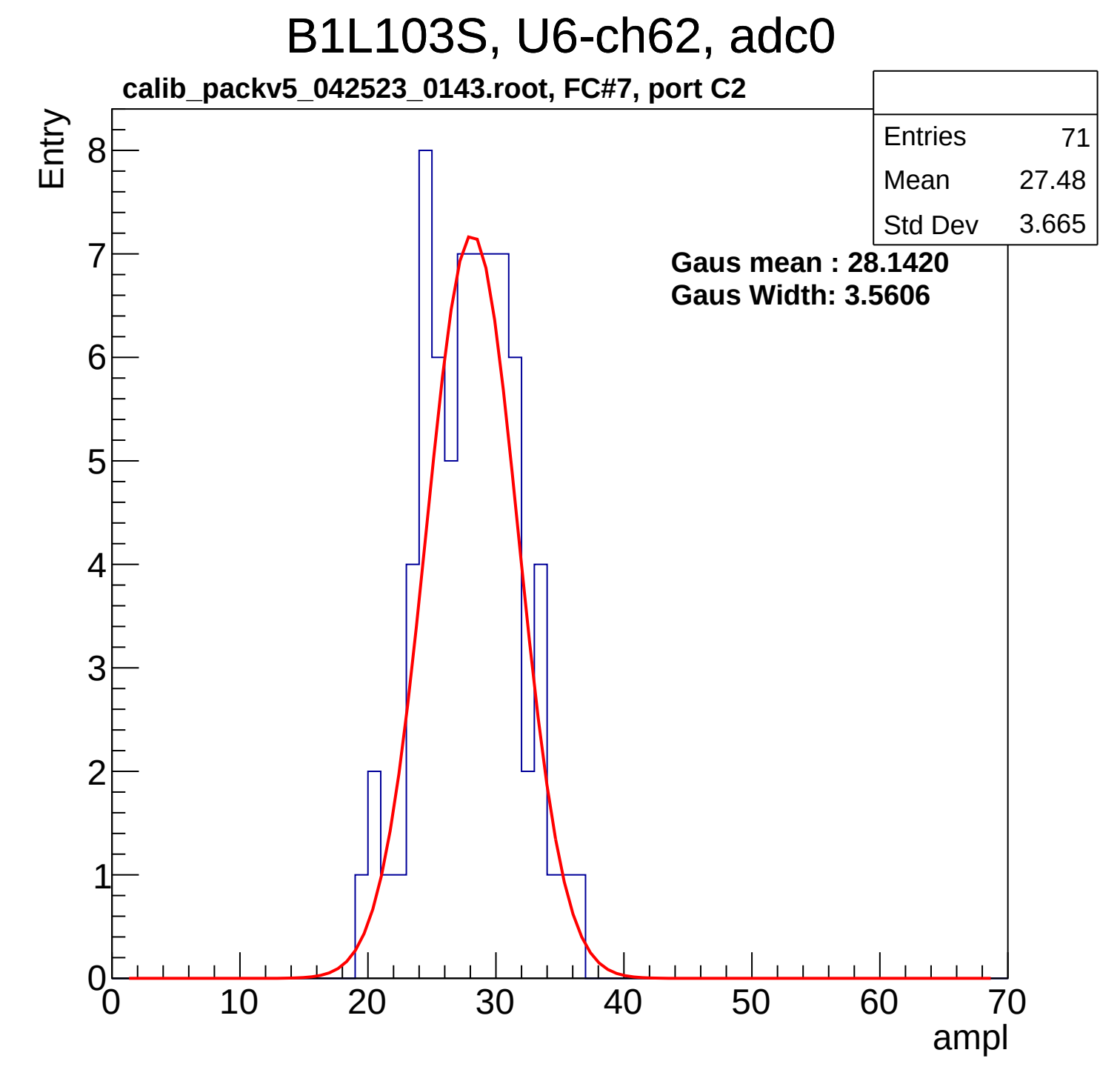
ampl


B1L103S, U6-ch62, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 27.48
Std Dev 3.665
Gaus mean : 28.1420
Gaus Width: 3.5606
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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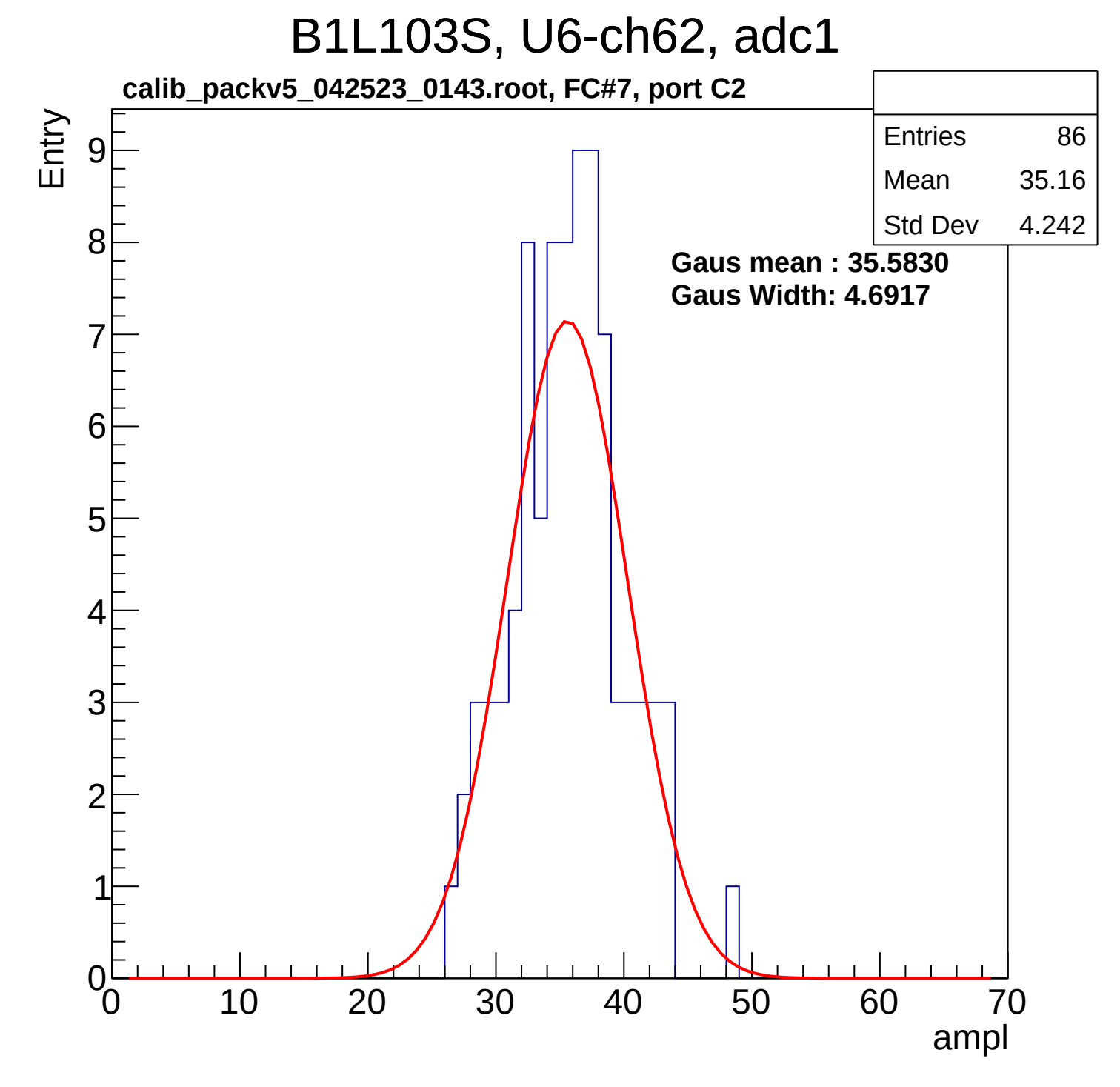
B1L103S, U6-ch62, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 35.16
Std Dev 4.242
Gaus mean : 35.5830
Gaus Width: 4.6917
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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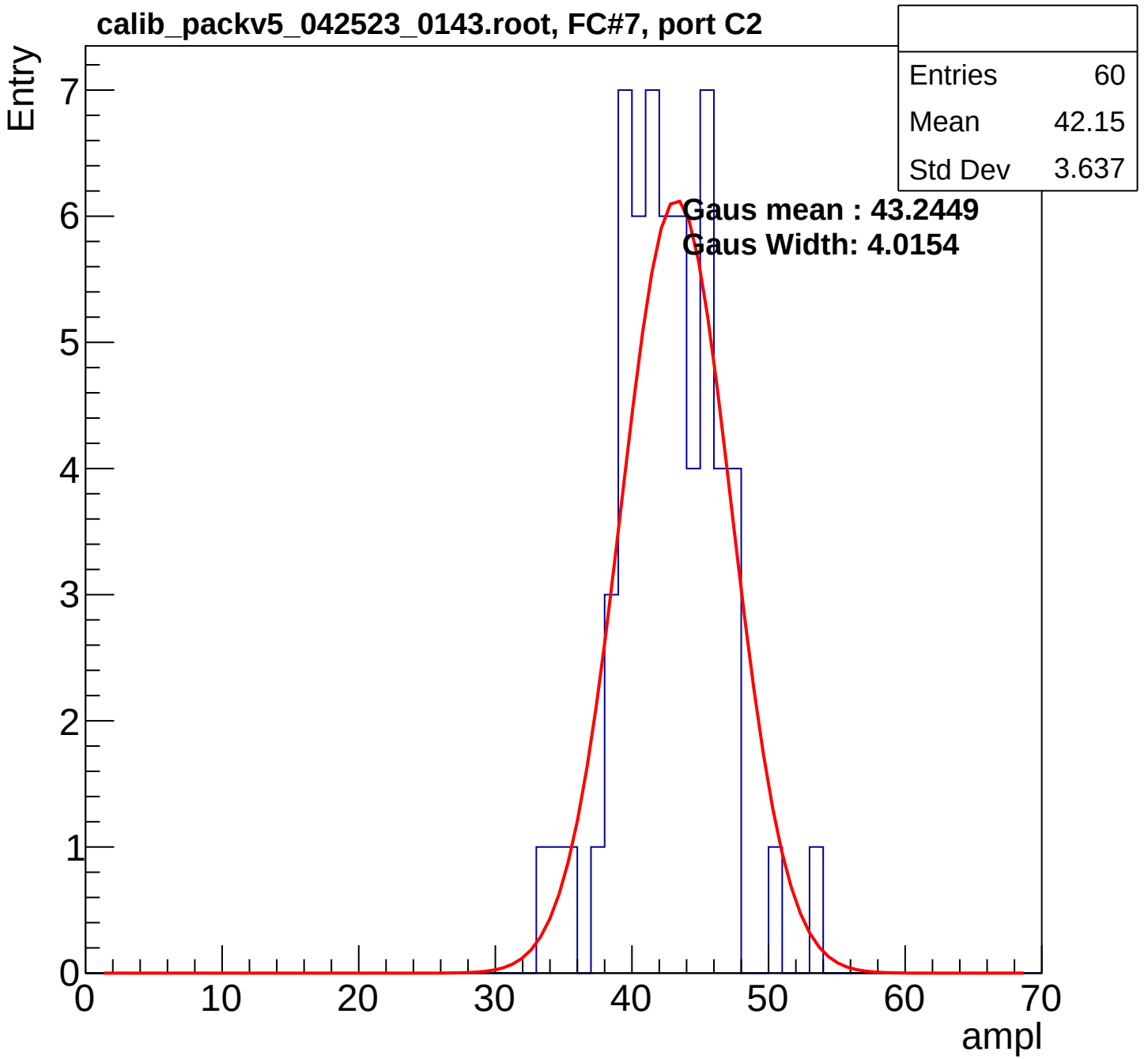
B1L103S, U6-ch62, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 42.15
Std Dev 3.637
Gaus mean : 43.2449
Gaus Width: 4.0154
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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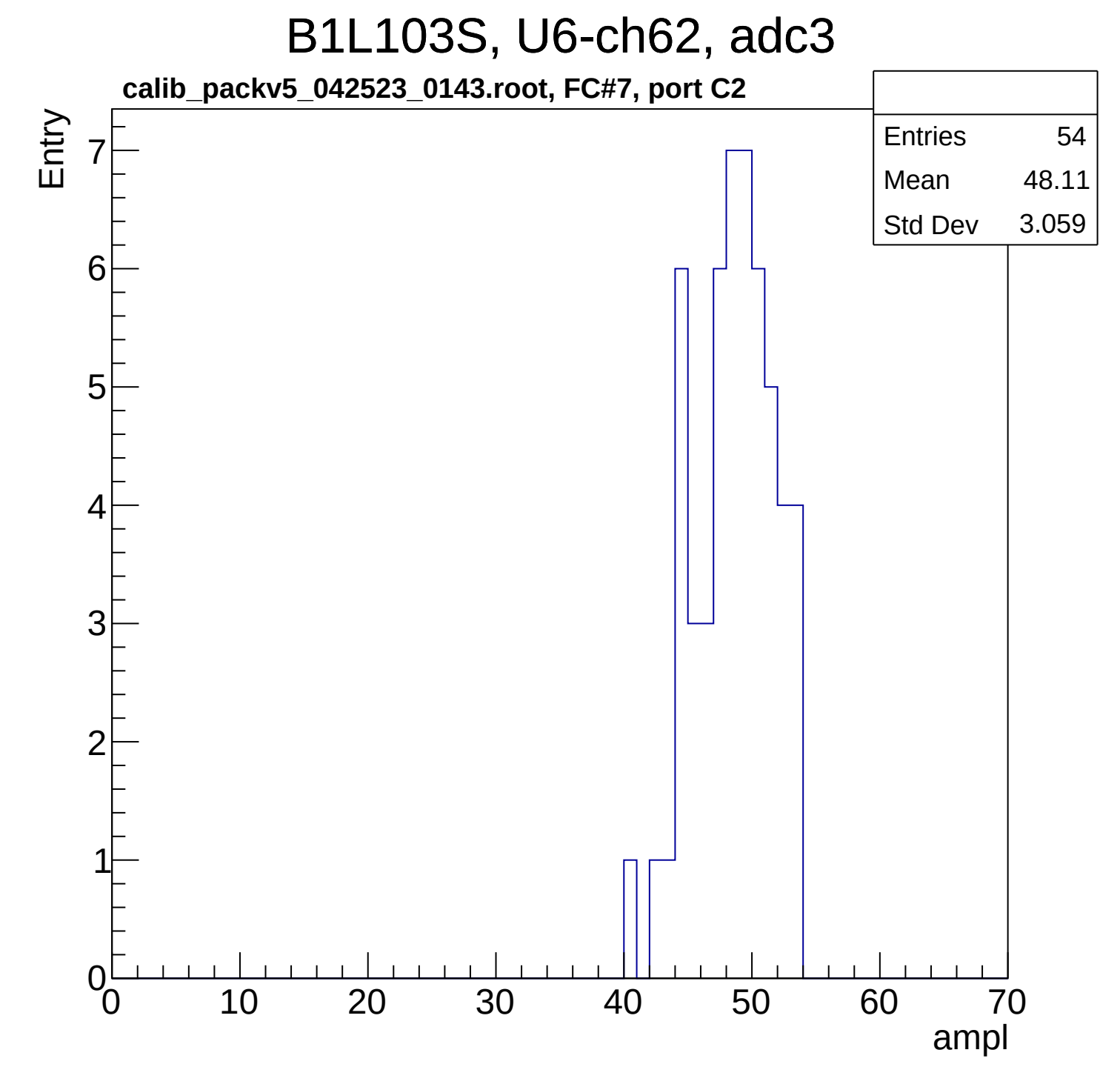
B1L103S, U6-ch62, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 48.11
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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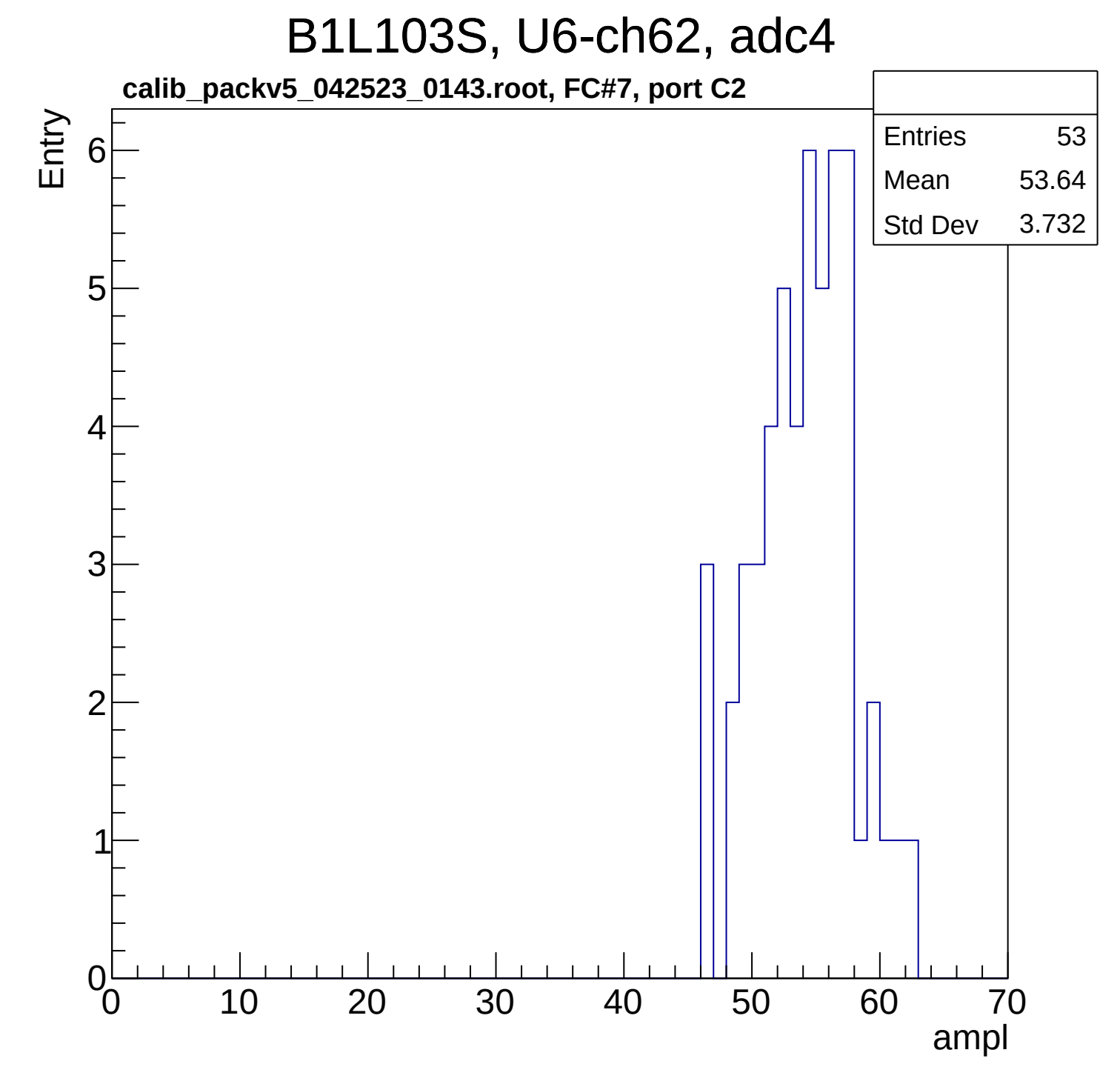
B1L103S, U6-ch62, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 53.64
Std Dev 3.732
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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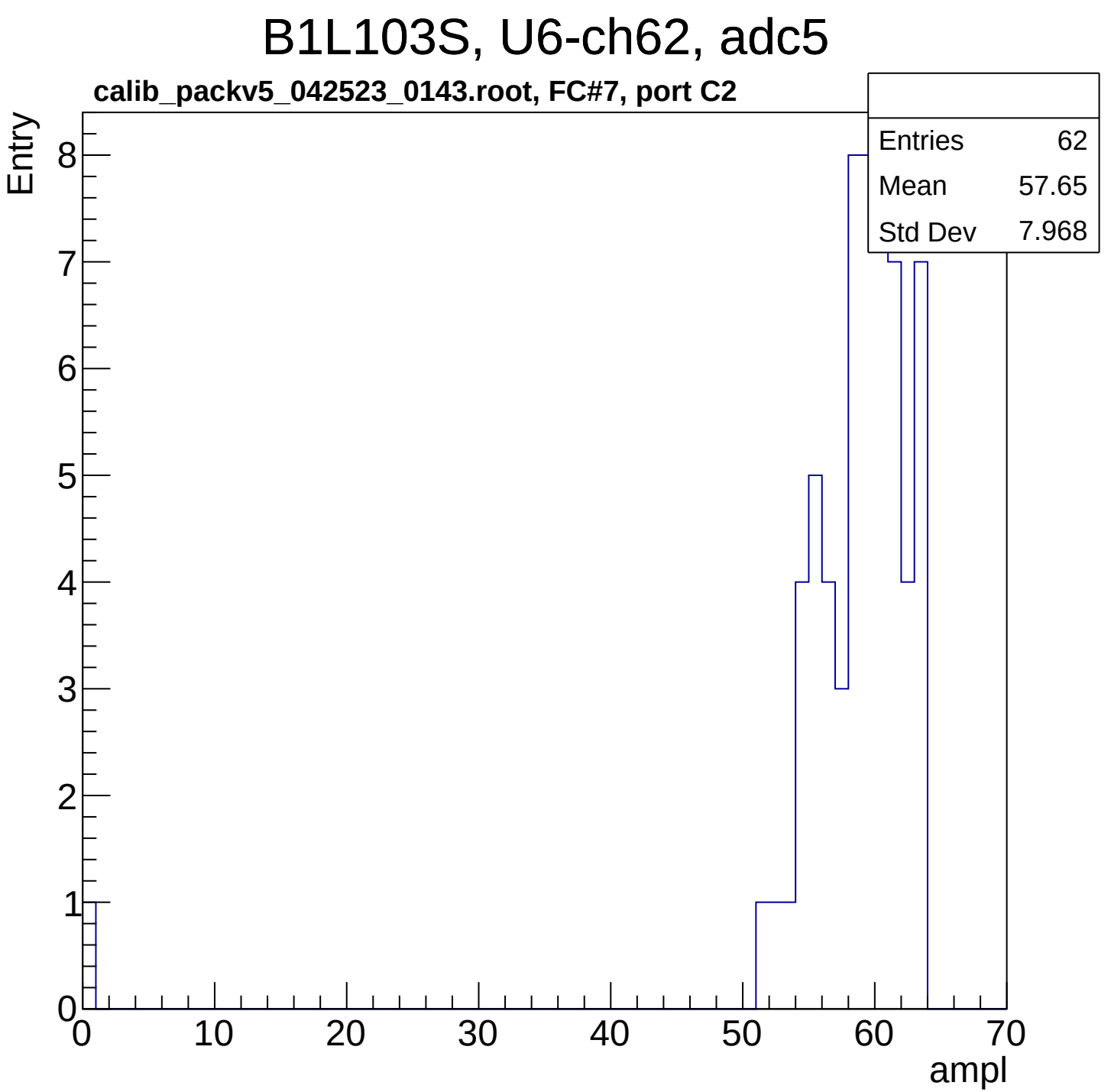
B1L103S, U6-ch62, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 57.65
Std Dev 7.968
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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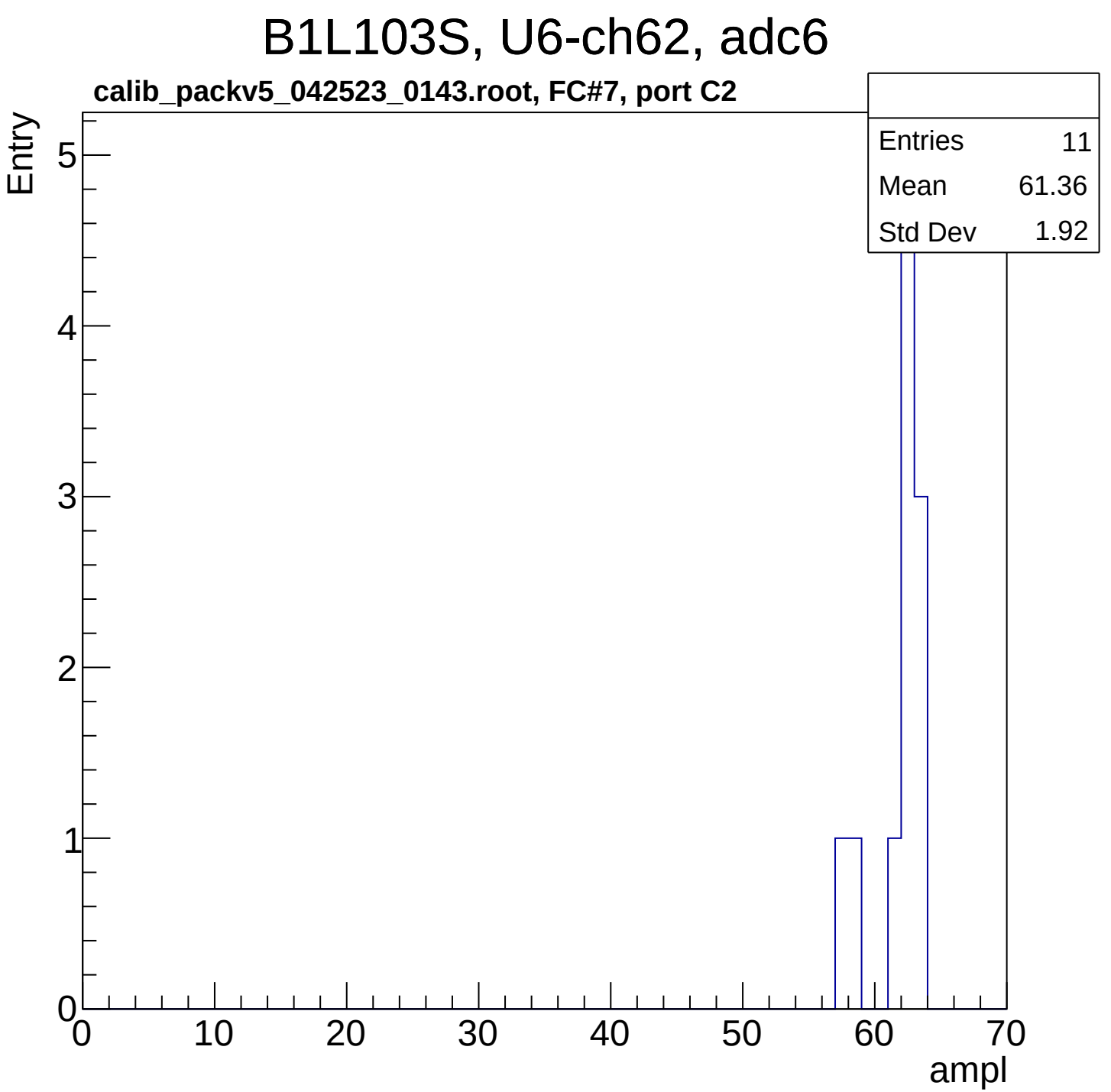
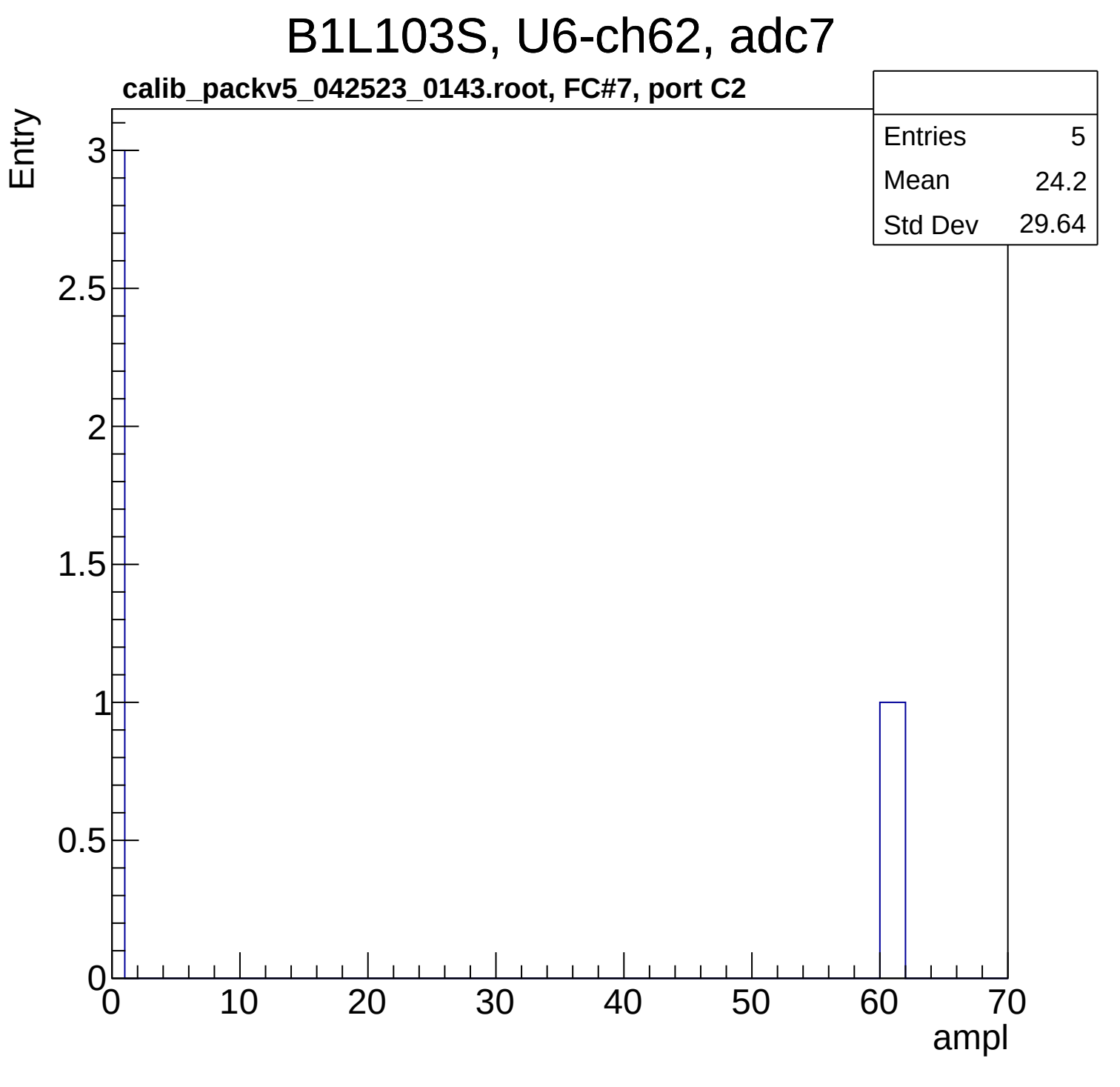
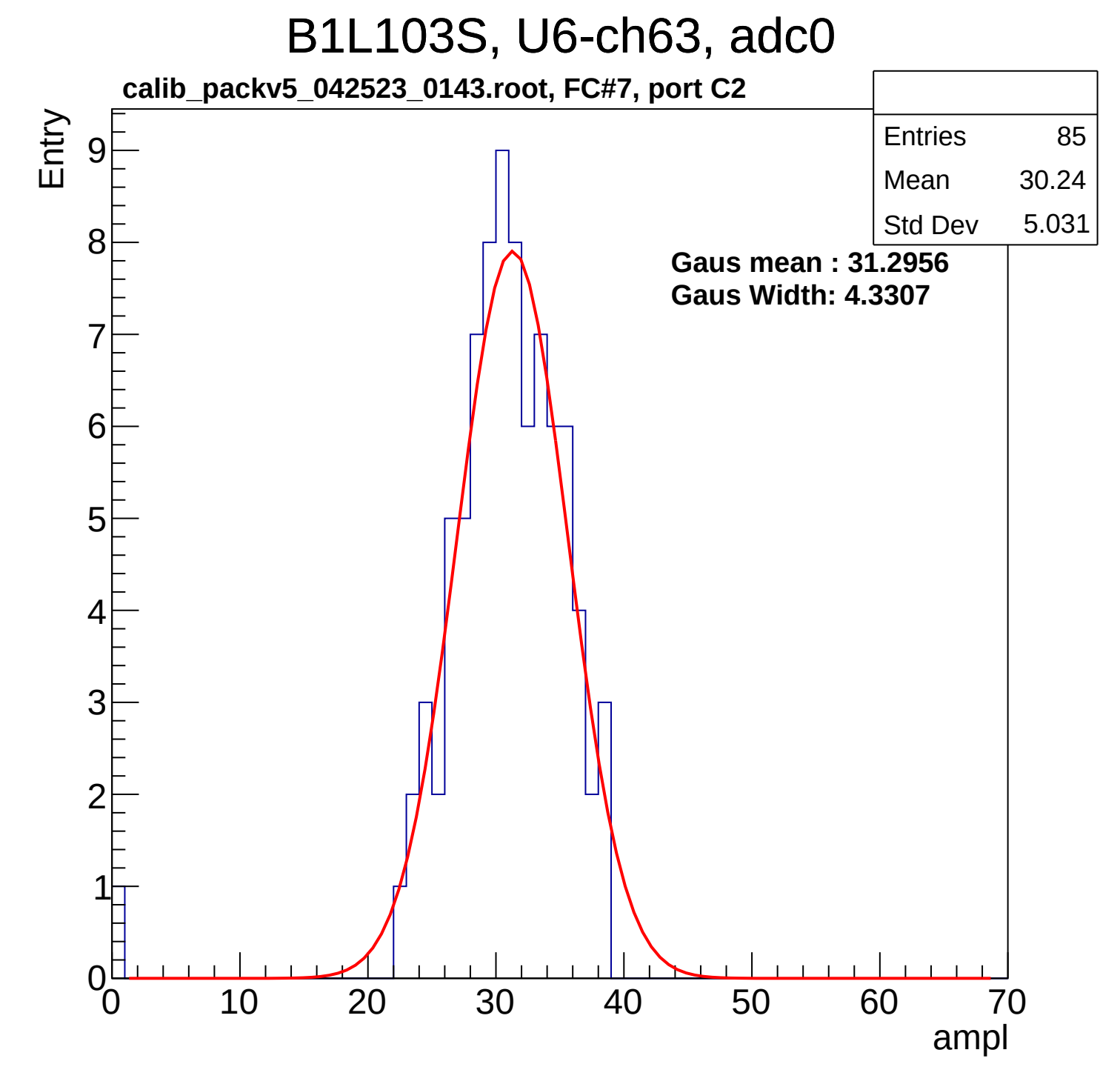

B1L103S, U6-ch62, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61.36
Std Dev 1.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch62, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 24.2
Std Dev 29.64
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch63, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 30.24
Std Dev 5.031
Gaus mean : 31.2956
Gaus Width: 4.3307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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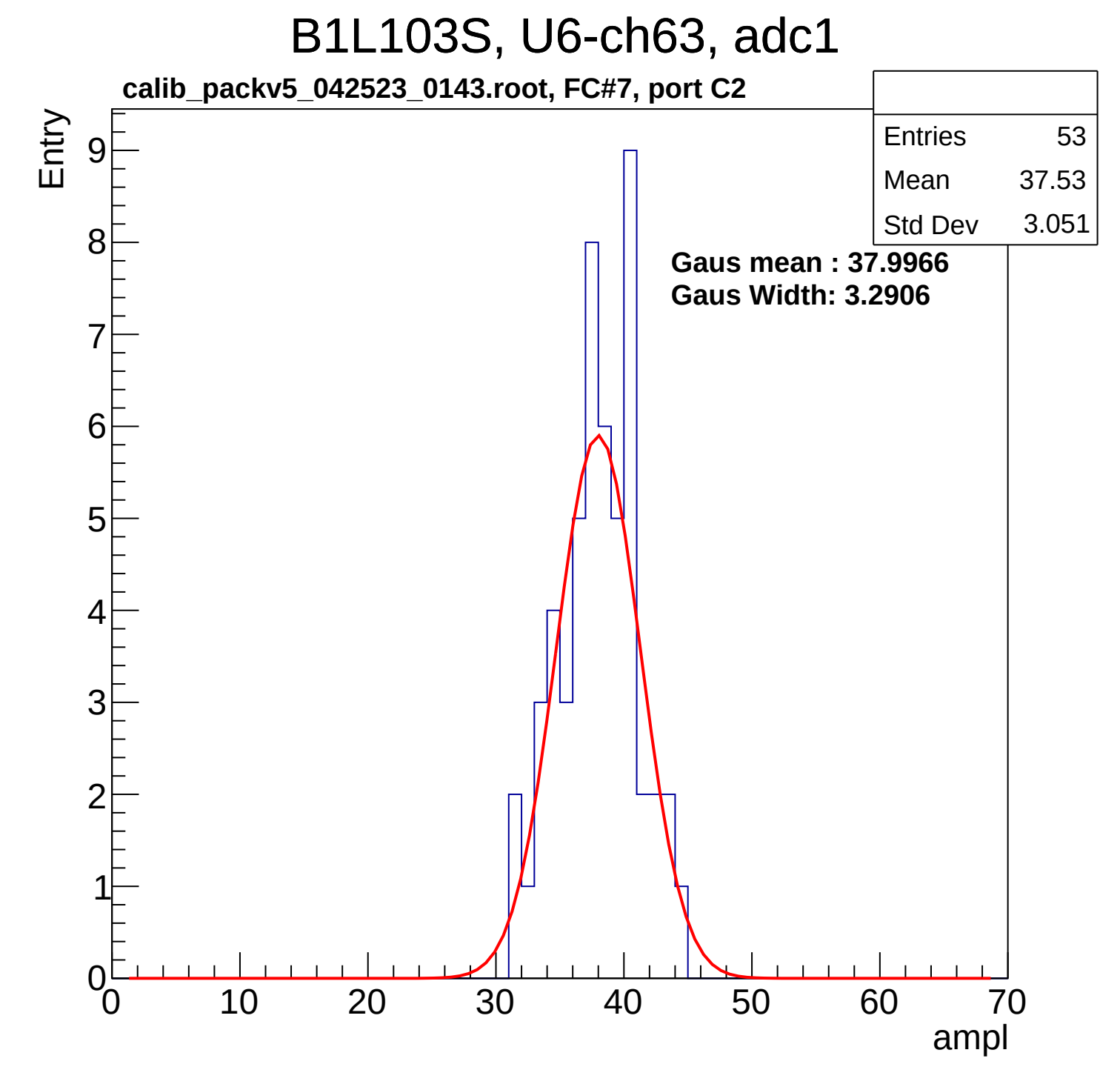
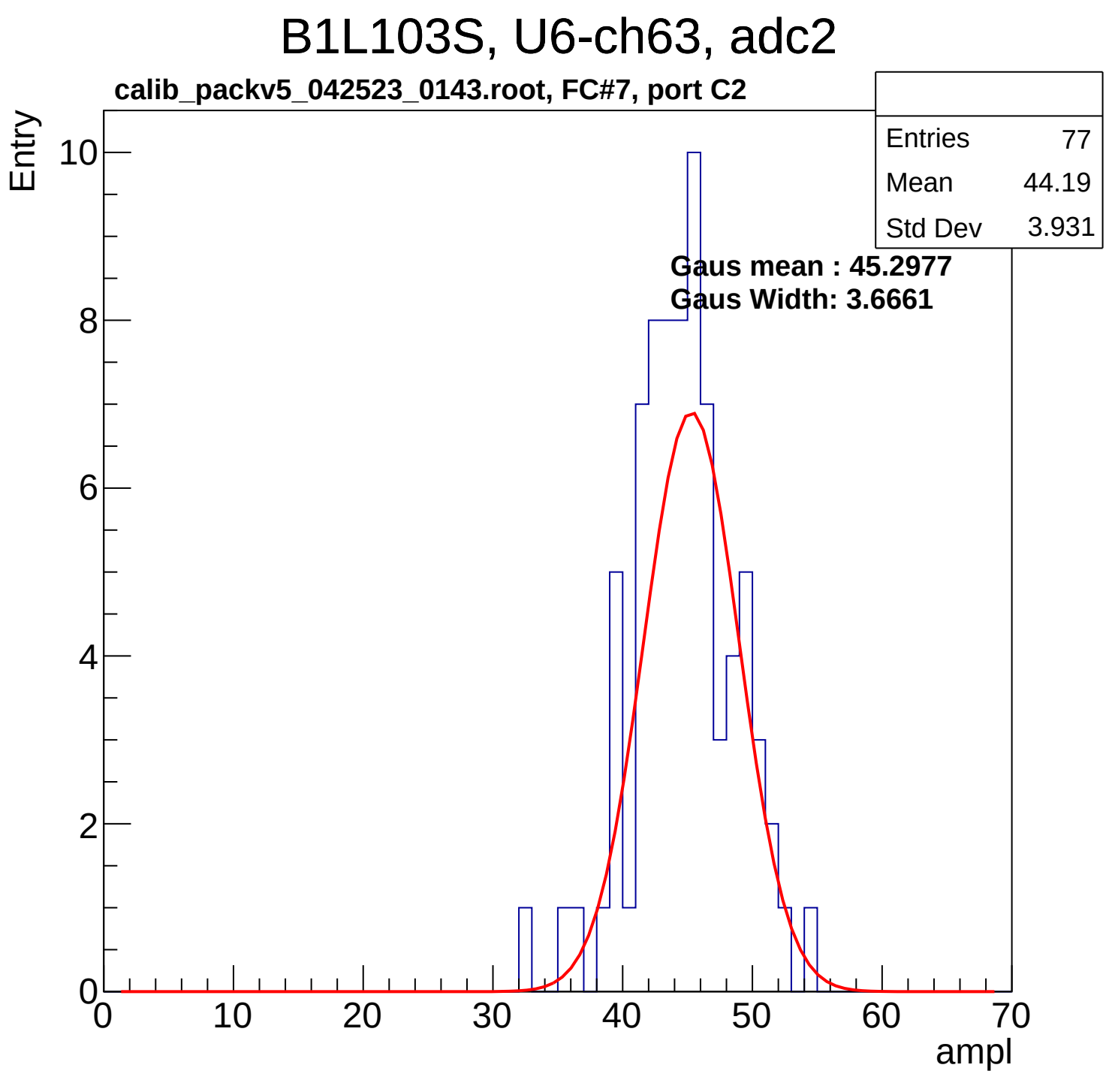
60
70


B1L103S, U6-ch63, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 37.53
Std Dev 3.051
Gaus mean : 37.9966
Gaus Width: 3.2906
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U6-ch63, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 44.19
Std Dev 3.931
Gaus mean : 45.2977
Gaus Width: 3.6661
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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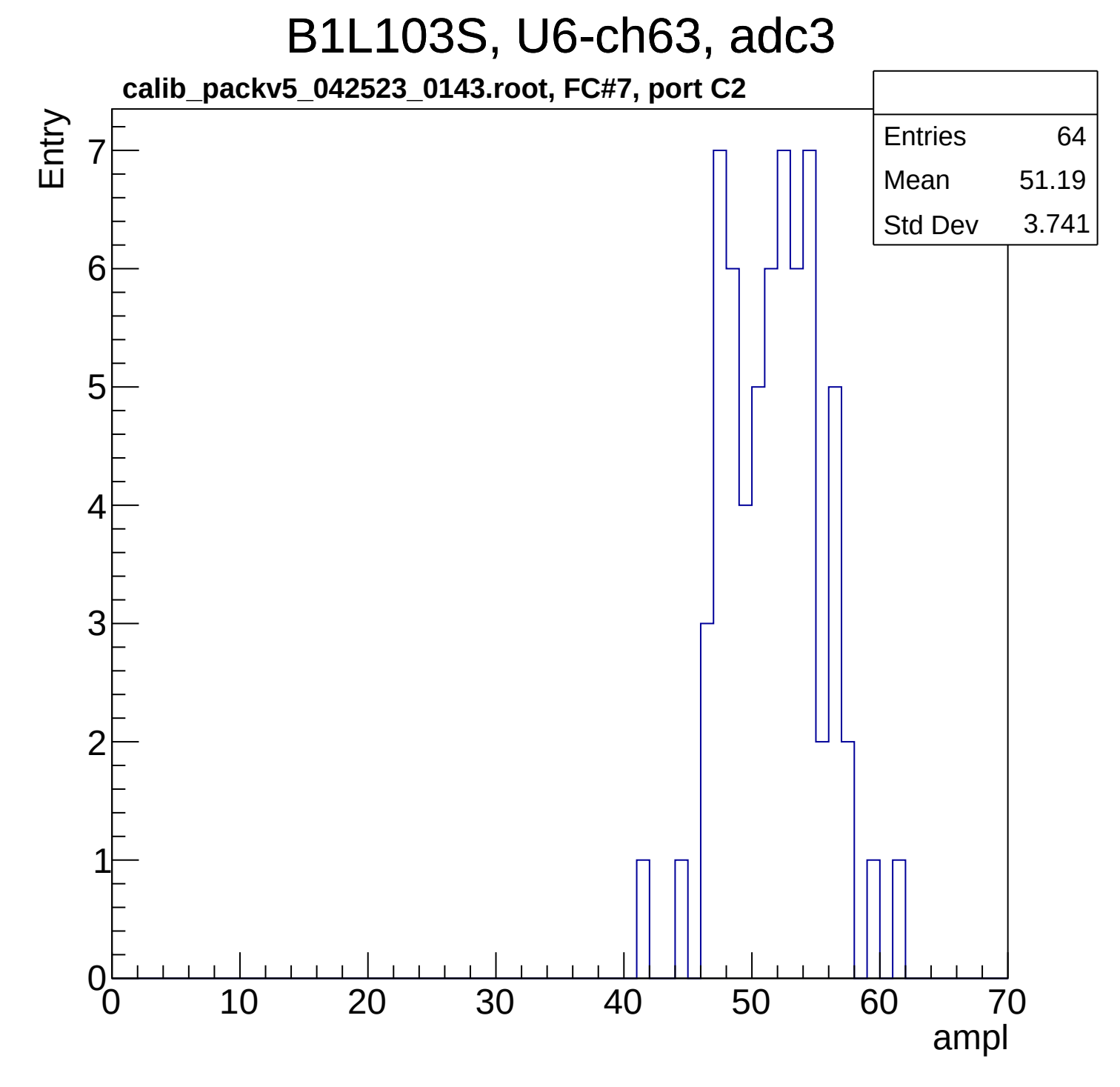
B1L103S, U6-ch63, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 51.19
Std Dev 3.741
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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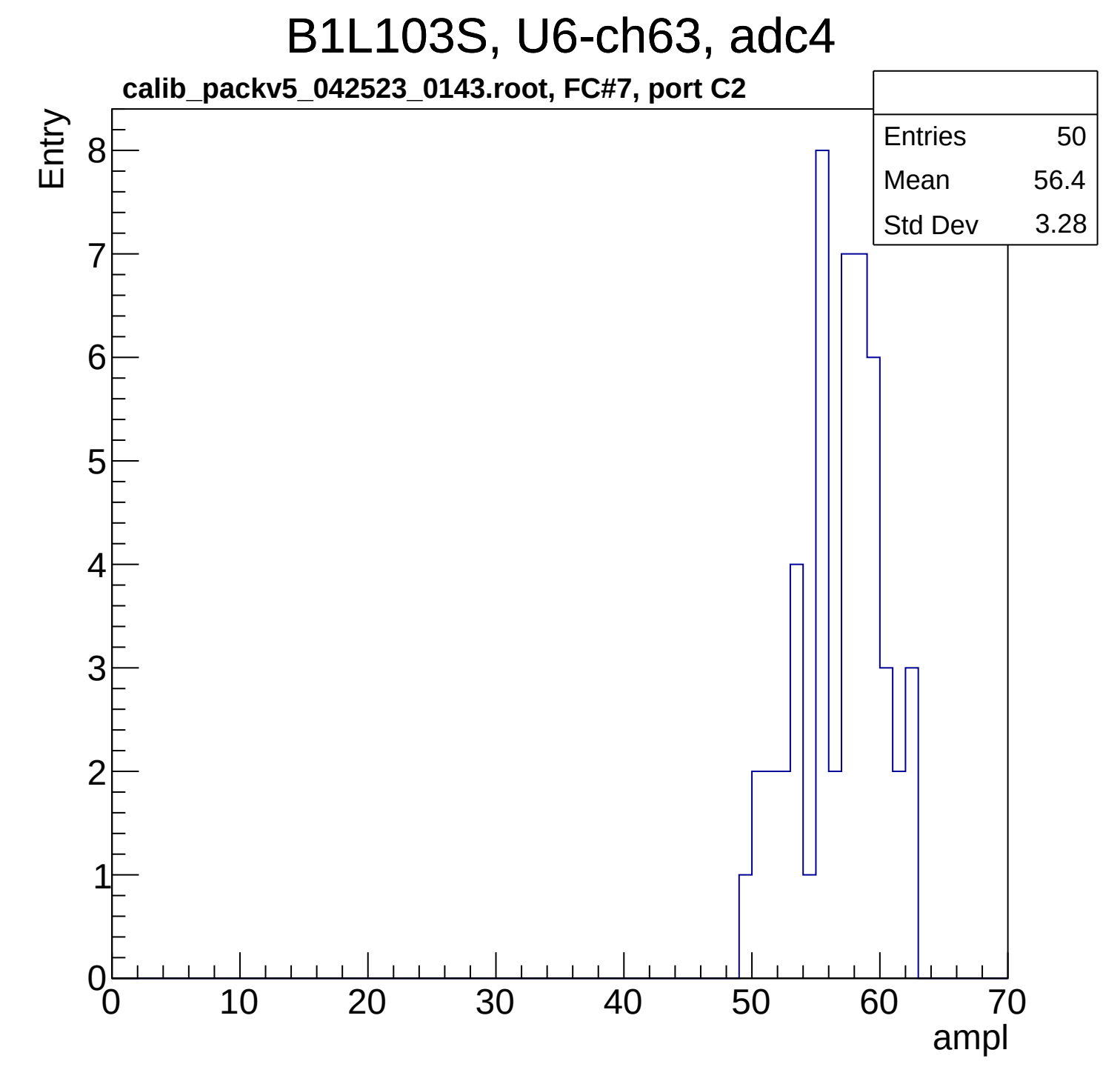
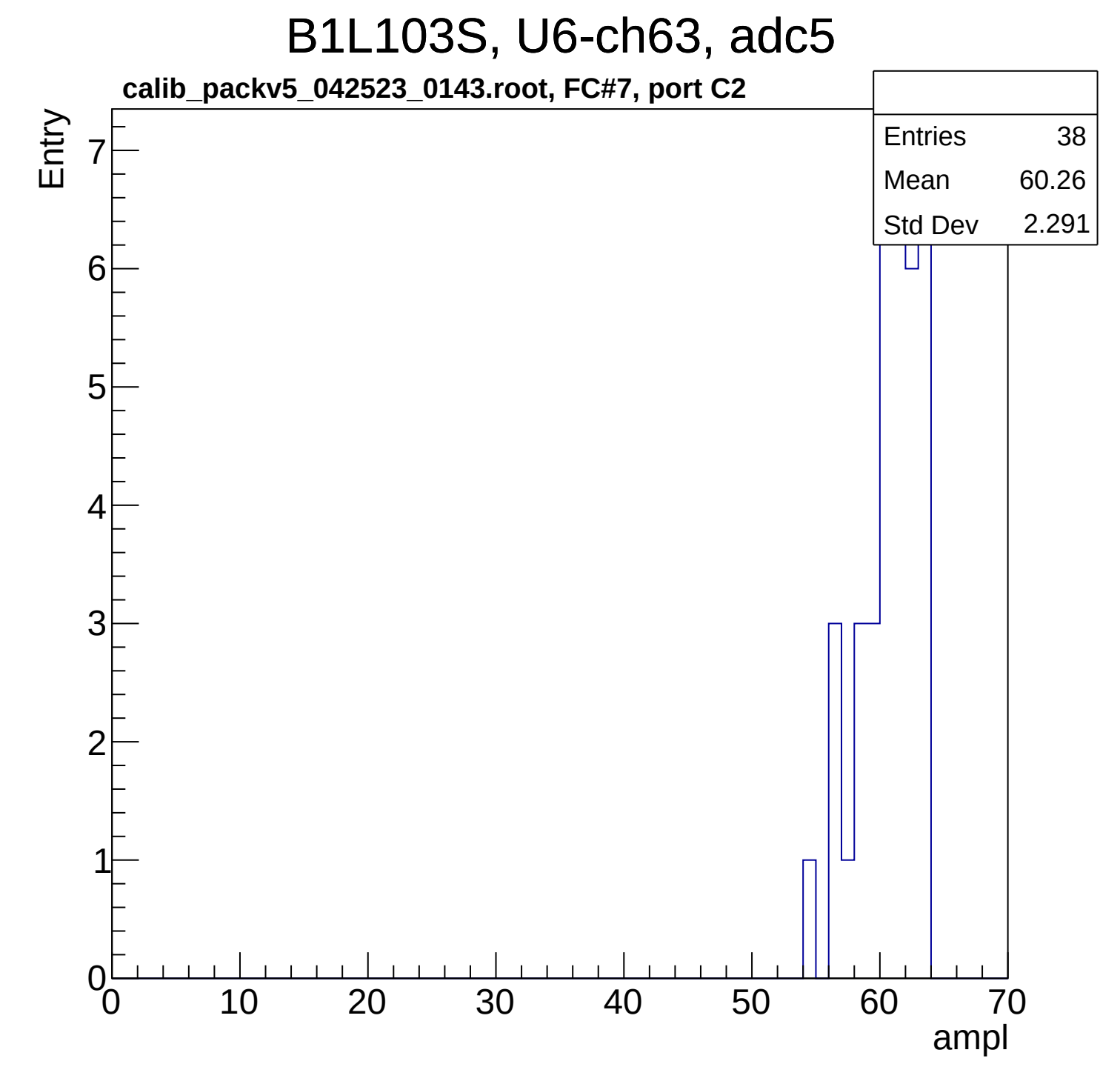
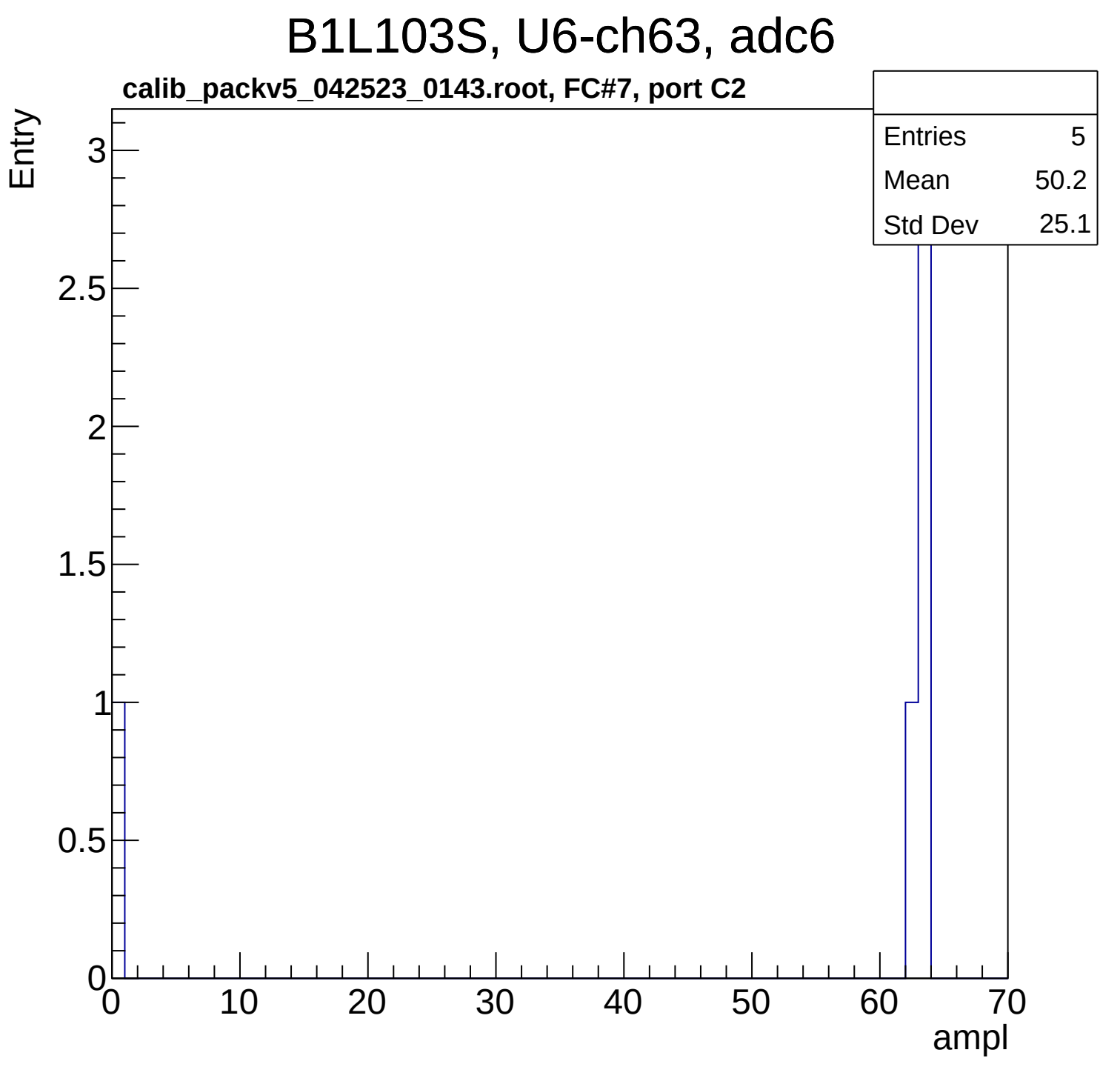
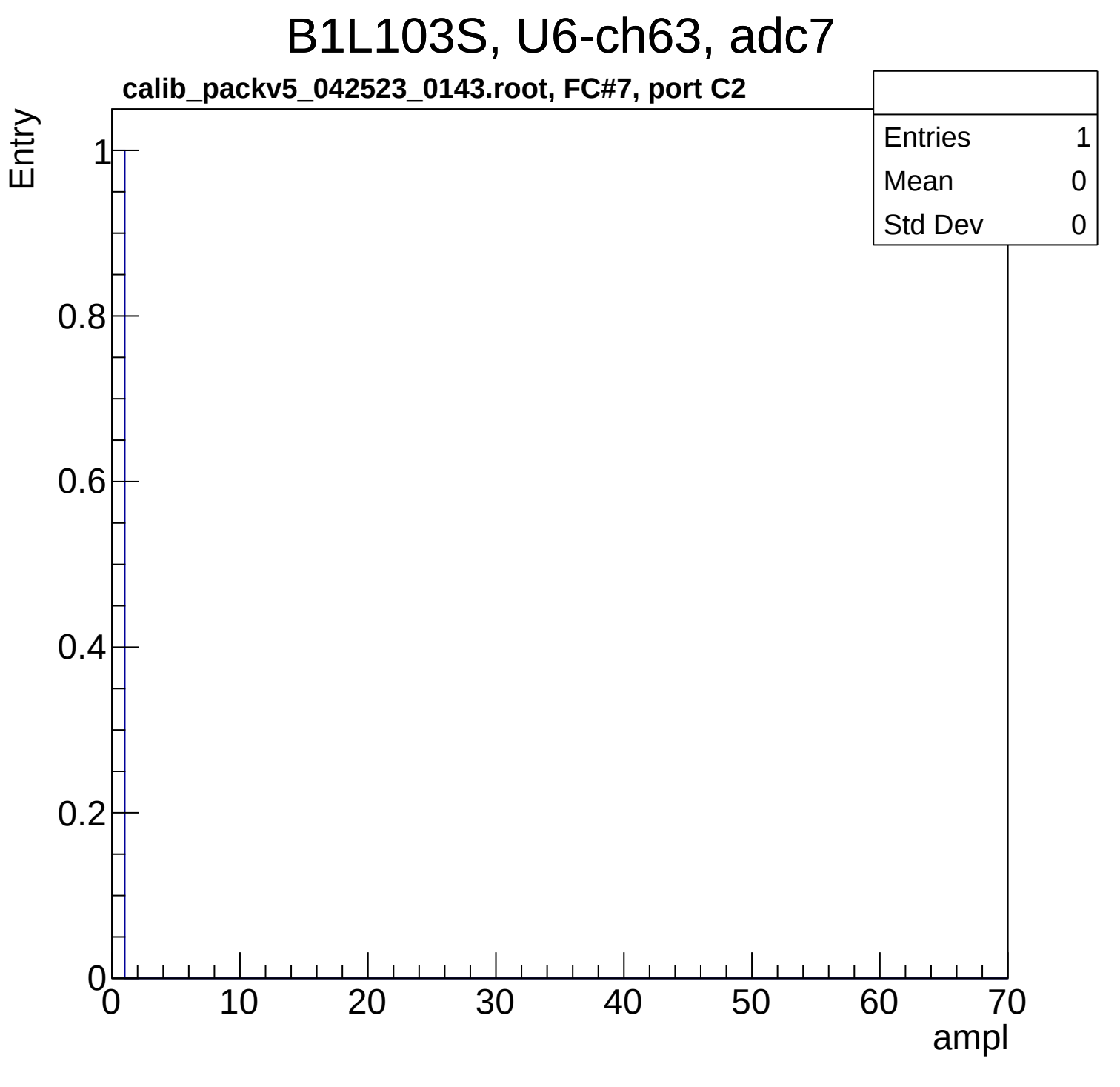
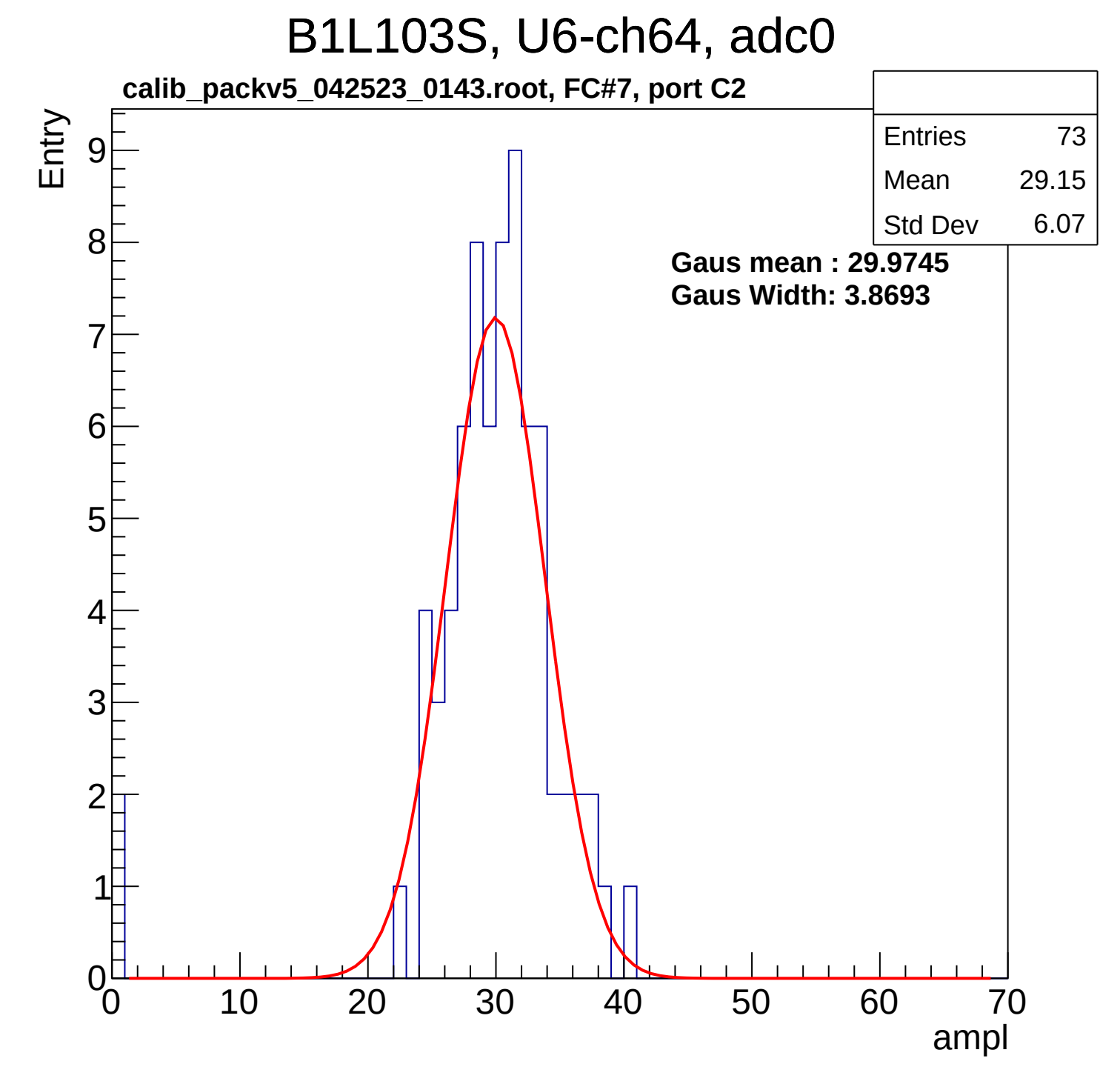

B1L103S, U6-ch63, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 56.4
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch63, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 60.26
Std Dev 2.291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch63, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch63, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch64, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.15
Std Dev 6.07
Gaus mean : 29.9745
Gaus Width: 3.8693
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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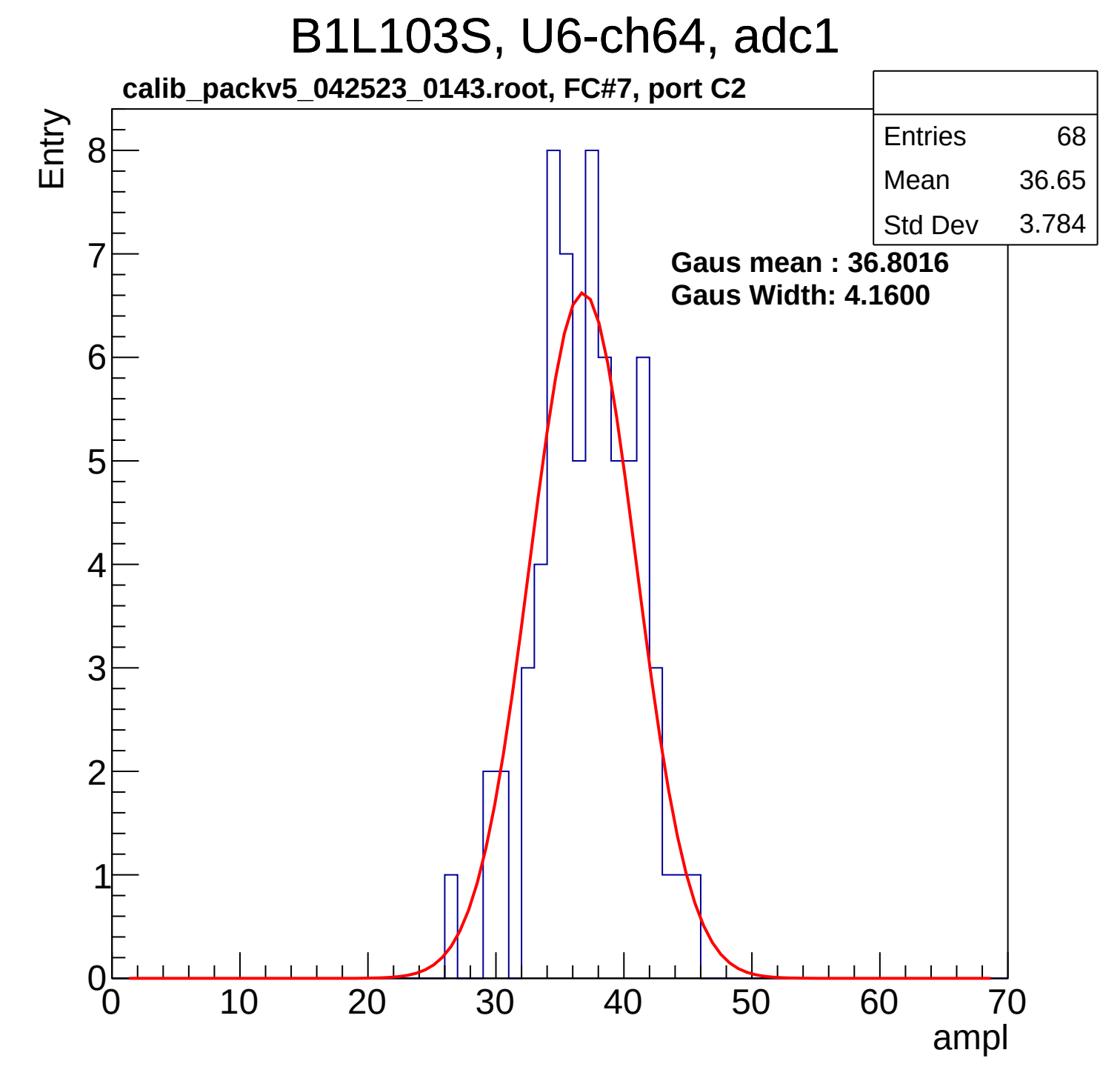
B1L103S, U6-ch64, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.65
Std Dev 3.784
Gaus mean : 36.8016
Gaus Width: 4.1600
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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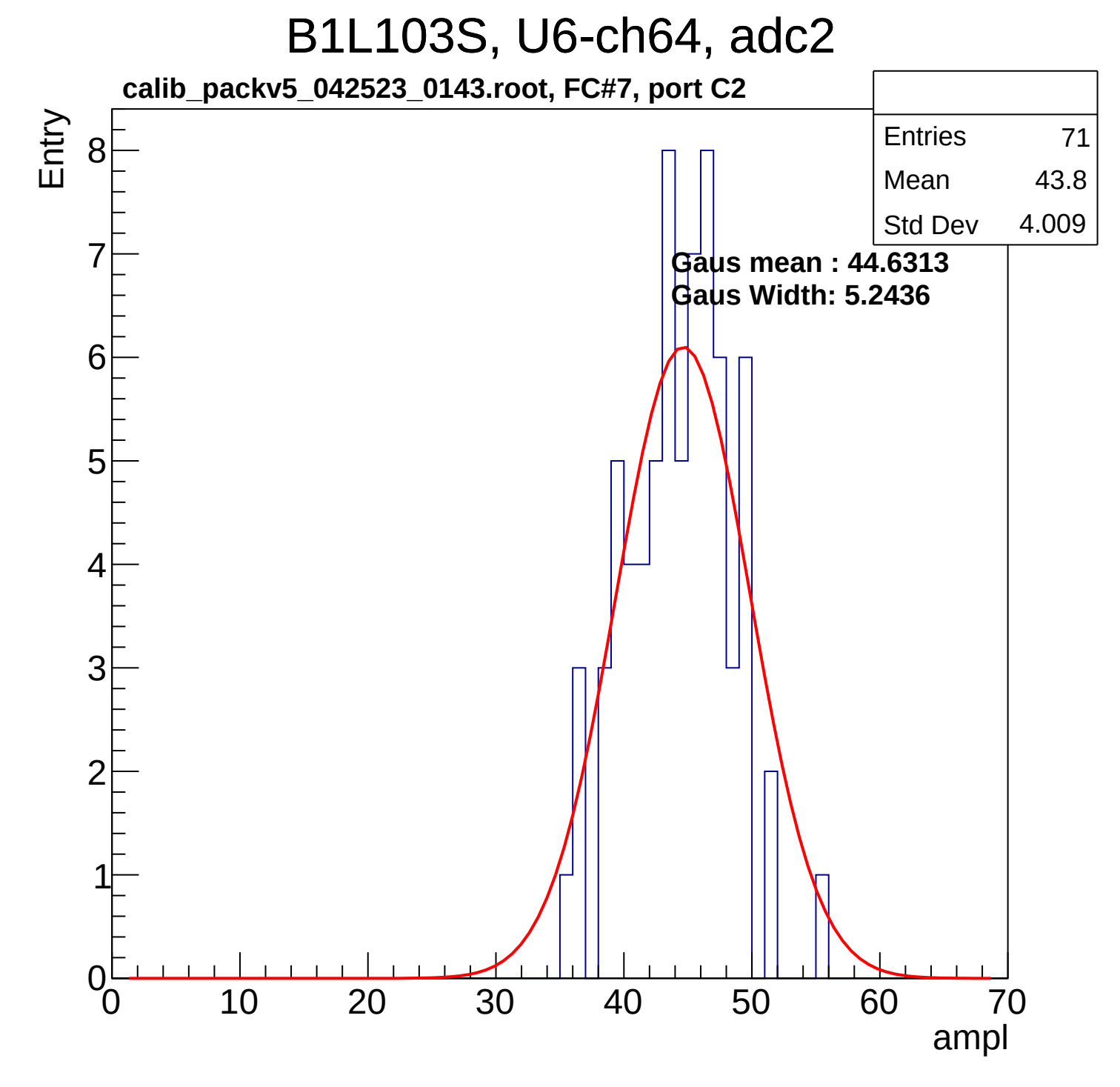
B1L103S, U6-ch64, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 43.8
Std Dev 4.009
Gaus mean : 44.6313
Gaus Width: 5.2436
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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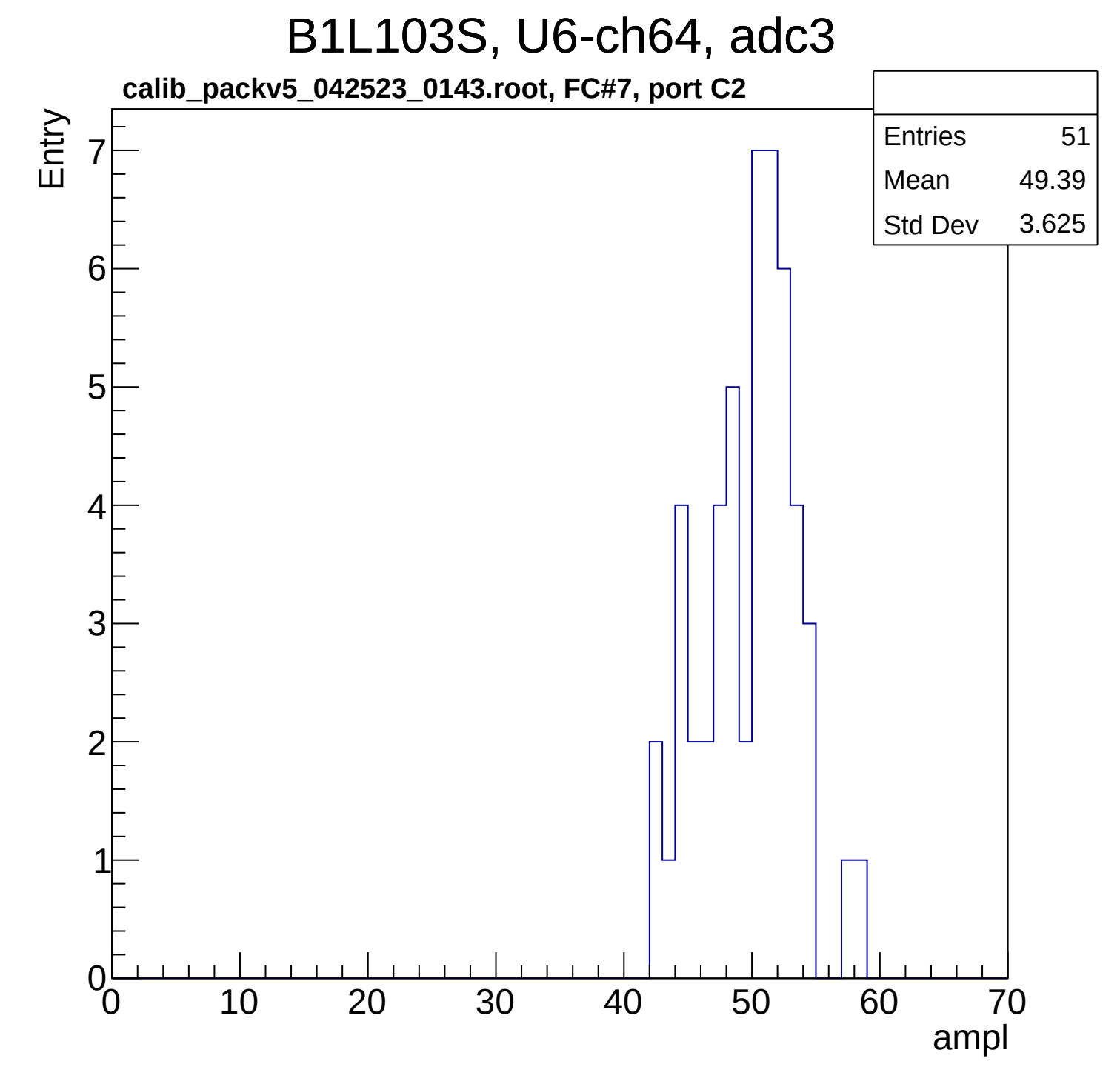
B1L103S, U6-ch64, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 49.39
Std Dev 3.625
Entry
ampl
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3
4
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0
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20
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40
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60
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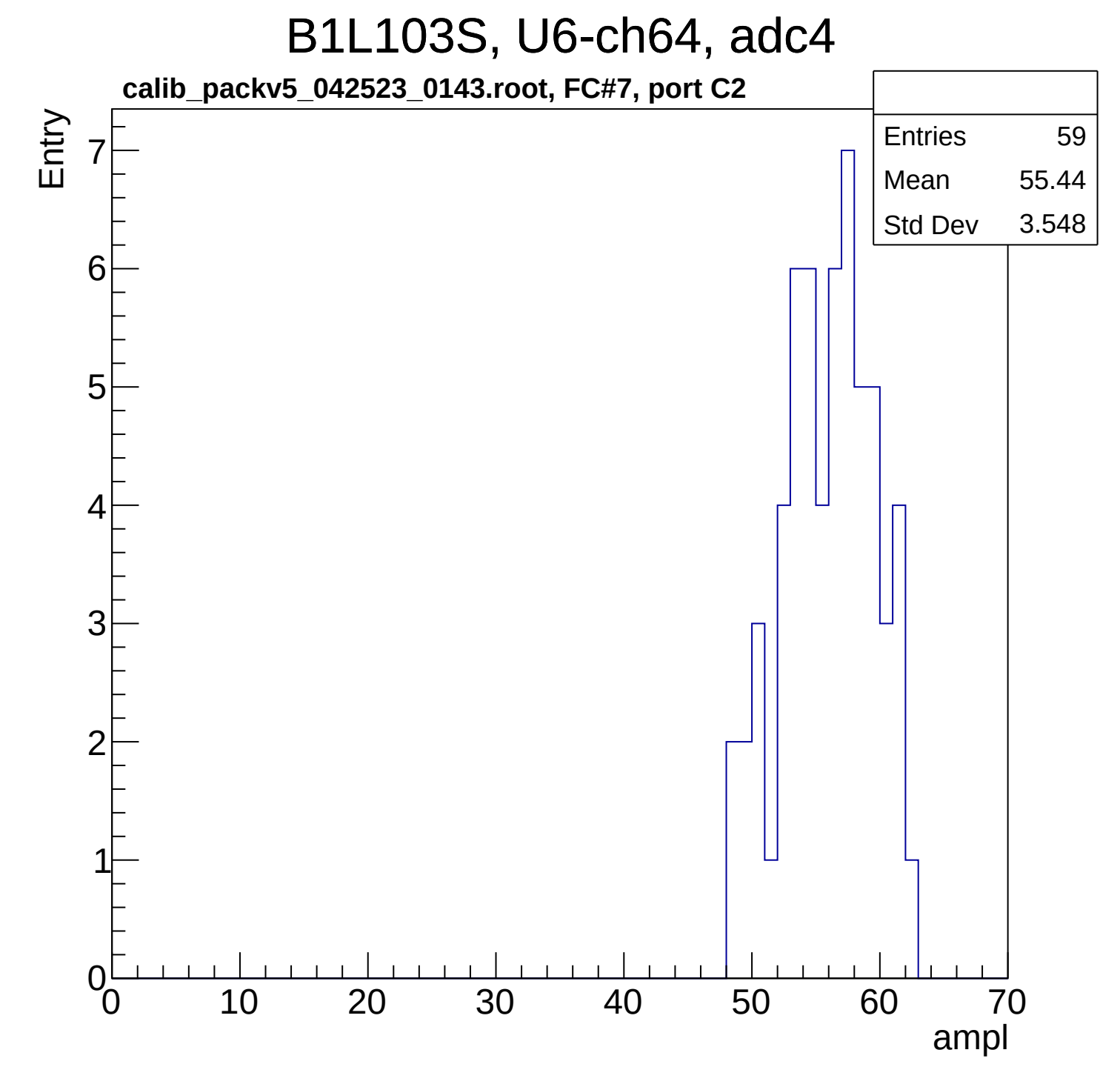
B1L103S, U6-ch64, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.44
Std Dev 3.548
Entry
ampl
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1
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3
4
5
6
7
0
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20
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60
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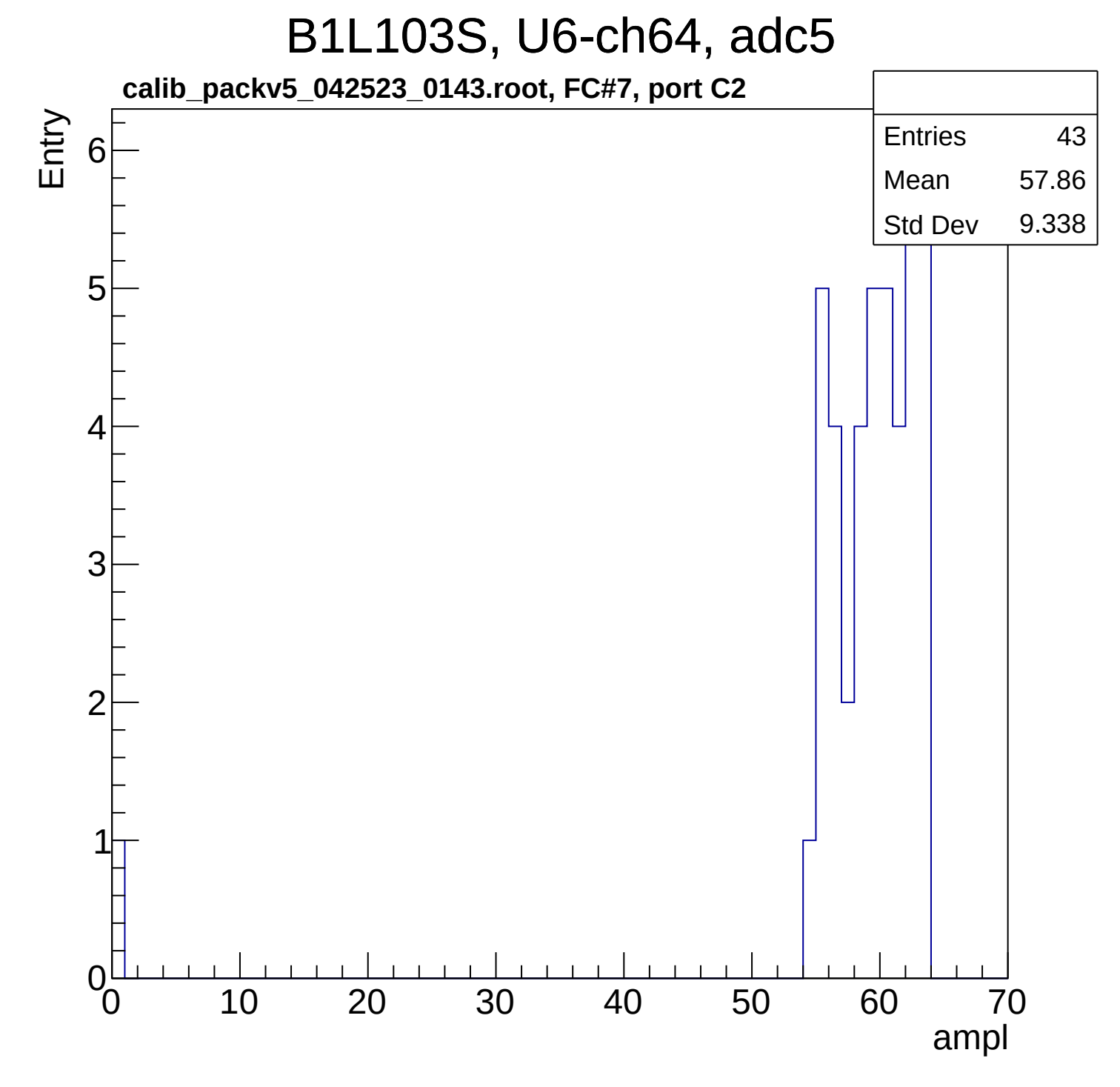
B1L103S, U6-ch64, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 57.86
Std Dev 9.338
Entry
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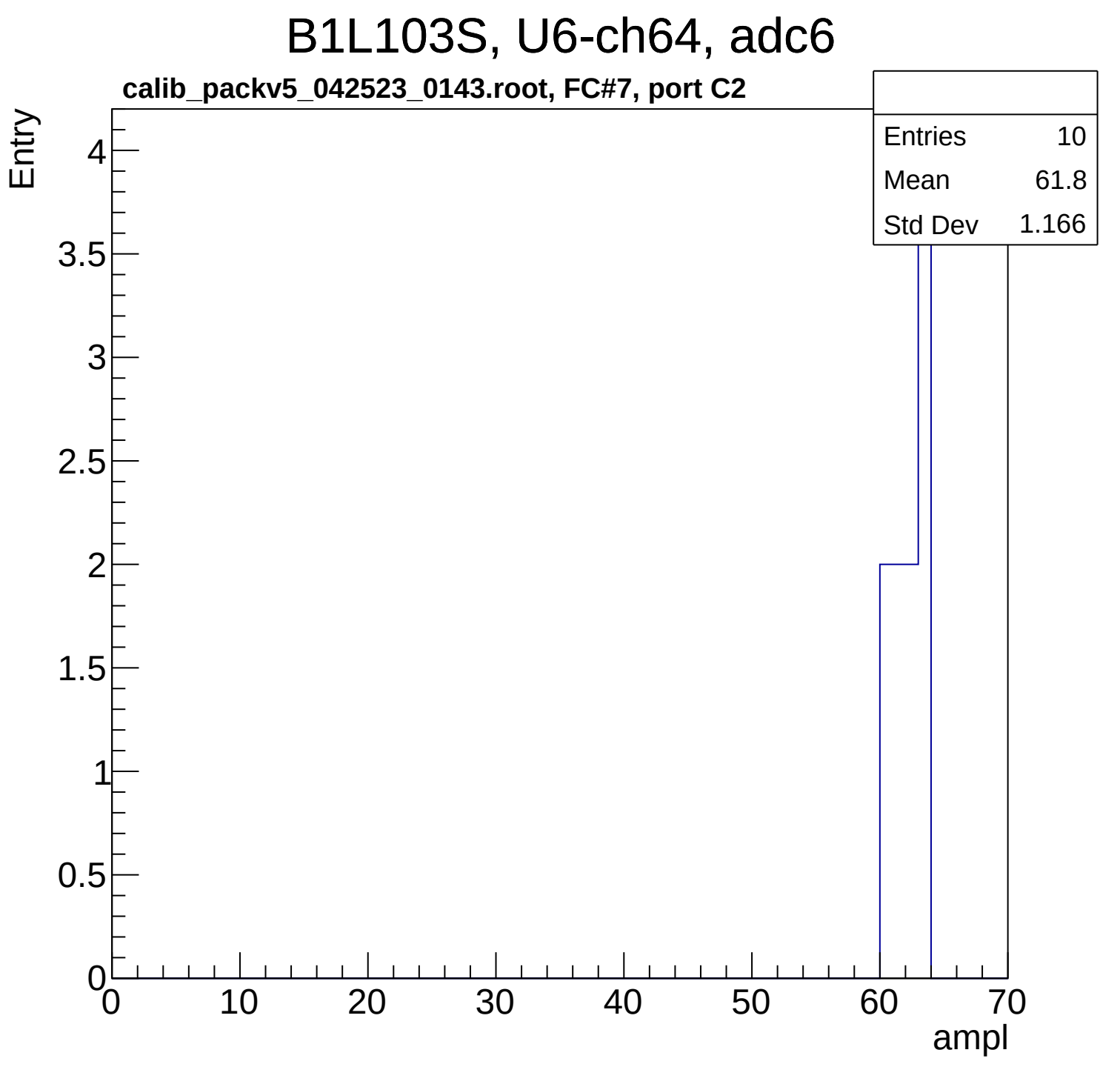
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch64, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.8
Std Dev 1.166
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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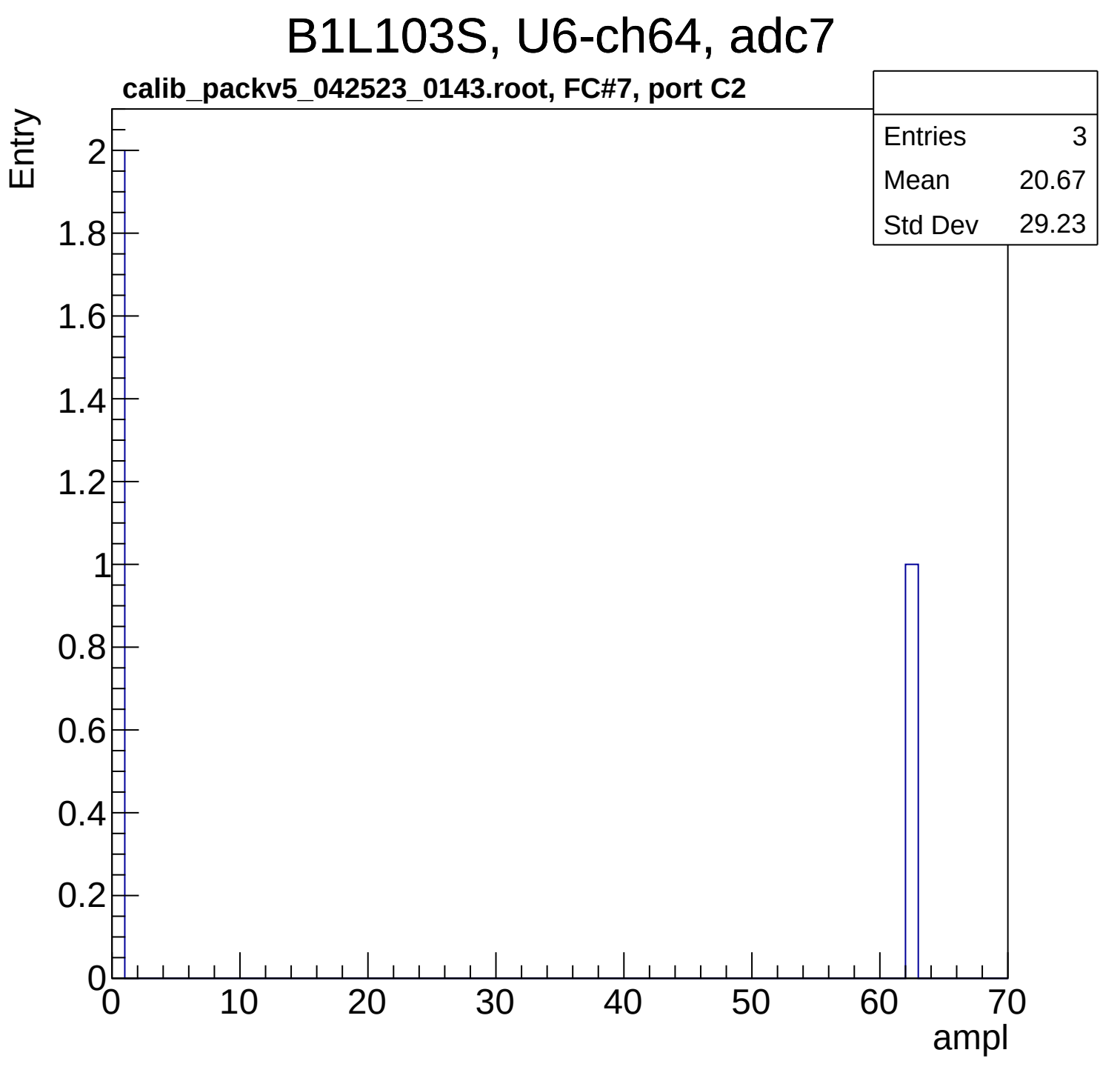
B1L103S, U6-ch64, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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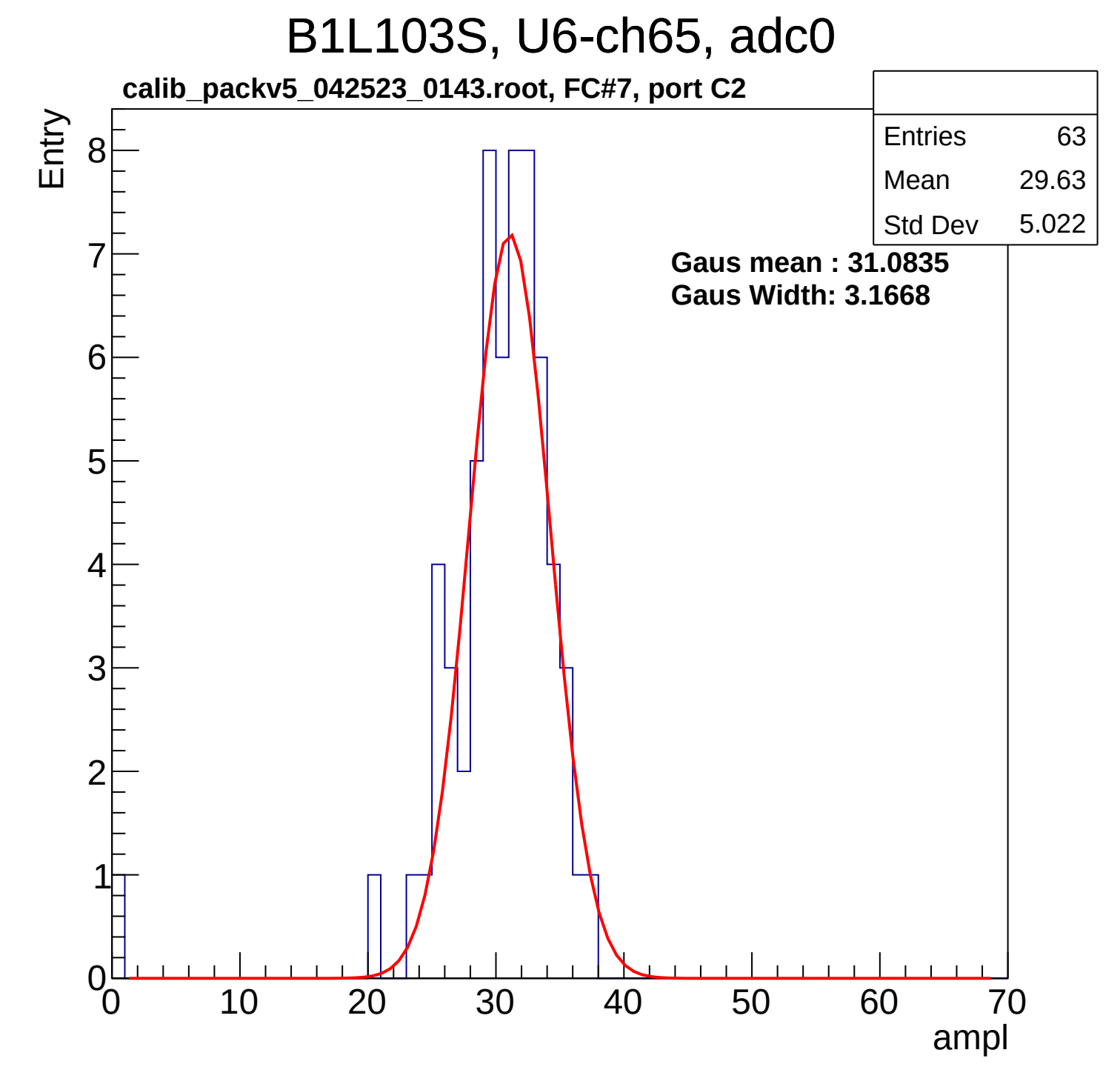
B1L103S, U6-ch65, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.63
Std Dev 5.022
Gaus mean : 31.0835
Gaus Width: 3.1668
Entry
ampl
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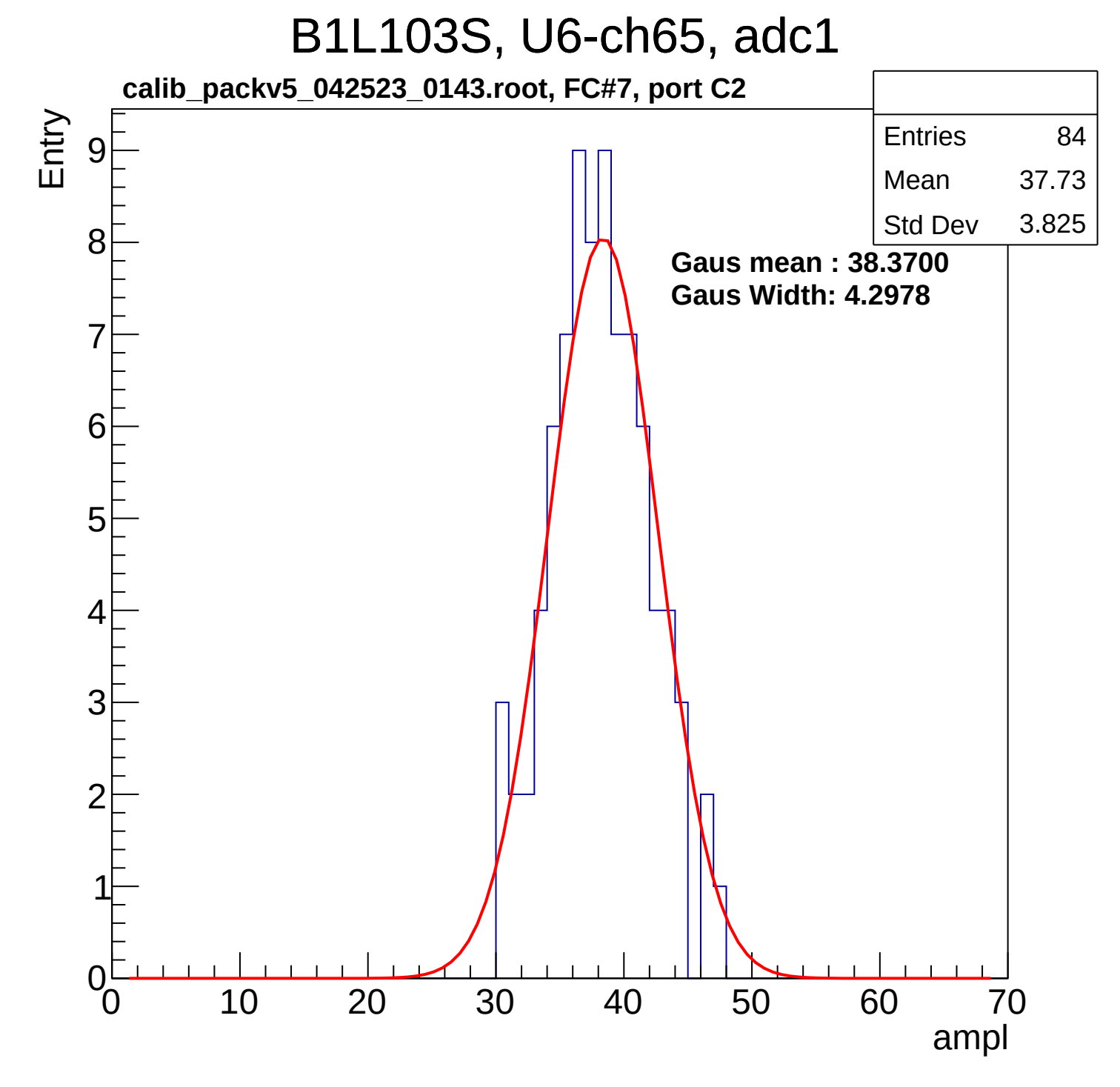
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch65, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 37.73
Std Dev 3.825
Gaus mean : 38.3700
Gaus Width: 4.2978
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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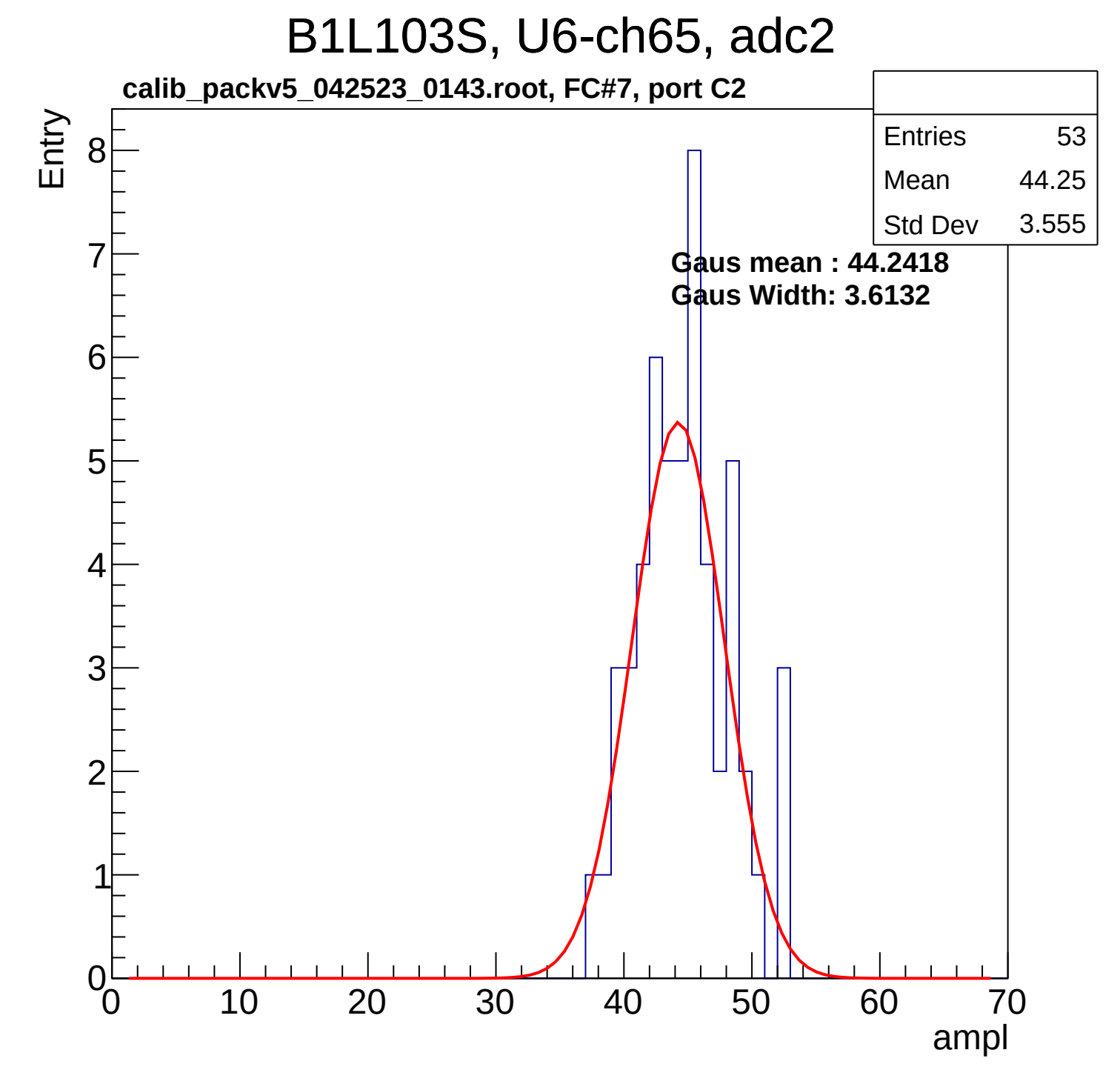
B1L103S, U6-ch65, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 44.25
Std Dev 3.555
Gaus mean : 44.2418
Gaus Width: 3.6132
Entry
ampl
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1
2
3
4
5
6
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0
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60
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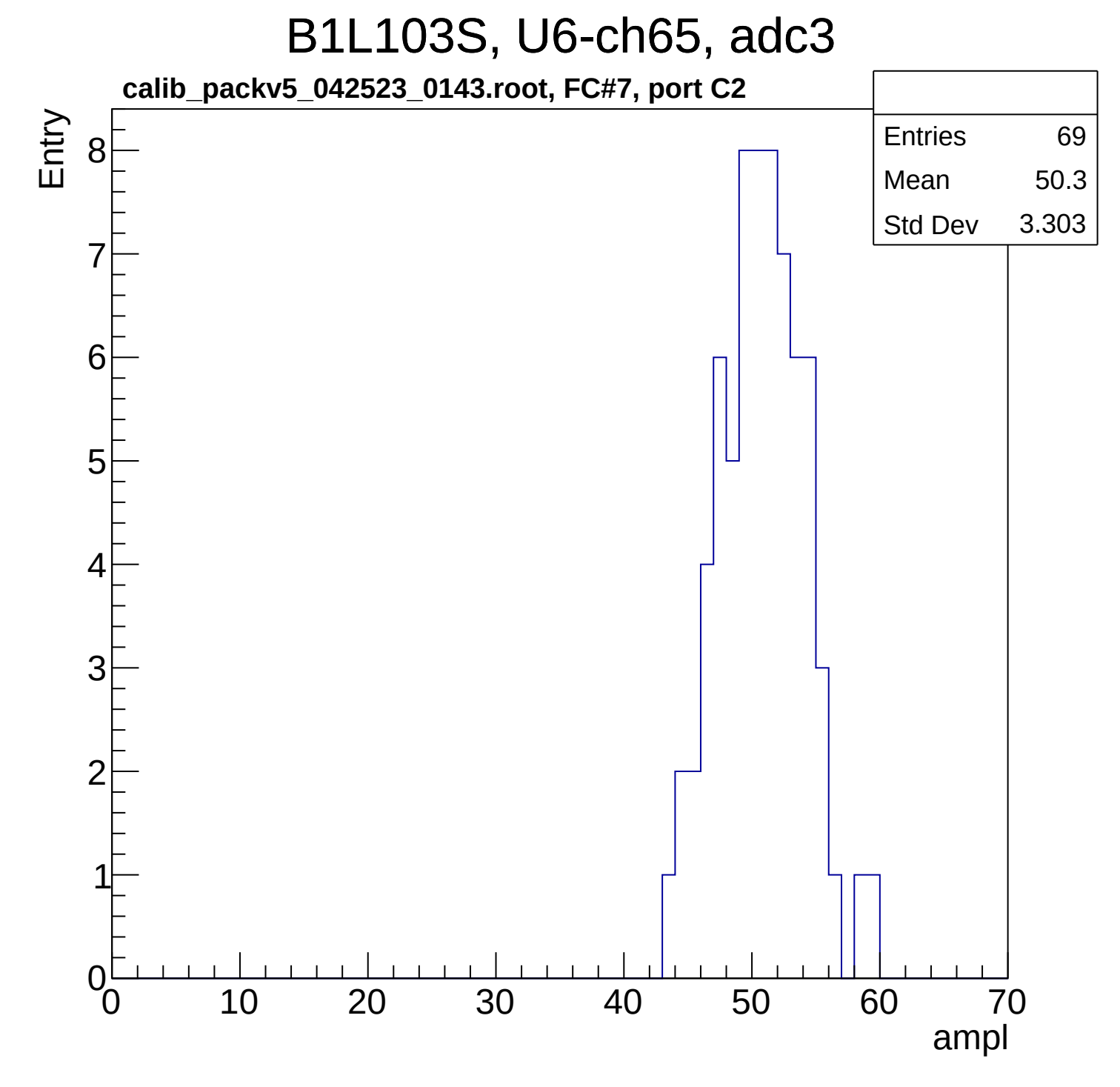
B1L103S, U6-ch65, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.3
Std Dev 3.303
Entry
ampl
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2
3
4
5
6
7
8
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10
20
30
40
50
60
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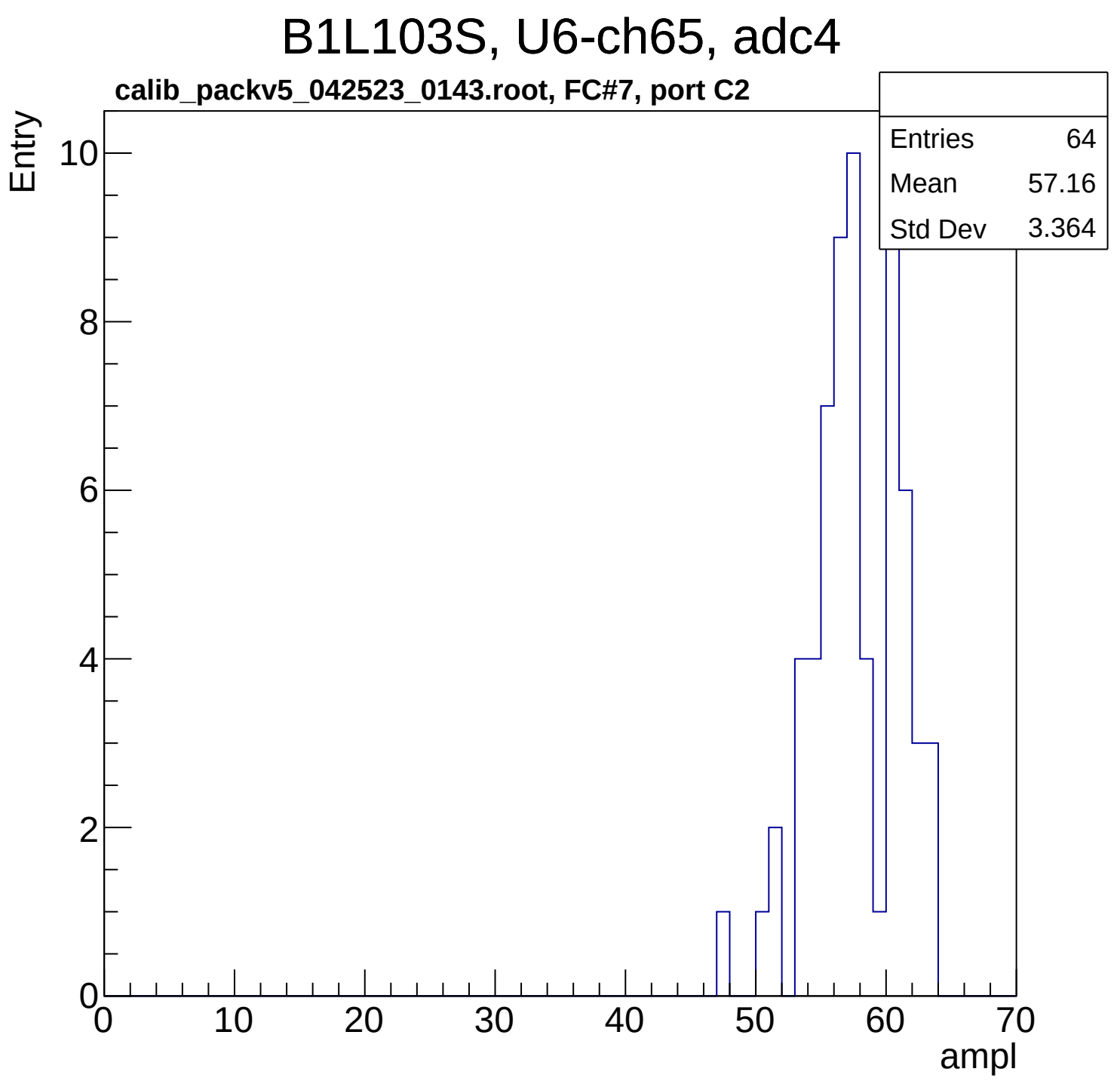
B1L103S, U6-ch65, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 57.16
Std Dev 3.364
Entry
ampl
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2
4
6
8
10
0
10
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40
50
60
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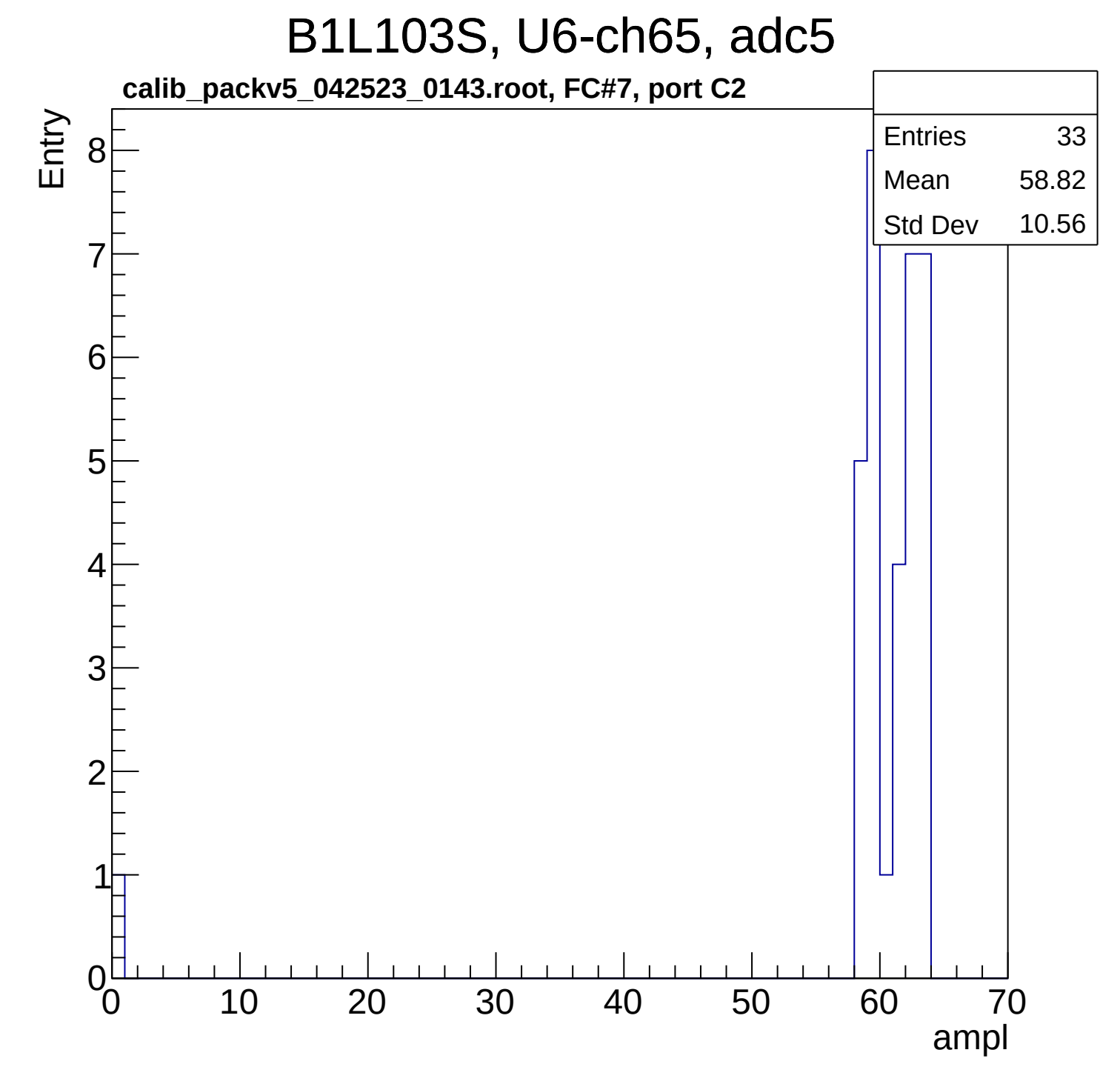
B1L103S, U6-ch65, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.82
Std Dev 10.56
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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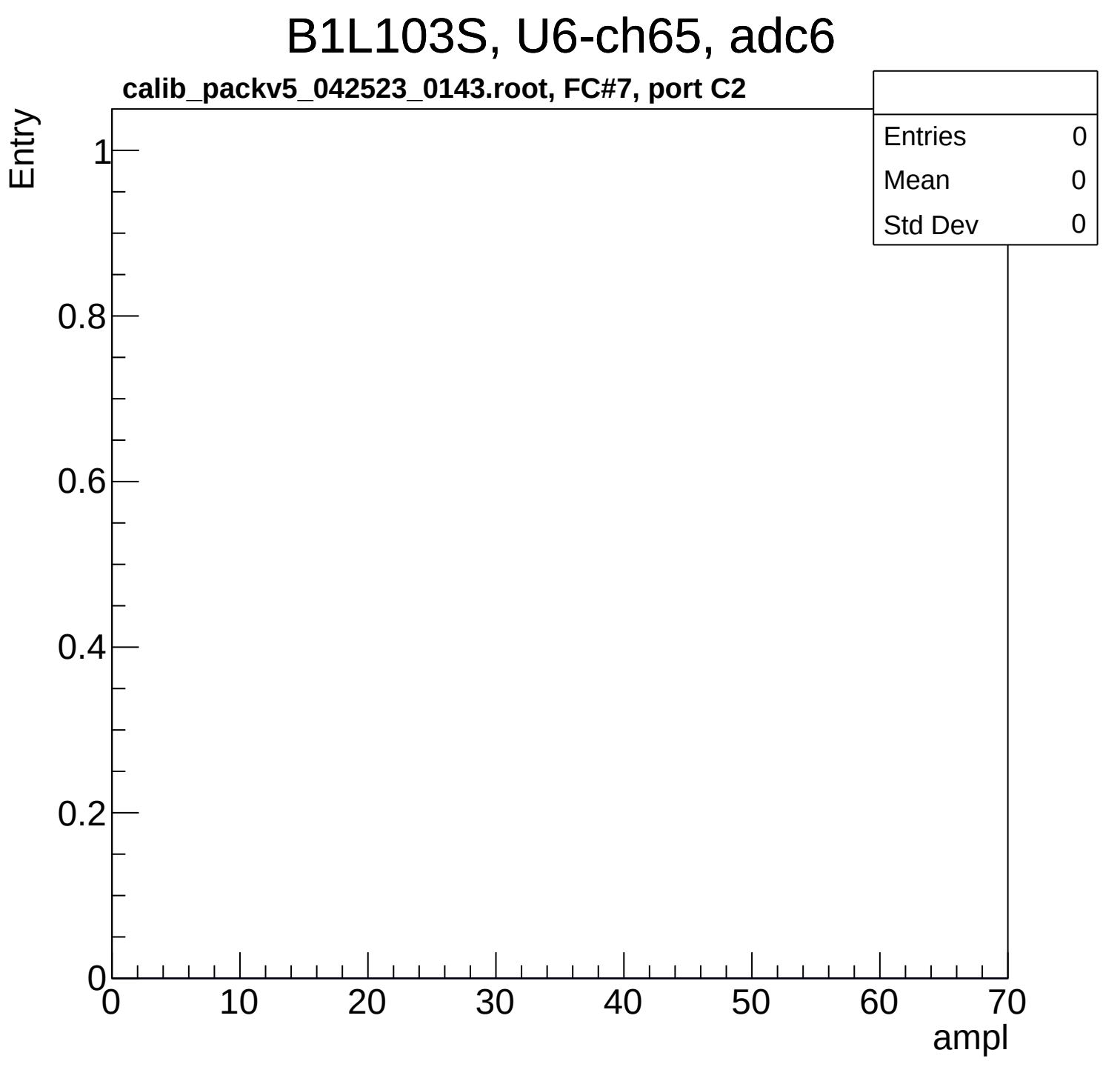
B1L103S, U6-ch65, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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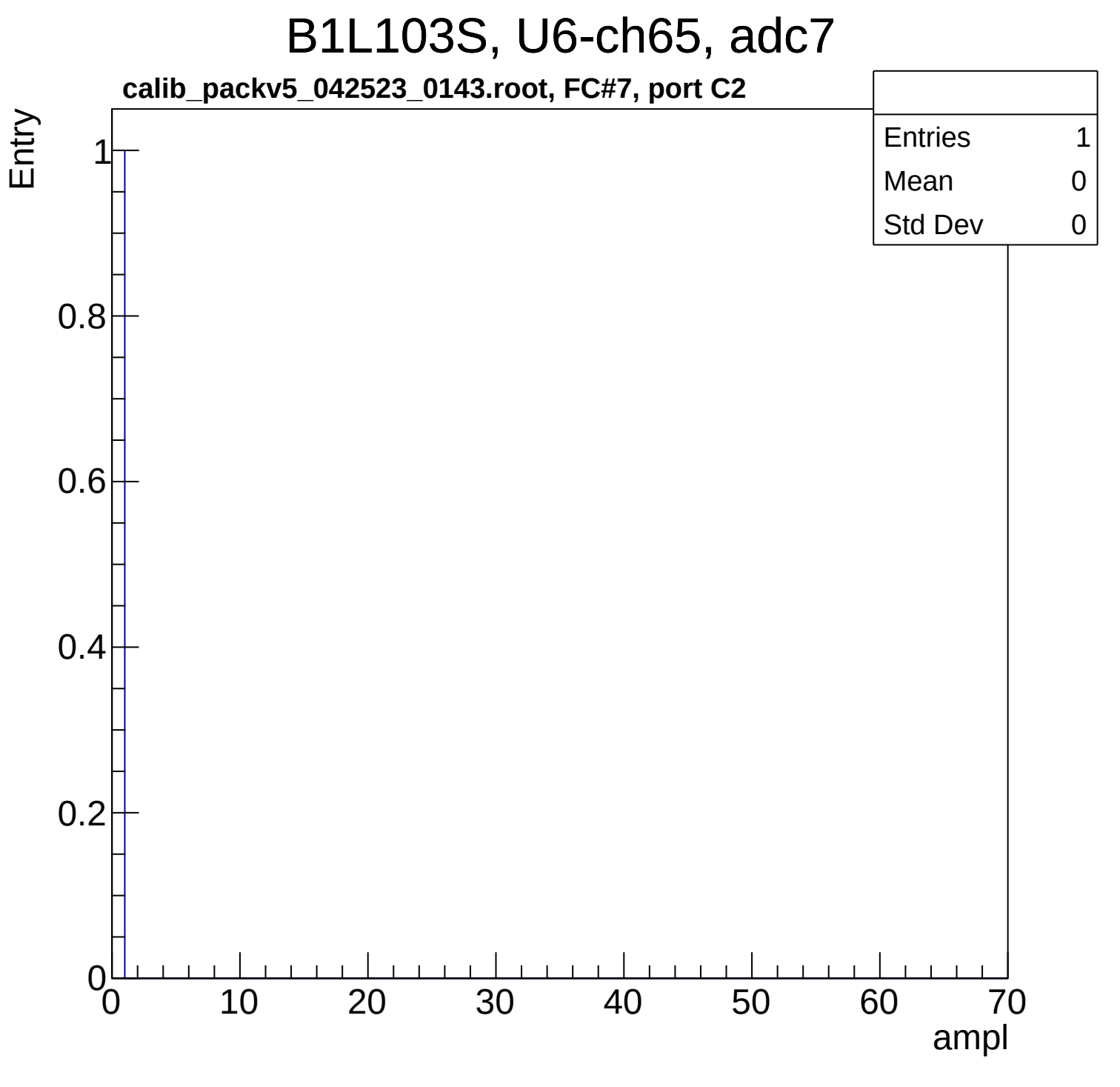
50
60
70
ampl


B1L103S, U6-ch65, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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30
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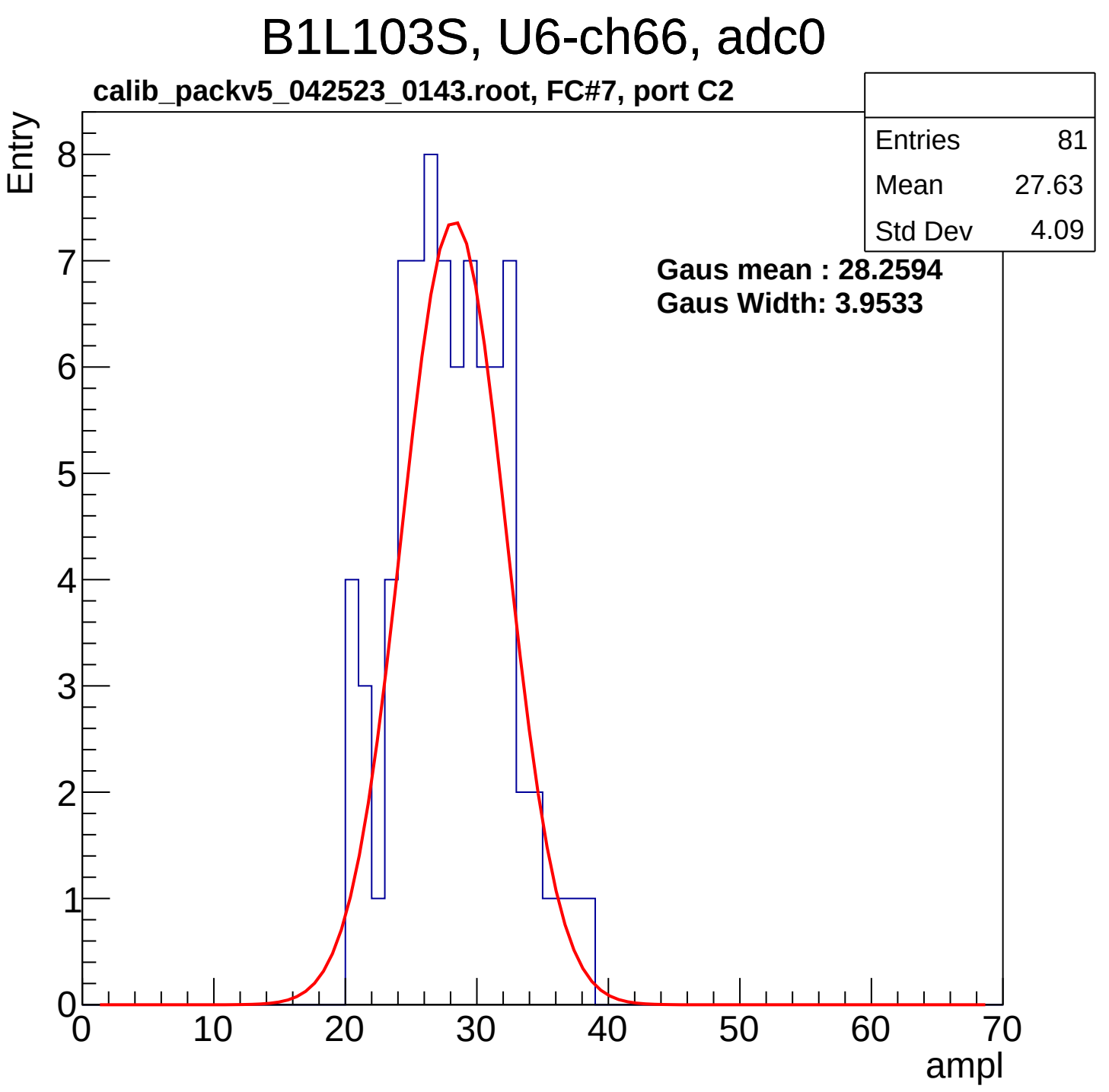
50
60
70
ampl


B1L103S, U6-ch66, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 27.63
Std Dev 4.09
Gaus mean : 28.2594
Gaus Width: 3.9533
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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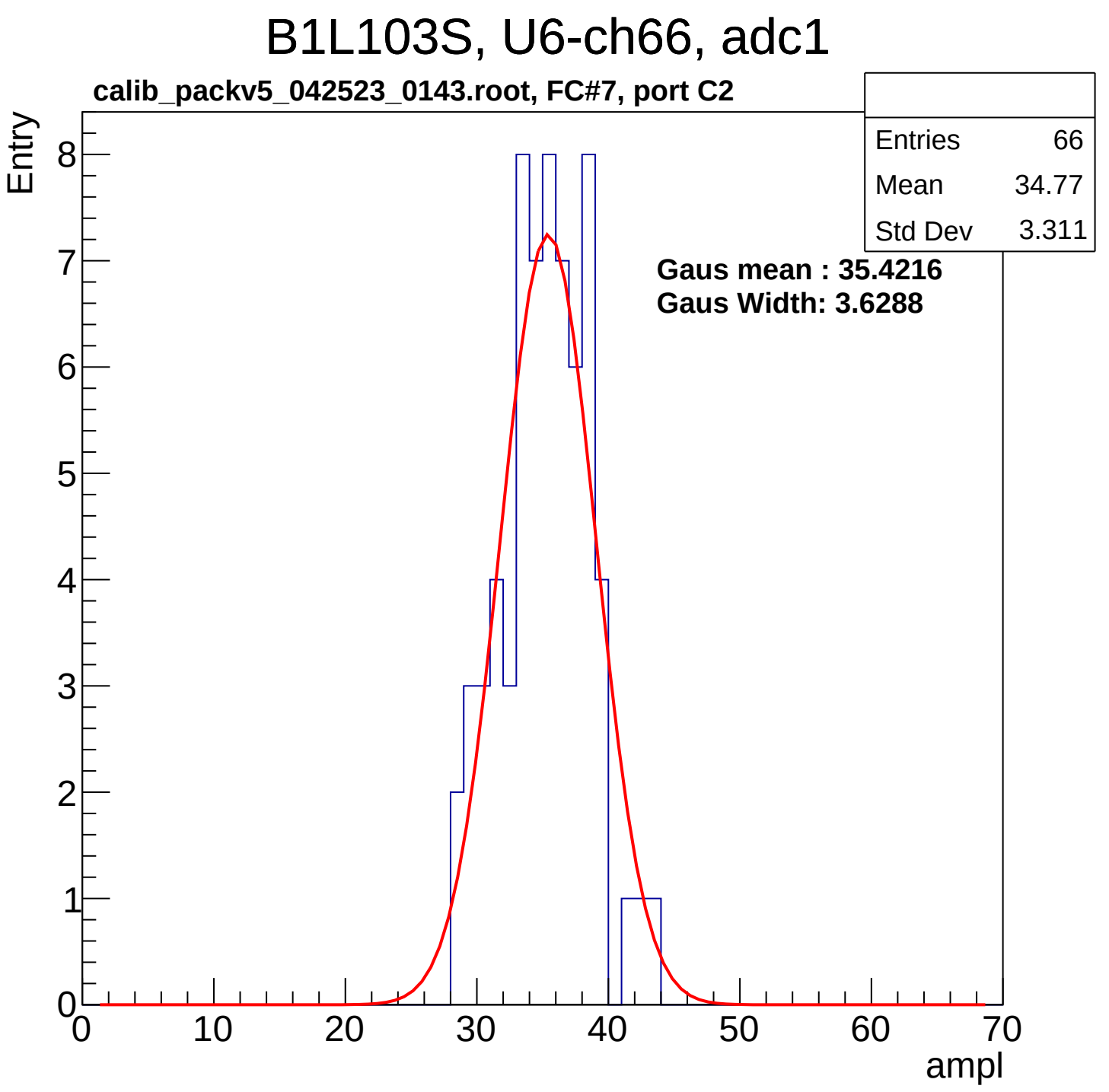
B1L103S, U6-ch66, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 34.77
Std Dev 3.311
Gaus mean : 35.4216
Gaus Width: 3.6288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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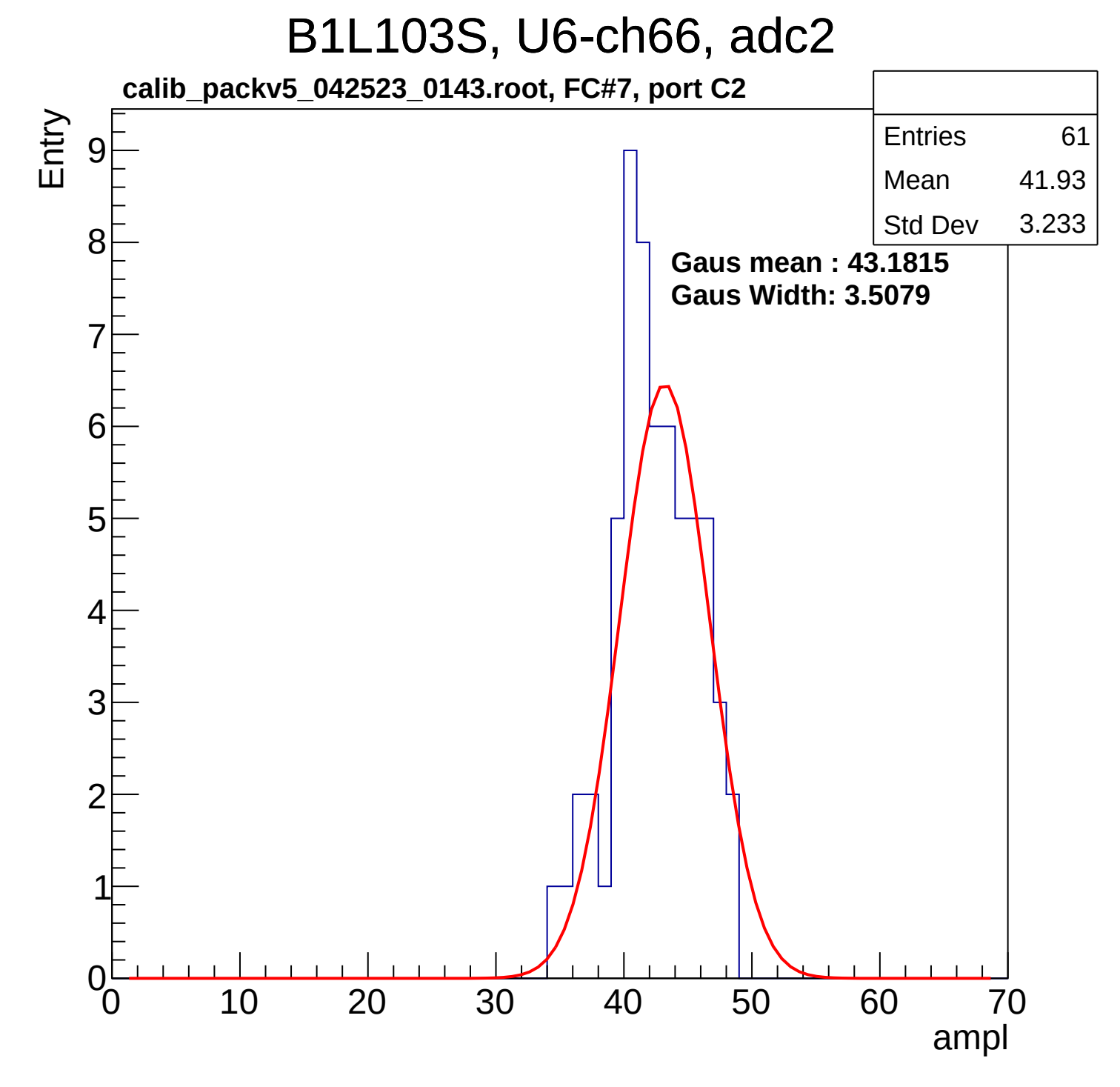
B1L103S, U6-ch66, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.93
Std Dev 3.233
Gaus mean : 43.1815
Gaus Width: 3.5079
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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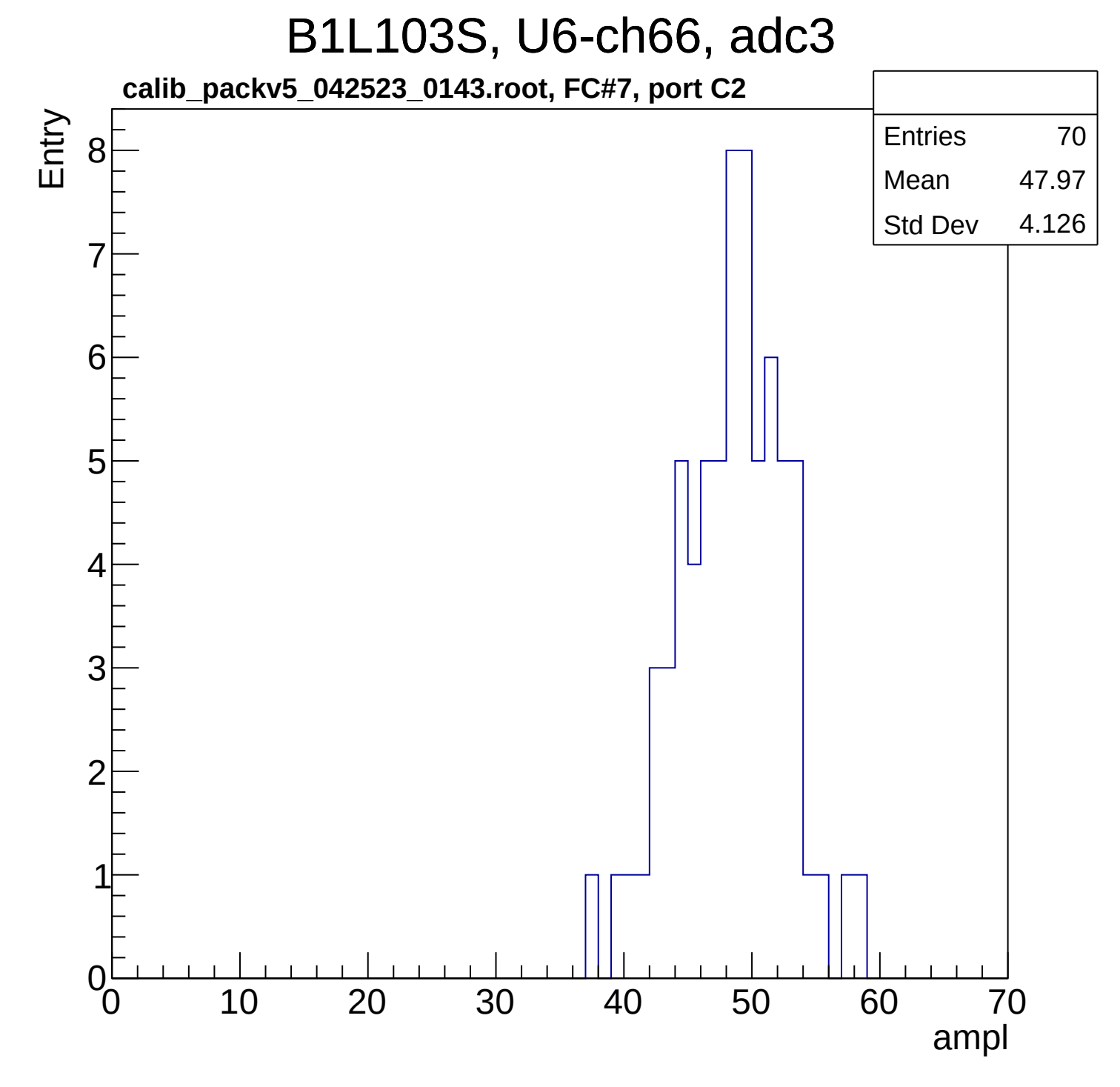
B1L103S, U6-ch66, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 47.97
Std Dev 4.126
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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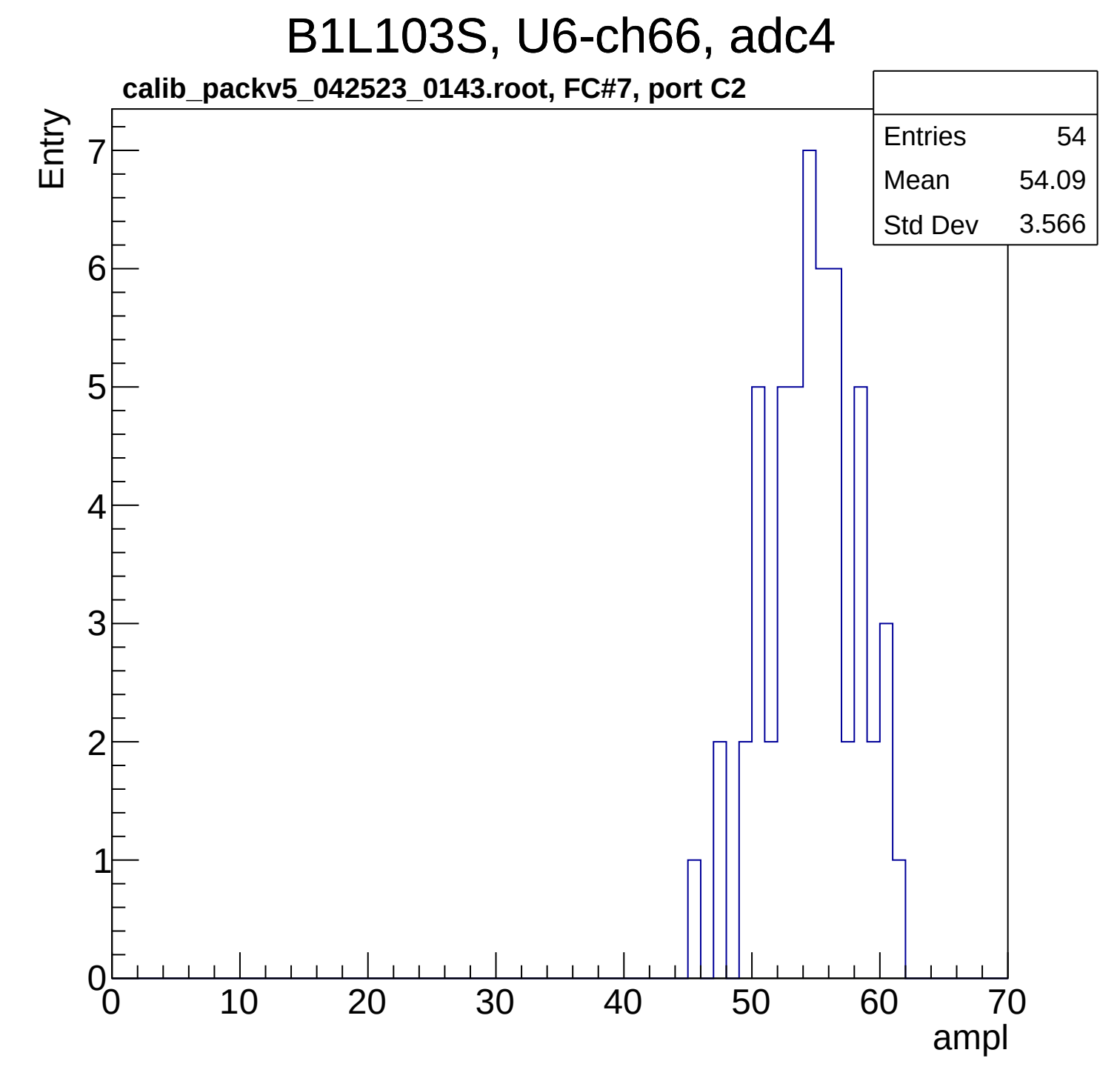
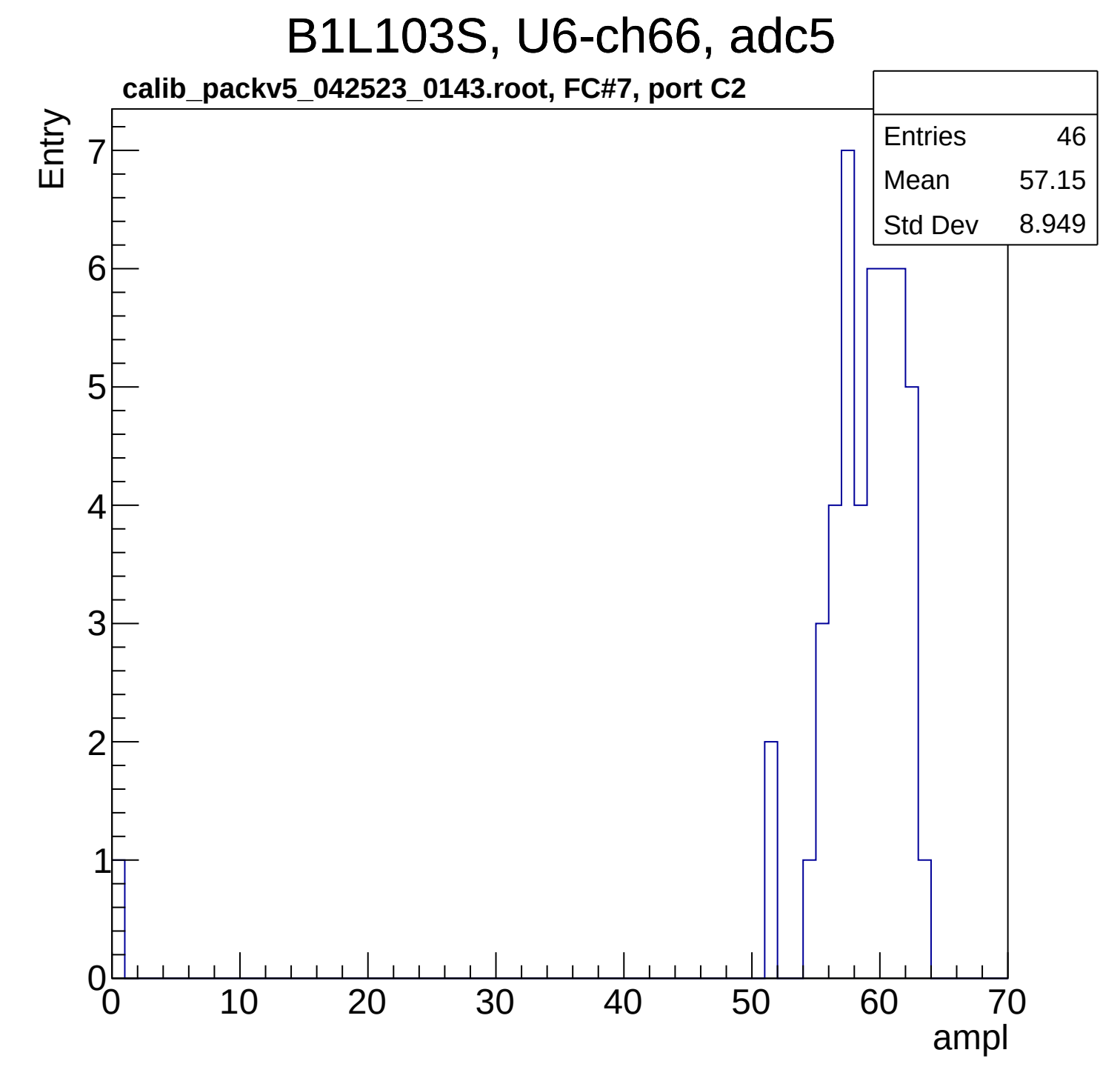

B1L103S, U6-ch66, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 54.09
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch66, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 57.15
Std Dev 8.949
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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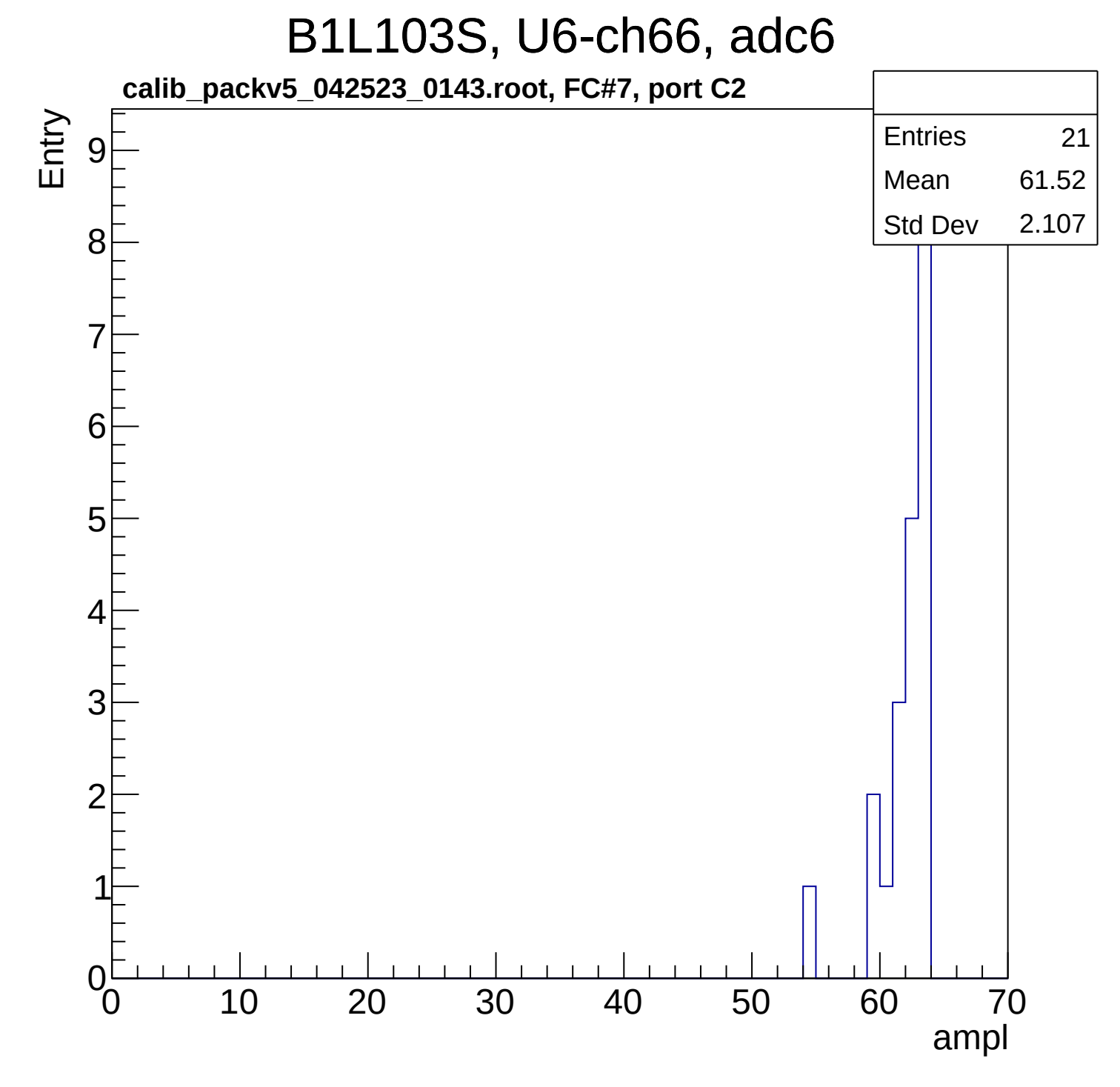
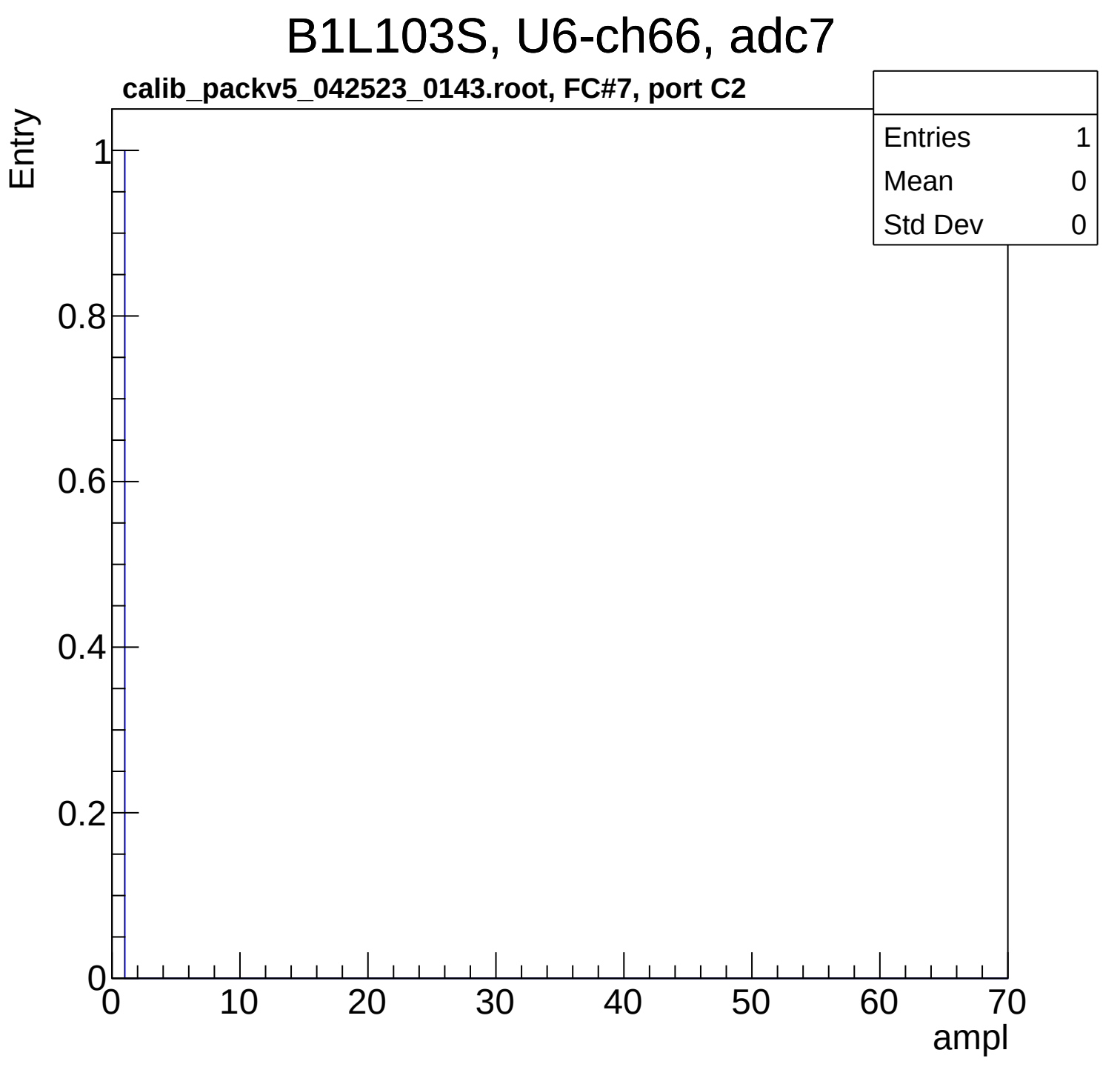
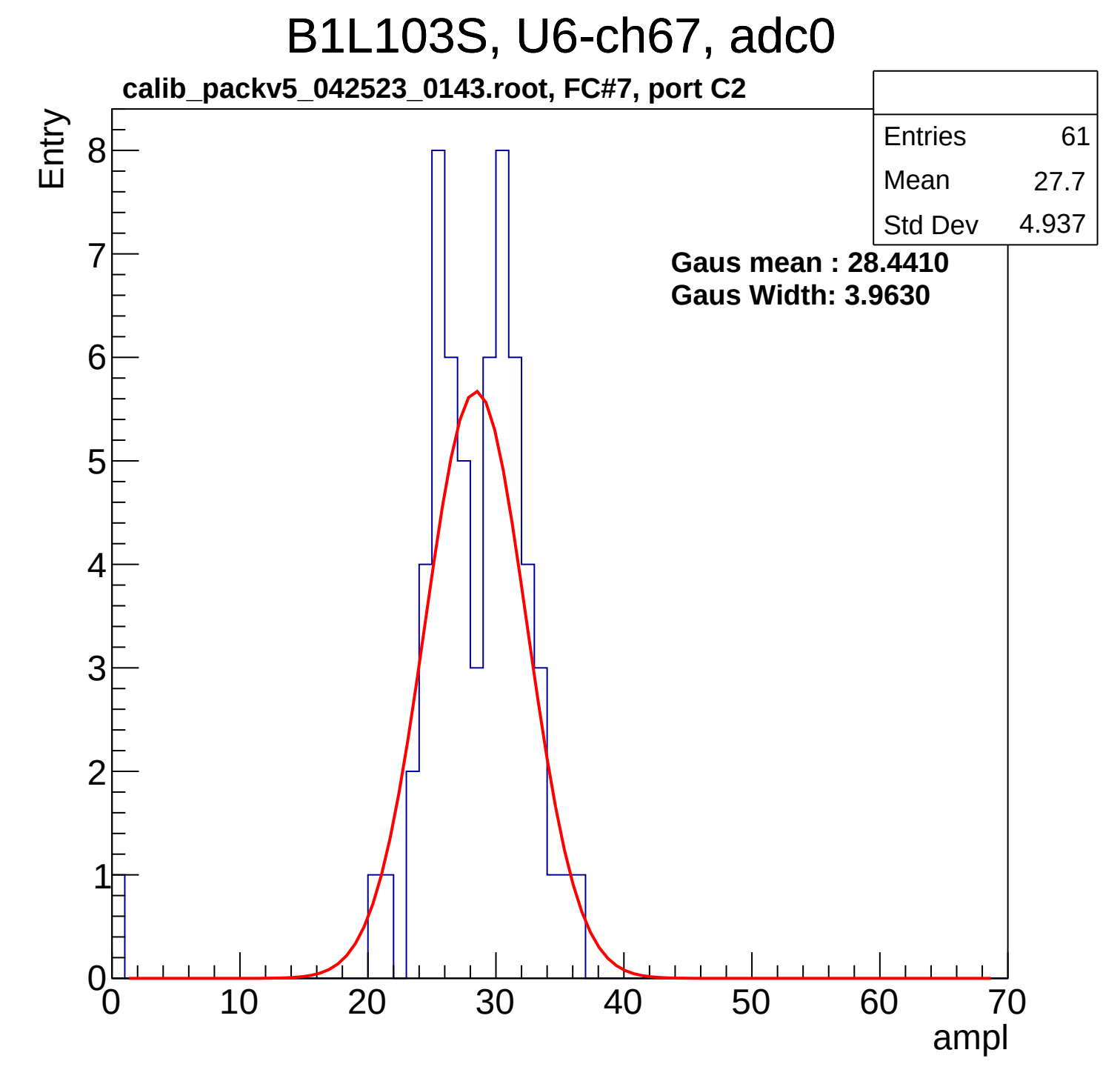
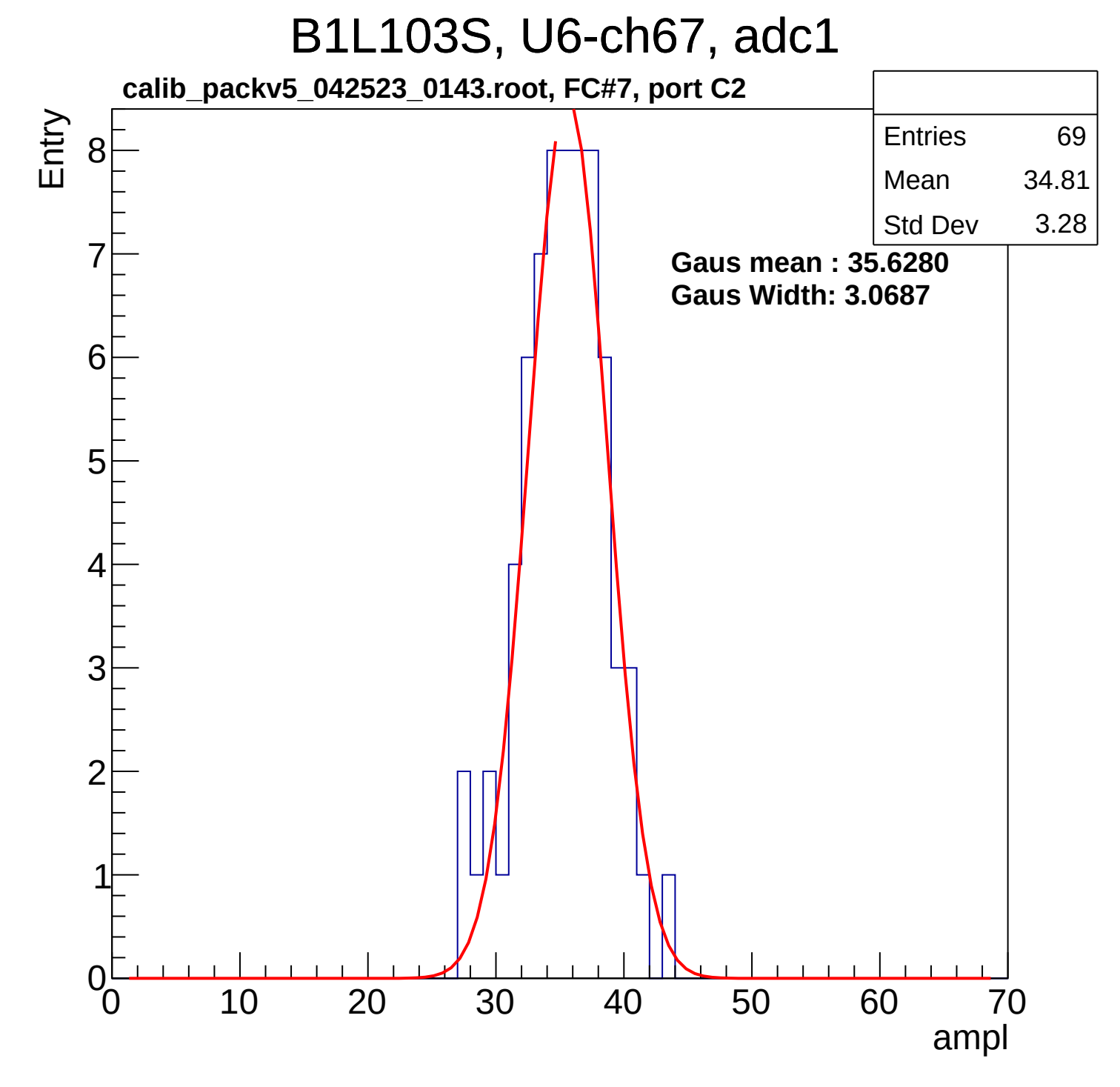

B1L103S, U6-ch66, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 61.52
Std Dev 2.107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch66, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch67, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 27.7
Std Dev 4.937
Gaus mean : 28.4410
Gaus Width: 3.9630
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch67, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 34.81
Std Dev 3.28
Gaus mean : 35.6280
Gaus Width: 3.0687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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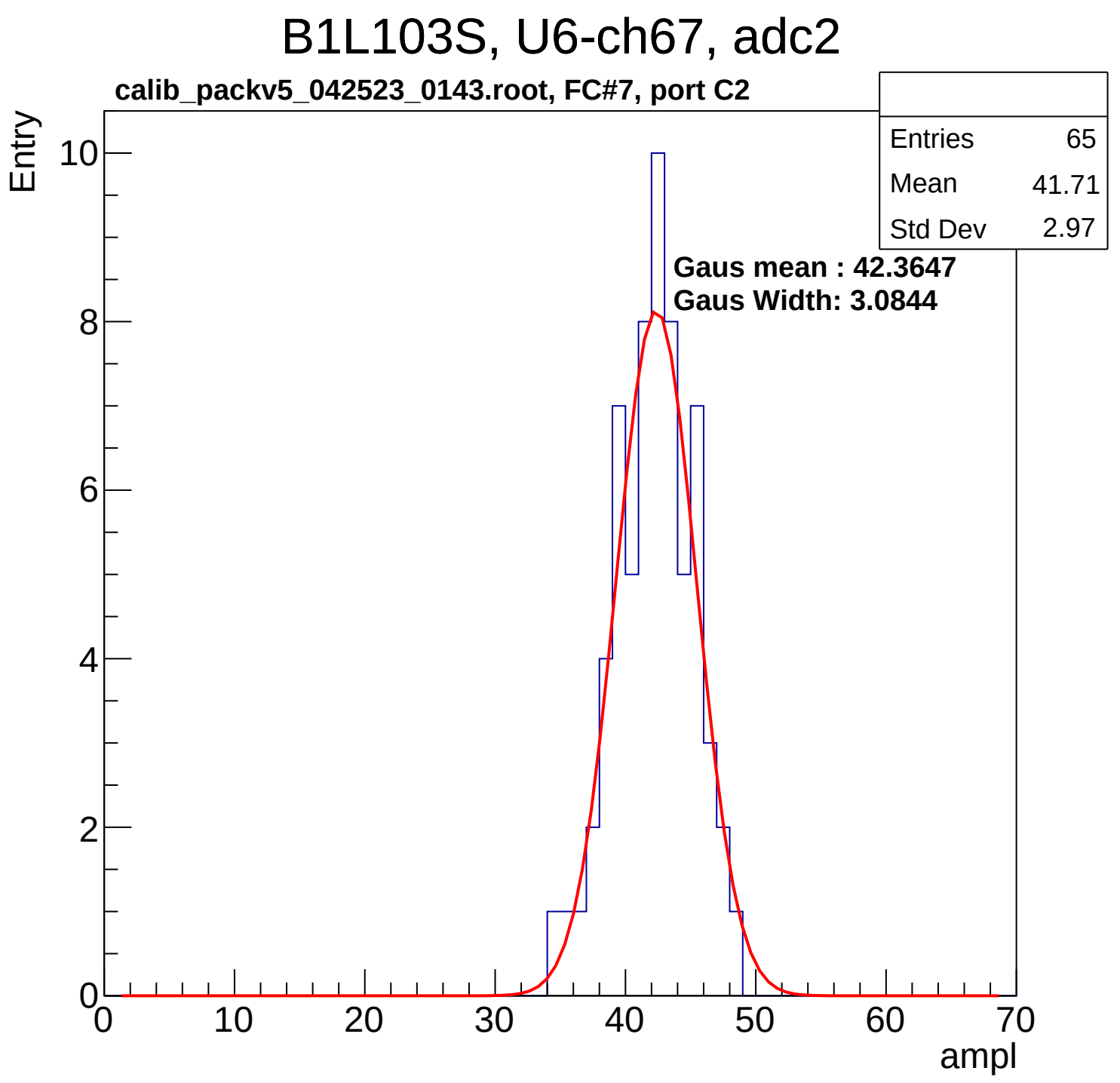
B1L103S, U6-ch67, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.71
Std Dev 2.97
Gaus mean : 42.3647
Gaus Width: 3.0844
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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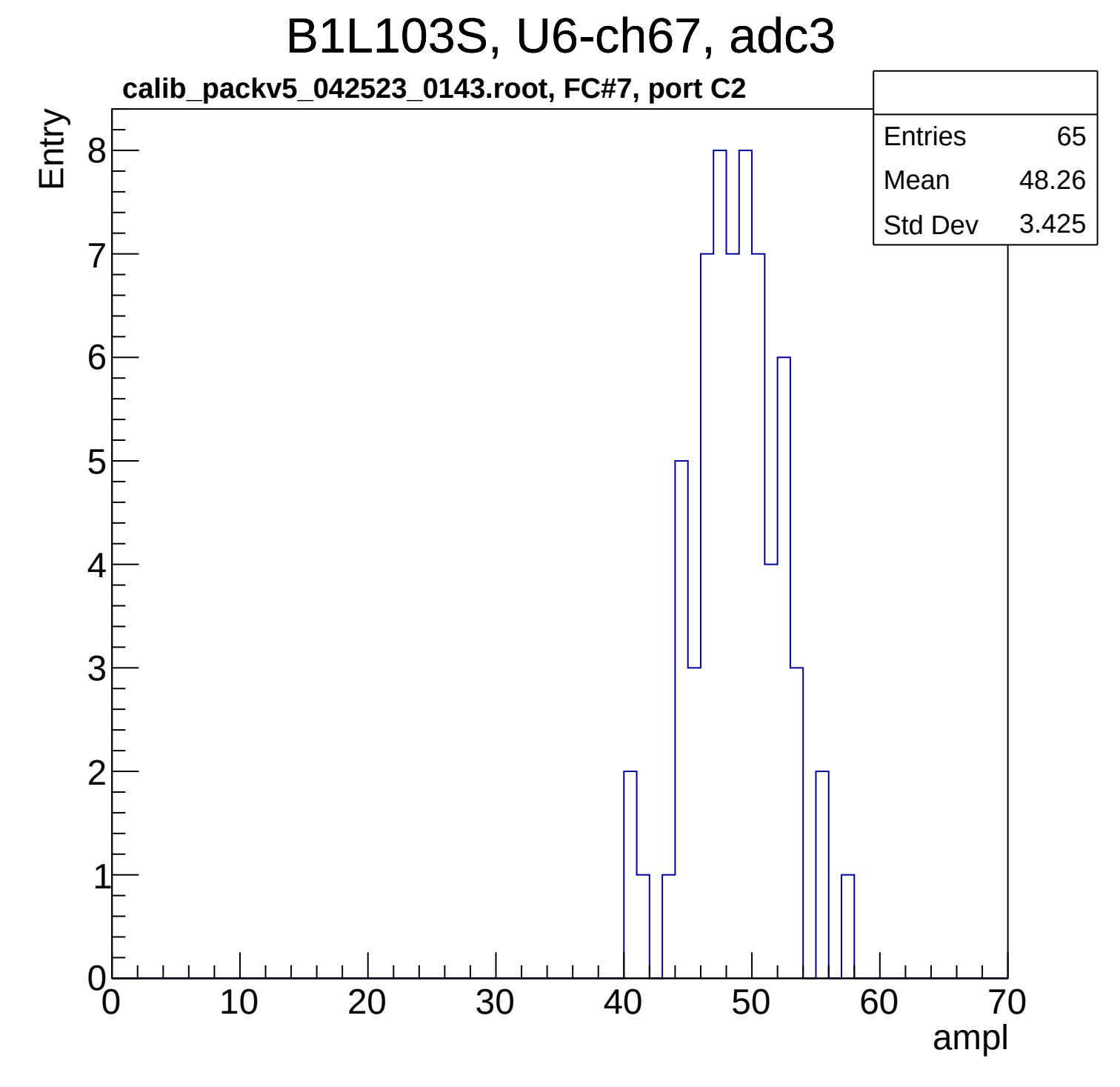
B1L103S, U6-ch67, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.26
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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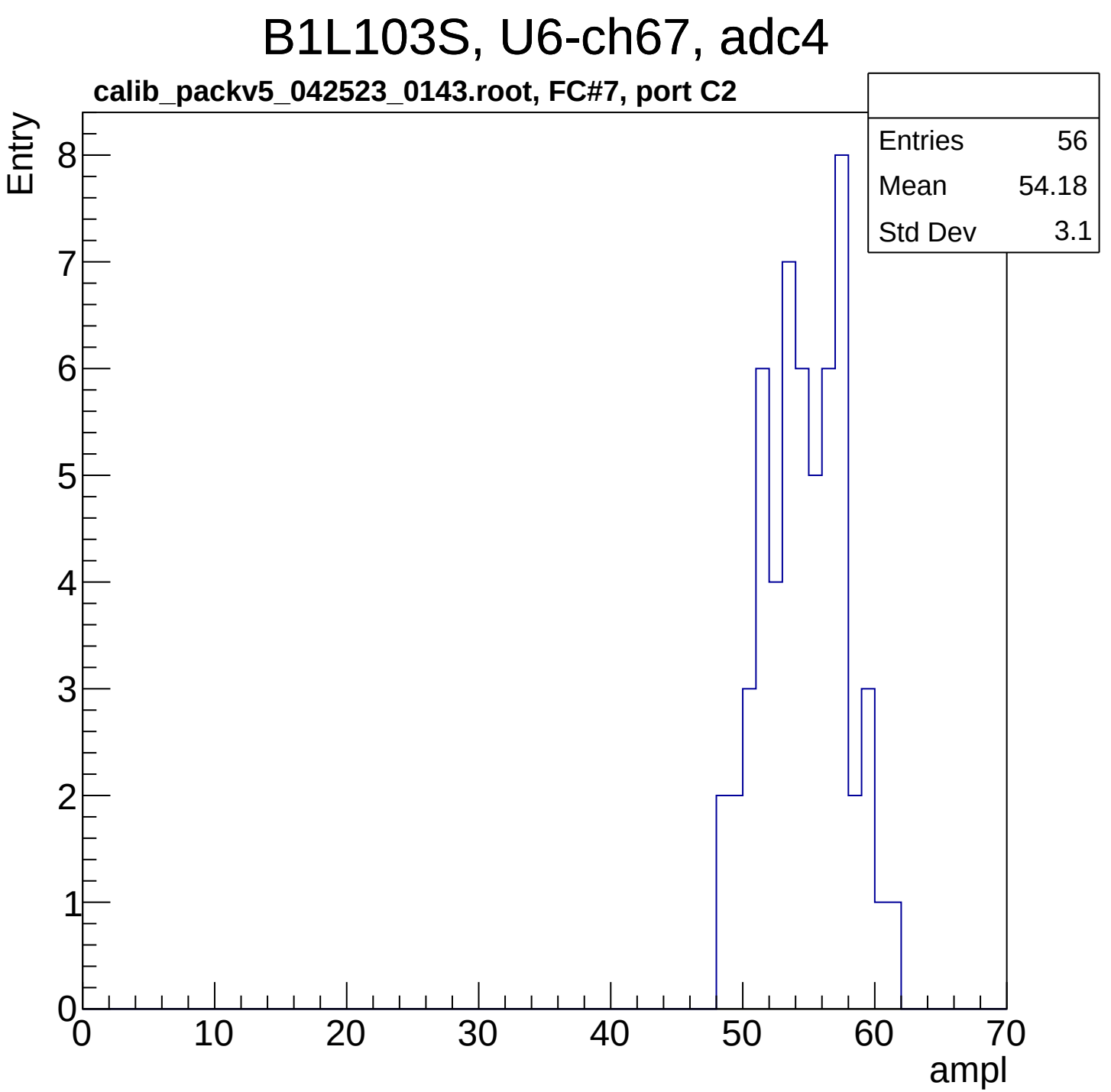
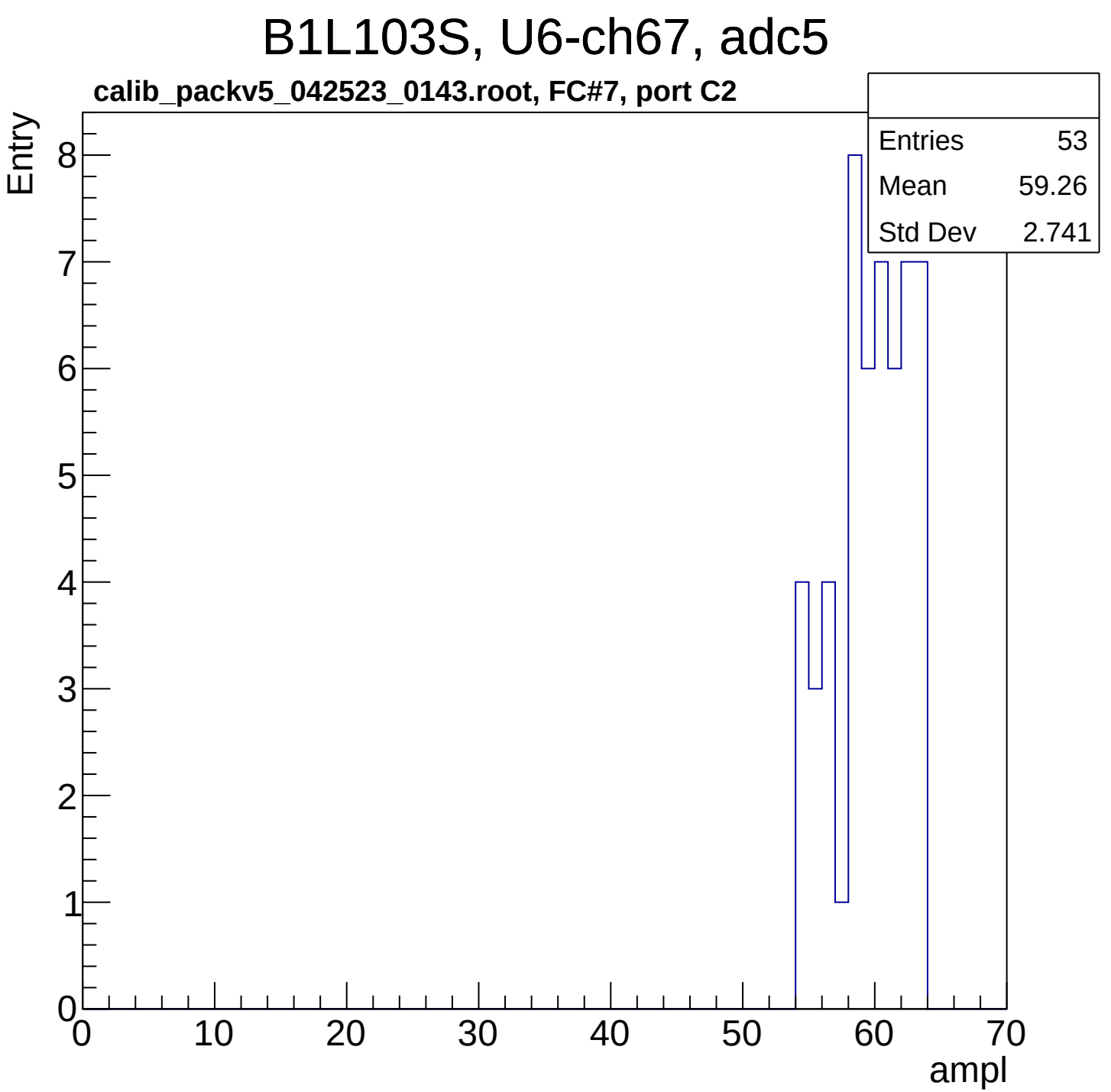

B1L103S, U6-ch67, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 54.18
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch67, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 59.26
Std Dev 2.741
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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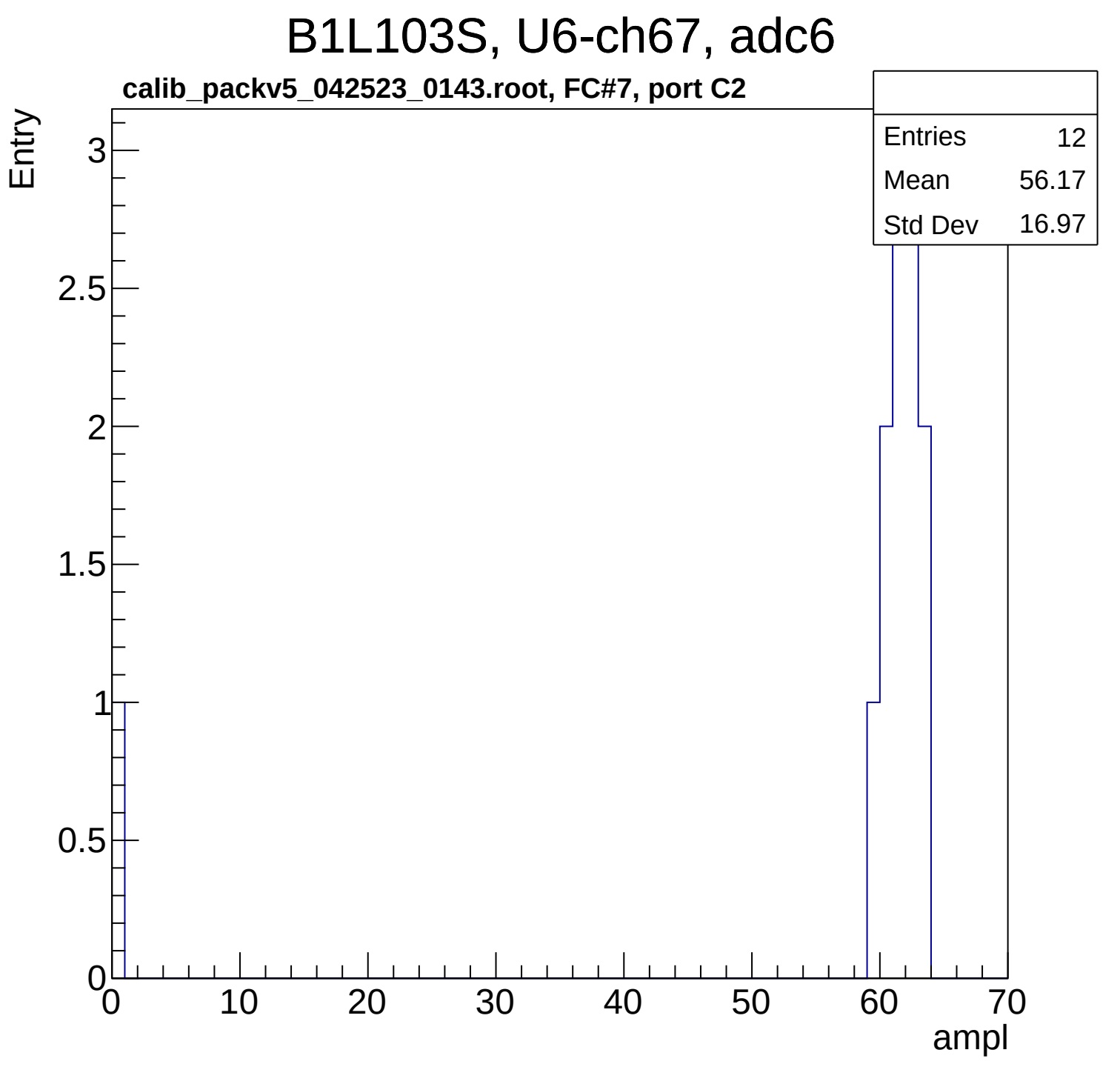
B1L103S, U6-ch67, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.17
Std Dev 16.97
Entry
ampl
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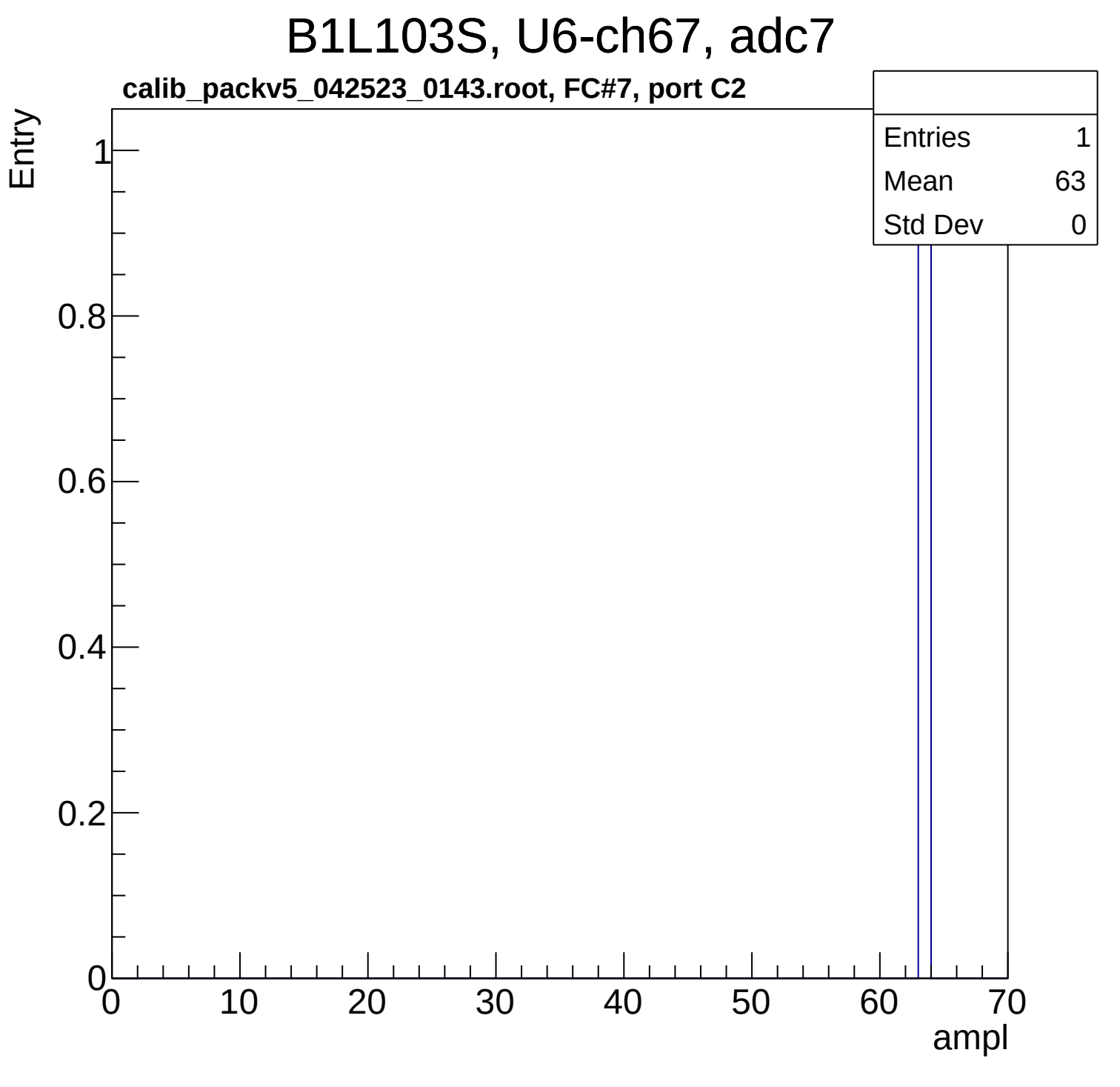
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch67, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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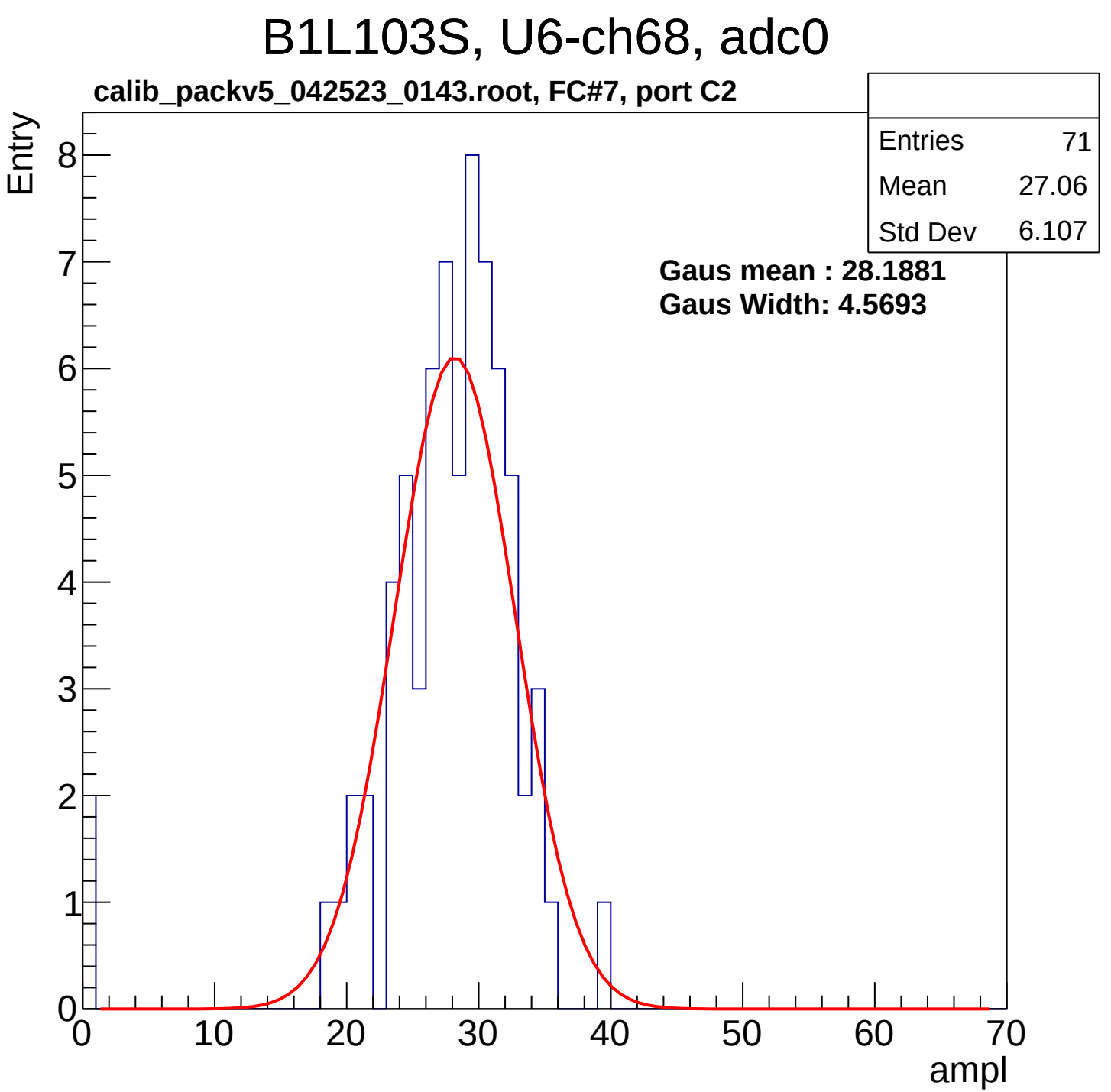
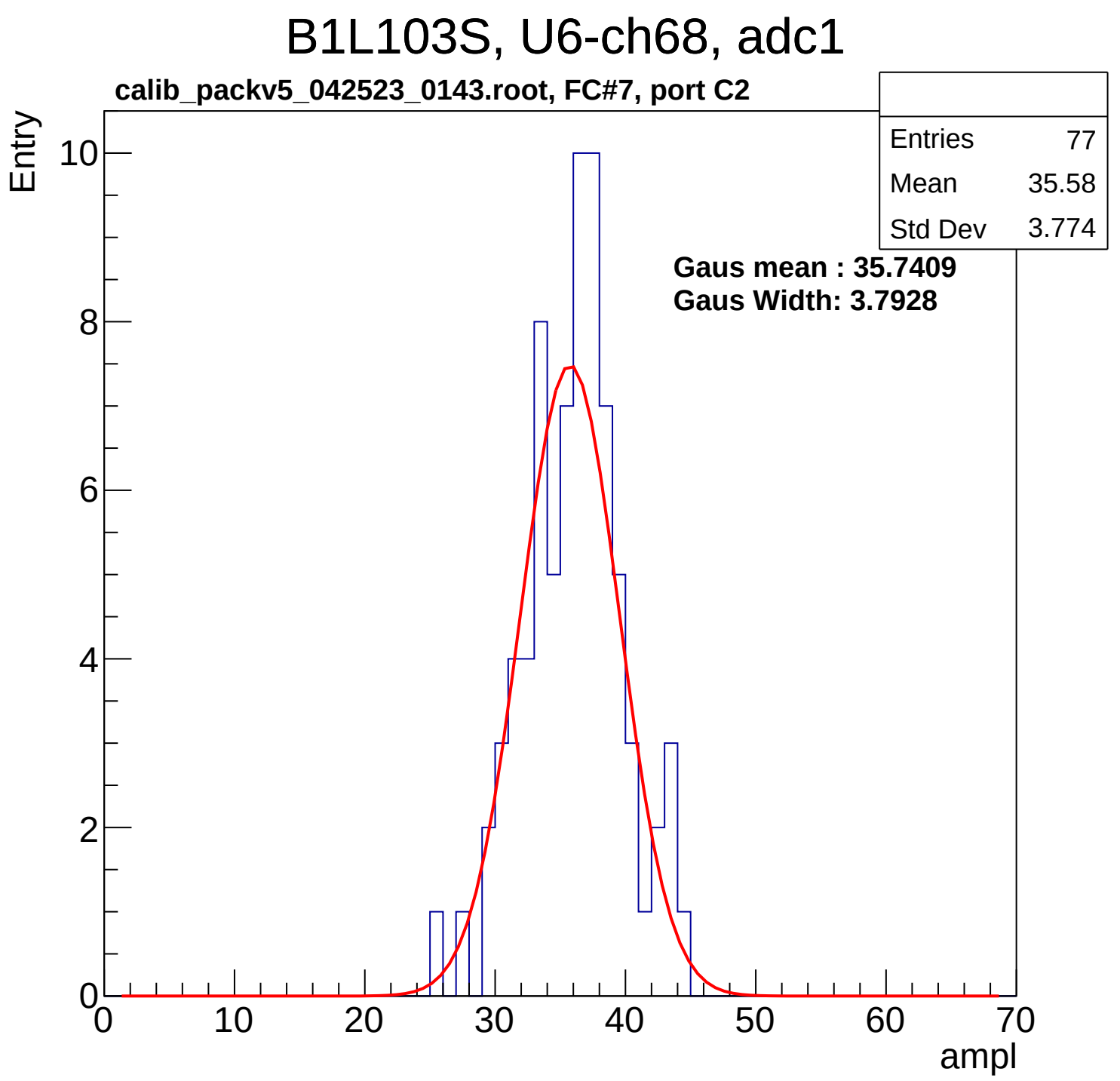
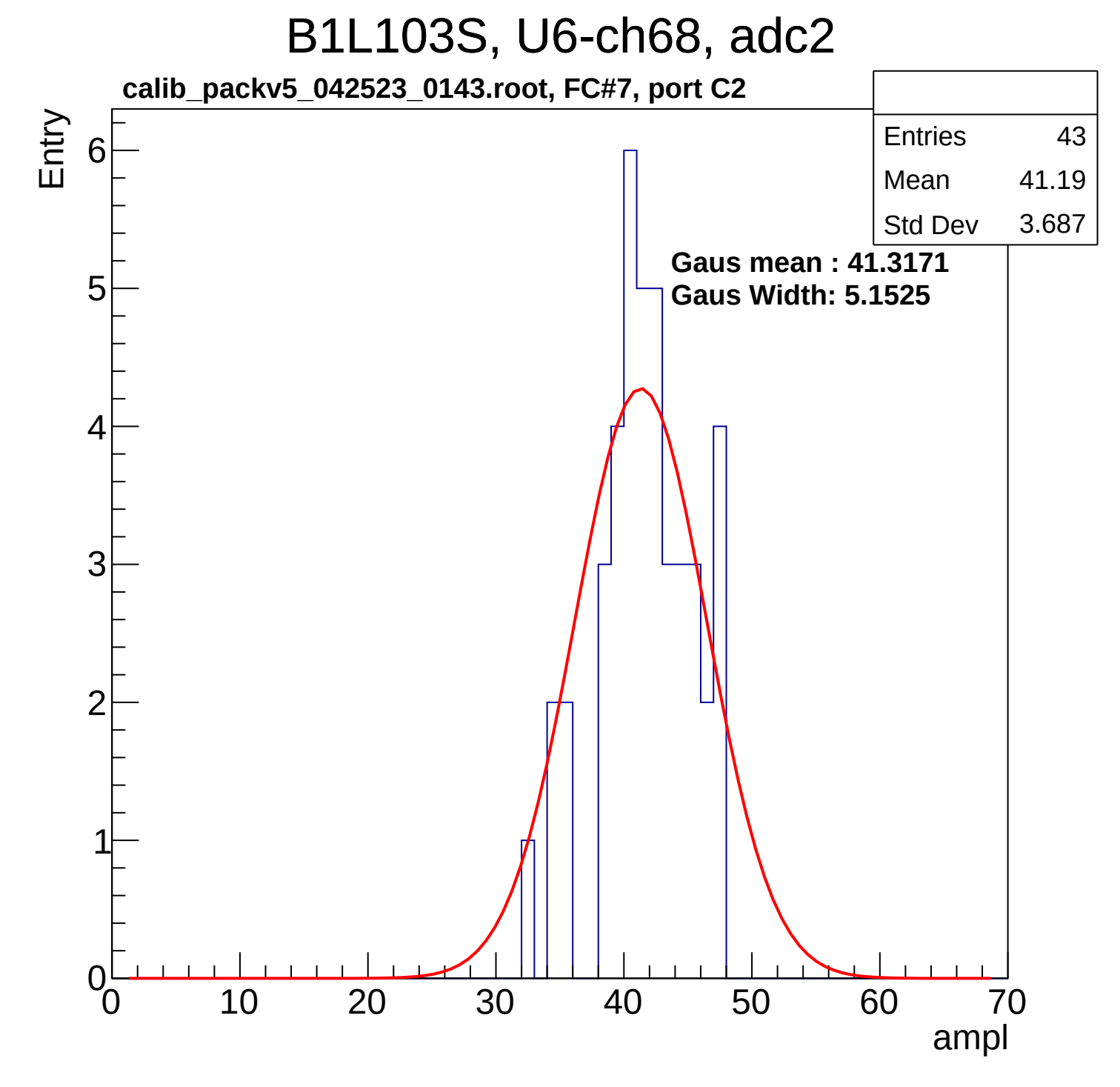
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch68, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 27.06
Std Dev 6.107
Gaus mean : 28.1881
Gaus Width: 4.5693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch68, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 35.58
Std Dev 3.774
Gaus mean : 35.7409
Gaus Width: 3.7928
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch68, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 41.19
Std Dev 3.687
Gaus mean : 41.3171
Gaus Width: 5.1525
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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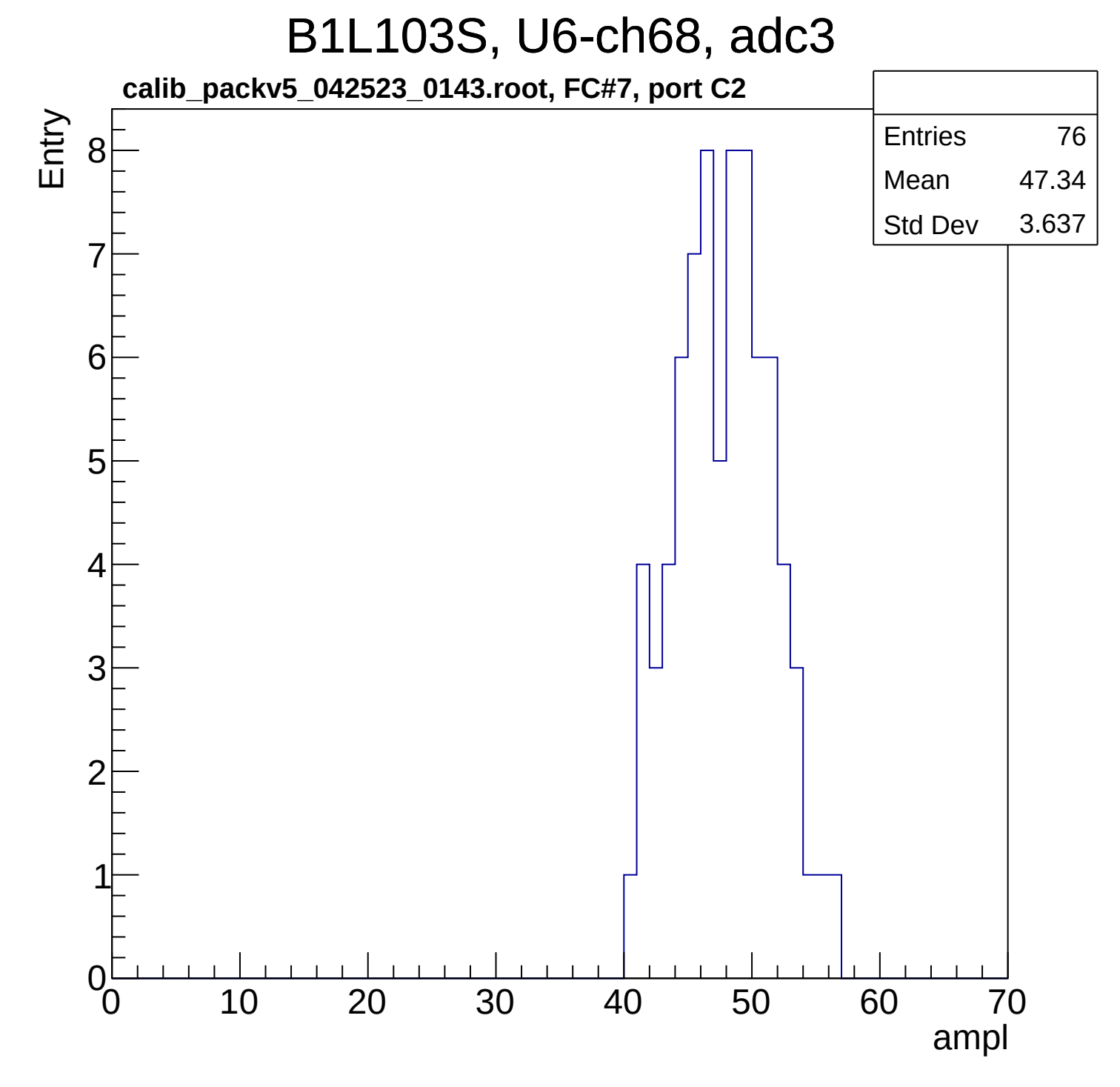
60
70


B1L103S, U6-ch68, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 47.34
Std Dev 3.637
Entry
ampl
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60
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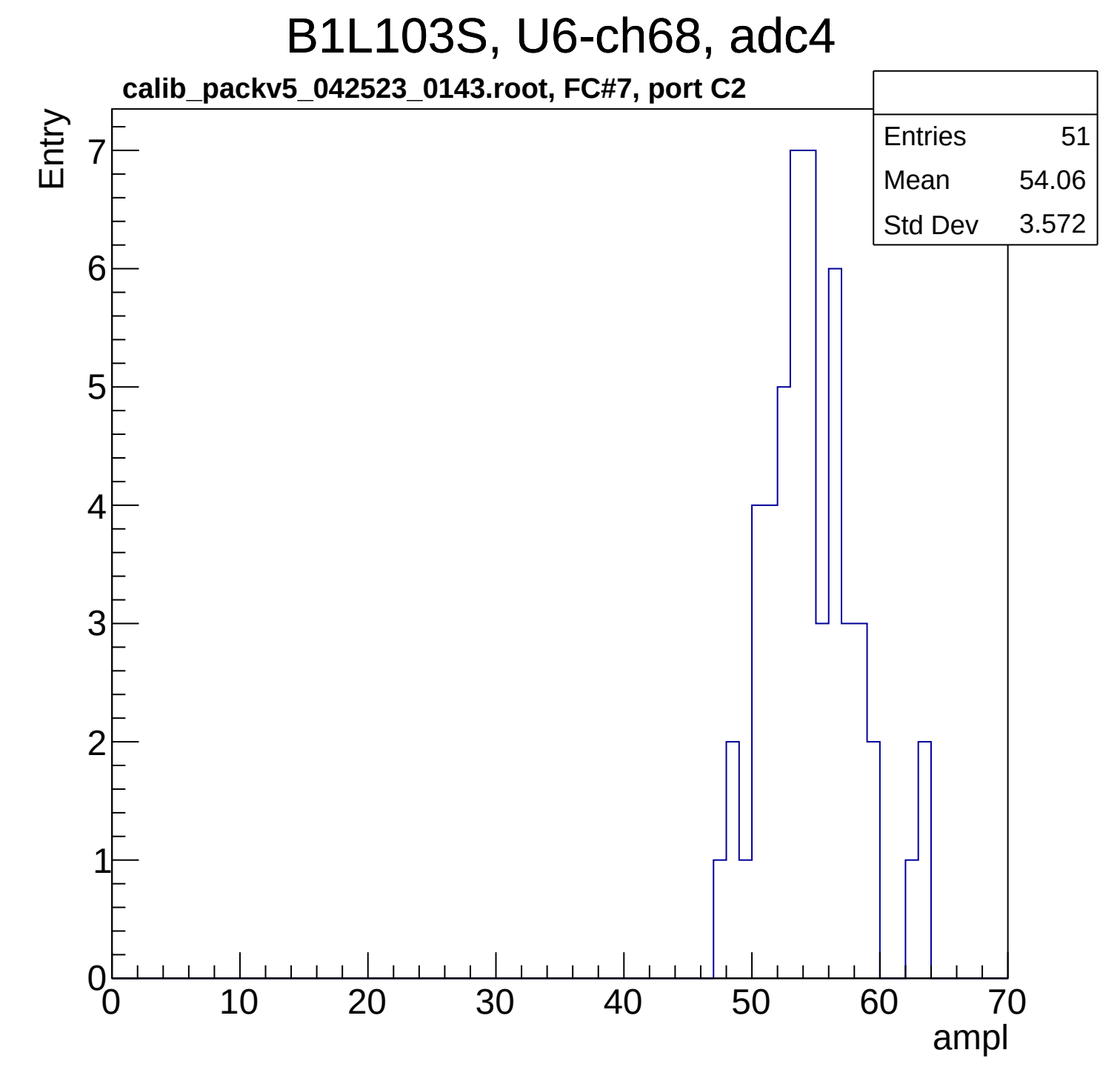
B1L103S, U6-ch68, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.06
Std Dev 3.572
Entry
ampl
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3
4
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7
0
10
20
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40
50
60
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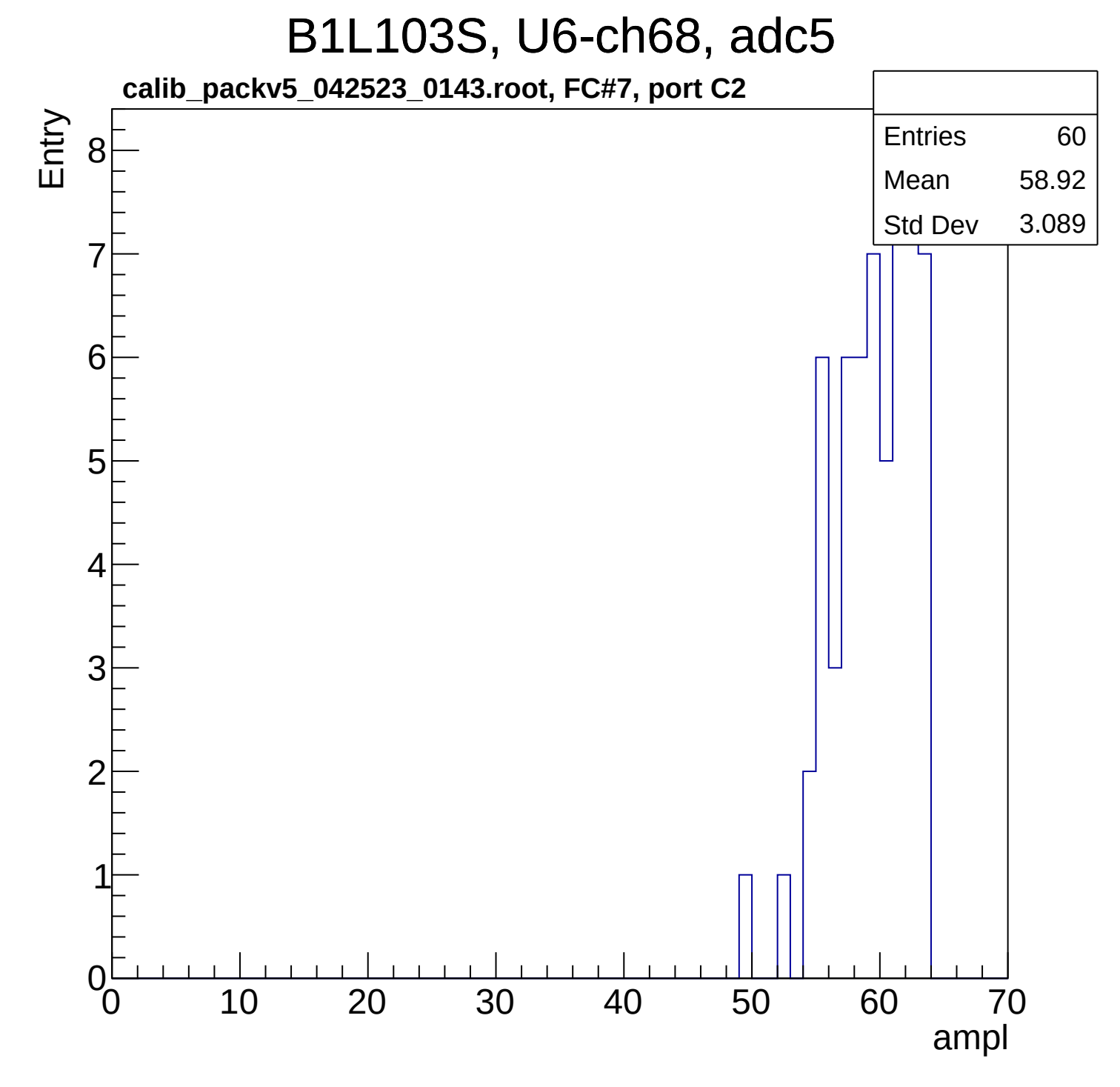
B1L103S, U6-ch68, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 58.92
Std Dev 3.089
Entry
ampl
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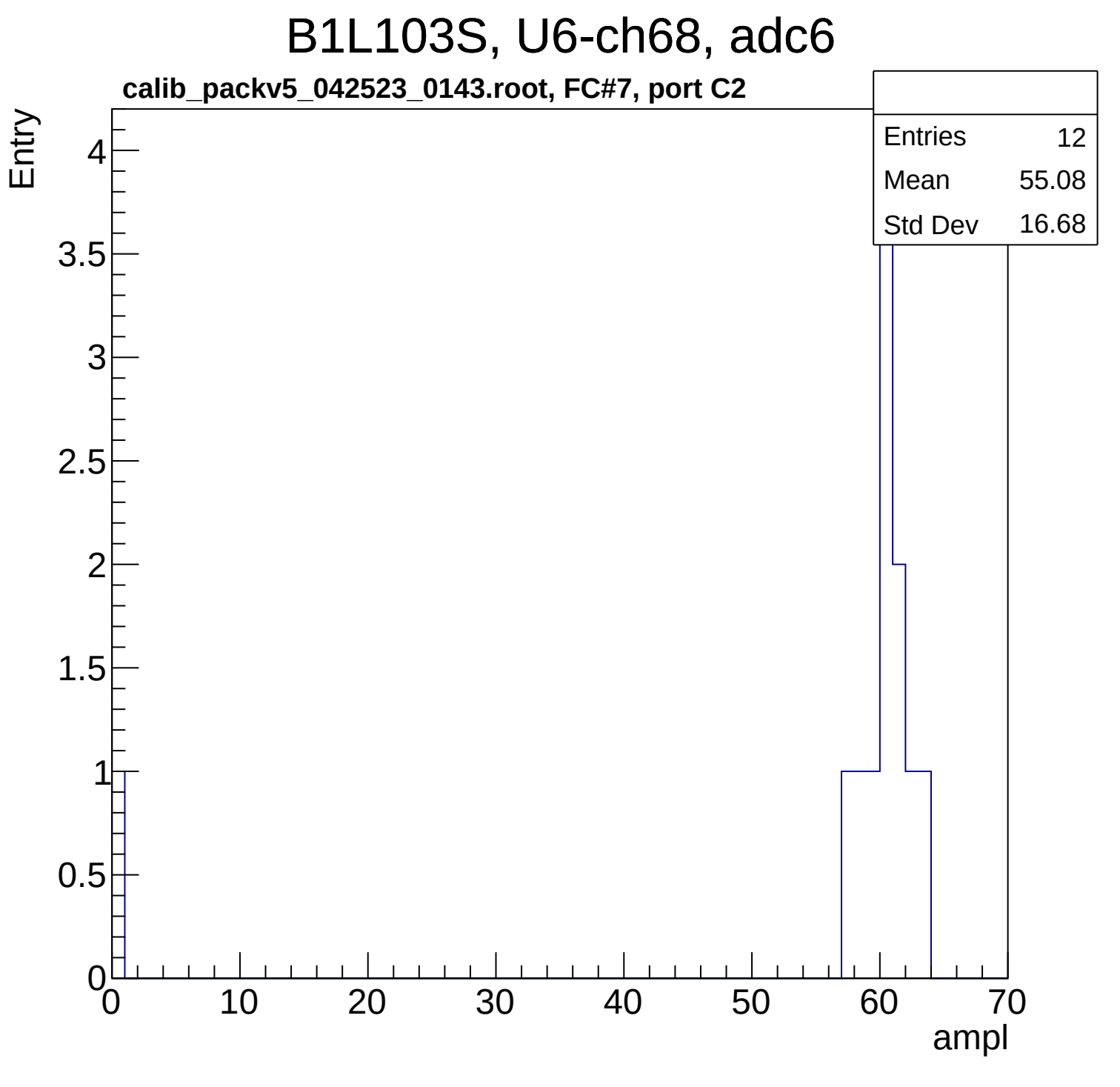
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch68, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 55.08
Std Dev 16.68
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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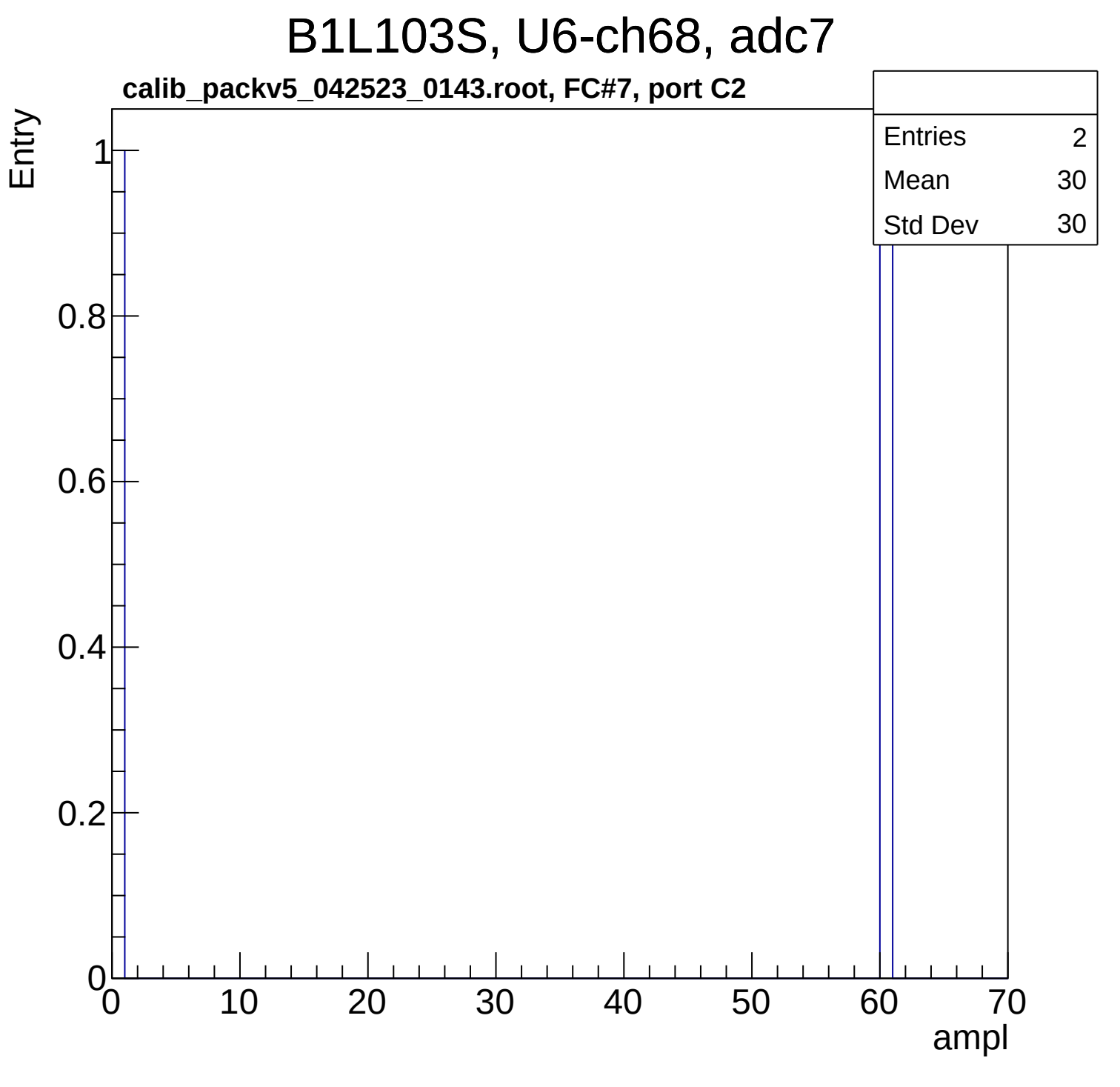
B1L103S, U6-ch68, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 30
Std Dev 30
Entry
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0.6
0.4
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0
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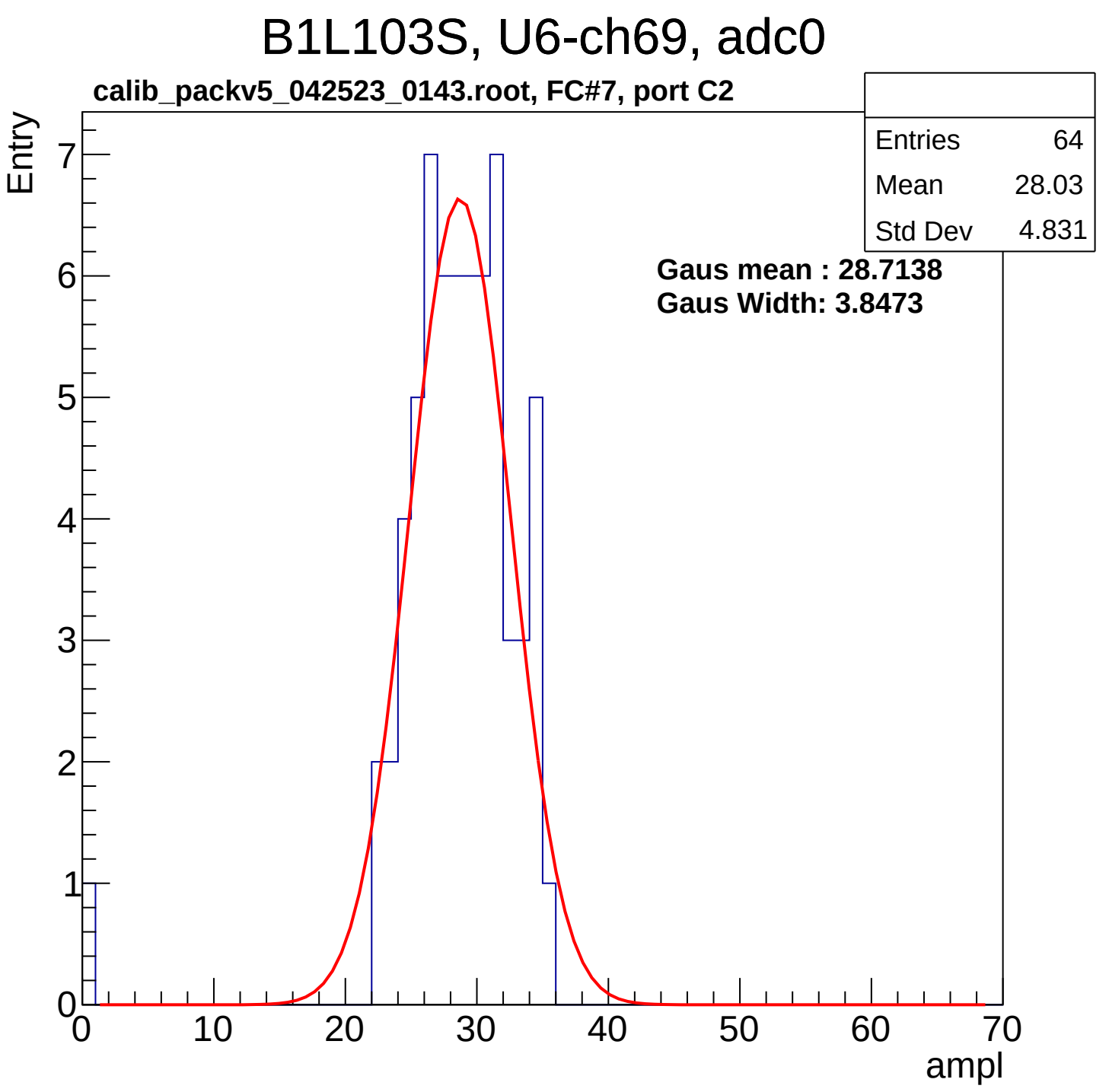
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch69, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 28.03
Std Dev 4.831
Gaus mean : 28.7138
Gaus Width: 3.8473
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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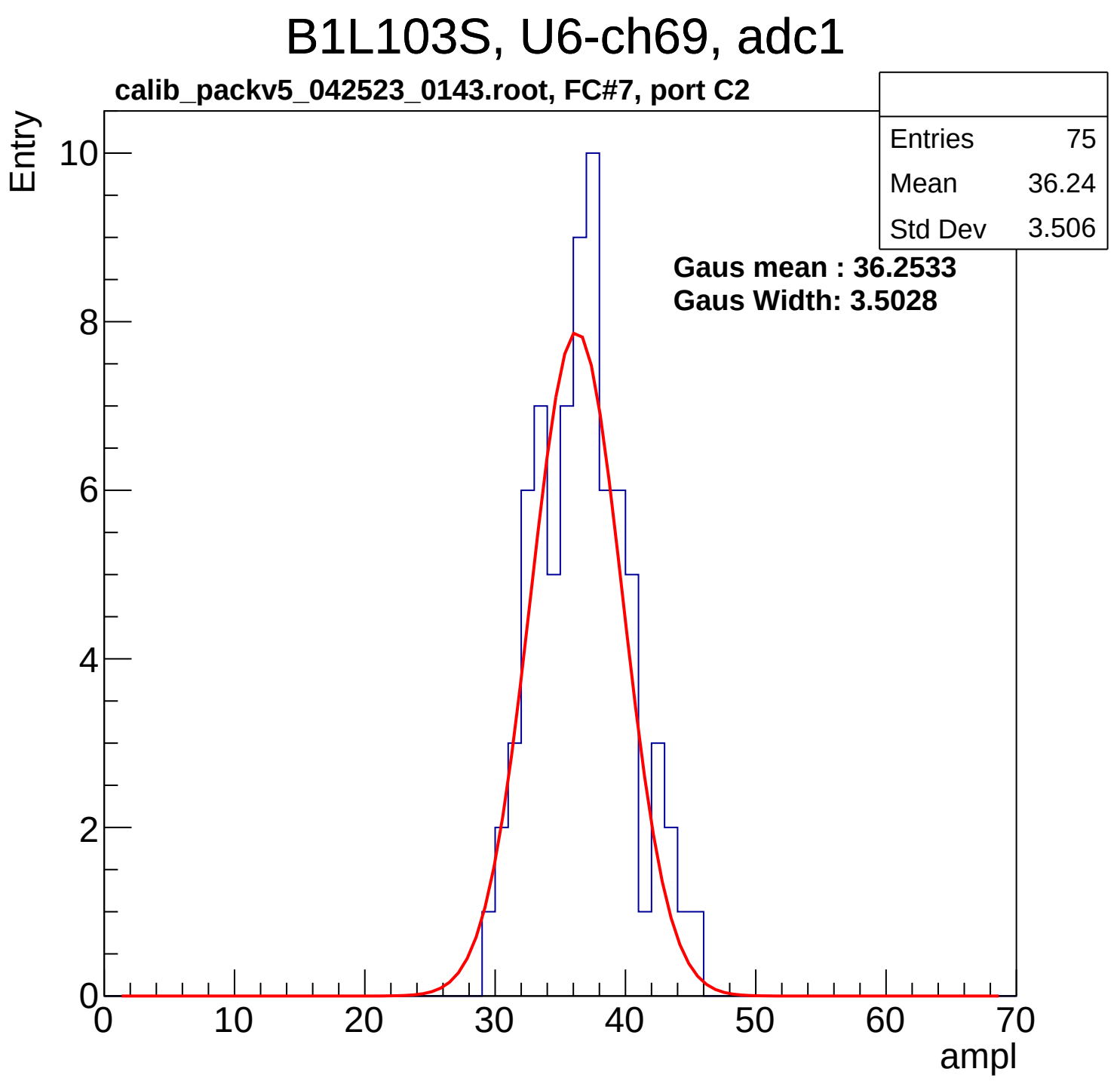
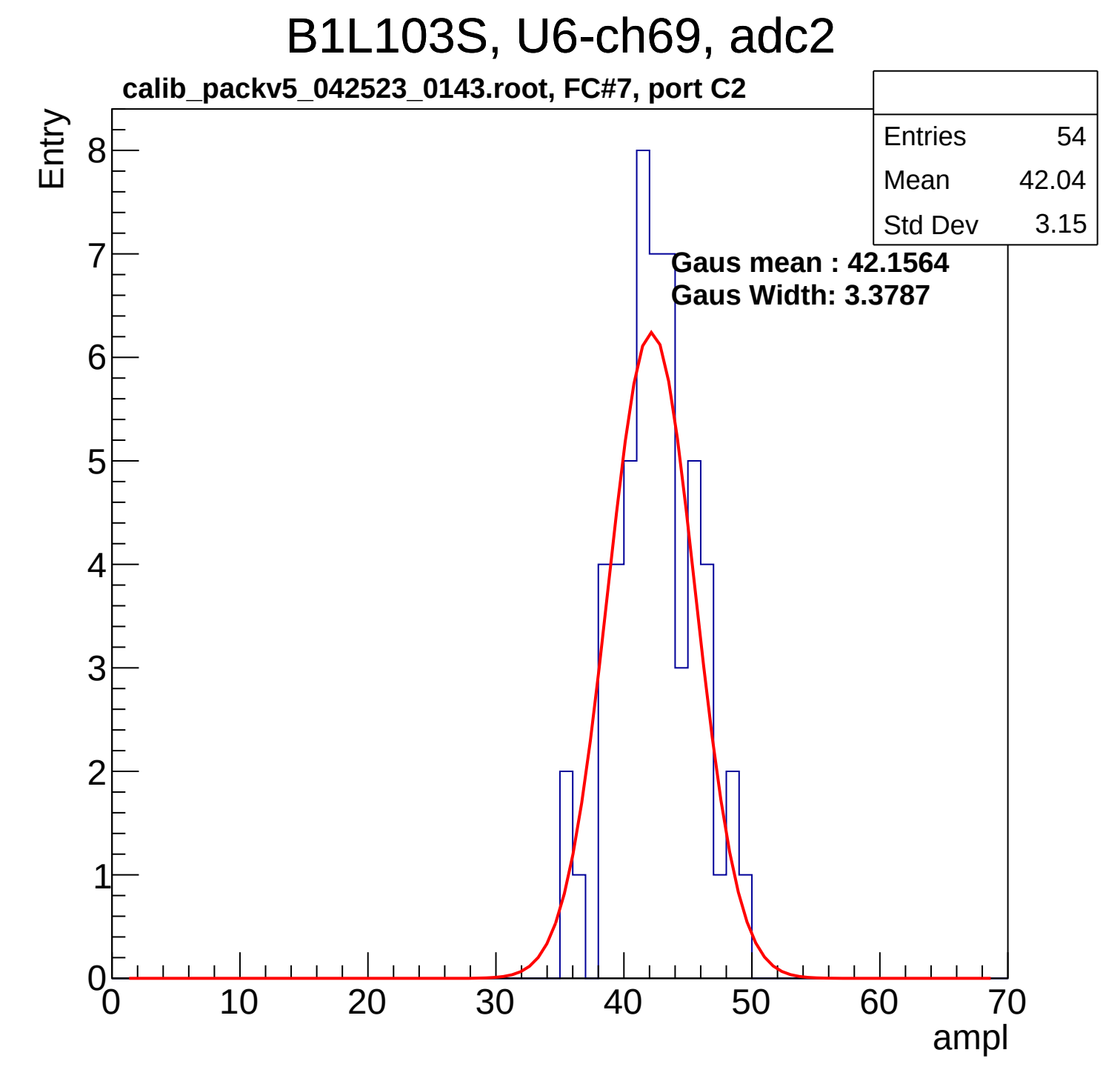

B1L103S, U6-ch69, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 36.24
Std Dev 3.506
Gaus mean : 36.2533
Gaus Width: 3.5028
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch69, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 42.04
Std Dev 3.15
Gaus mean : 42.1564
Gaus Width: 3.3787
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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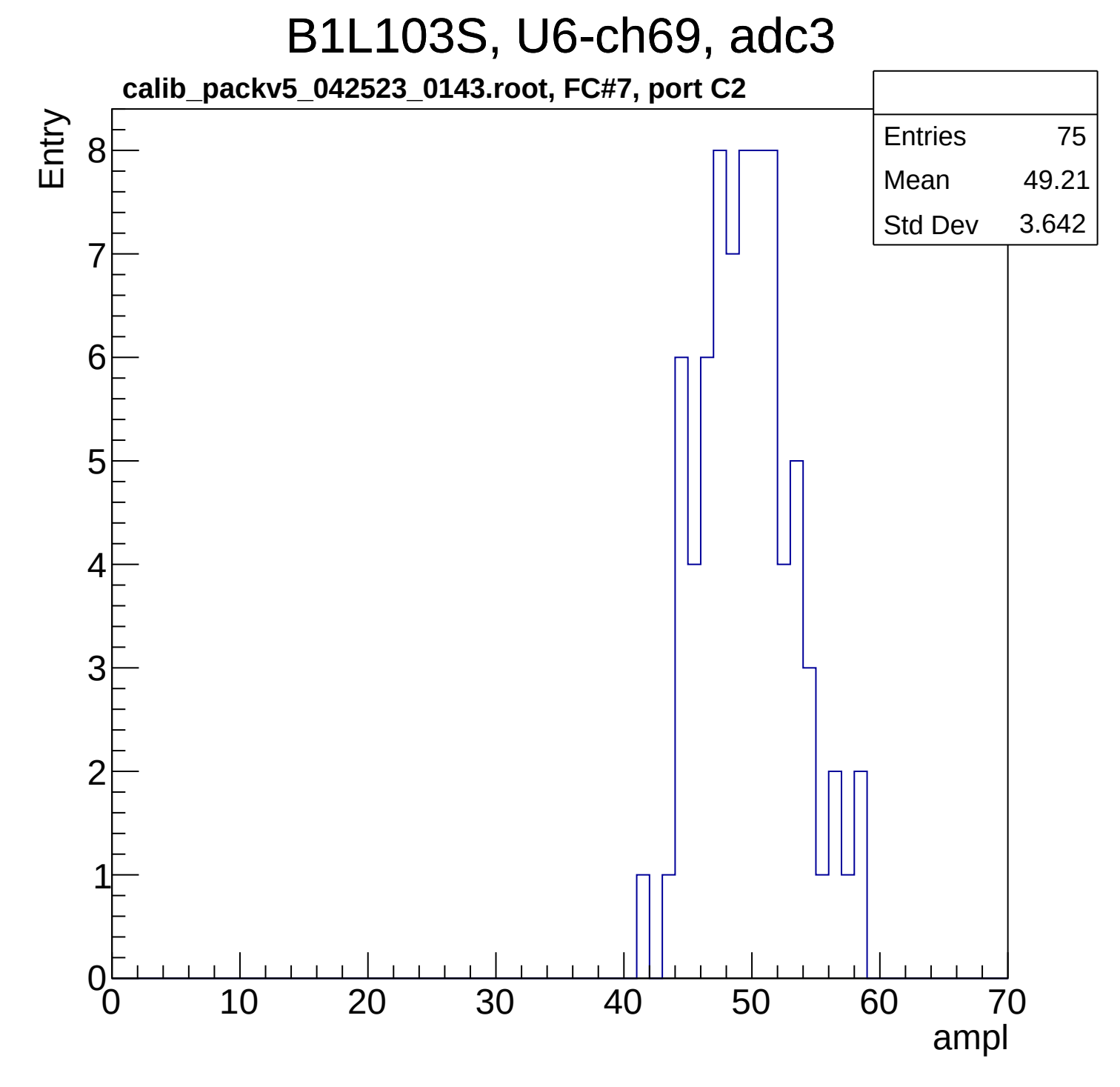
B1L103S, U6-ch69, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 49.21
Std Dev 3.642
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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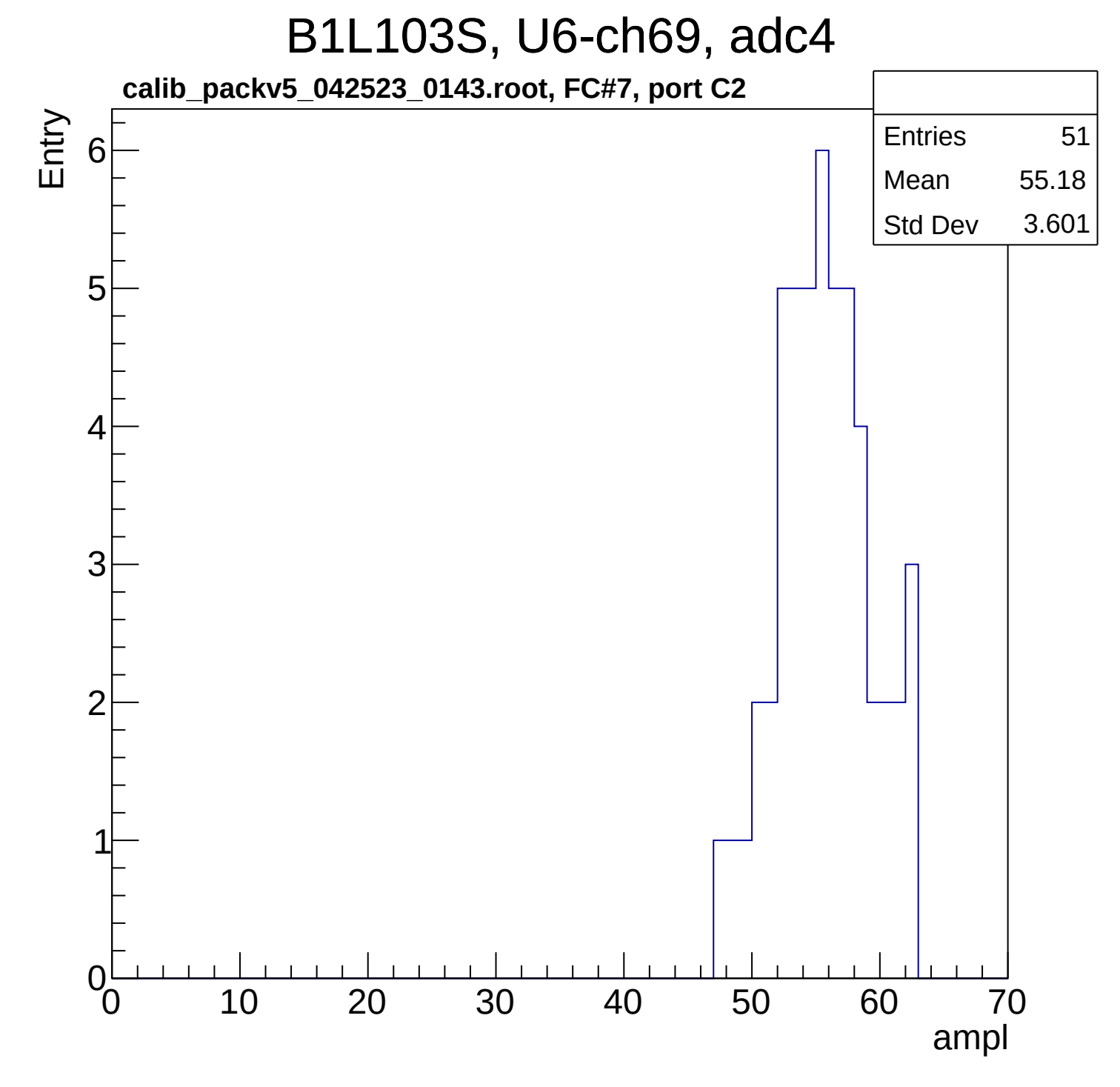
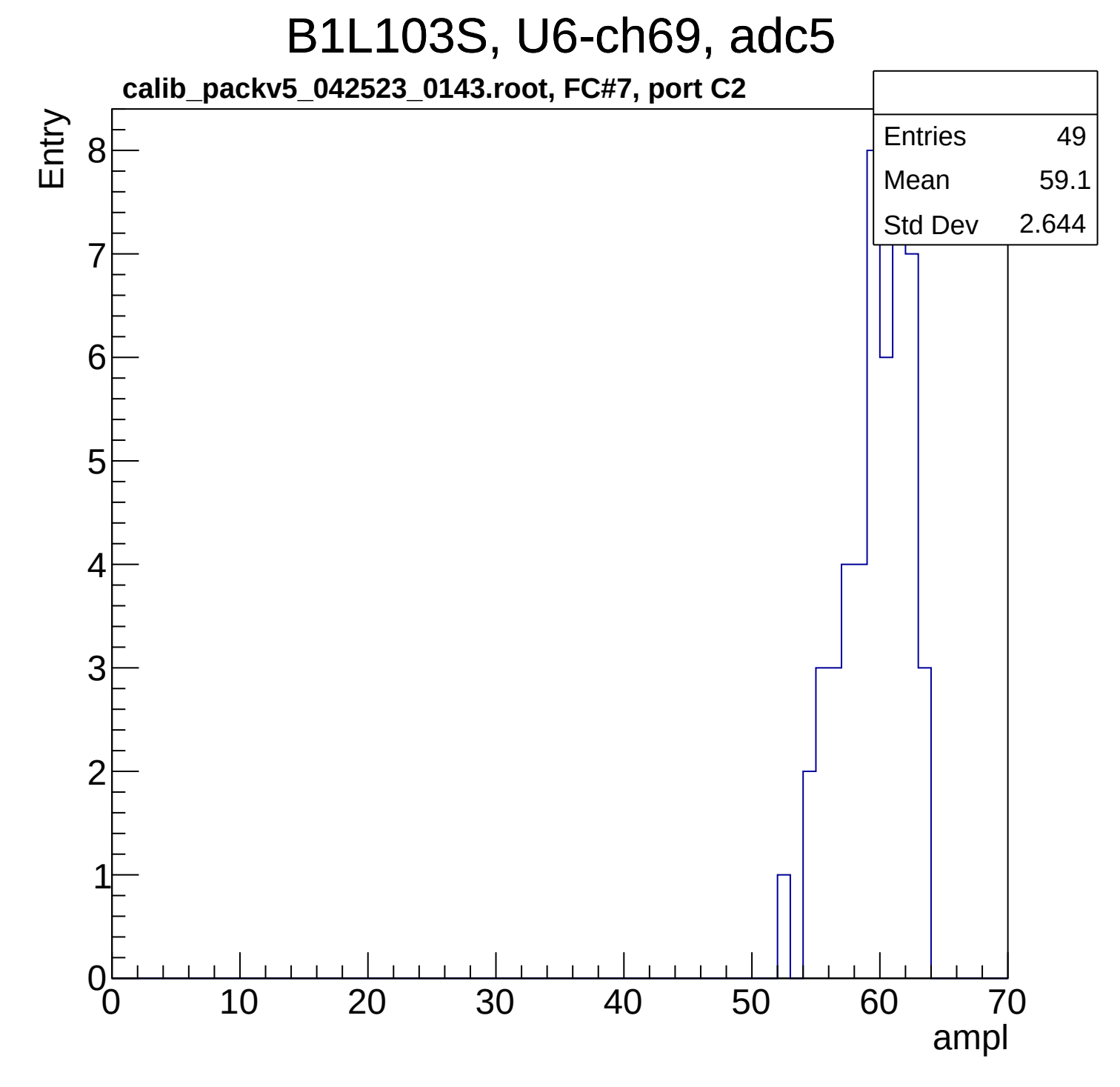

B1L103S, U6-ch69, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.18
Std Dev 3.601
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch69, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.1
Std Dev 2.644
Entry
ampl
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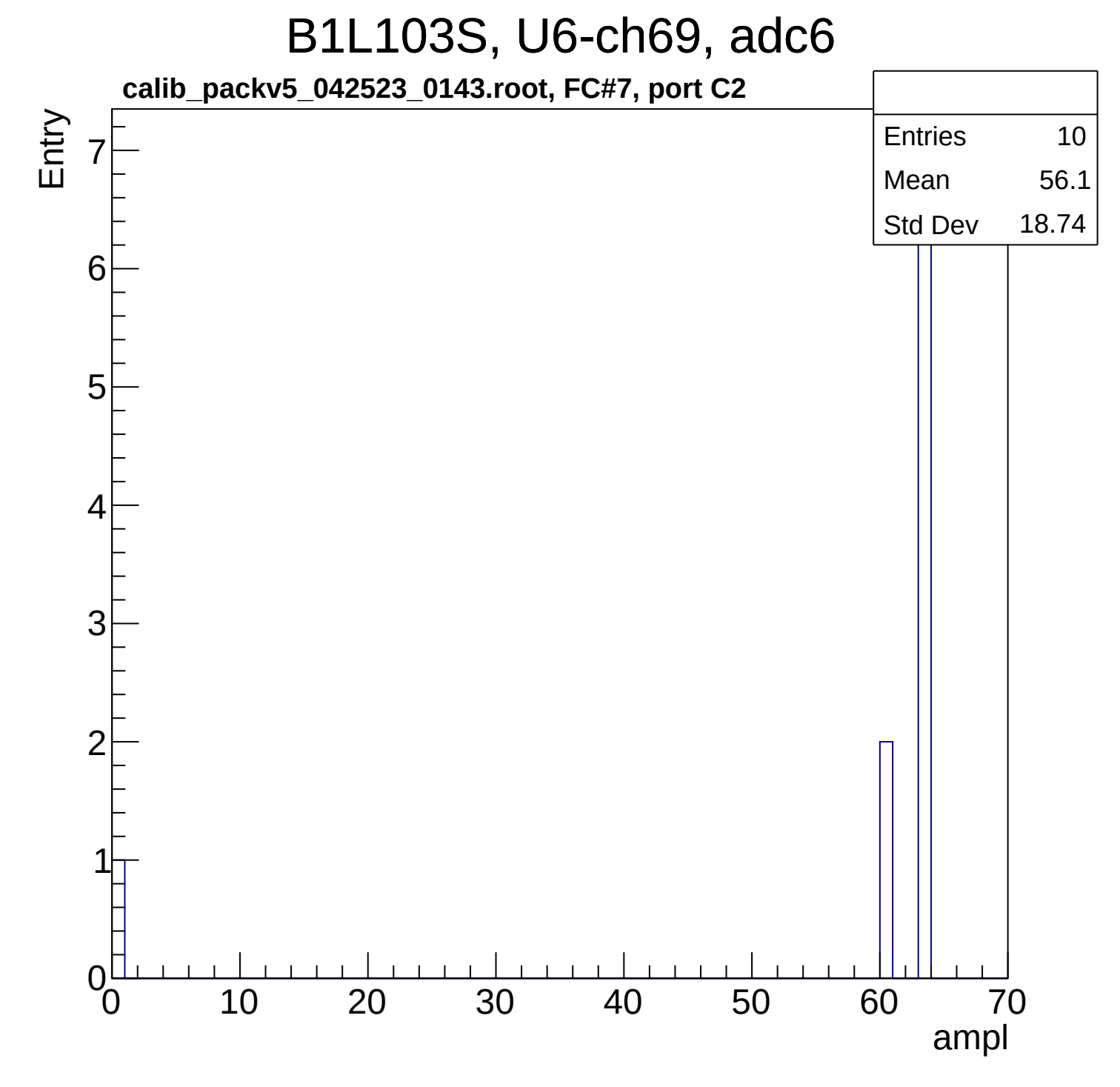
B1L103S, U6-ch69, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56.1
Std Dev 18.74
Entry
ampl
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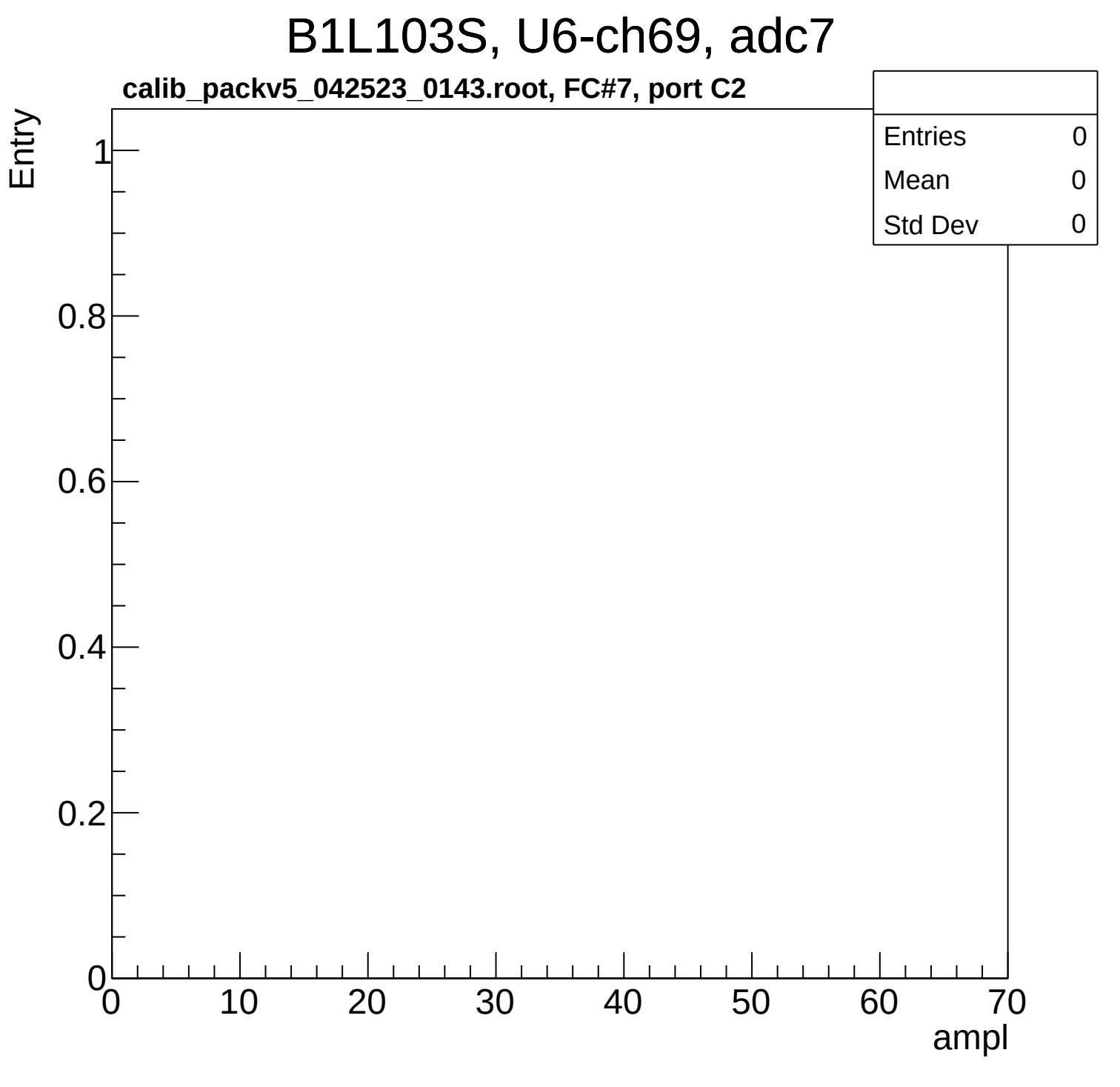
B1L103S, U6-ch69, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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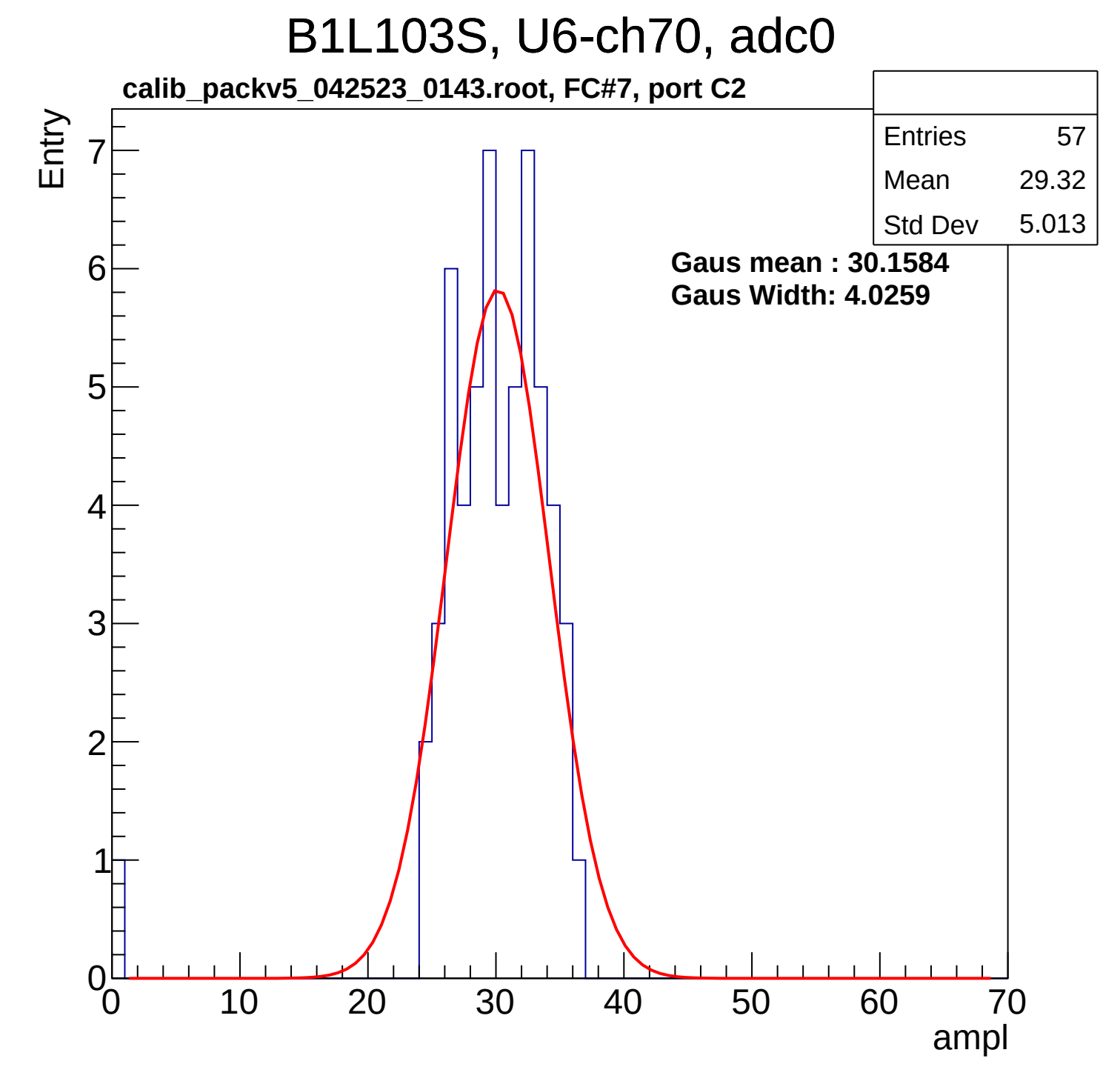
40
50
60
70
ampl


B1L103S, U6-ch70, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 29.32
Std Dev 5.013
Gaus mean : 30.1584
Gaus Width: 4.0259
Entry
ampl
0
1
2
3
4
5
6
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0
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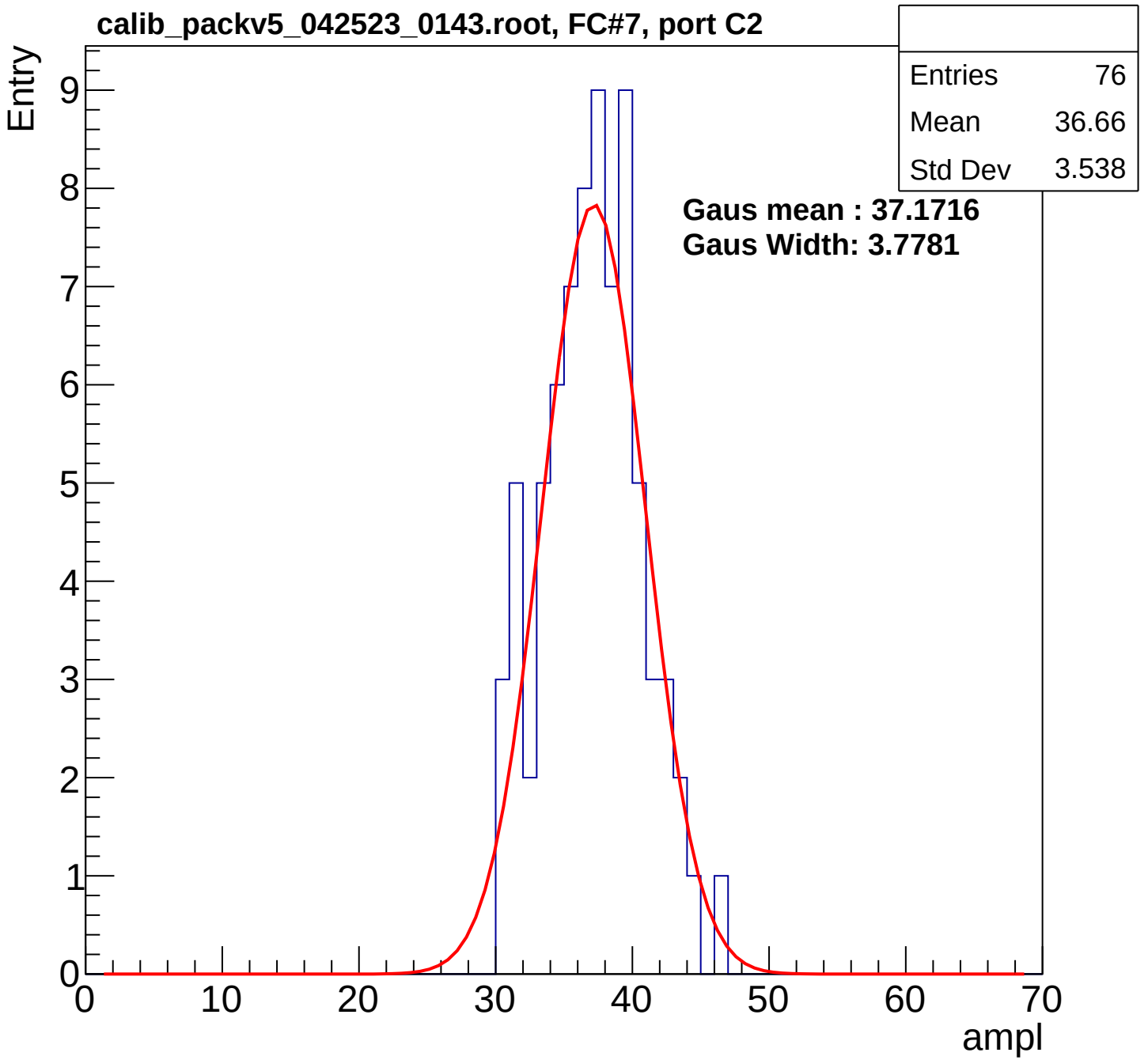
B1L103S, U6-ch70, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36.66
Std Dev 3.538
Gaus mean : 37.1716
Gaus Width: 3.7781
Entry
ampl
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3
4
5
6
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0
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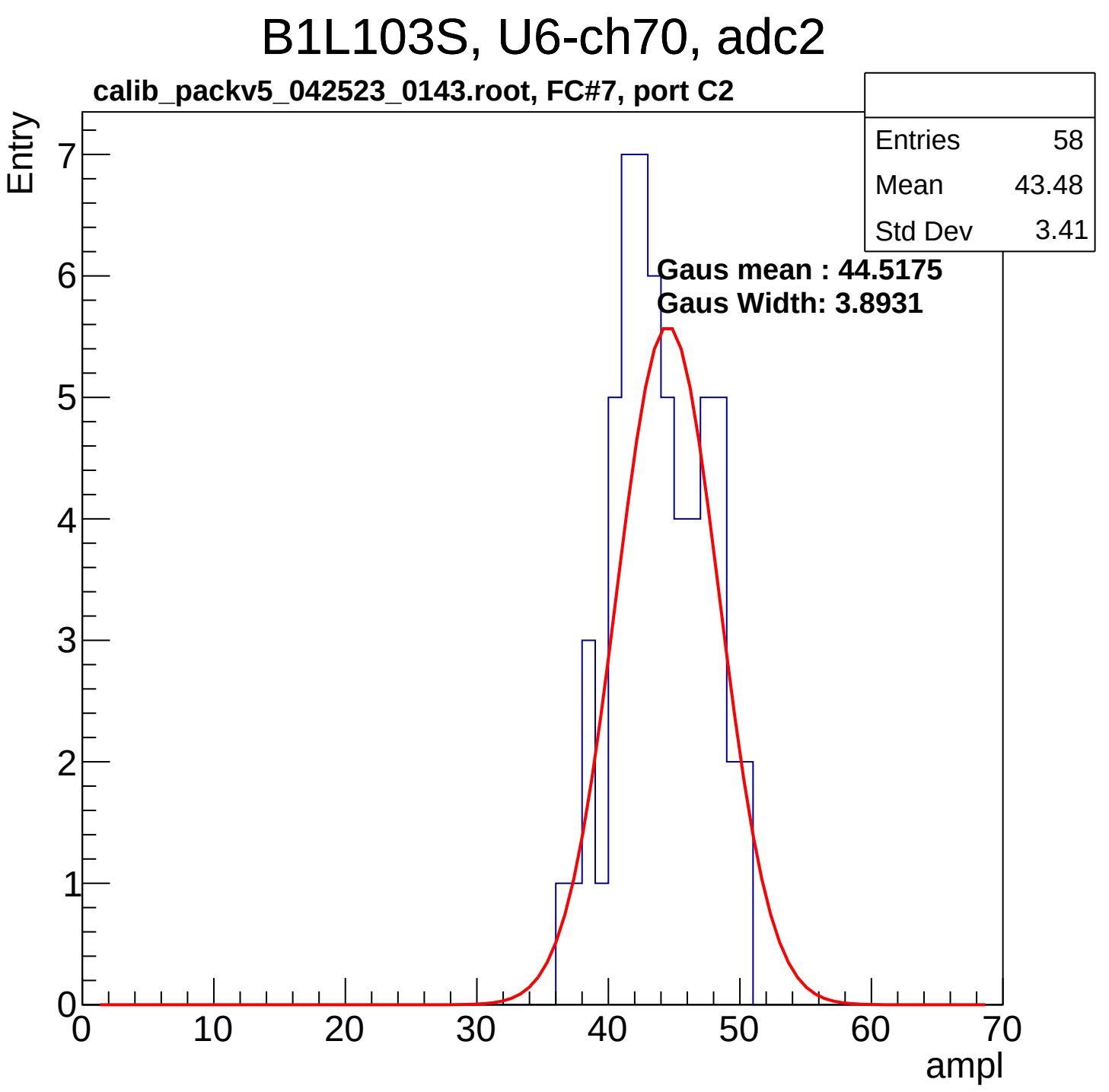
B1L103S, U6-ch70, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 43.48
Std Dev 3.41
Gaus mean : 44.5175
Gaus Width: 3.8931
Entry
ampl
0
1
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3
4
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6
7
0
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60
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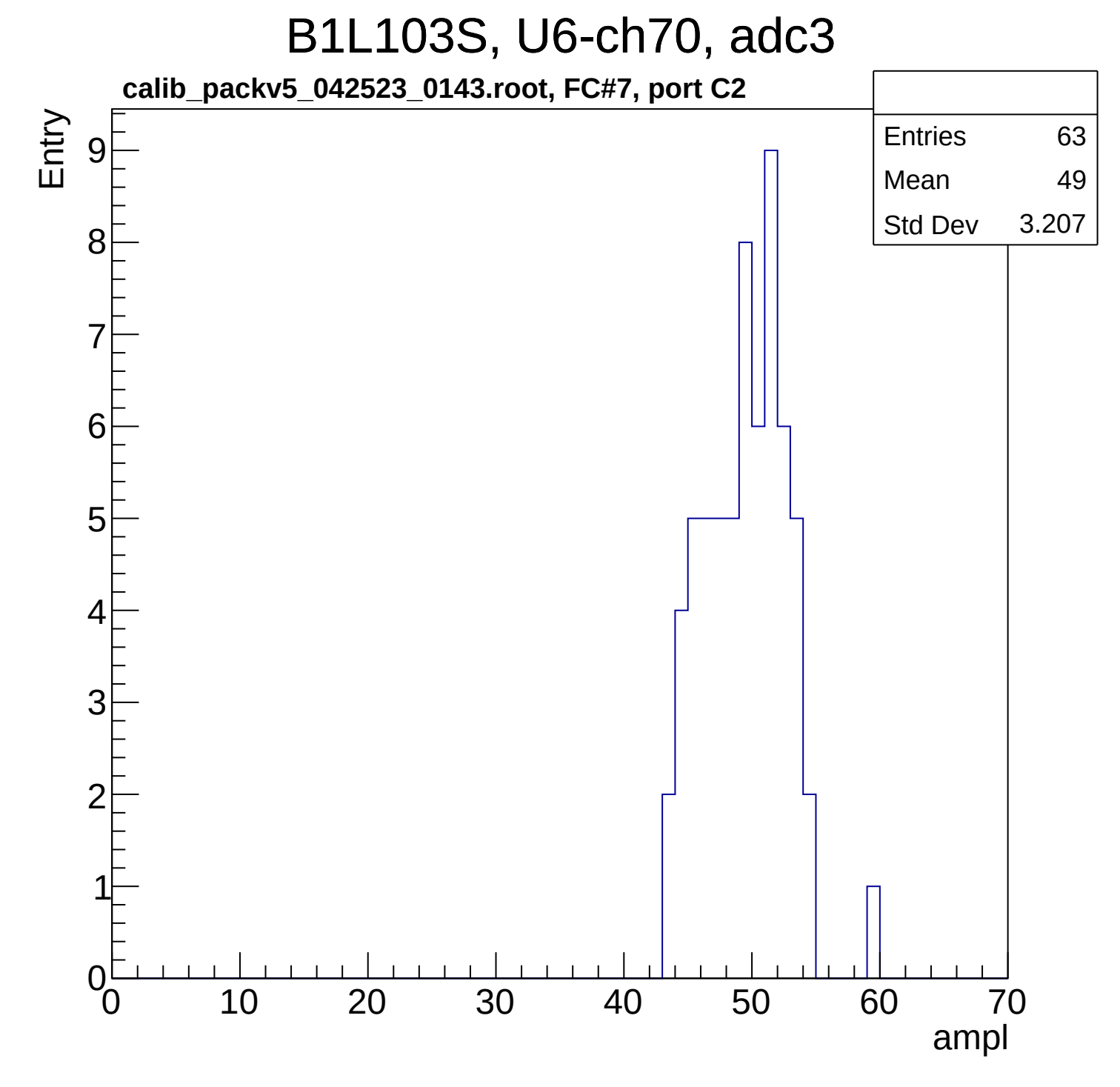
B1L103S, U6-ch70, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49
Std Dev 3.207
Entry
ampl
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6
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0
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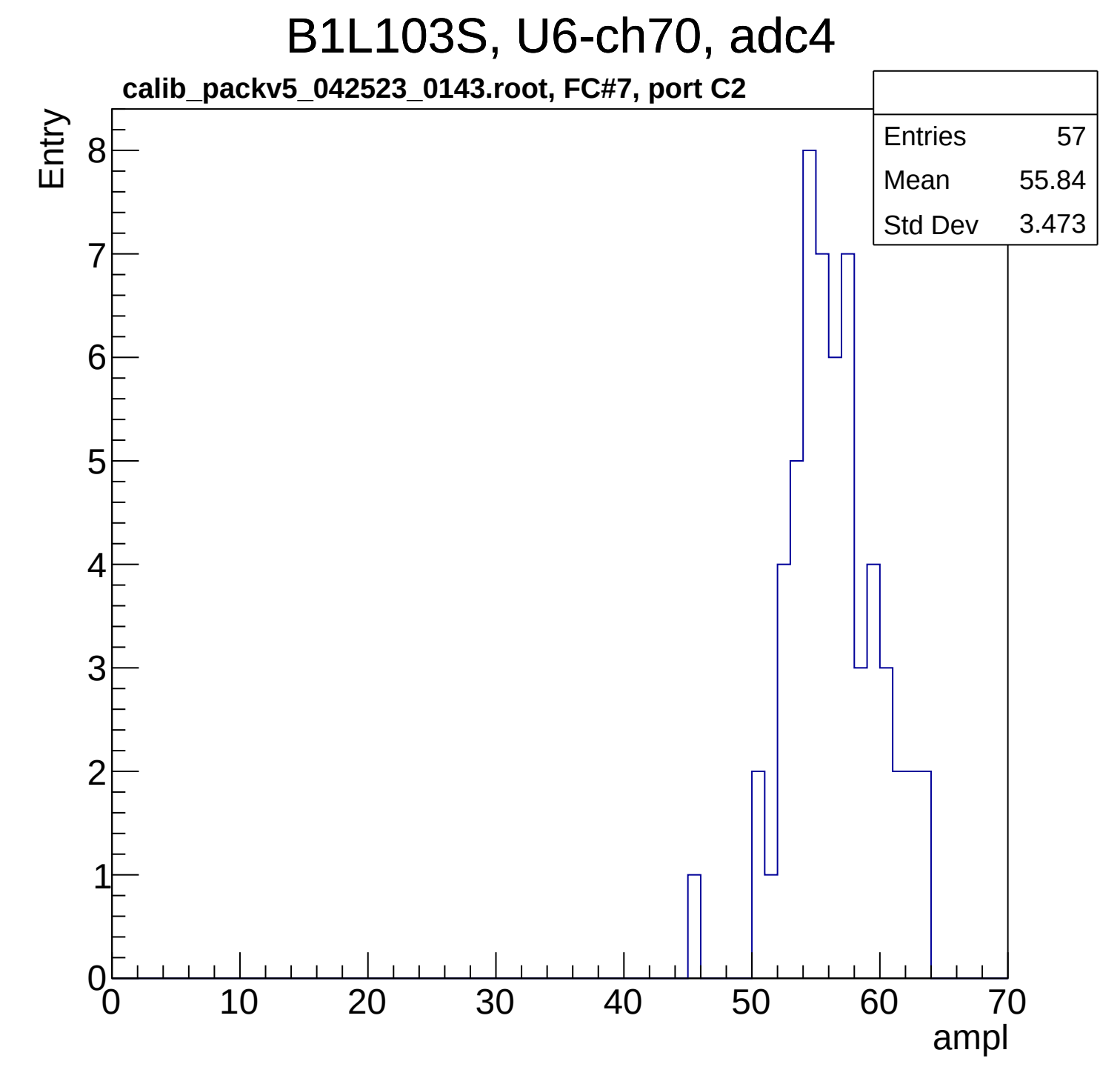
B1L103S, U6-ch70, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.84
Std Dev 3.473
Entry
ampl
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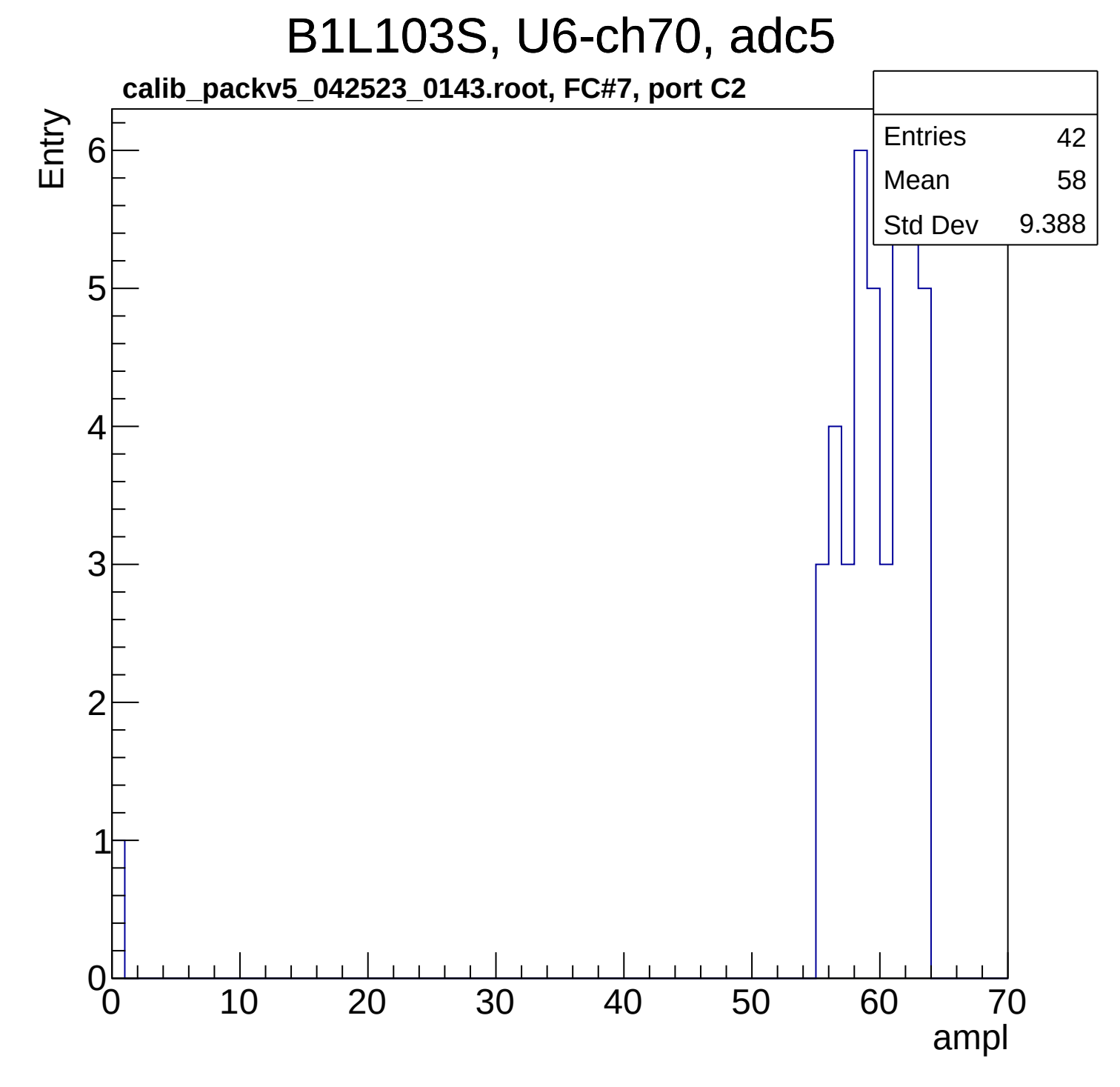
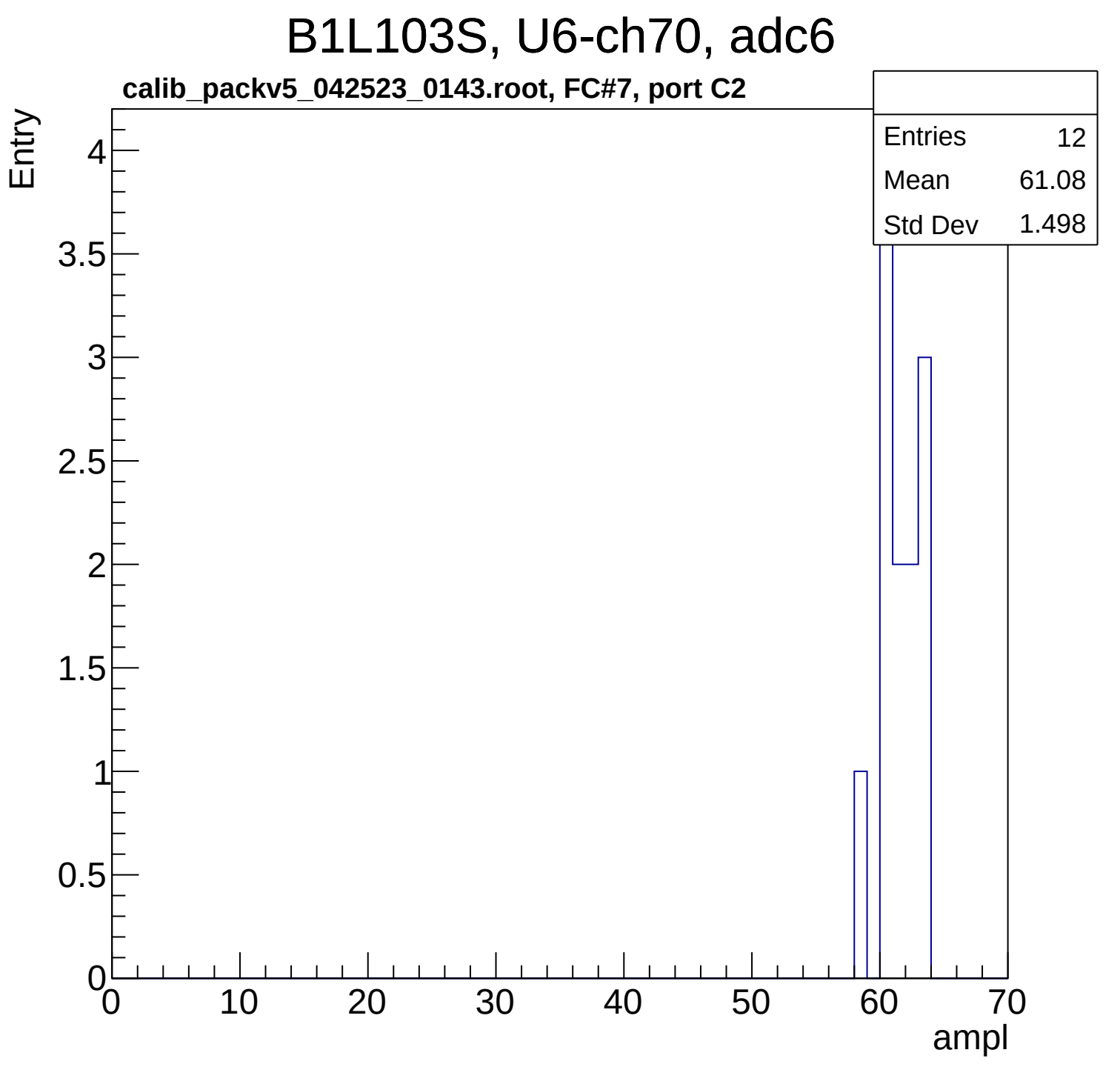
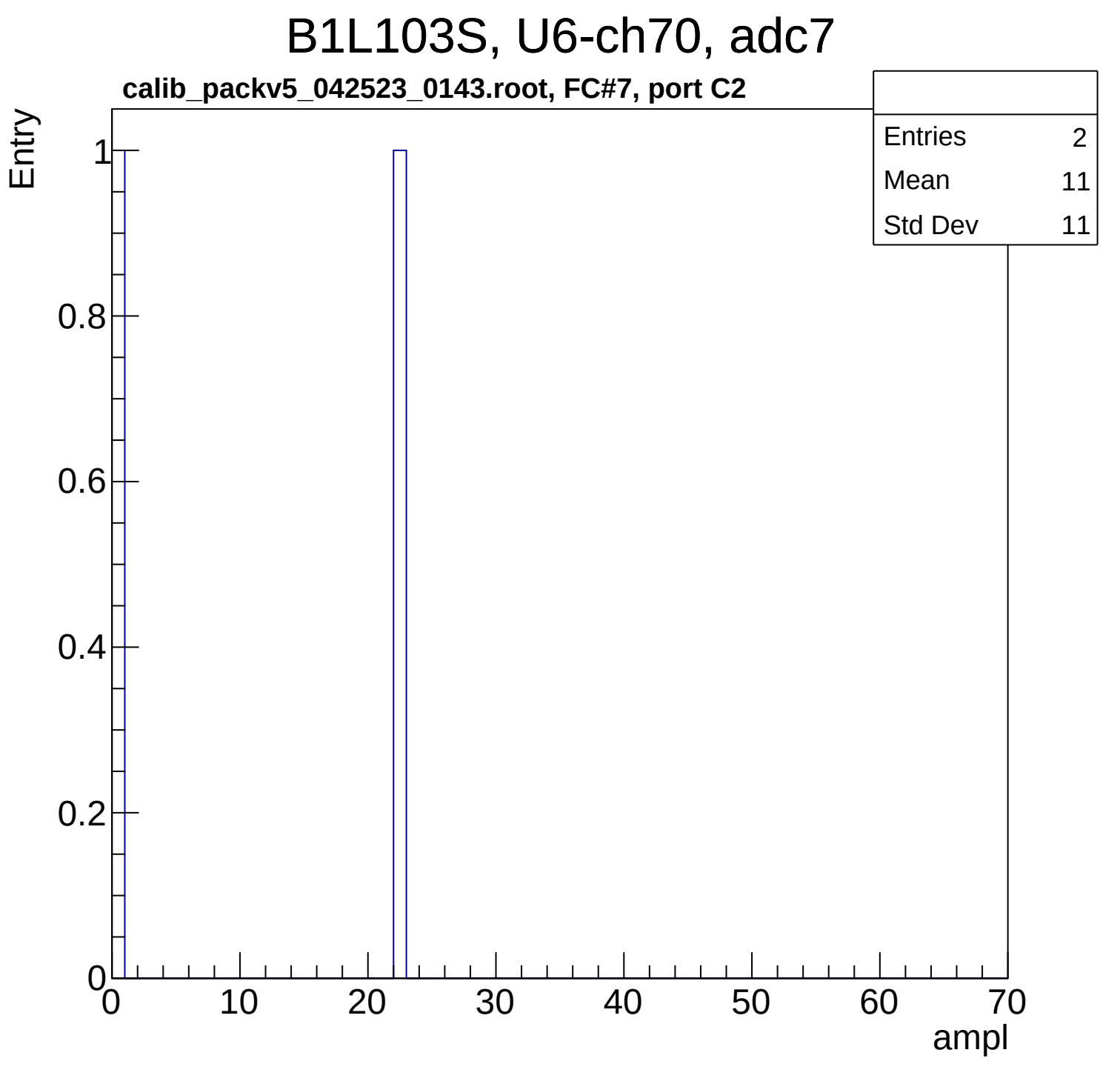

B1L103S, U6-ch70, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58
Std Dev 9.388
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch70, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.08
Std Dev 1.498
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch70, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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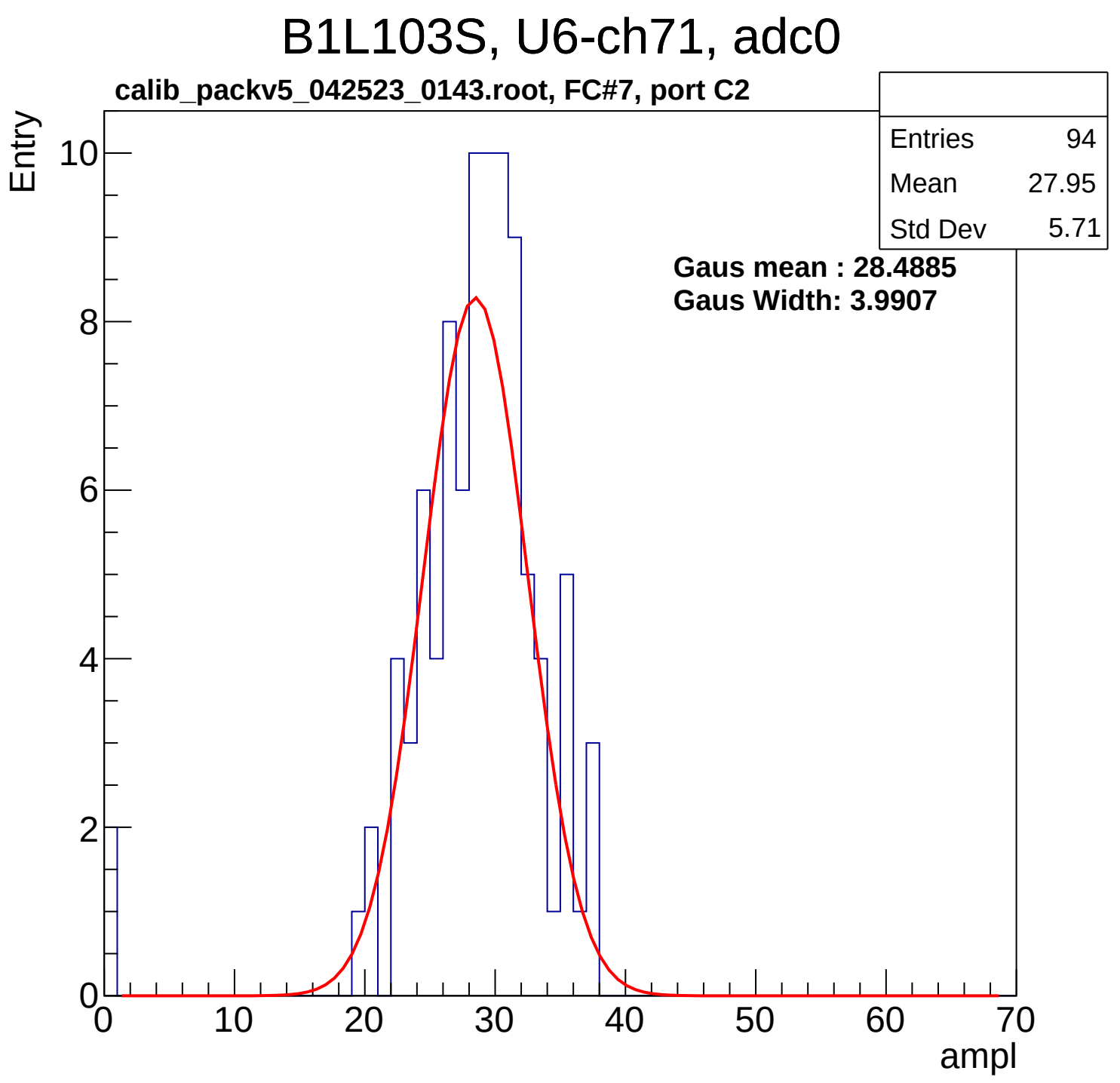
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch71, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 94
Mean 27.95
Std Dev 5.71
Gaus mean : 28.4885
Gaus Width: 3.9907
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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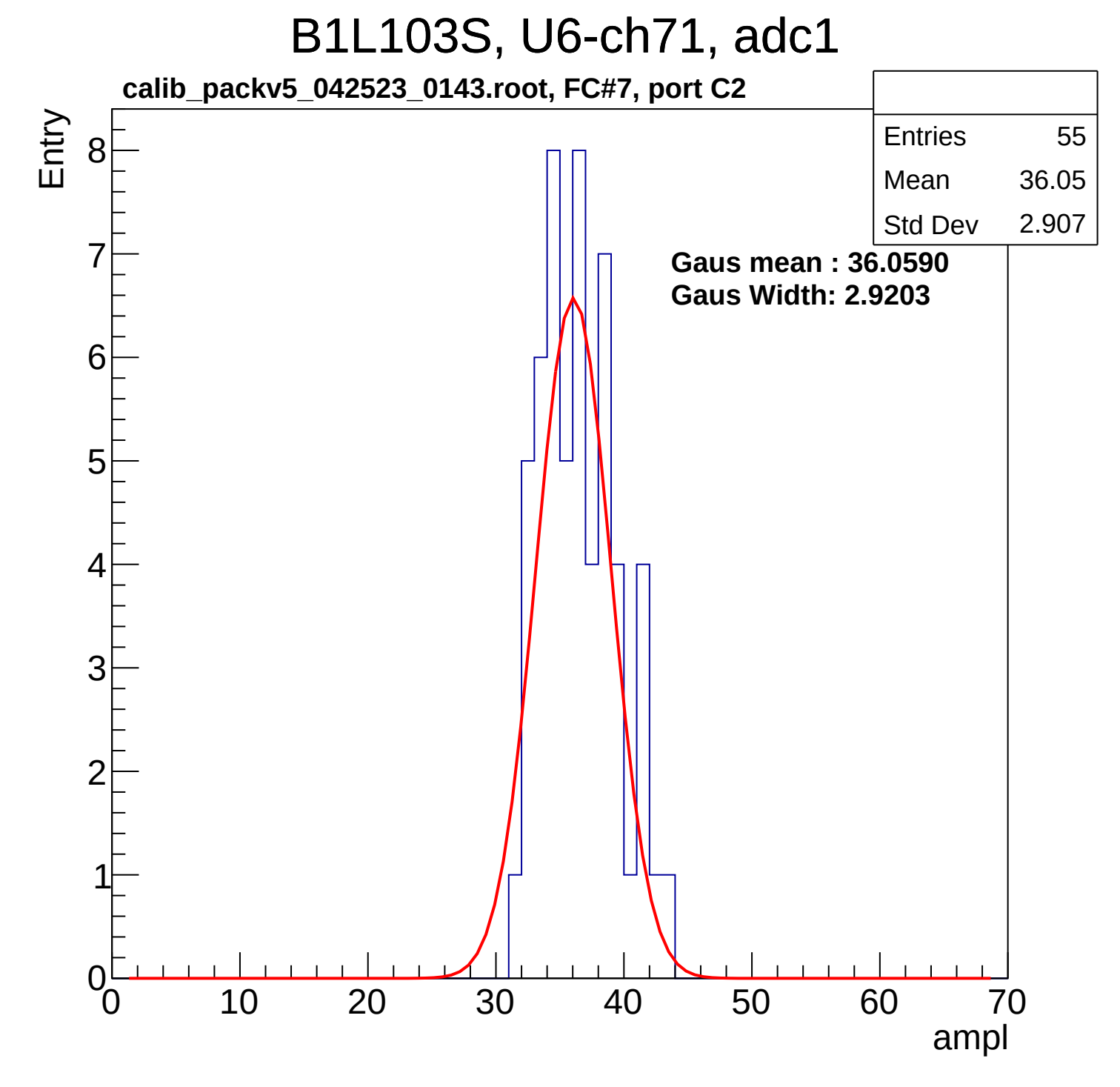
B1L103S, U6-ch71, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 36.05
Std Dev 2.907
Gaus mean : 36.0590
Gaus Width: 2.9203
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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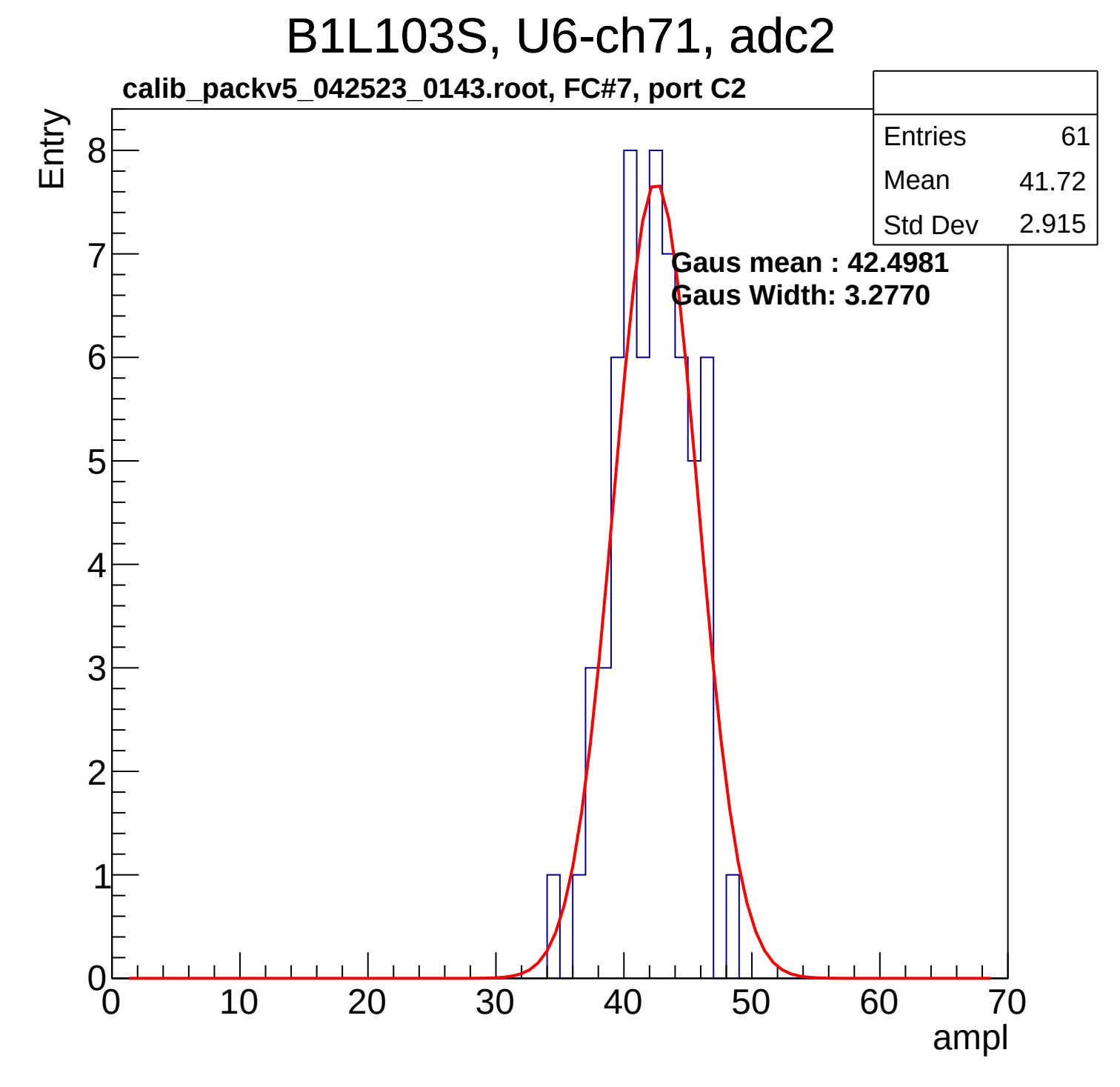
B1L103S, U6-ch71, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.72
Std Dev 2.915
Gaus mean : 42.4981
Gaus Width: 3.2770
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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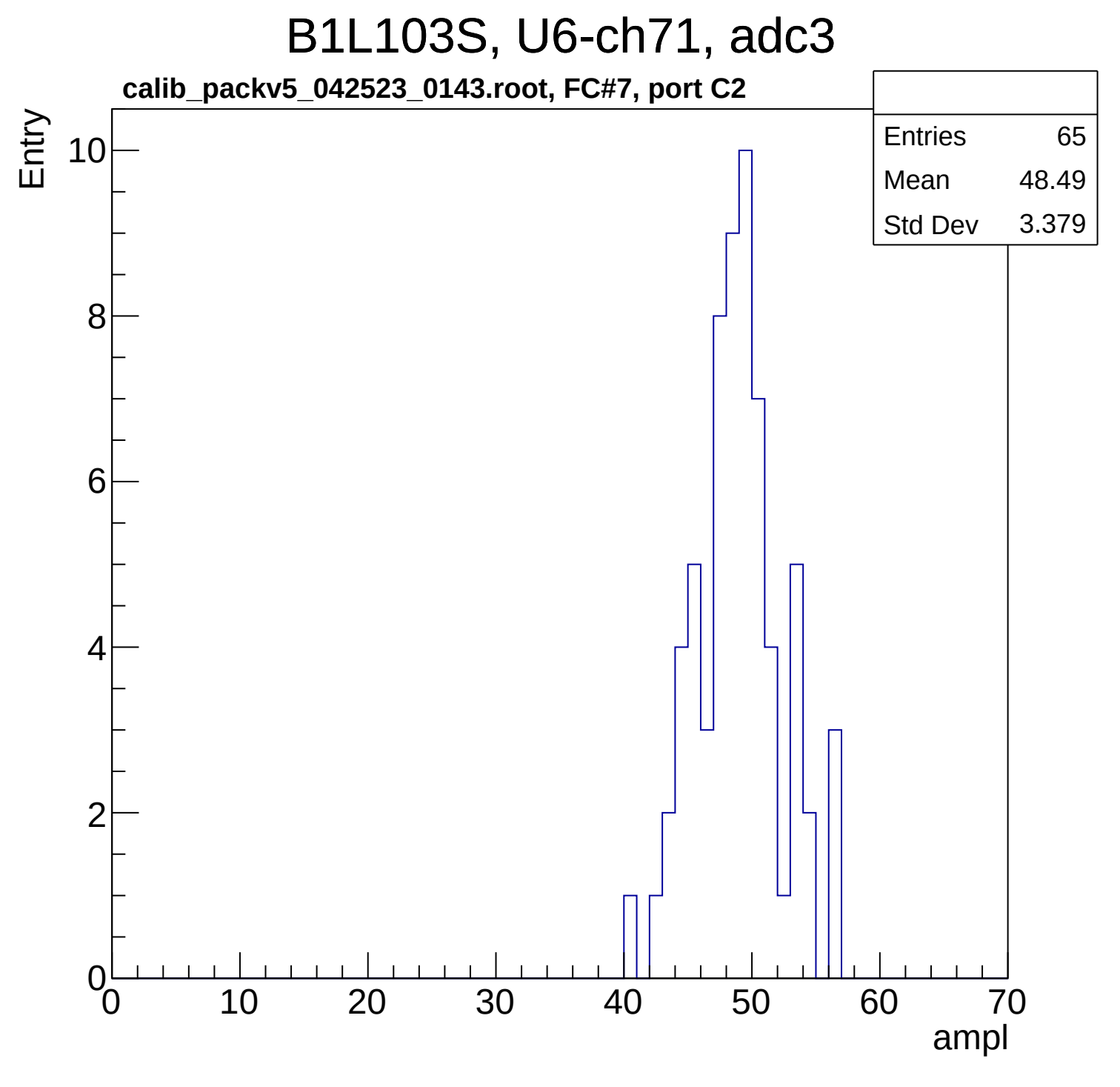
B1L103S, U6-ch71, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.49
Std Dev 3.379
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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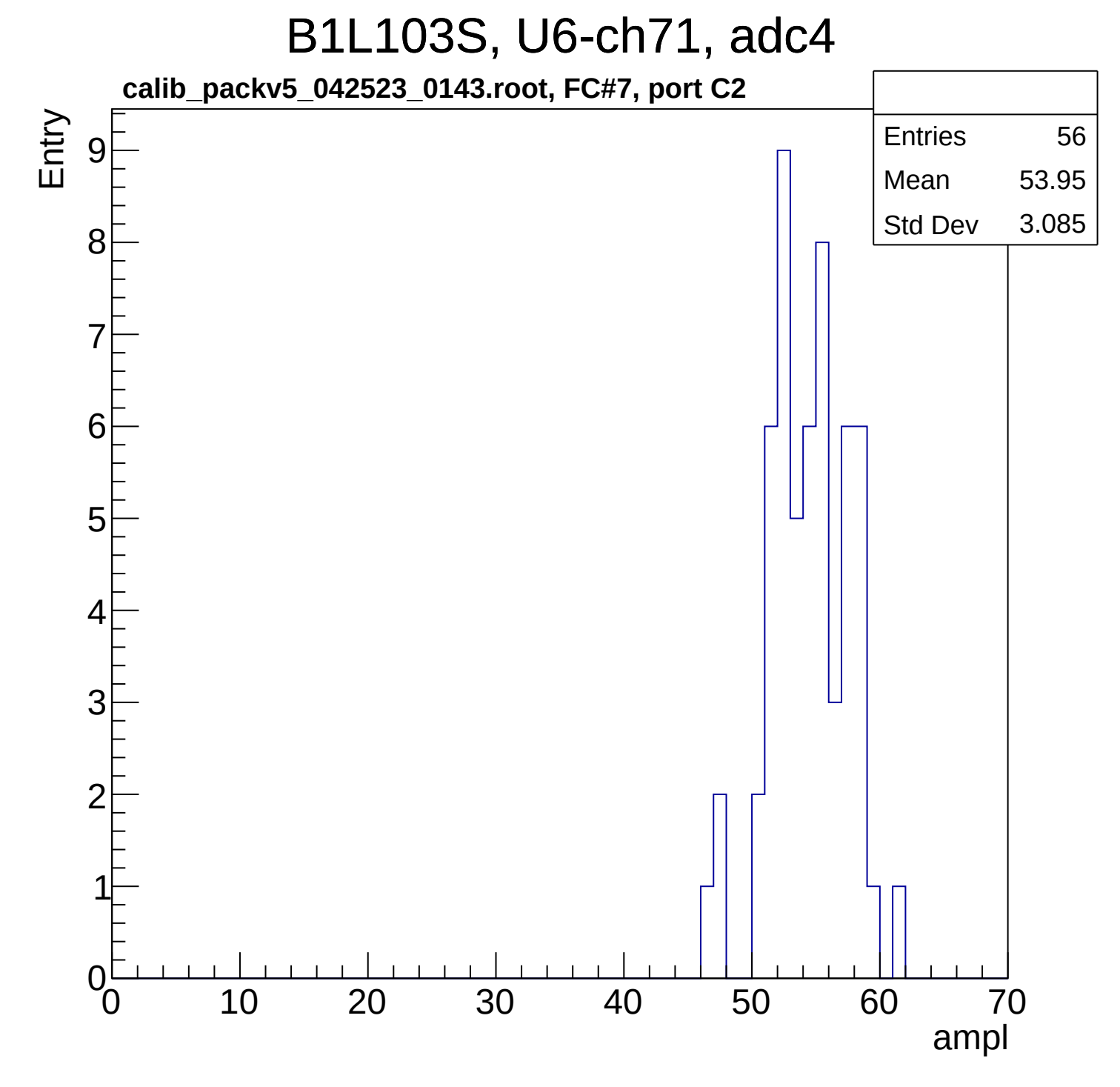
B1L103S, U6-ch71, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.95
Std Dev 3.085
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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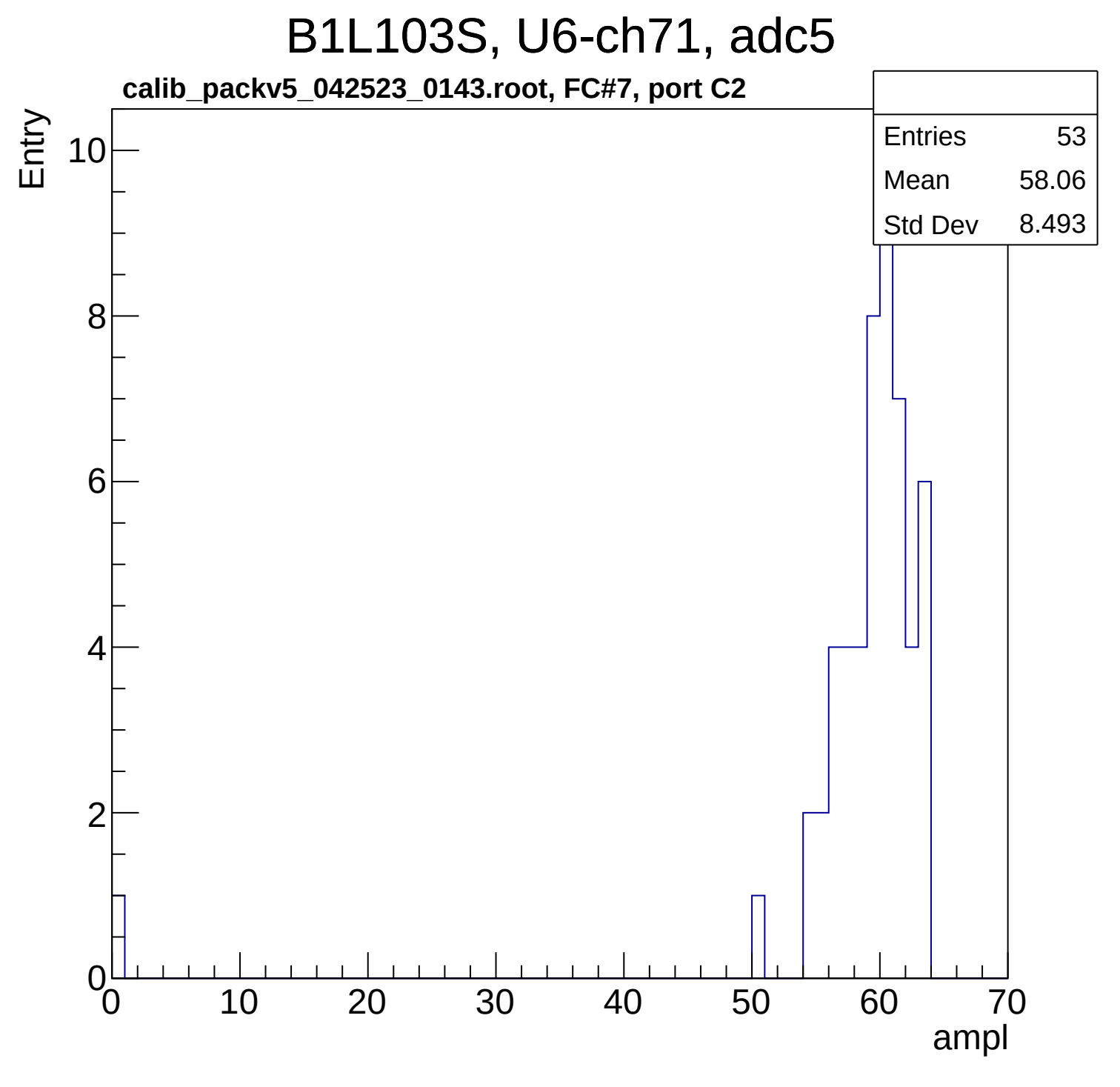
B1L103S, U6-ch71, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.06
Std Dev 8.493
Entry
ampl
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2
4
6
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0
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40
50
60
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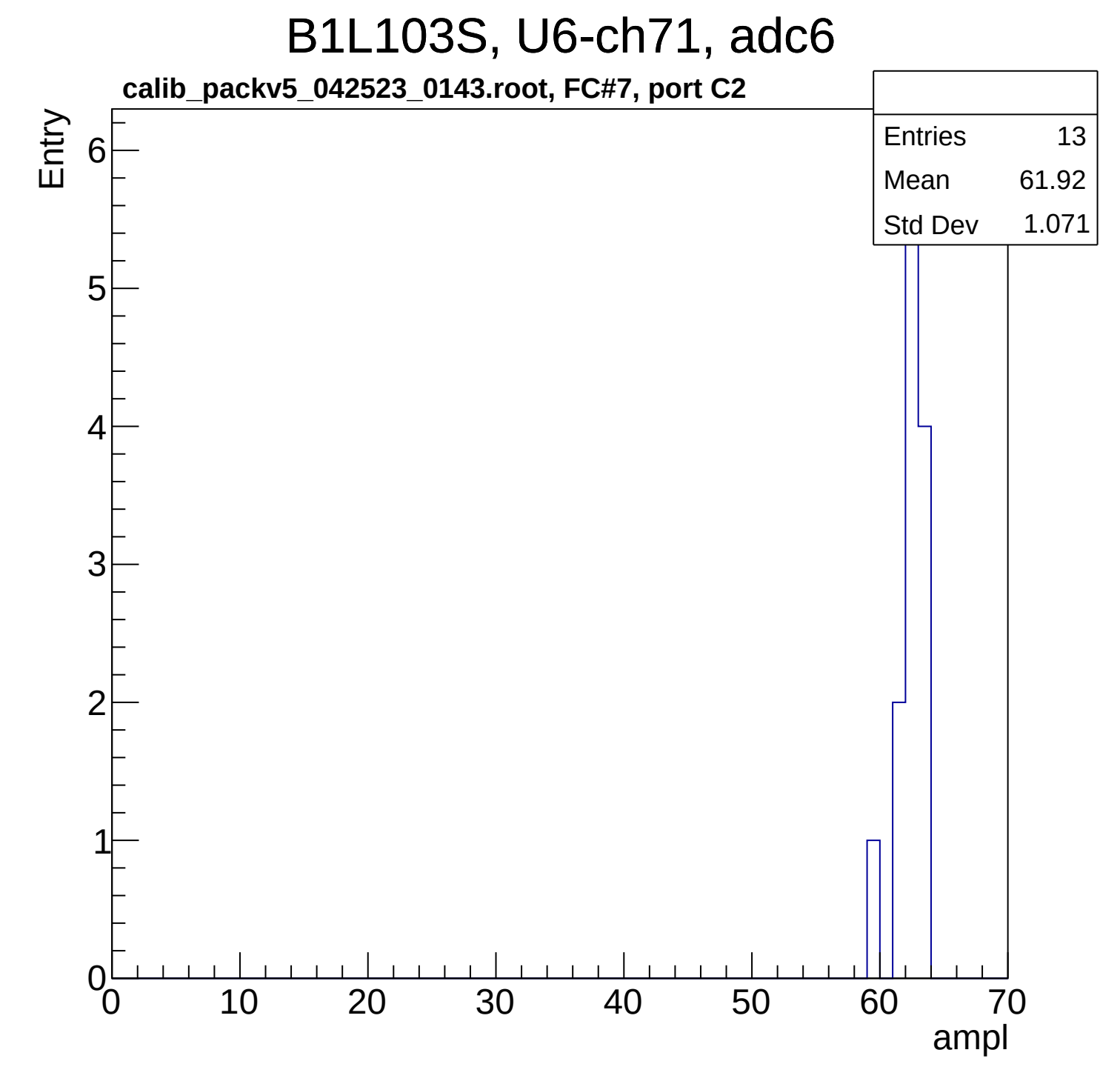
B1L103S, U6-ch71, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.92
Std Dev 1.071
Entry
ampl
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0
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60
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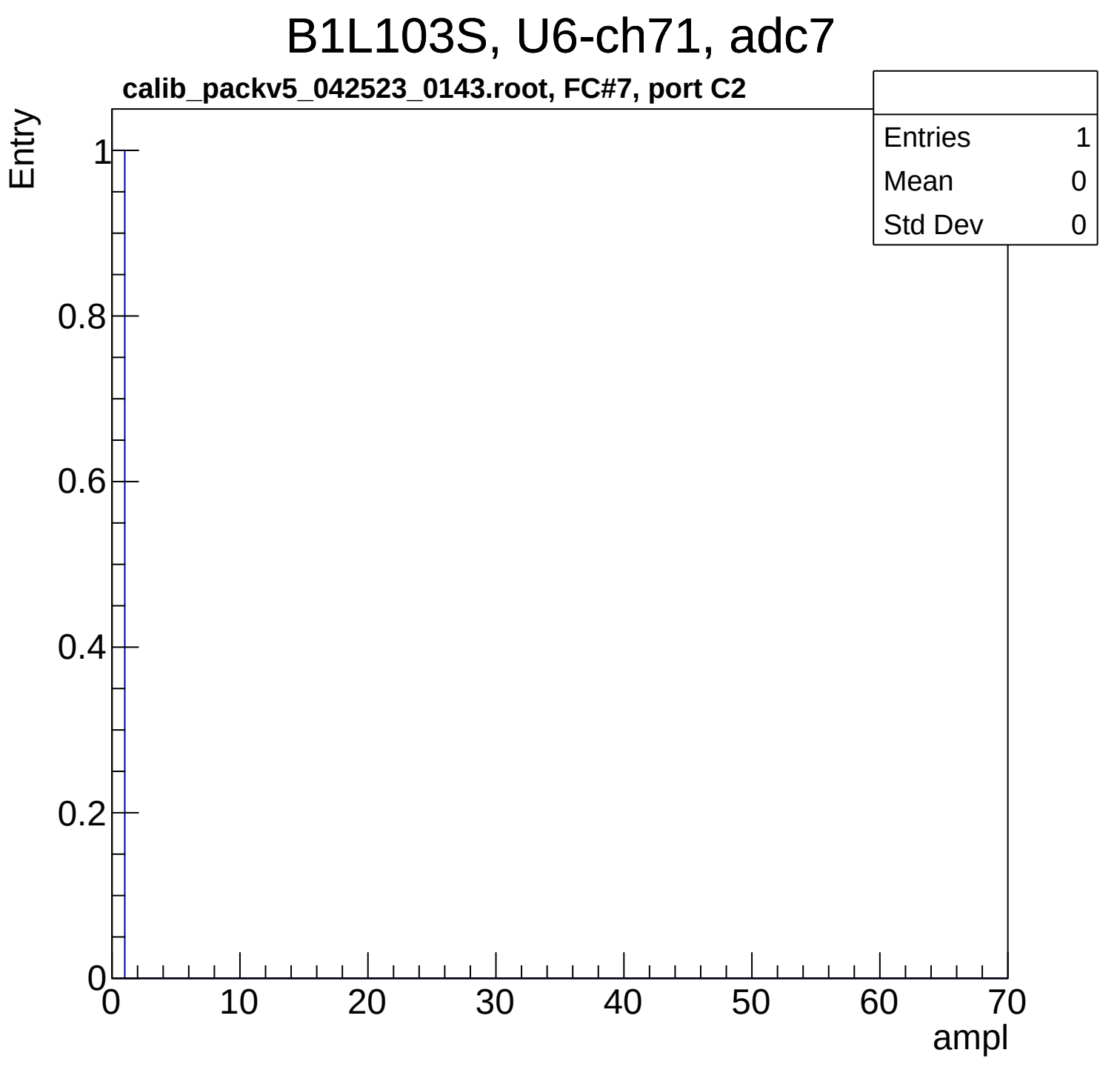
B1L103S, U6-ch71, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
20
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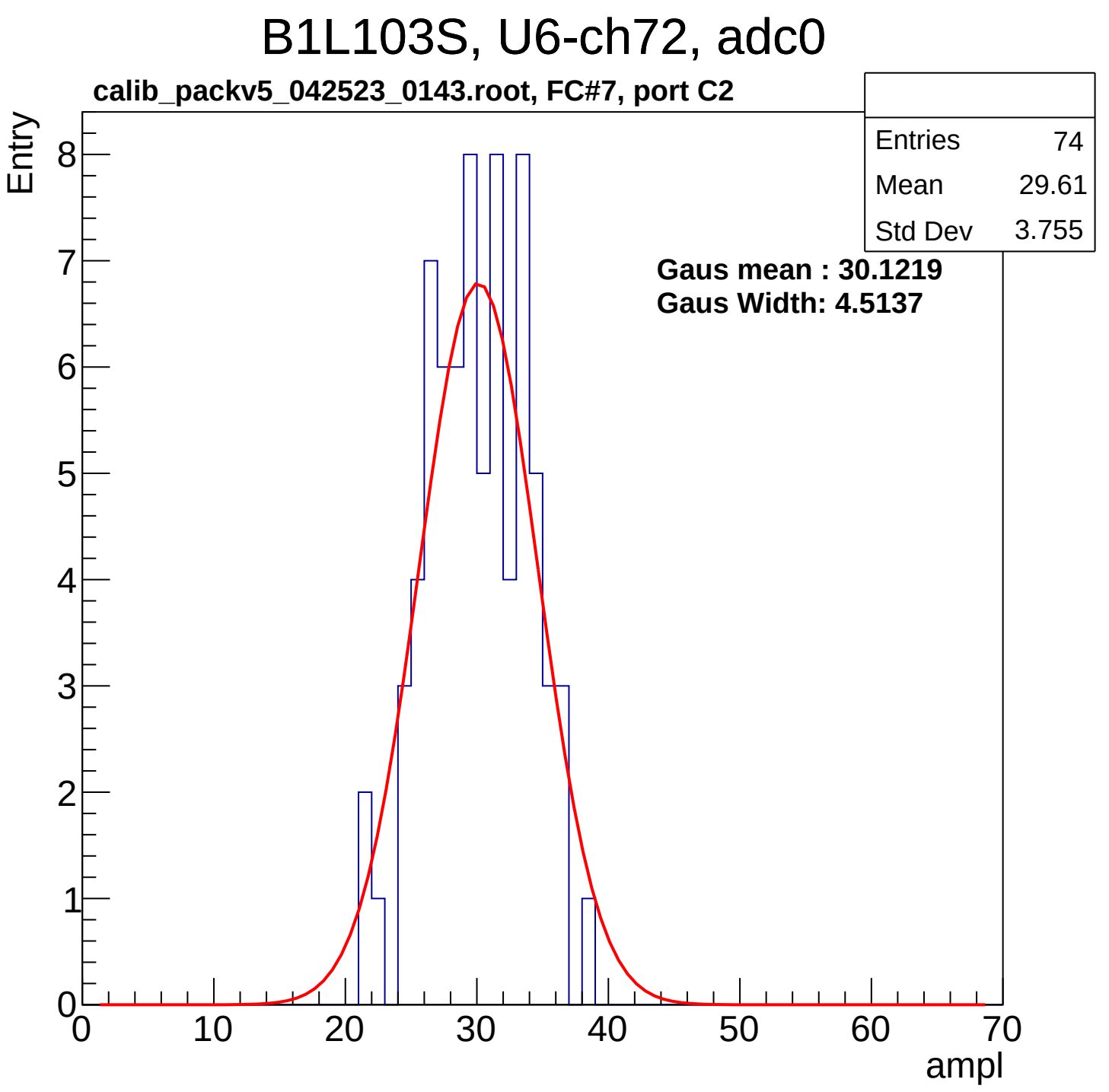
40
50
60
70
ampl


B1L103S, U6-ch72, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.61
Std Dev 3.755
Gaus mean : 30.1219
Gaus Width: 4.5137
Entry
ampl
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2
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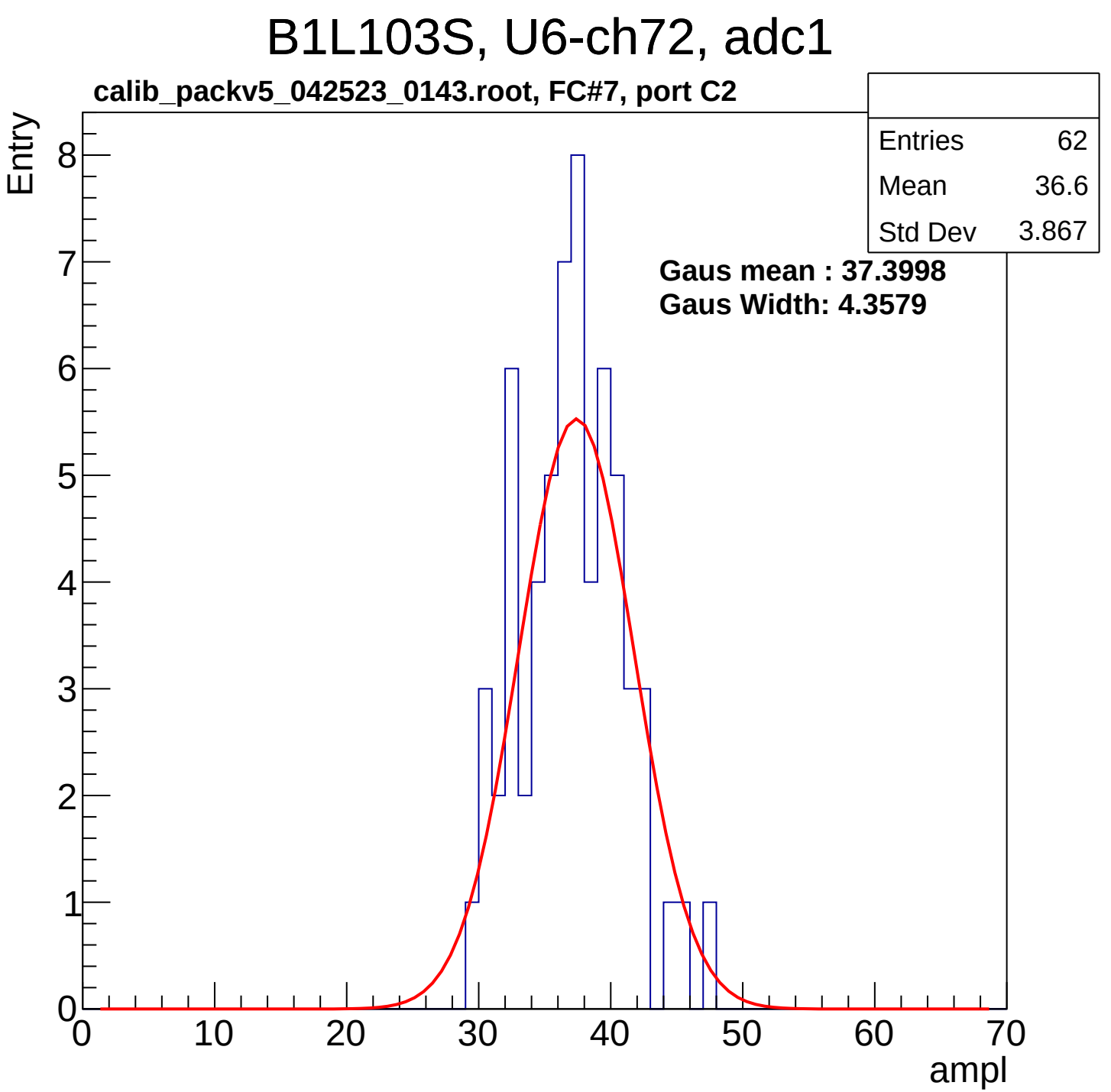
B1L103S, U6-ch72, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 36.6
Std Dev 3.867
Gaus mean : 37.3998
Gaus Width: 4.3579
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
30
40
50
60
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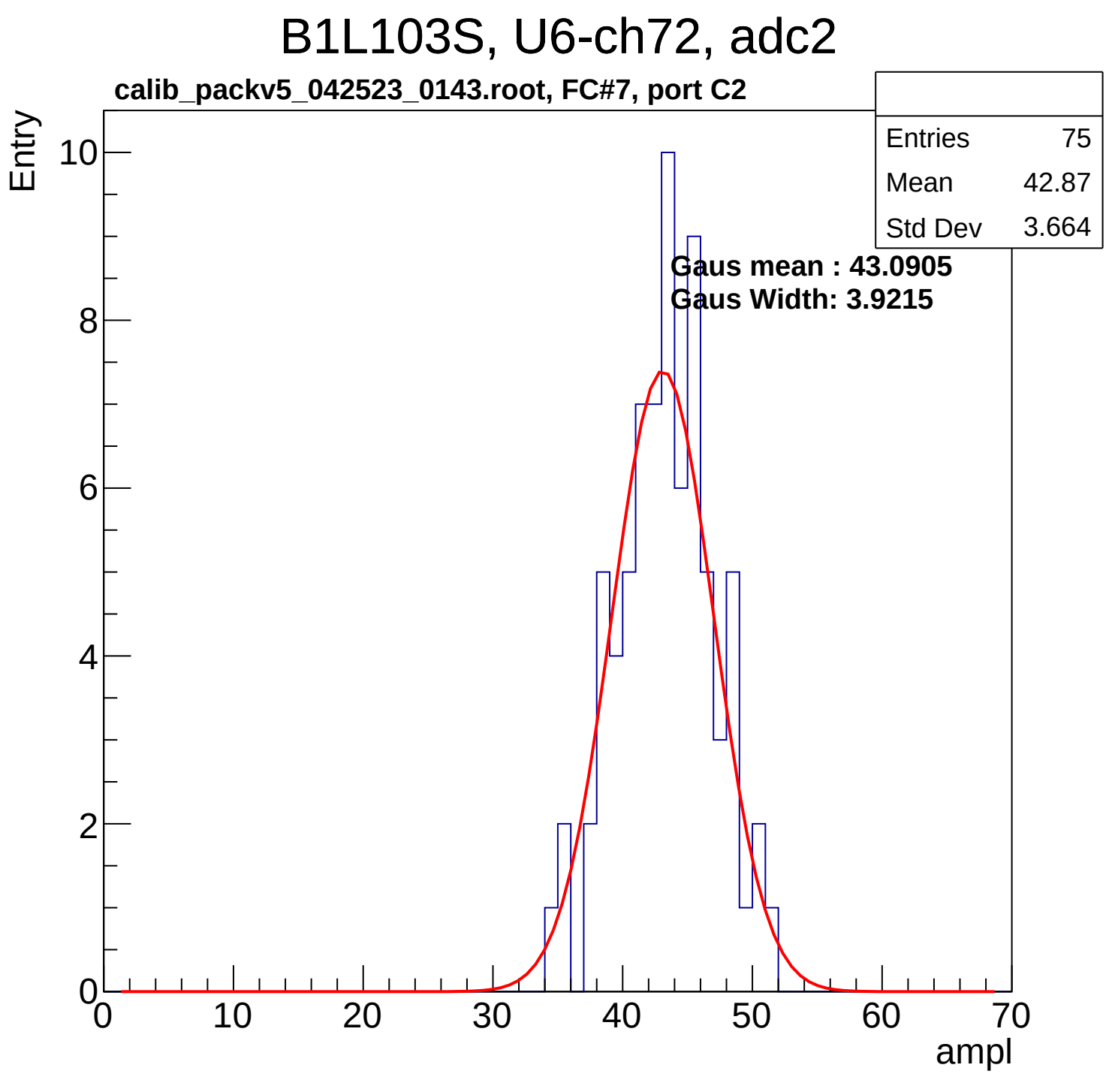
B1L103S, U6-ch72, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 42.87
Std Dev 3.664
Gaus mean : 43.0905
Gaus Width: 3.9215
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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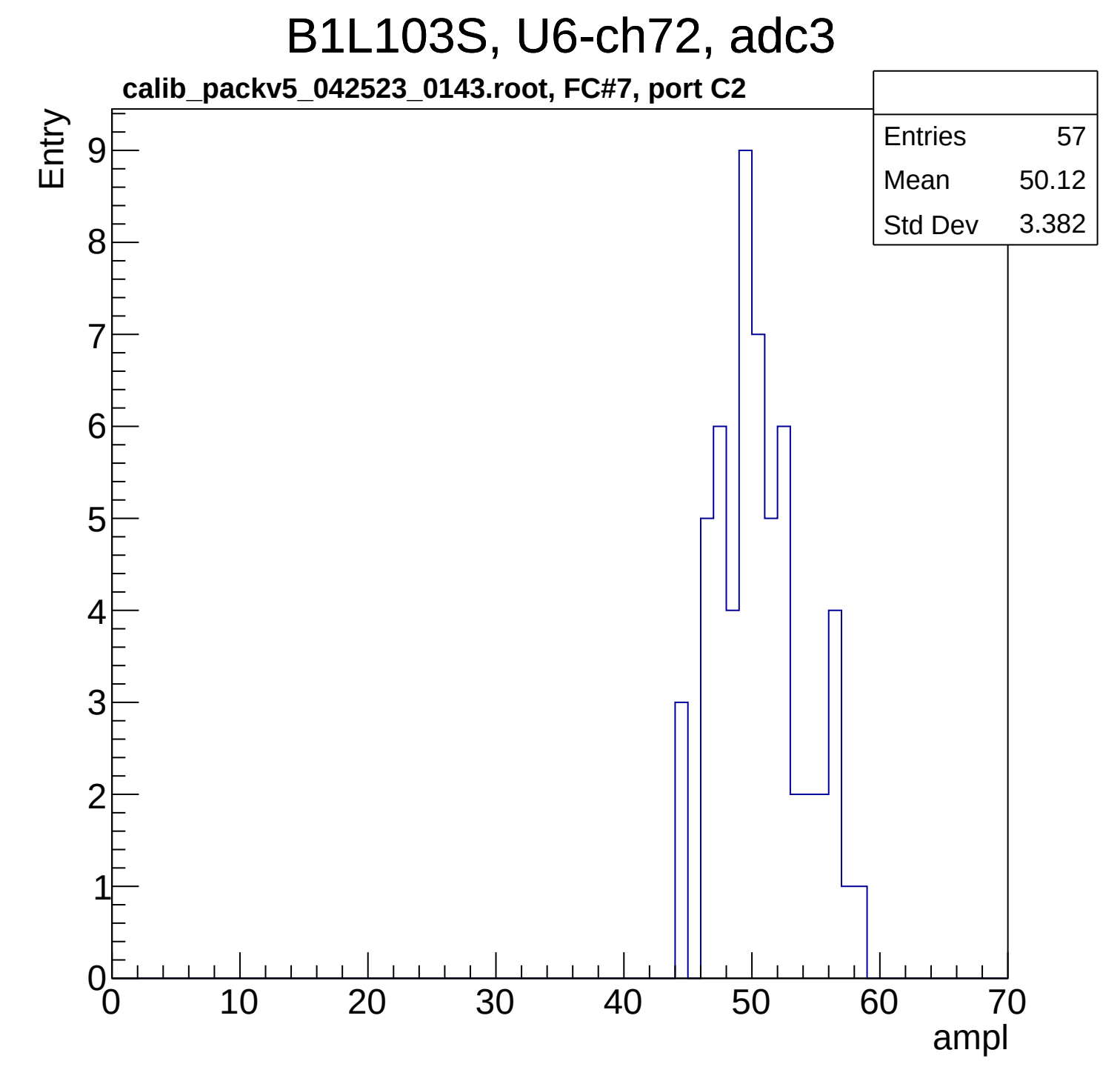
B1L103S, U6-ch72, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 50.12
Std Dev 3.382
Entry
ampl
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7
8
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0
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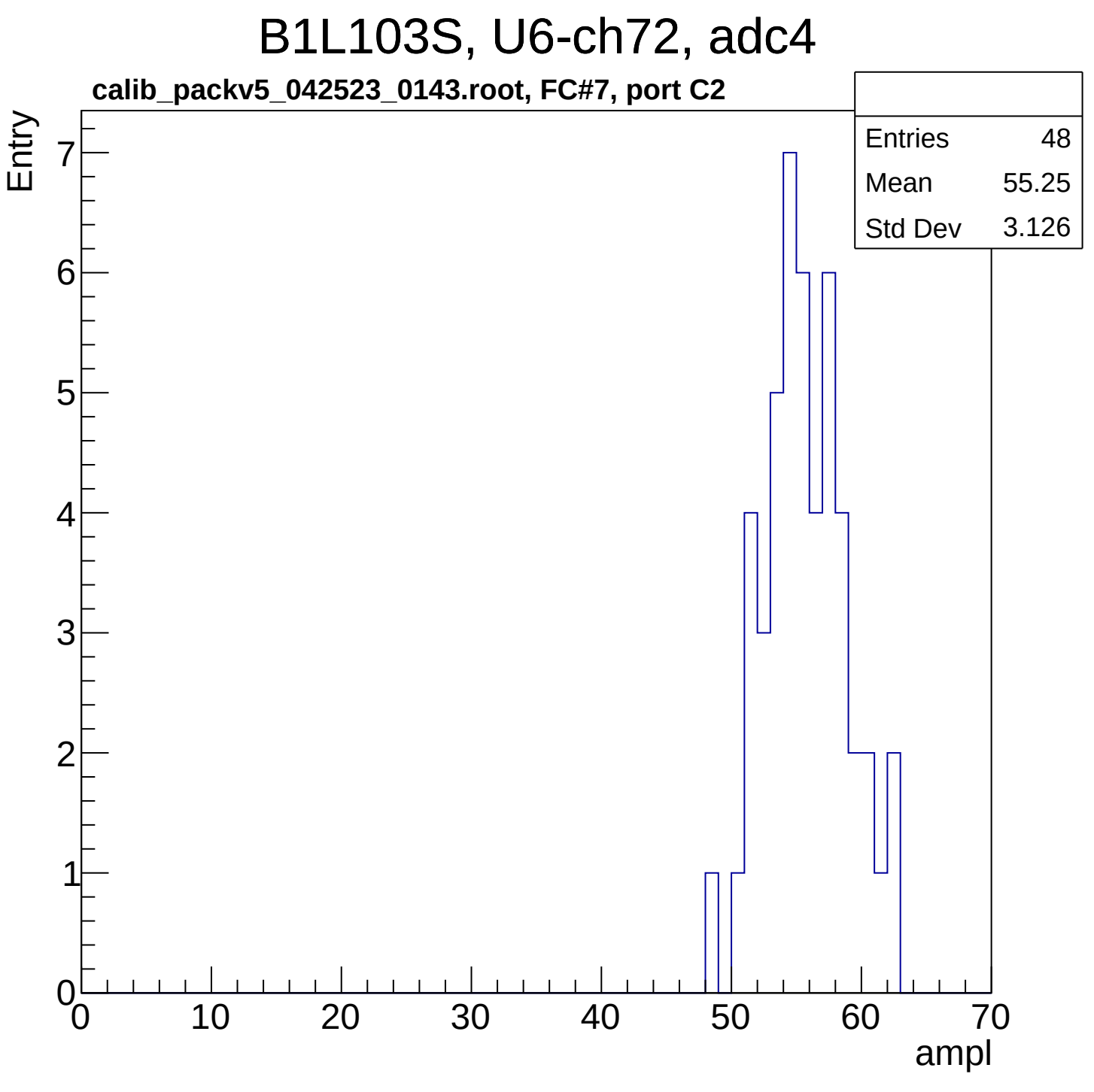
B1L103S, U6-ch72, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 55.25
Std Dev 3.126
Entry
ampl
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0
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60
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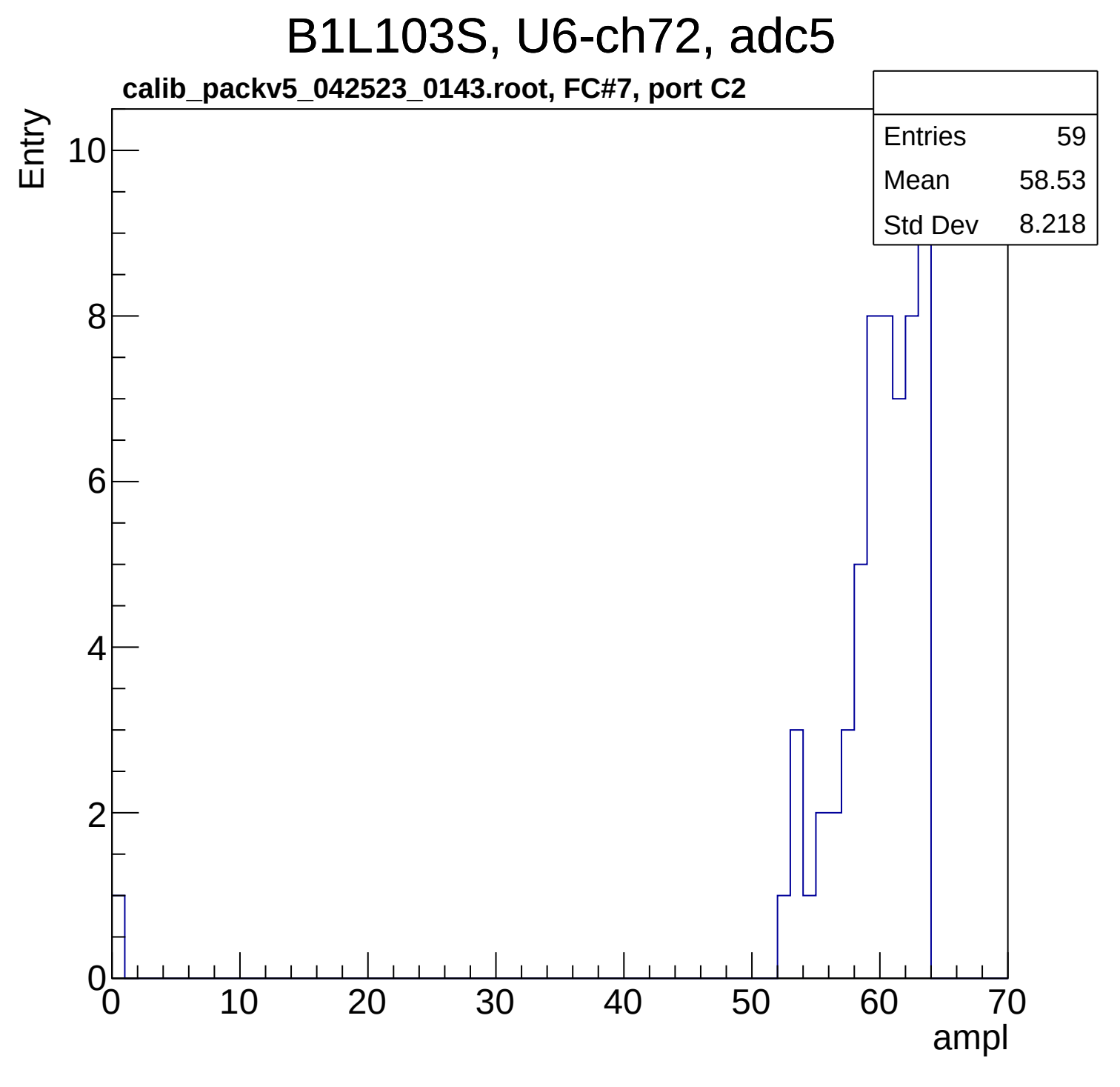
B1L103S, U6-ch72, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.53
Std Dev 8.218
Entry
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10
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10
20
30
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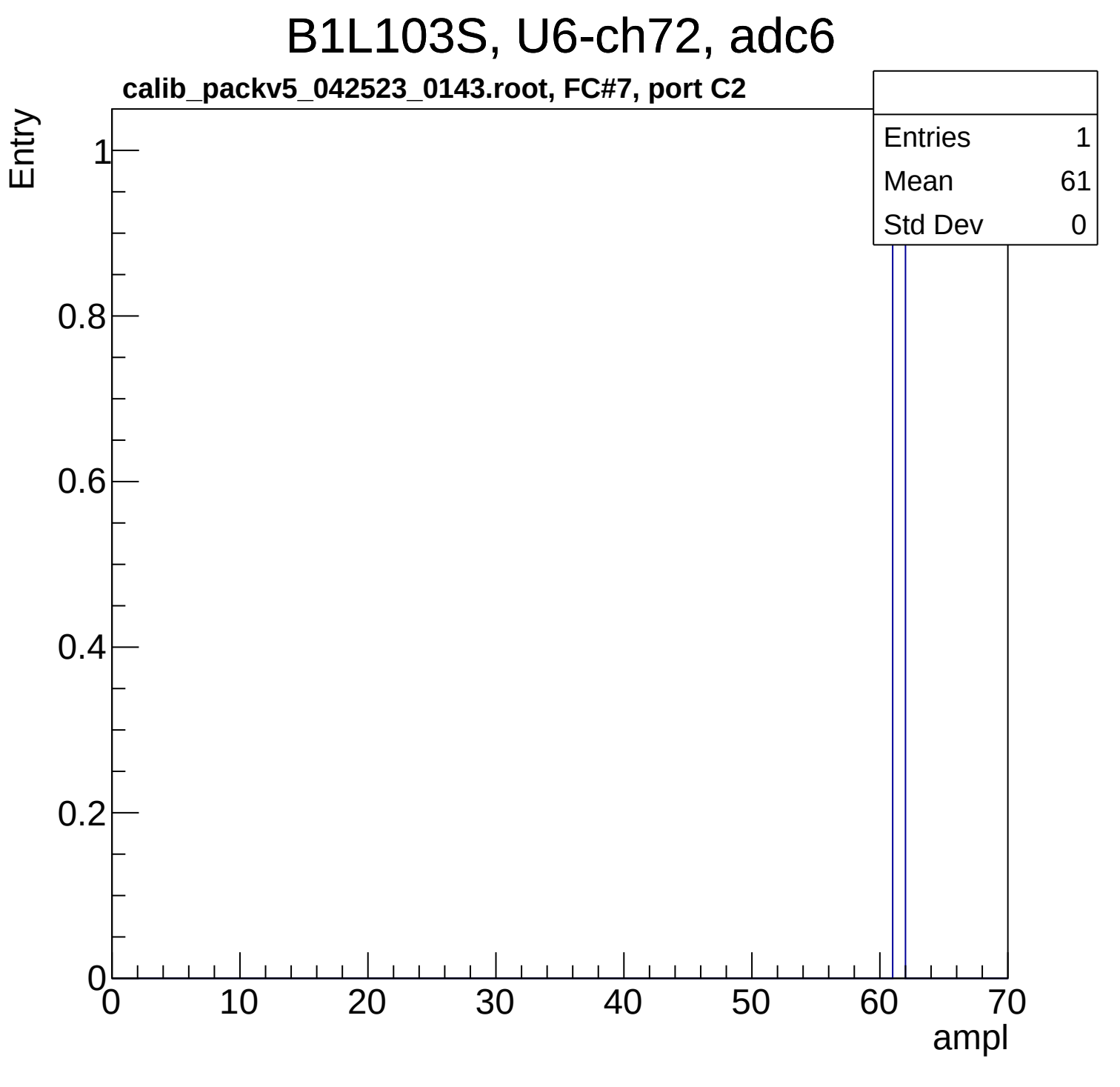
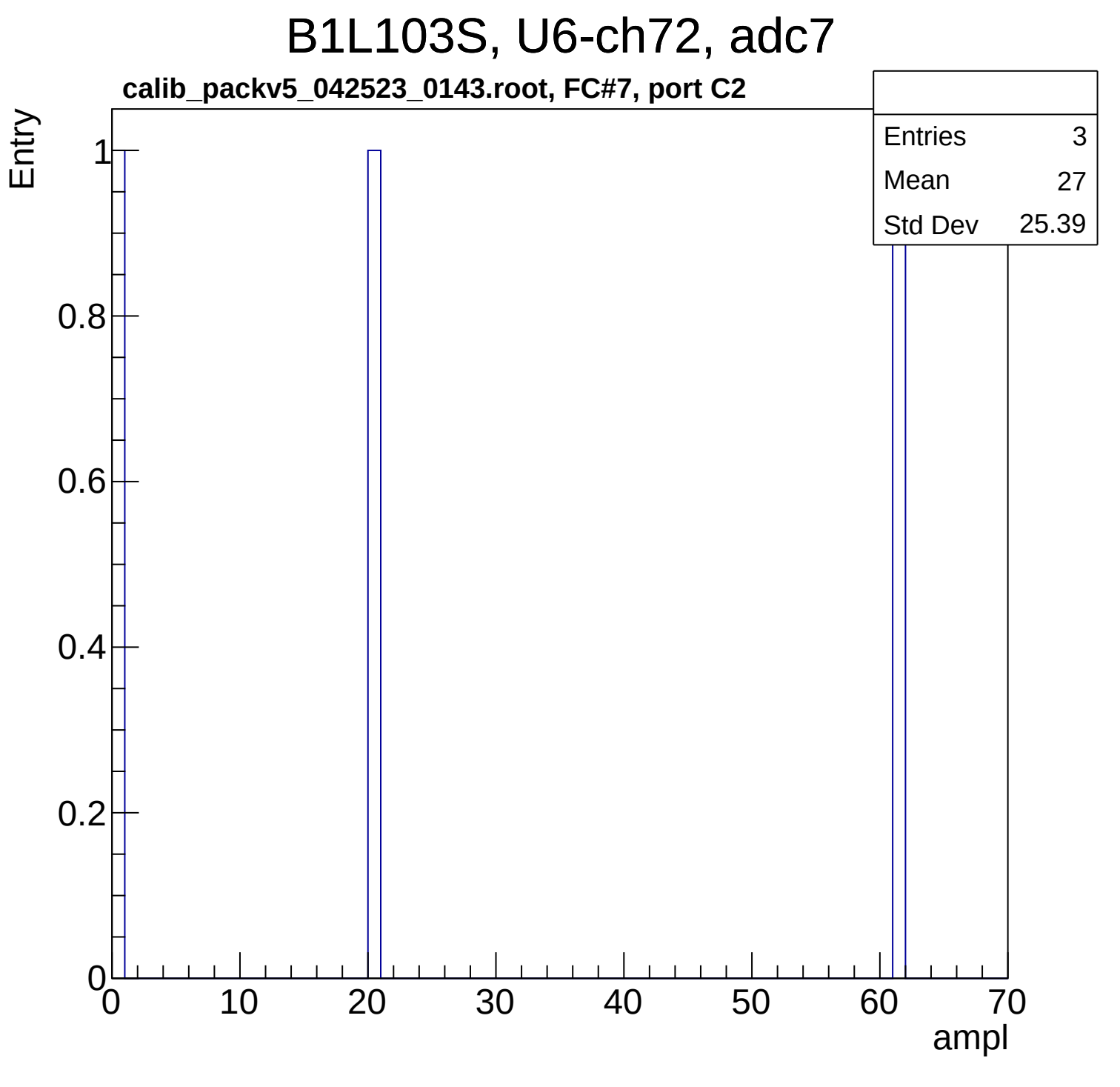
50
60
70
ampl


B1L103S, U6-ch72, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch72, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 27
Std Dev 25.39
Entry
ampl
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0.4
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0
10
20
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40
50
60
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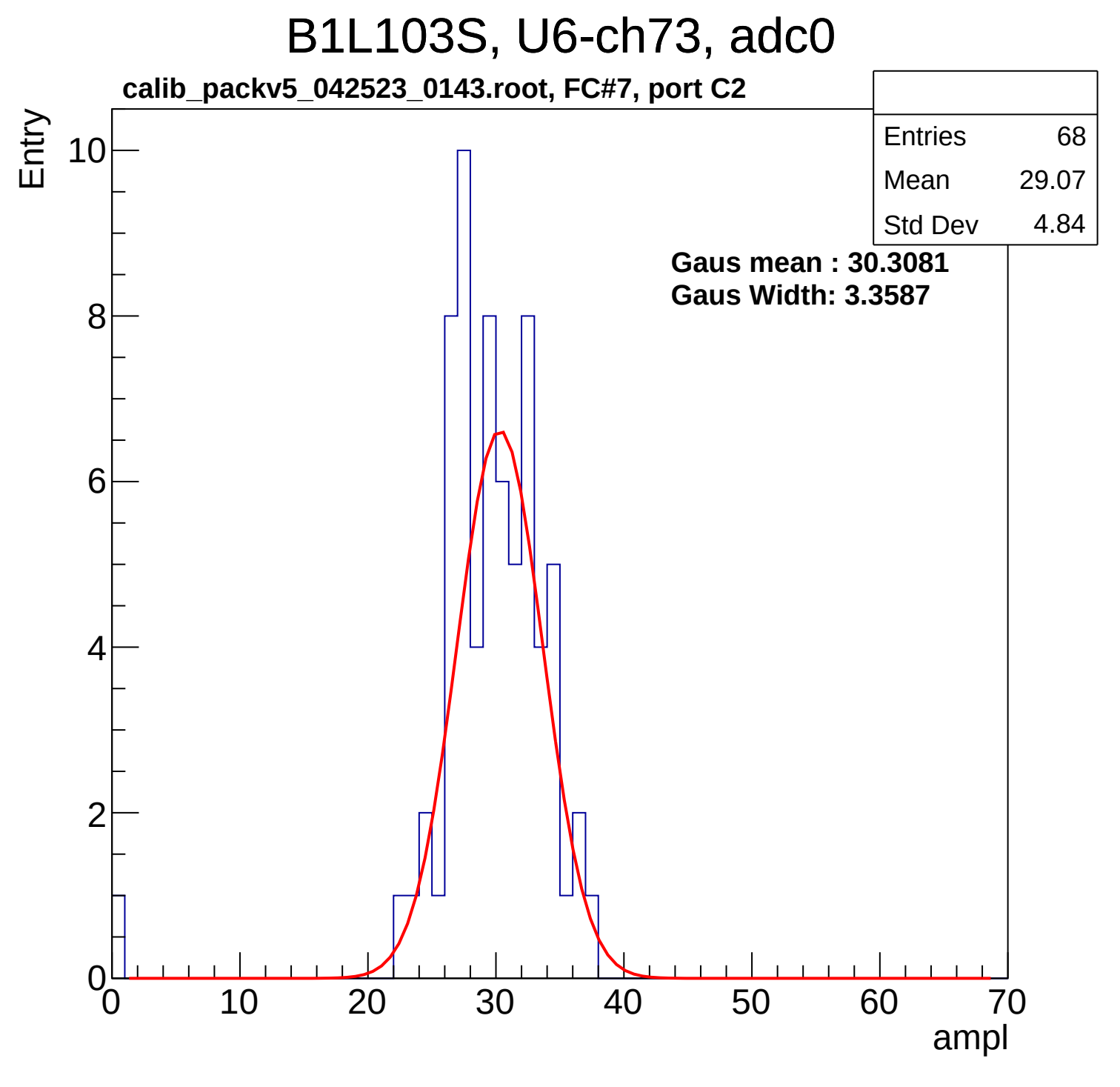
B1L103S, U6-ch73, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.07
Std Dev 4.84
Gaus mean : 30.3081
Gaus Width: 3.3587
Entry
ampl
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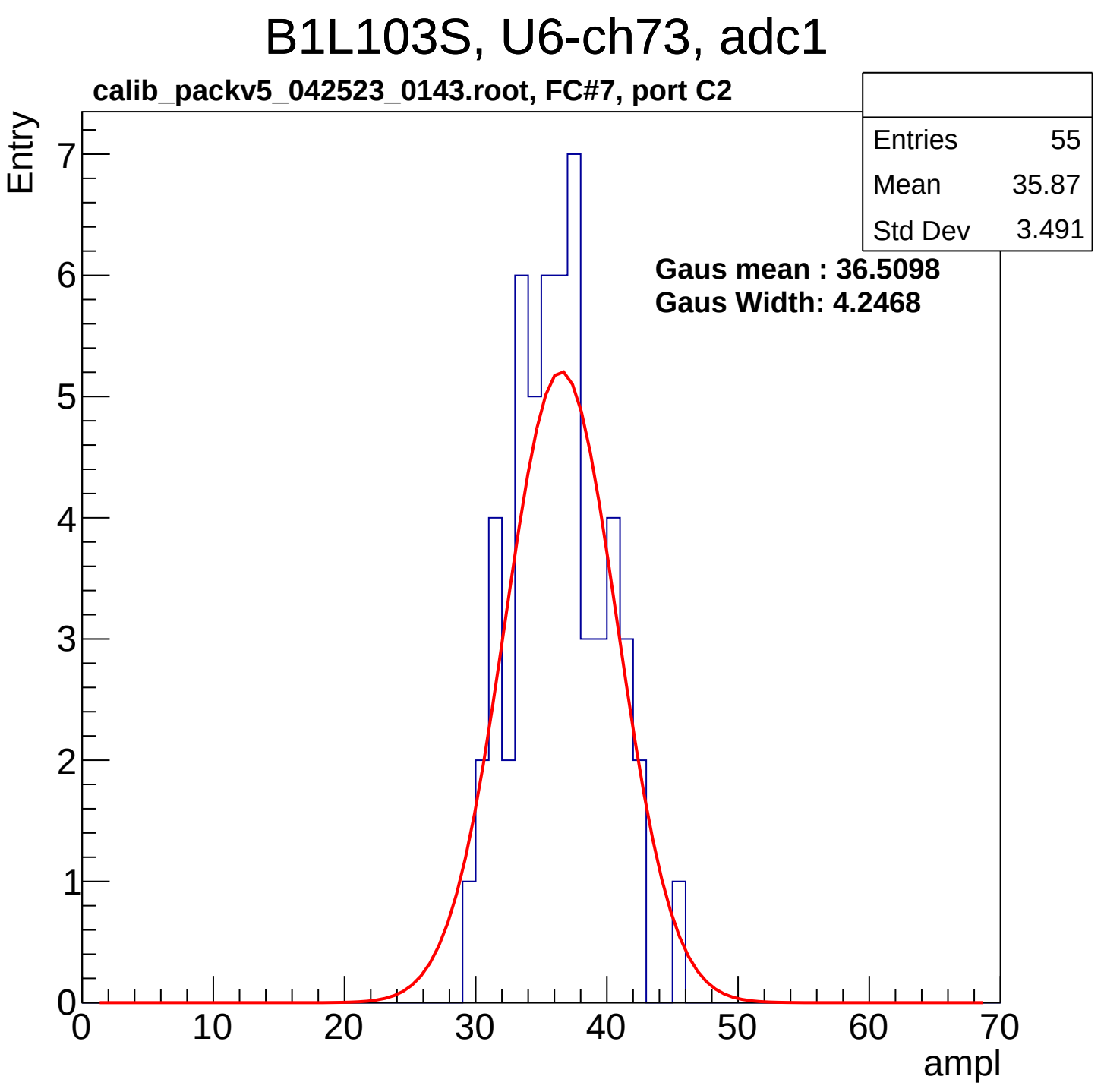
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch73, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 35.87
Std Dev 3.491
Gaus mean : 36.5098
Gaus Width: 4.2468
Entry
ampl
0
1
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3
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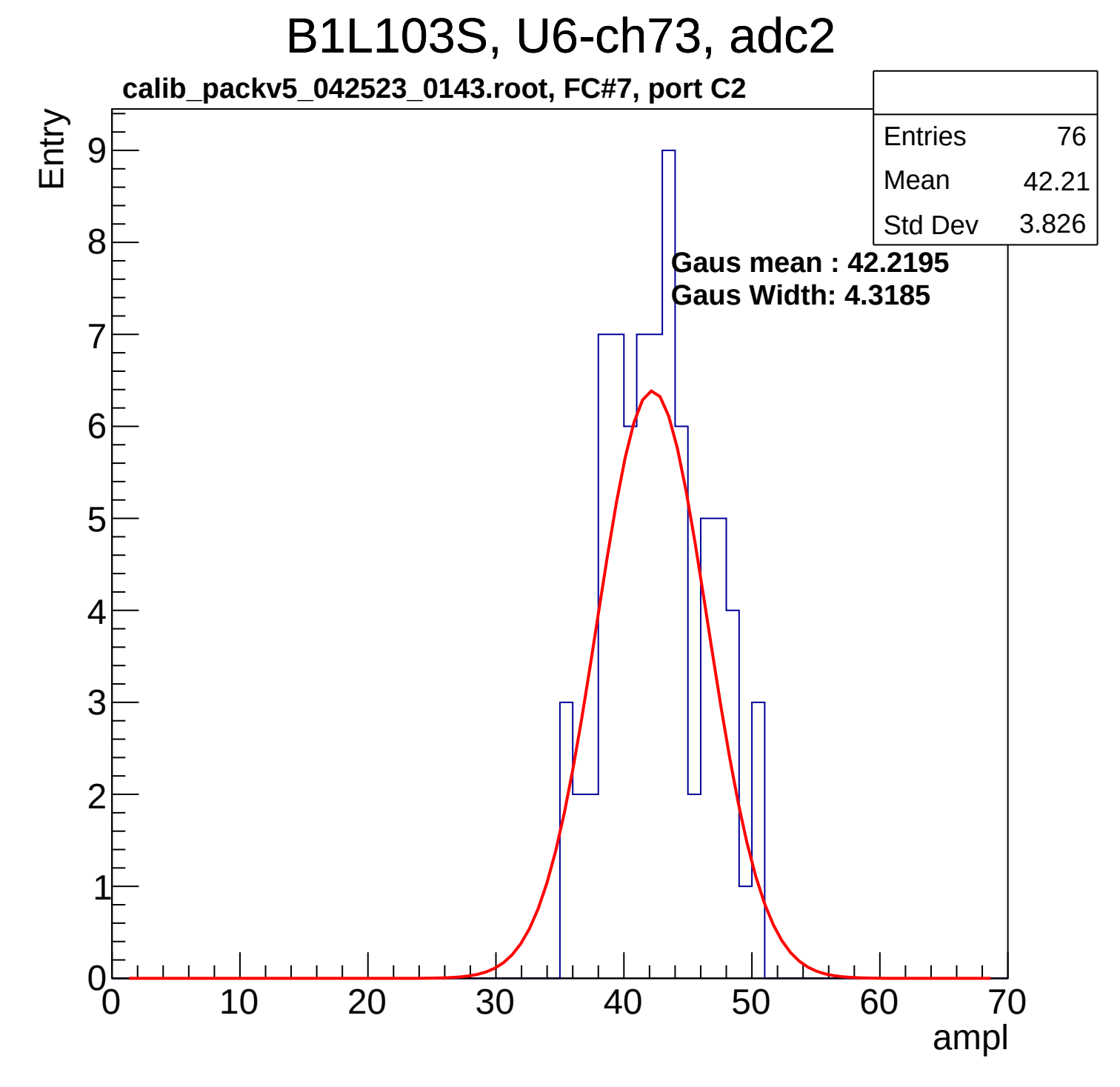
B1L103S, U6-ch73, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 42.21
Std Dev 3.826
Gaus mean : 42.2195
Gaus Width: 4.3185
Entry
ampl
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3
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5
6
7
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0
10
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40
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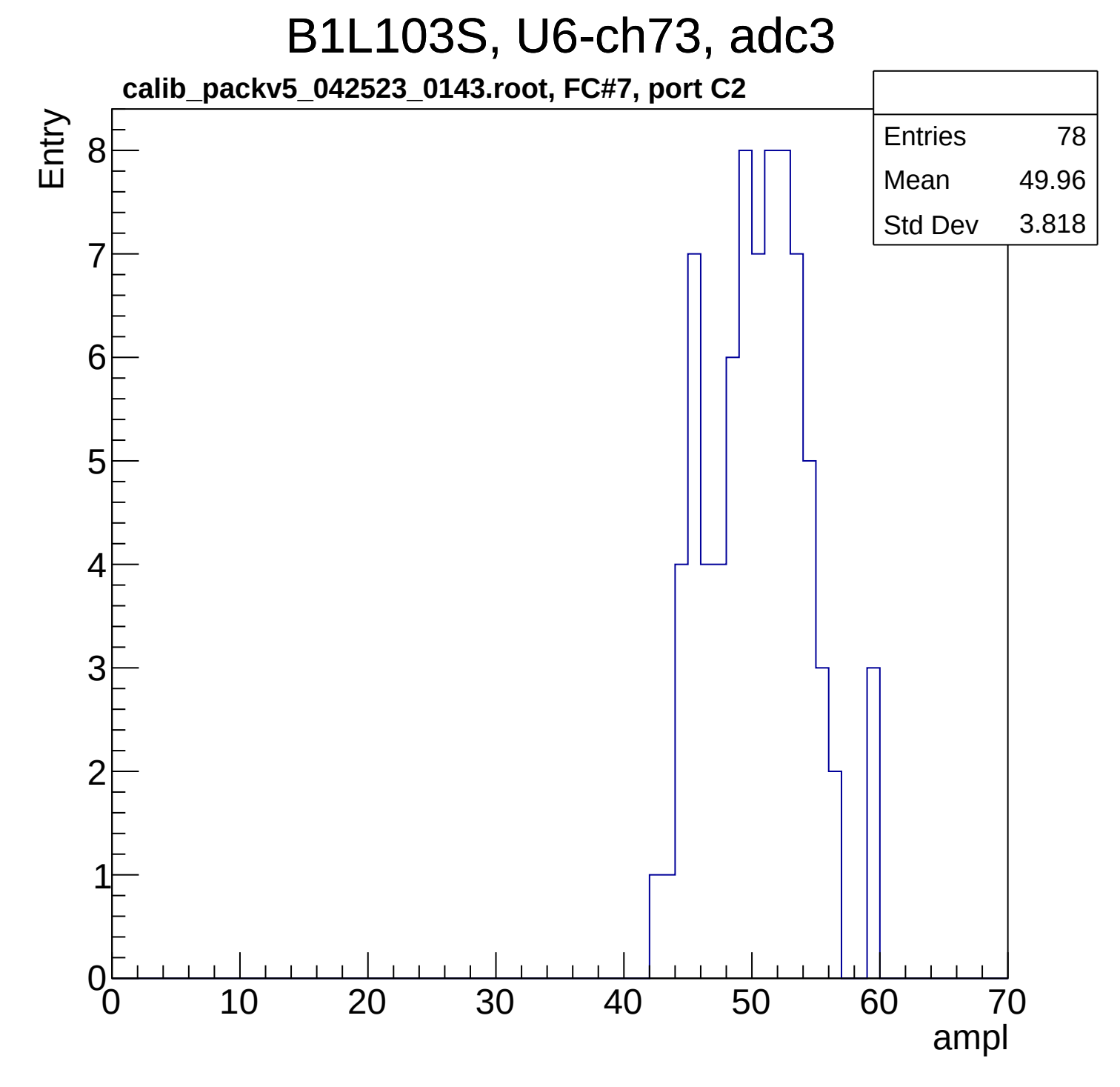
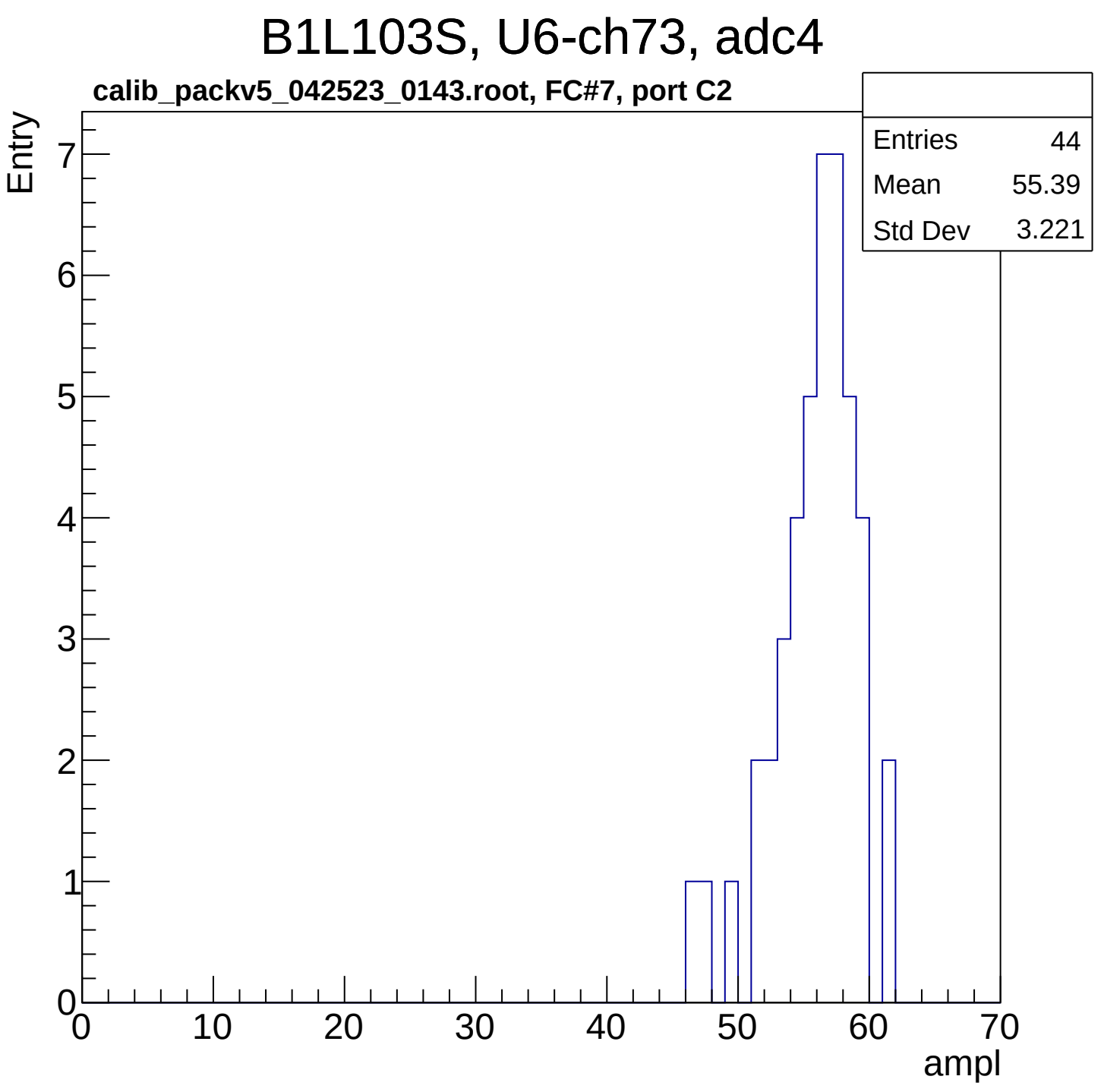

B1L103S, U6-ch73, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 49.96
Std Dev 3.818
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch73, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 55.39
Std Dev 3.221
Entry
ampl
0
1
2
3
4
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6
7
0
10
20
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40
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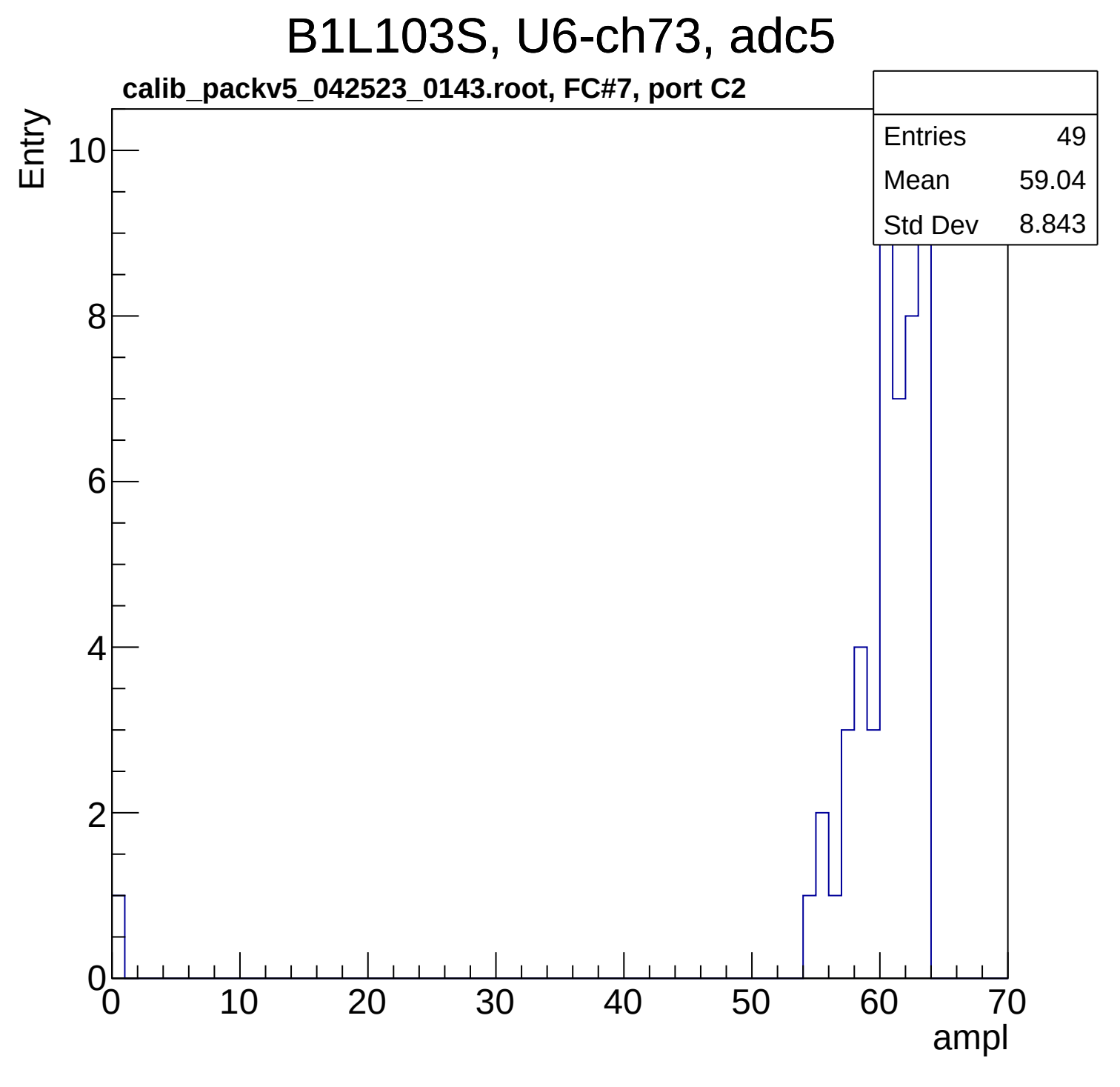
B1L103S, U6-ch73, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.04
Std Dev 8.843
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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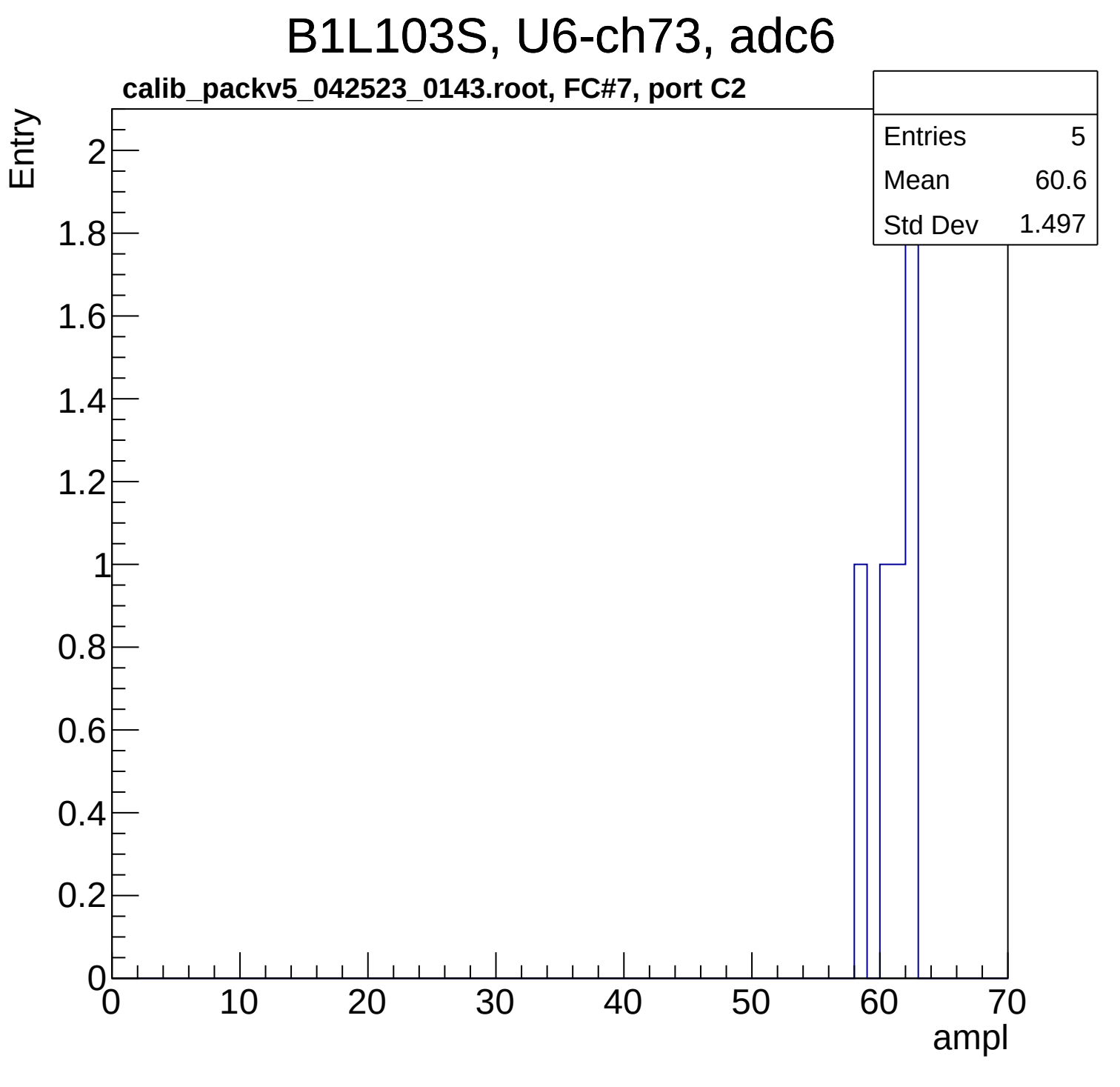
B1L103S, U6-ch73, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 60.6
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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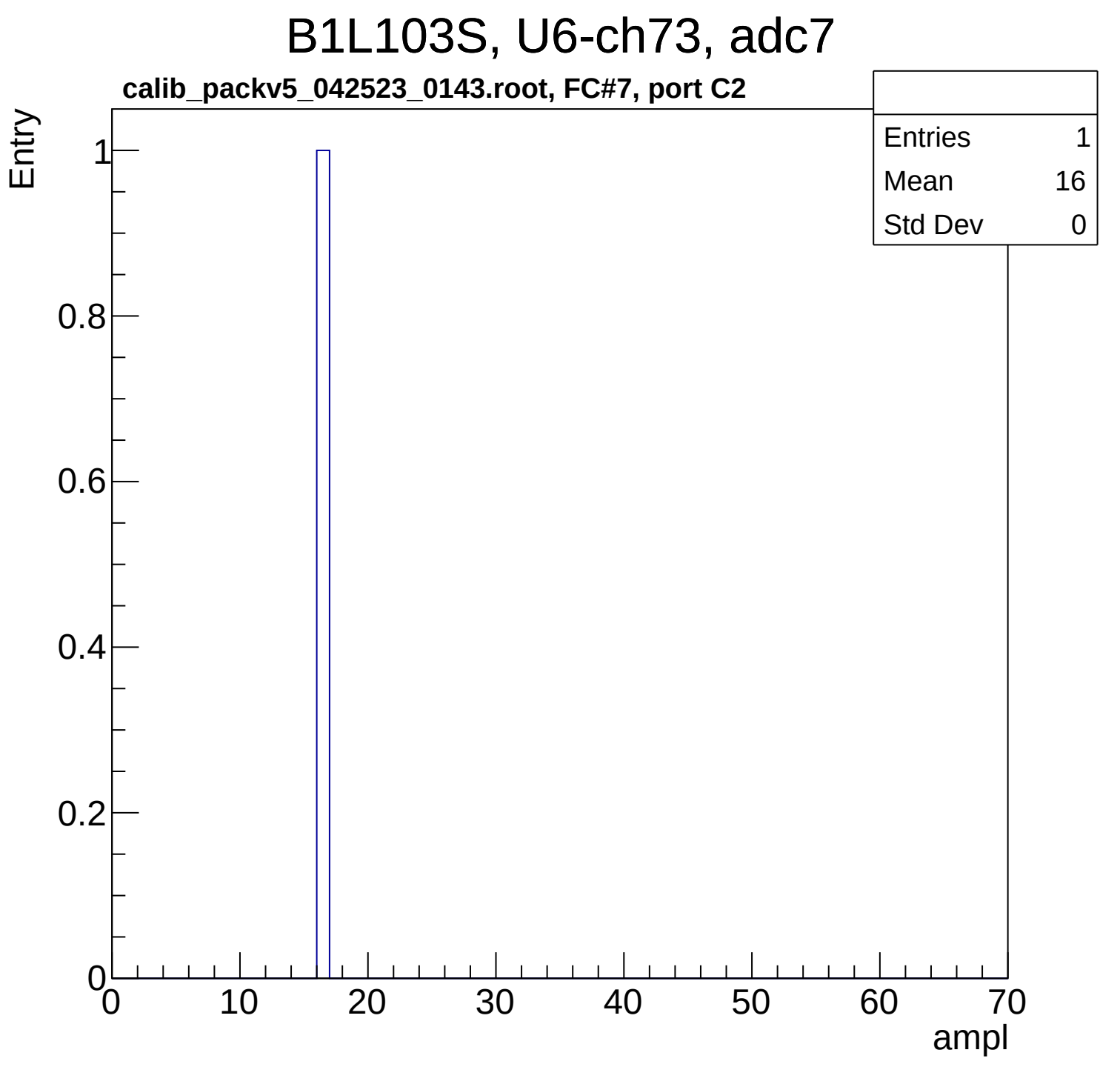
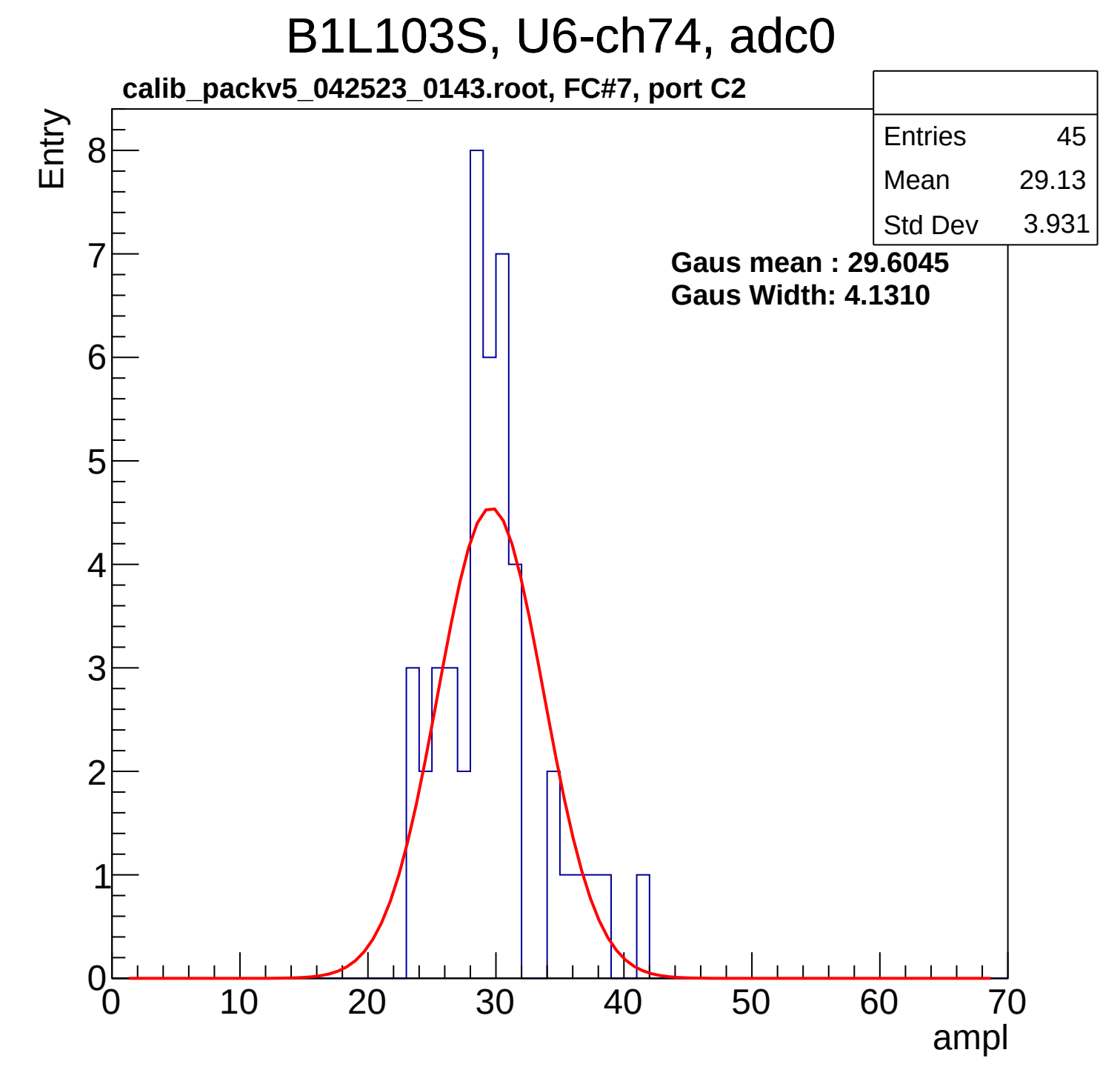
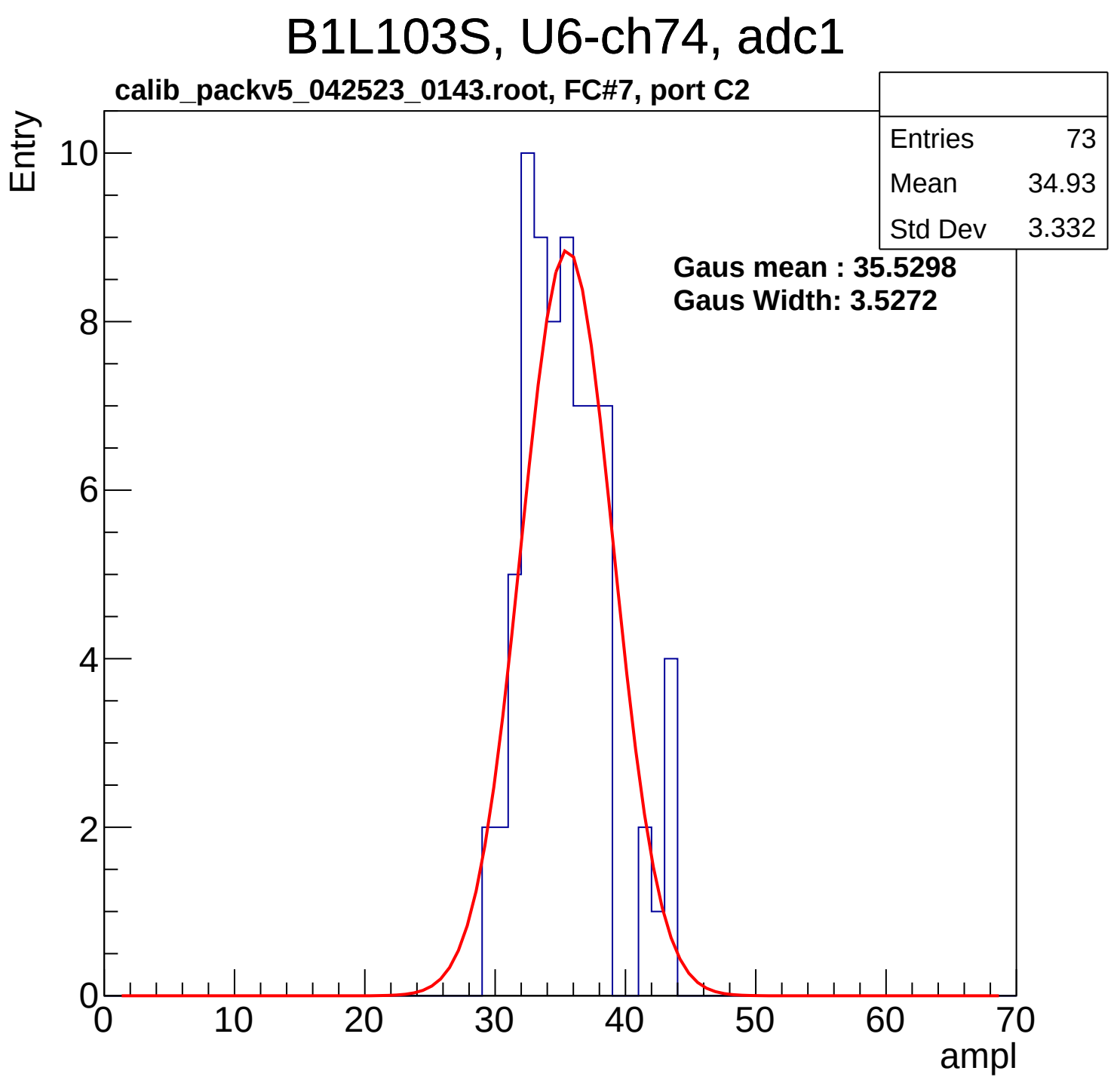
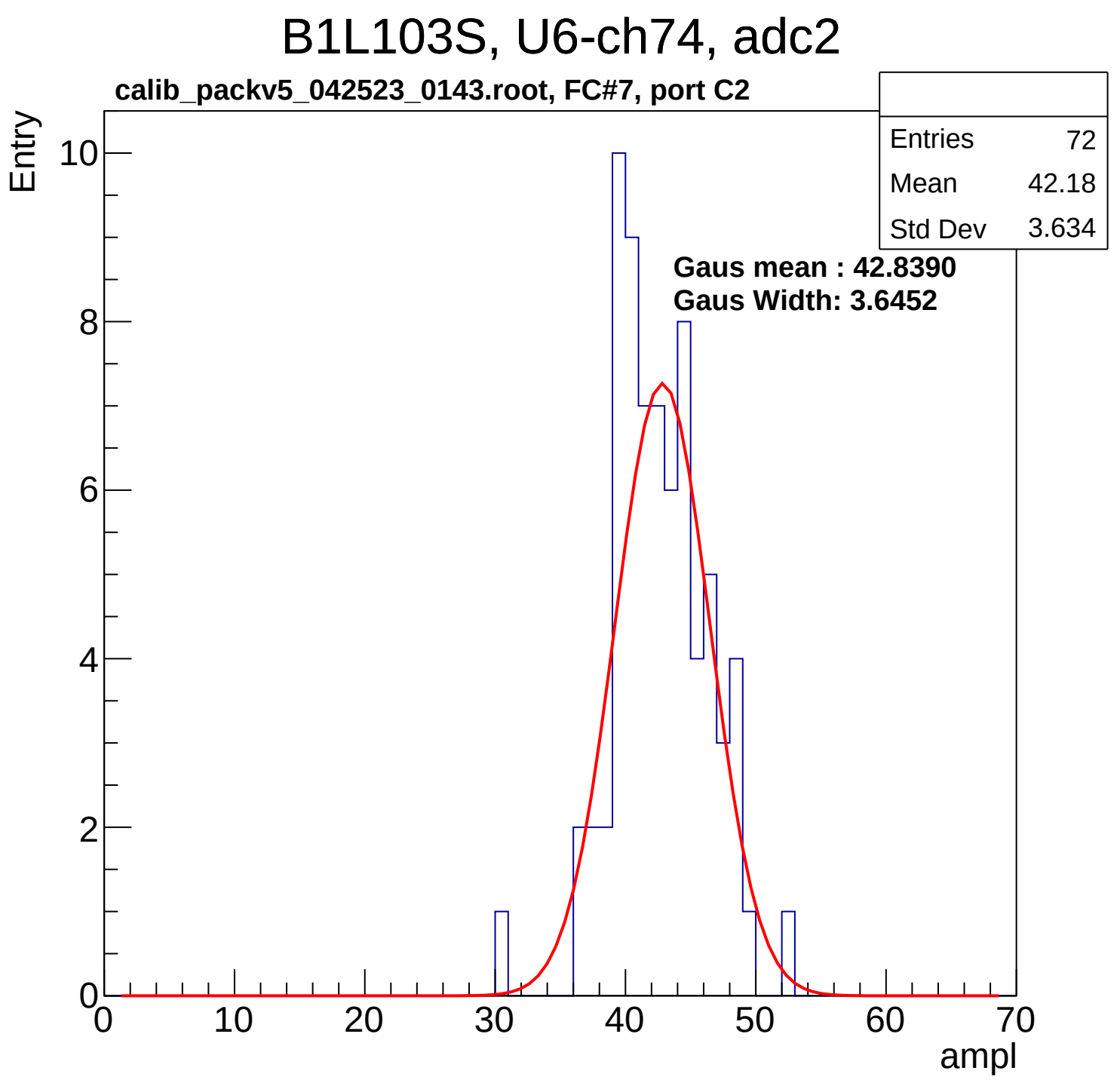

B1L103S, U6-ch73, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 16
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch74, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 29.13
Std Dev 3.931
Gaus mean : 29.6045
Gaus Width: 4.1310
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch74, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 34.93
Std Dev 3.332
Gaus mean : 35.5298
Gaus Width: 3.5272
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch74, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 42.18
Std Dev 3.634
Gaus mean : 42.8390
Gaus Width: 3.6452
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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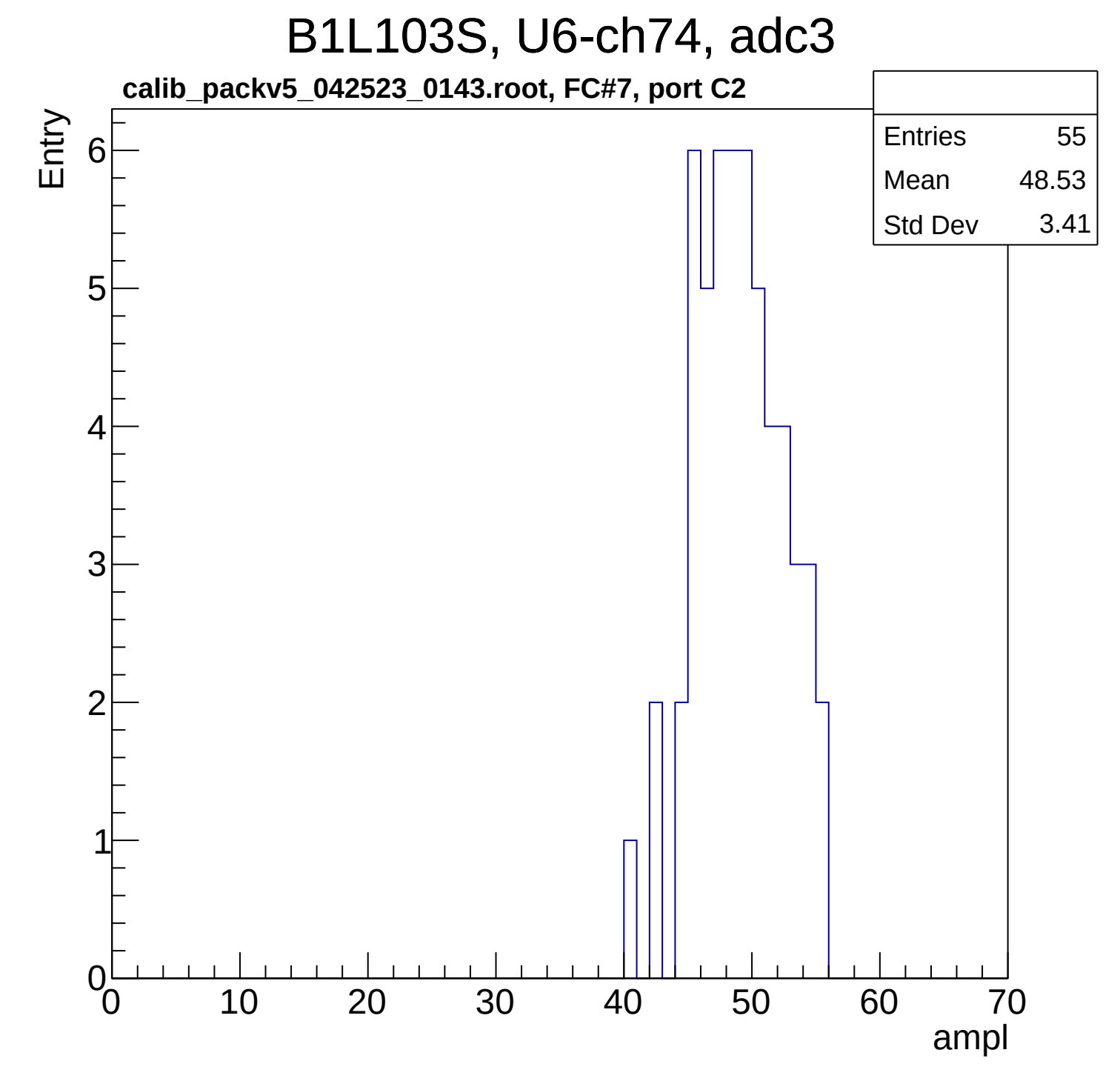
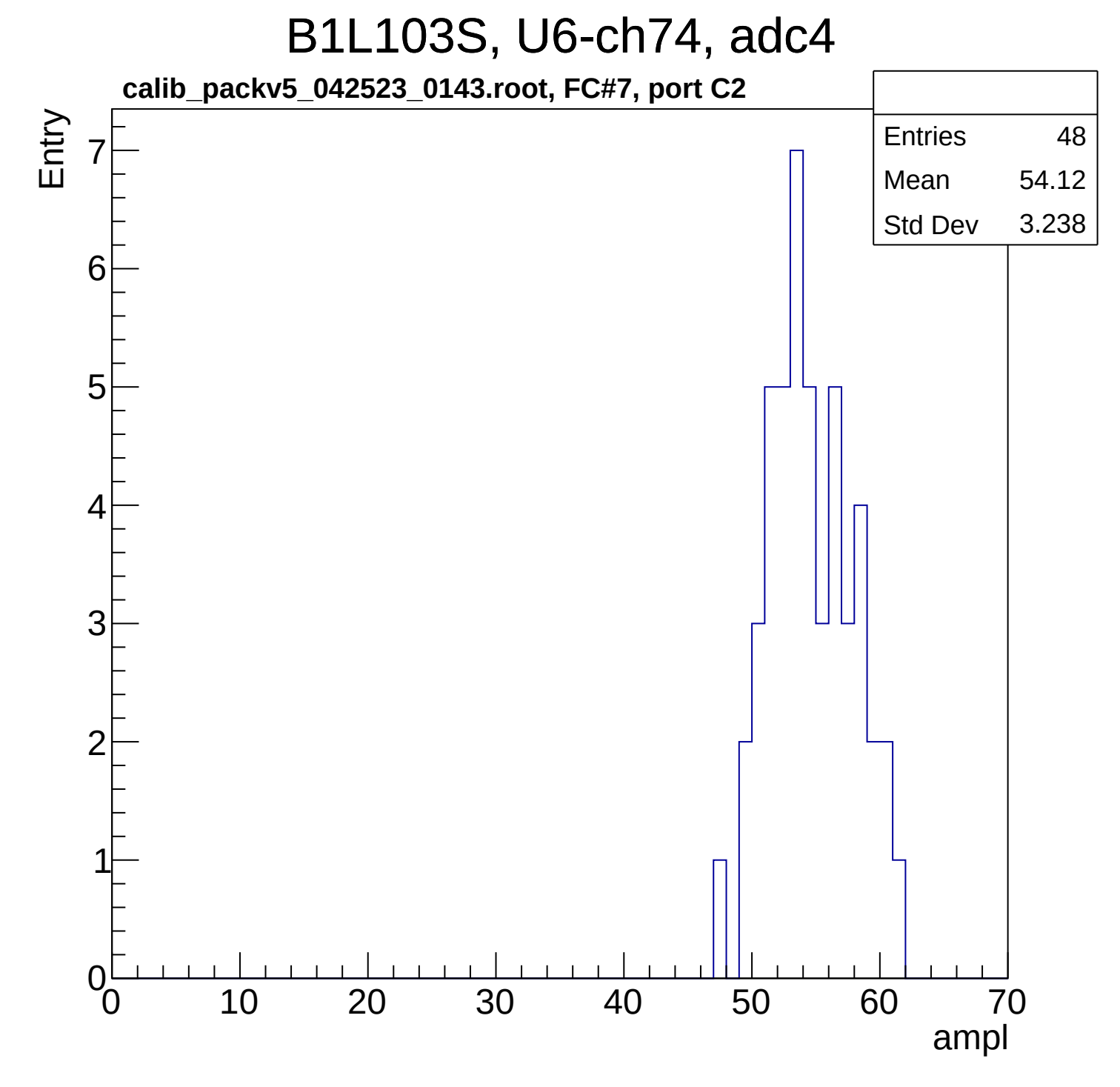
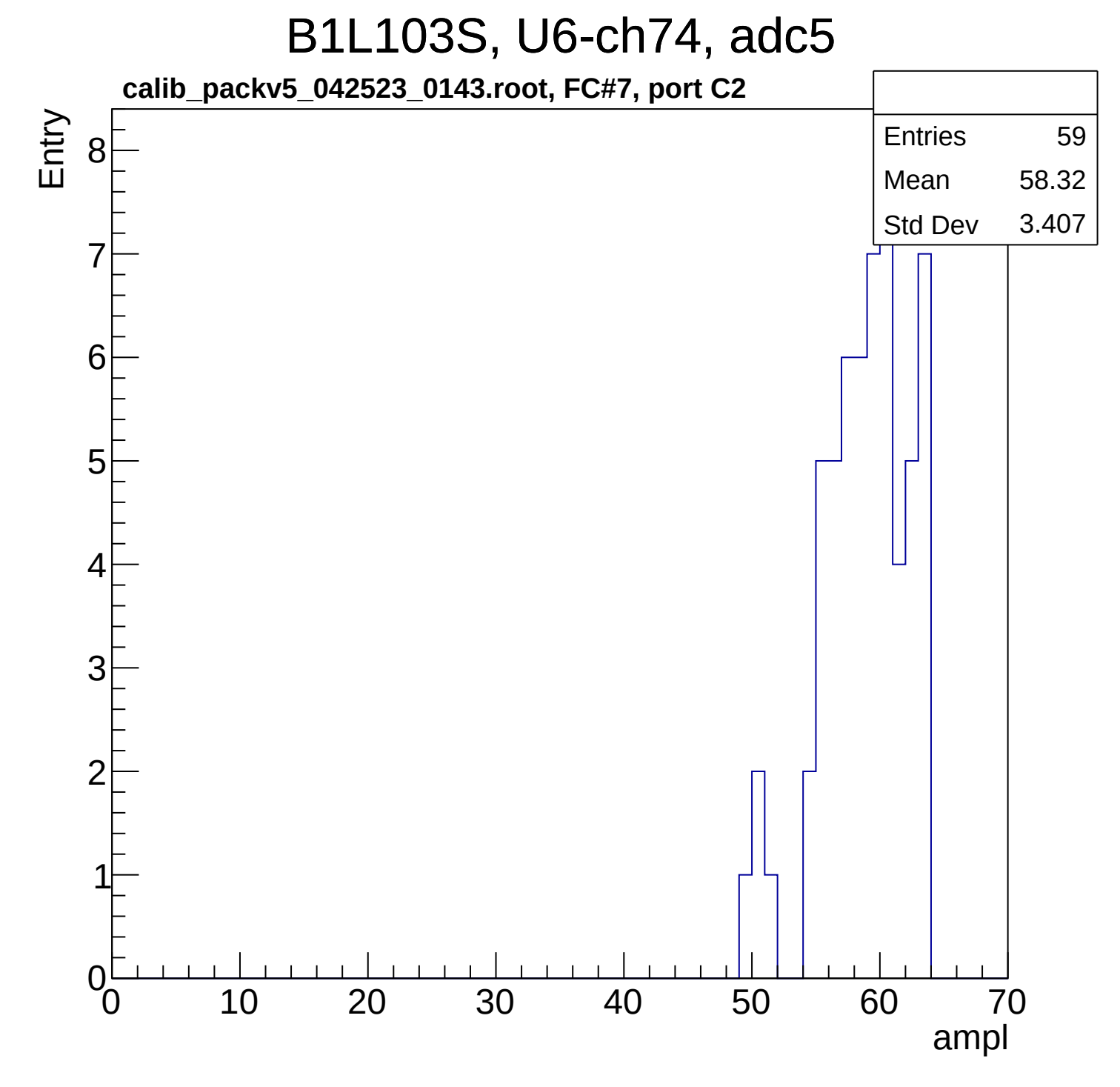
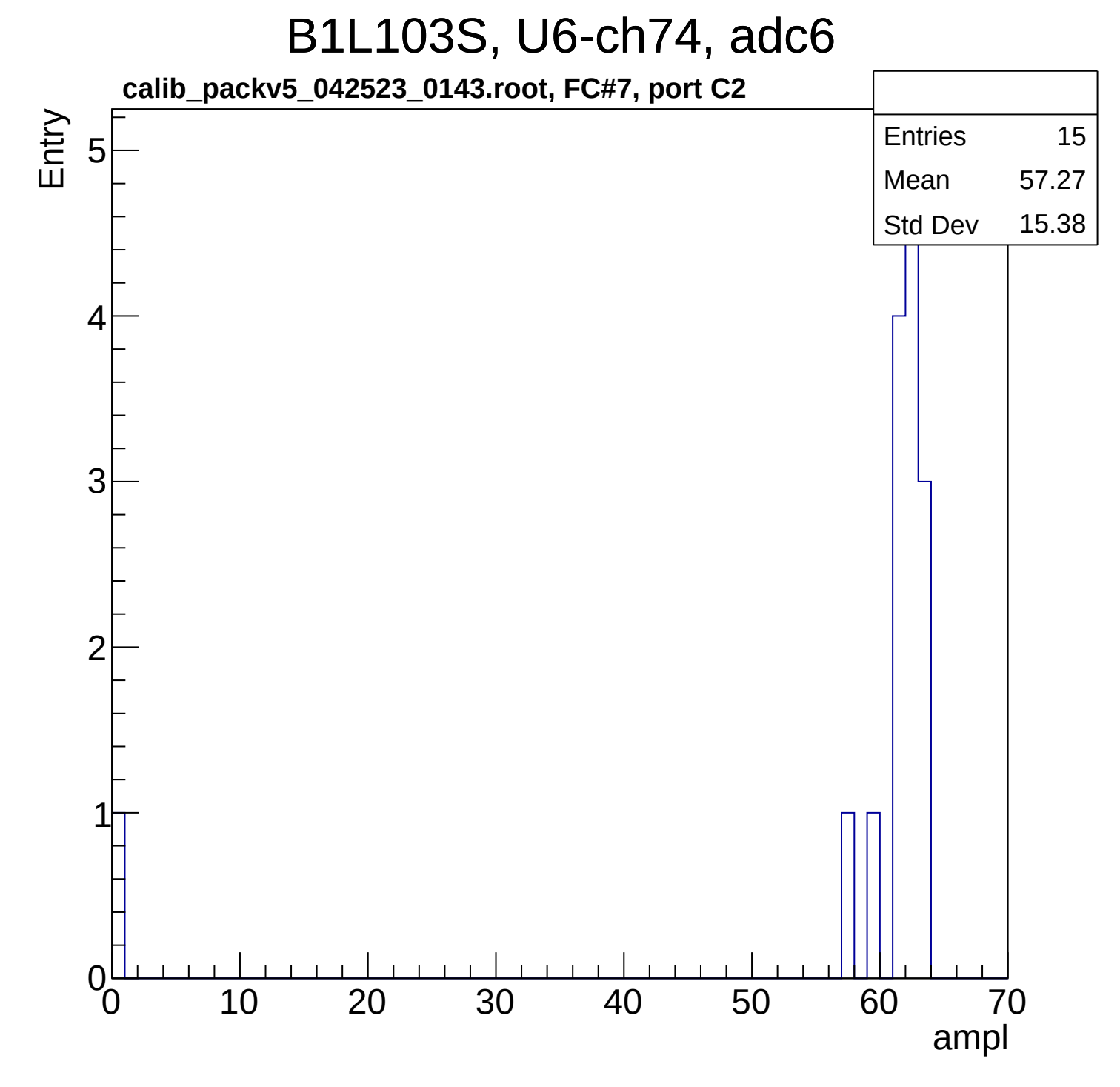
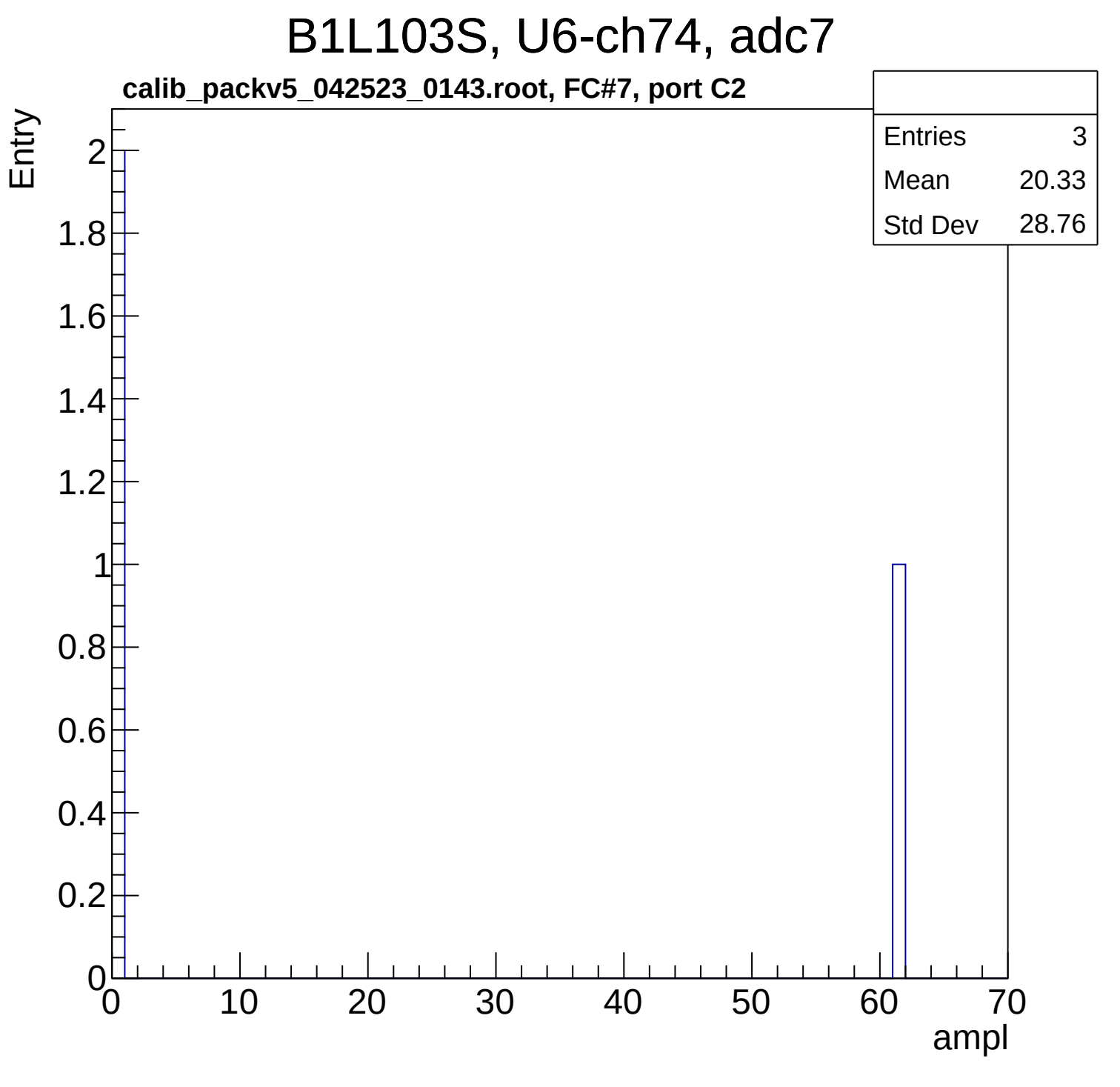

B1L103S, U6-ch74, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 48.53
Std Dev 3.41
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch74, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.12
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch74, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.32
Std Dev 3.407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch74, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 57.27
Std Dev 15.38
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch74, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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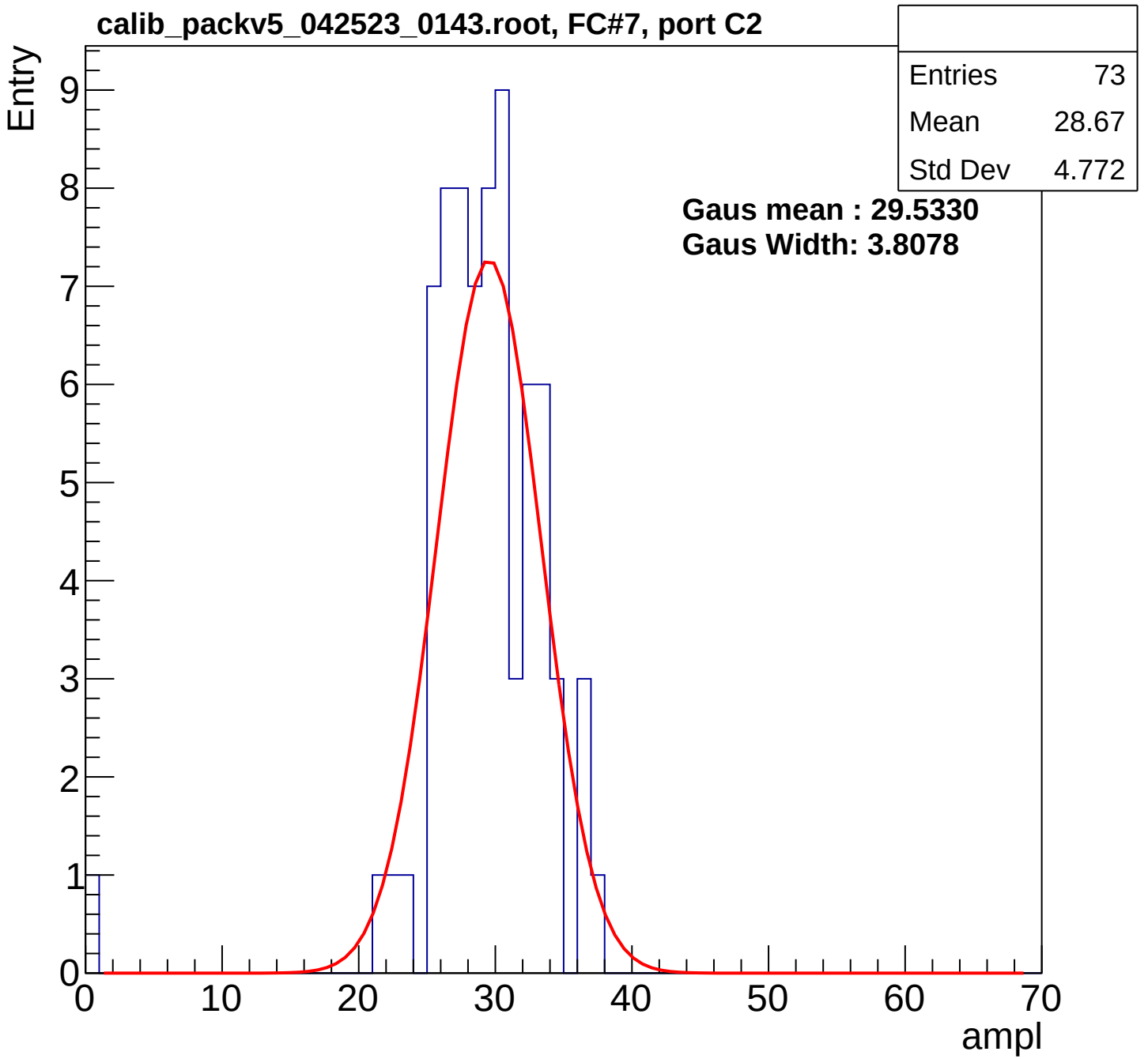
B1L103S, U6-ch75, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 28.67
Std Dev 4.772
Gaus mean : 29.5330
Gaus Width: 3.8078
Entry
ampl
0
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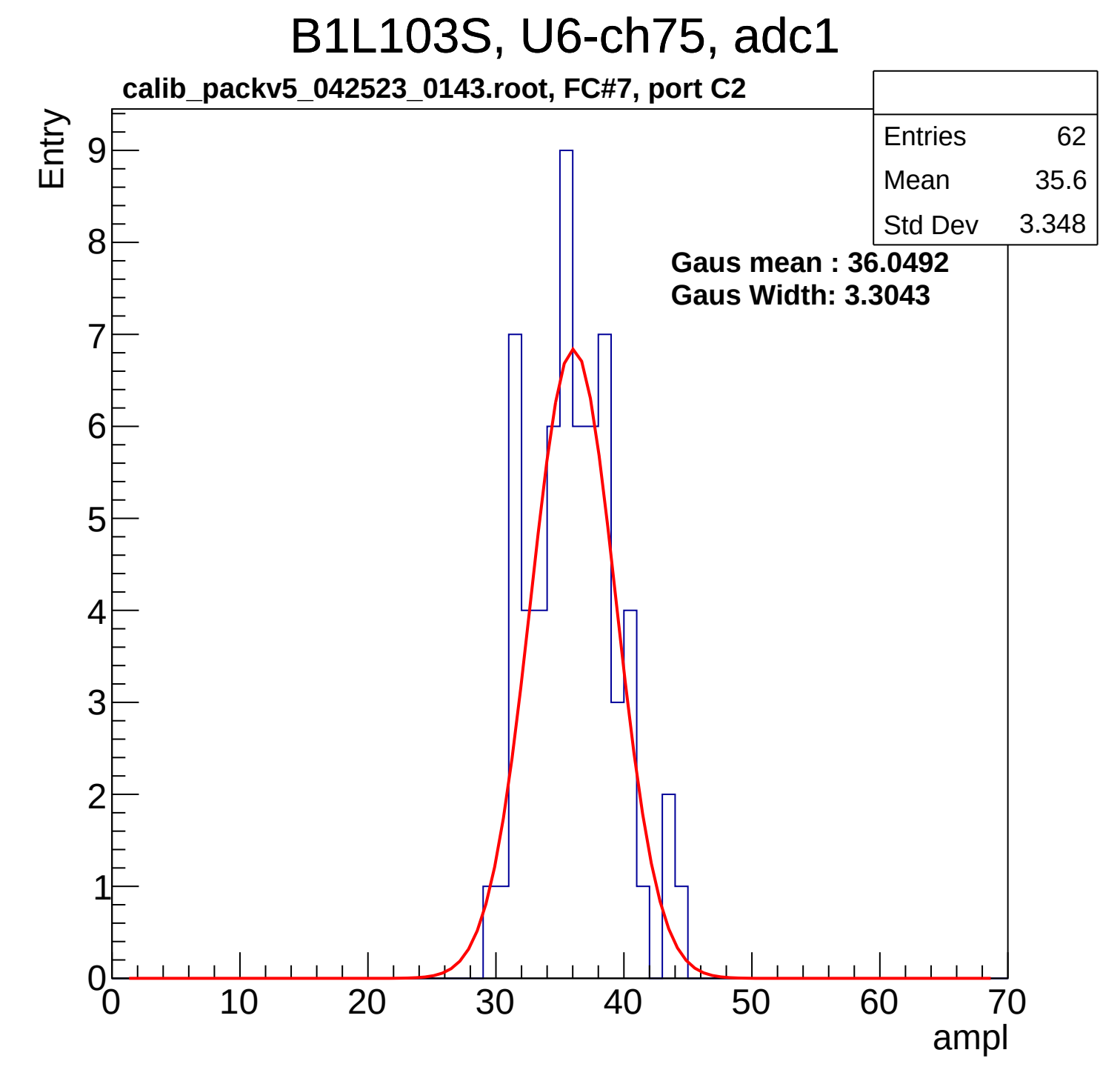
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch75, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.6
Std Dev 3.348
Gaus mean : 36.0492
Gaus Width: 3.3043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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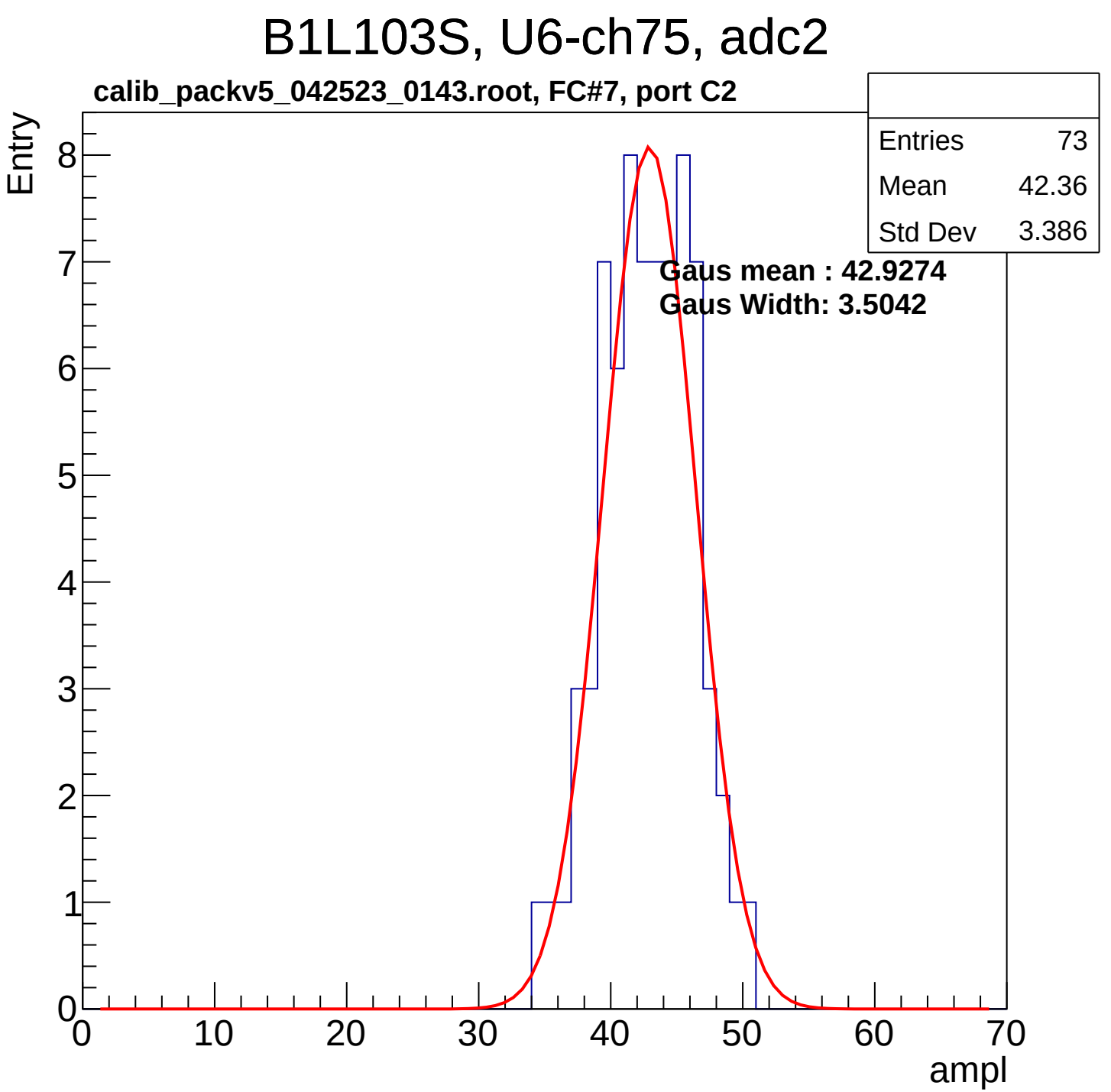
B1L103S, U6-ch75, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 42.36
Std Dev 3.386
Gaus mean : 42.9274
Gaus Width: 3.5042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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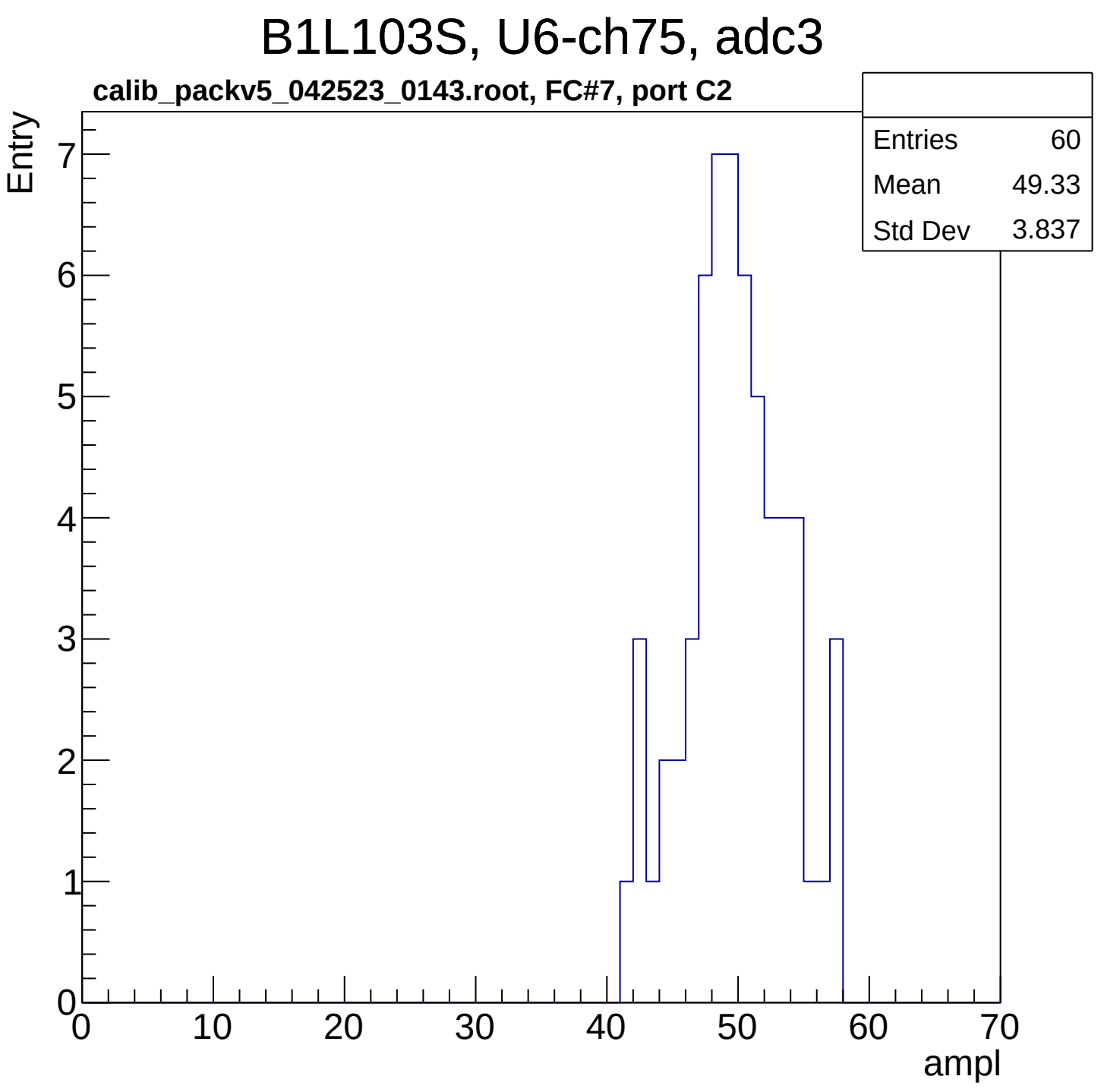
B1L103S, U6-ch75, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.33
Std Dev 3.837
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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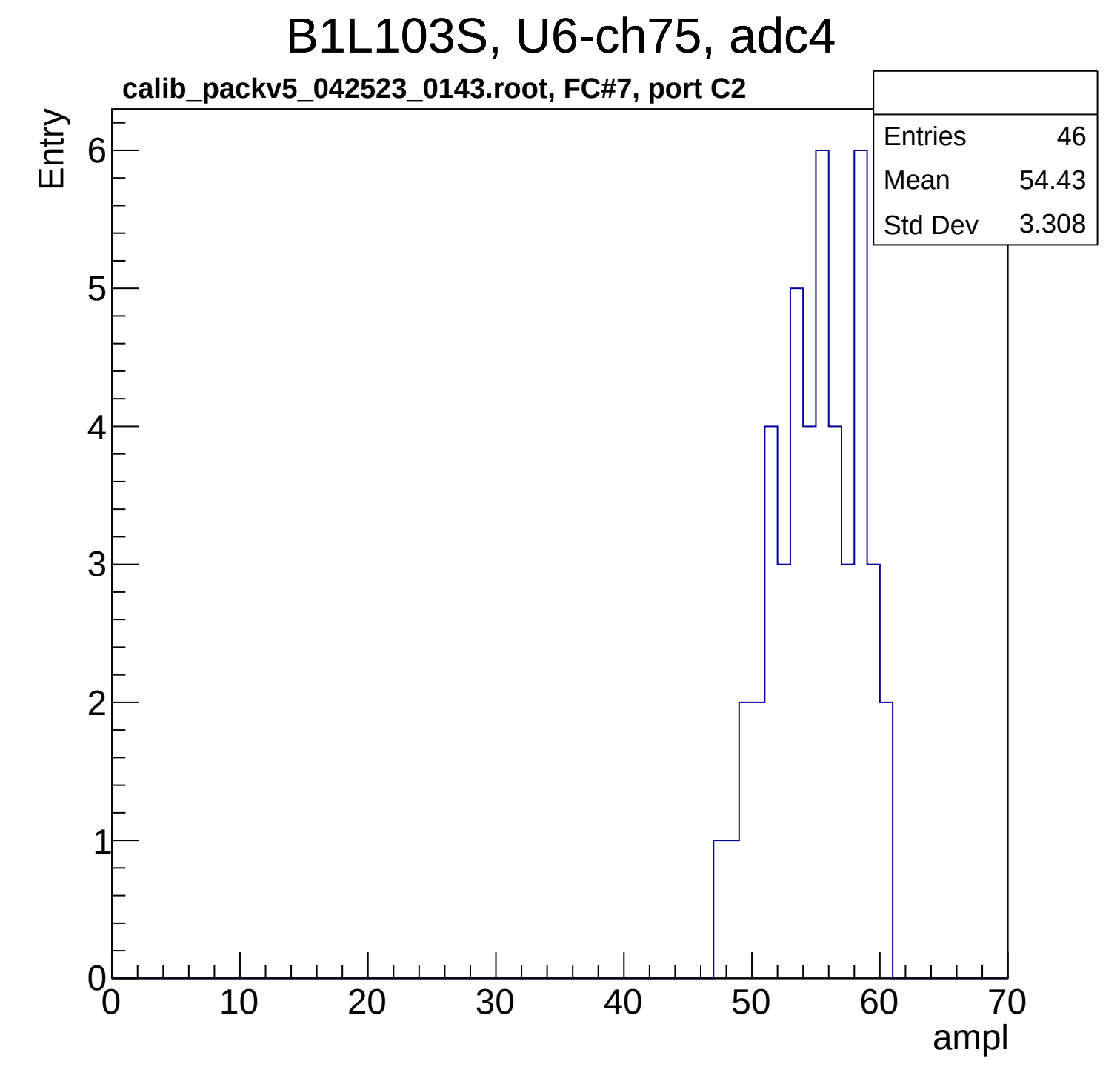
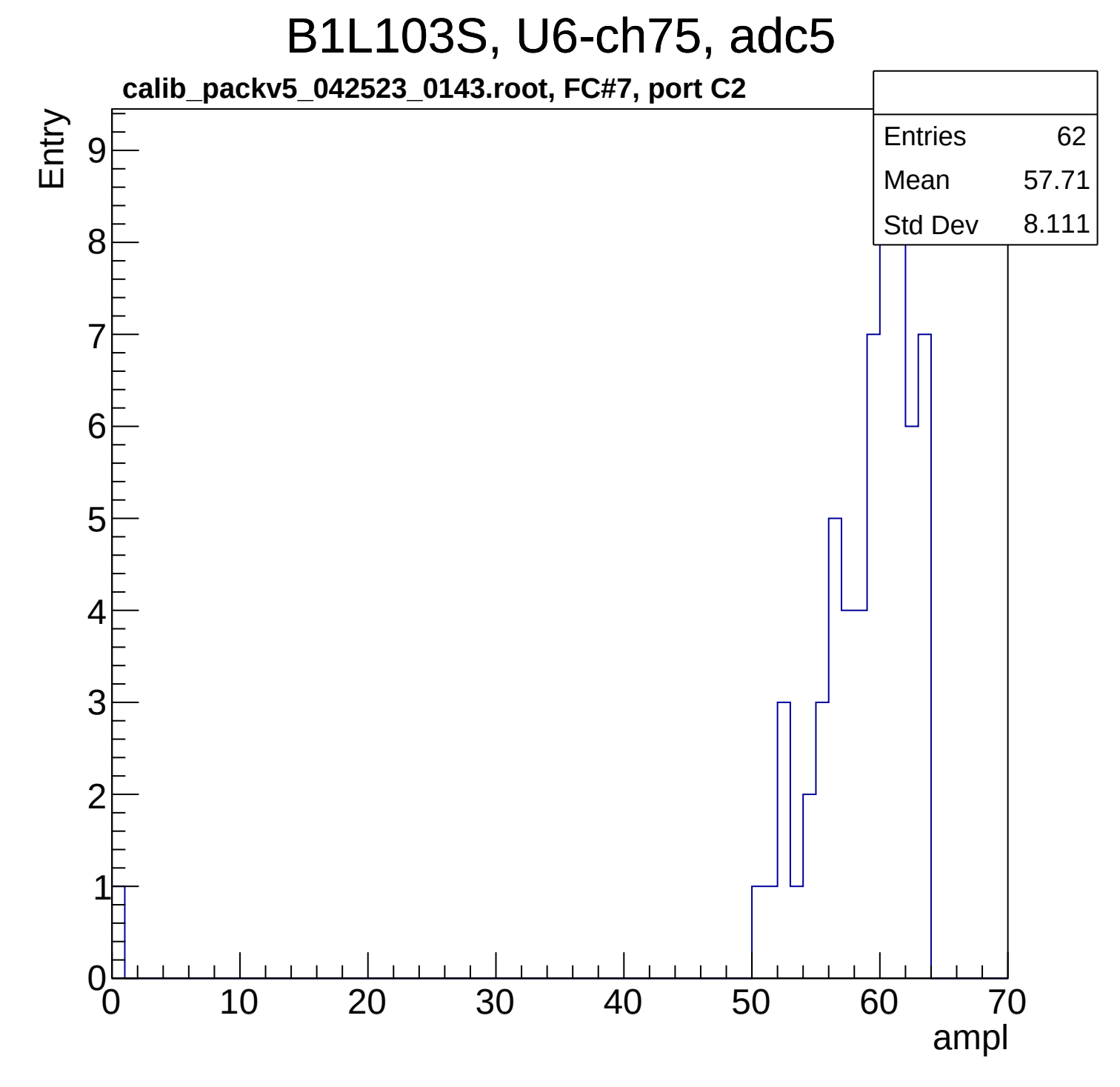

B1L103S, U6-ch75, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 54.43
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch75, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 57.71
Std Dev 8.111
Entry
ampl
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3
4
5
6
7
8
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0
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30
40
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60
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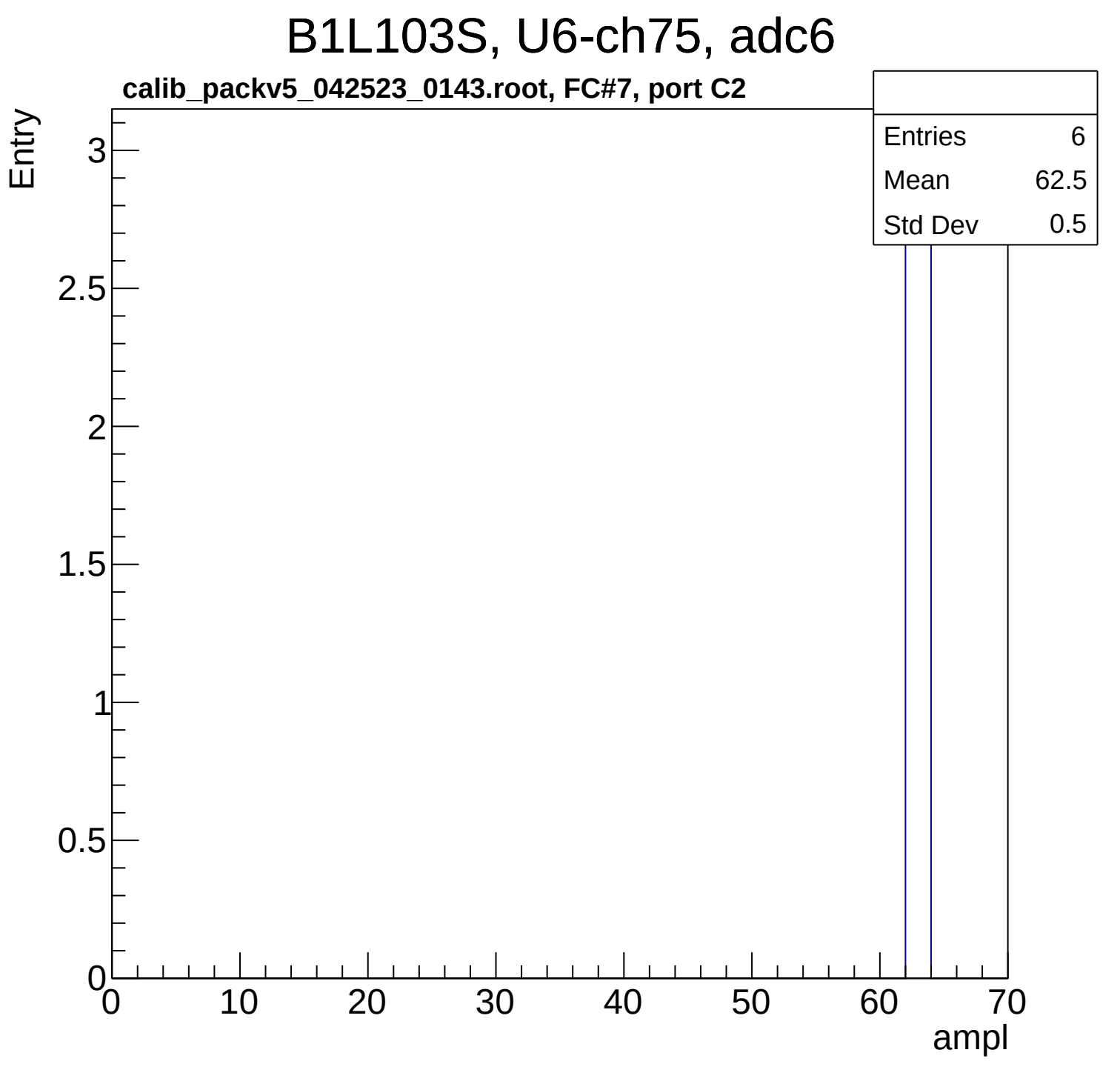
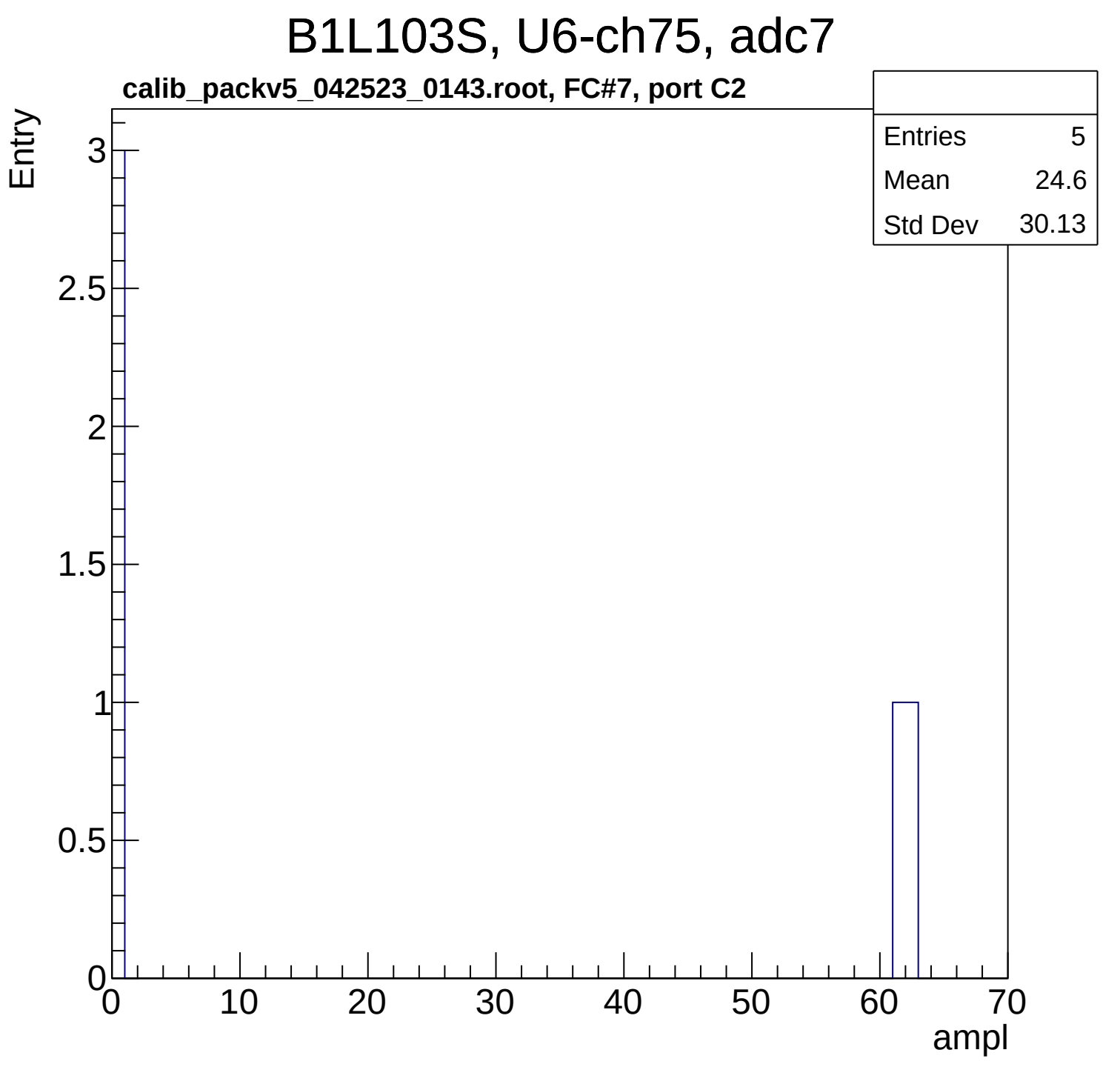

B1L103S, U6-ch75, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch75, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 24.6
Std Dev 30.13
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
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40
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60
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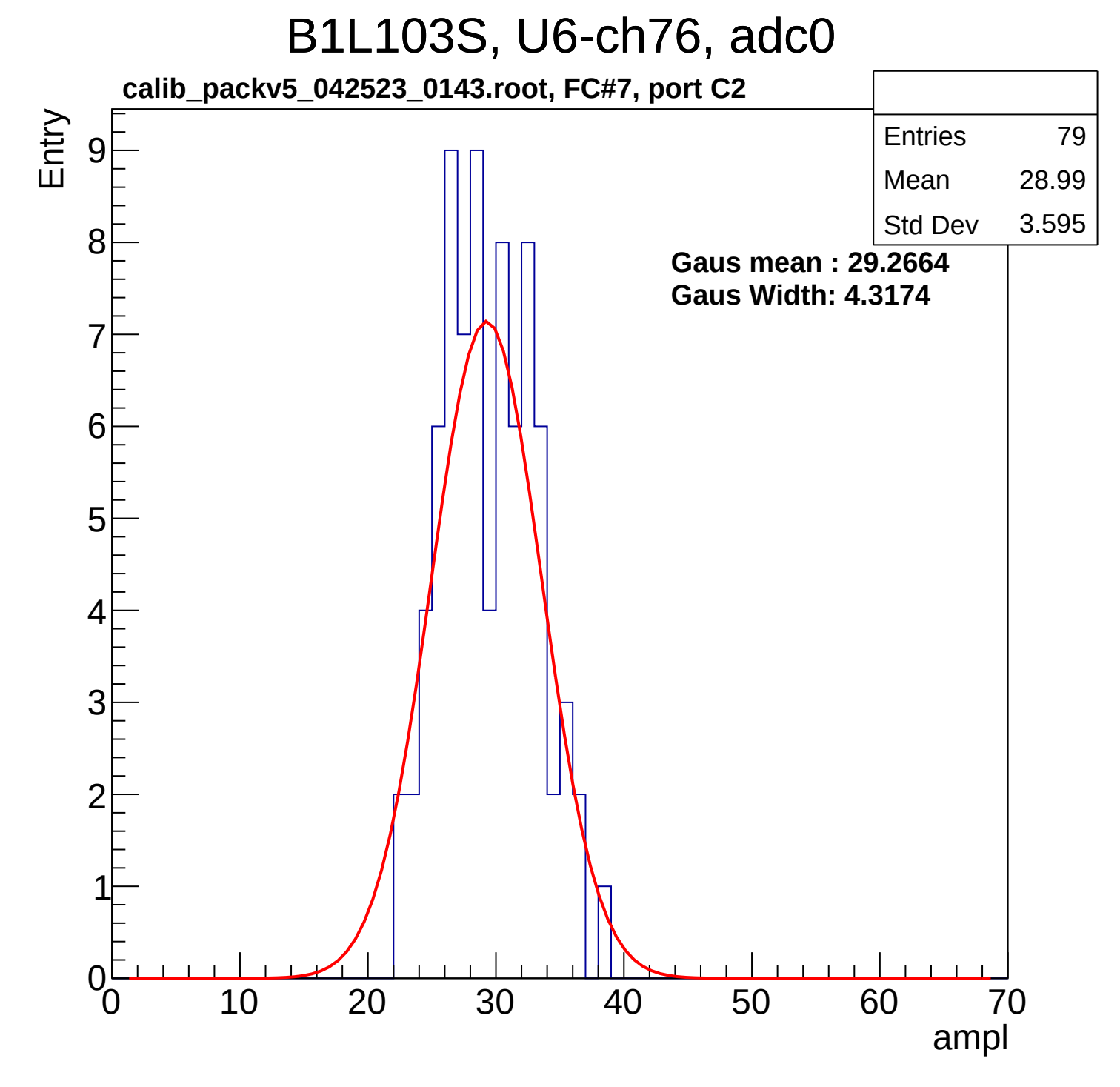
B1L103S, U6-ch76, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 28.99
Std Dev 3.595
Gaus mean : 29.2664
Gaus Width: 4.3174
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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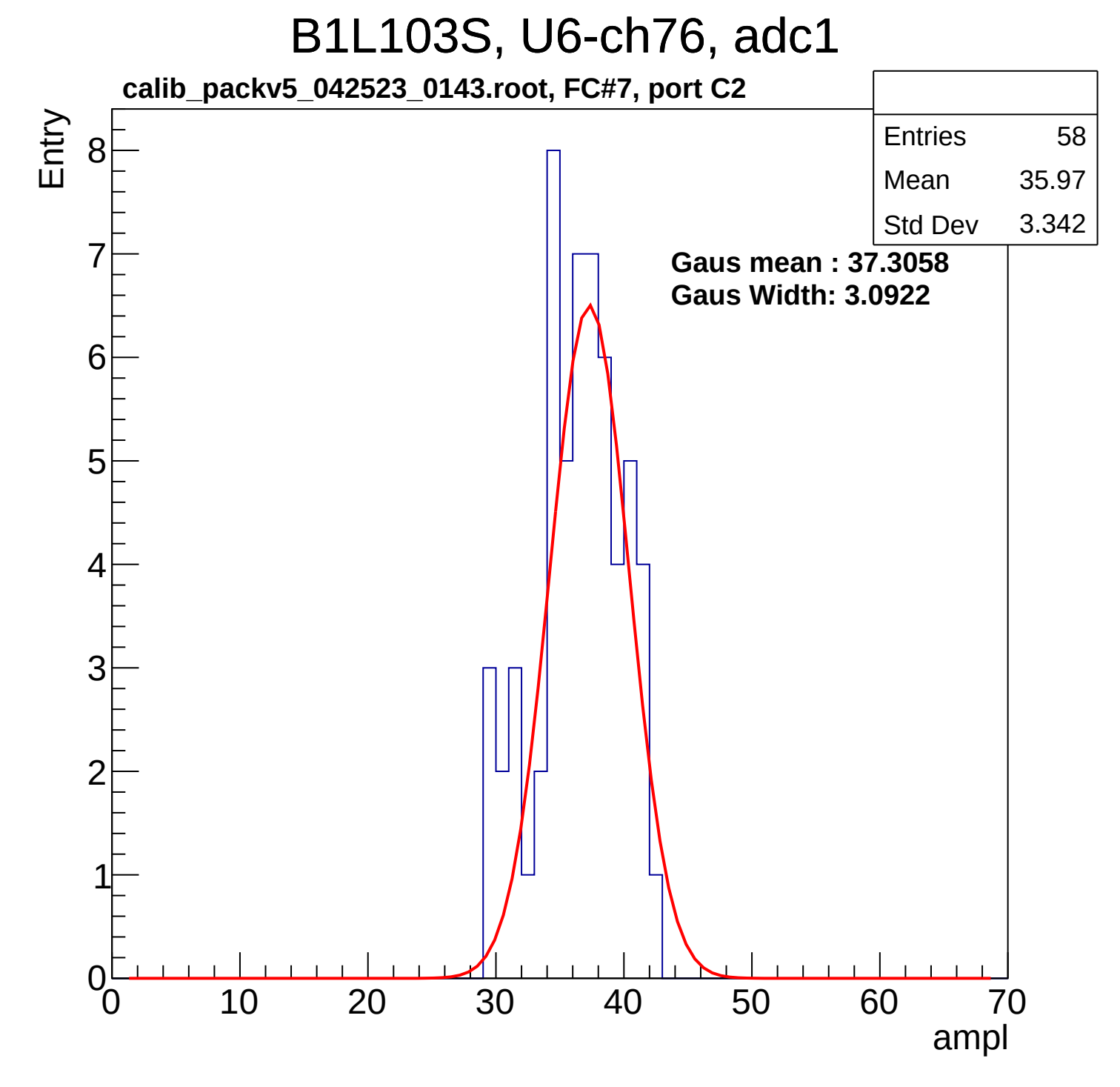
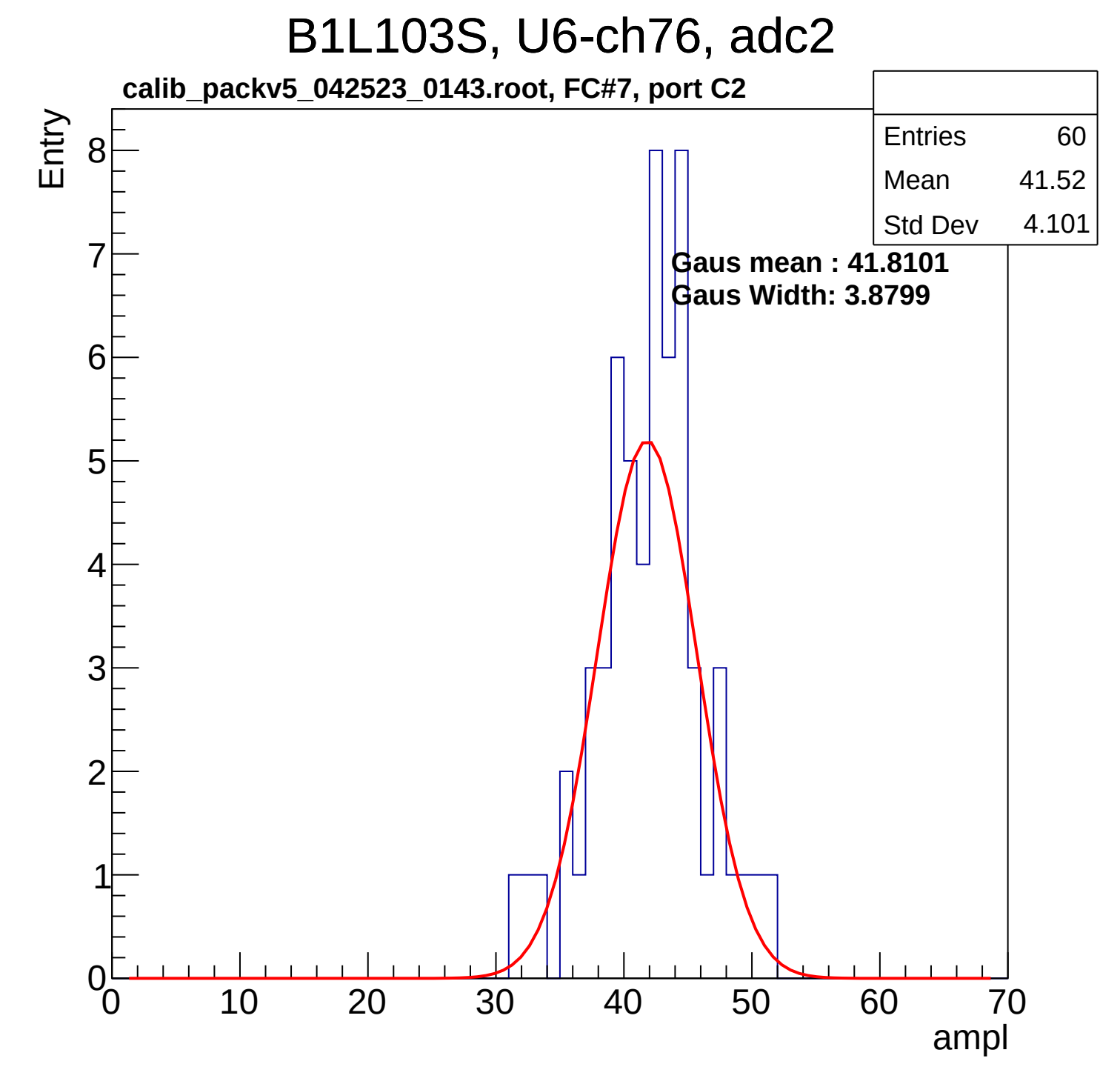

B1L103S, U6-ch76, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 35.97
Std Dev 3.342
Gaus mean : 37.3058
Gaus Width: 3.0922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch76, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 41.52
Std Dev 4.101
Gaus mean : 41.8101
Gaus Width: 3.8799
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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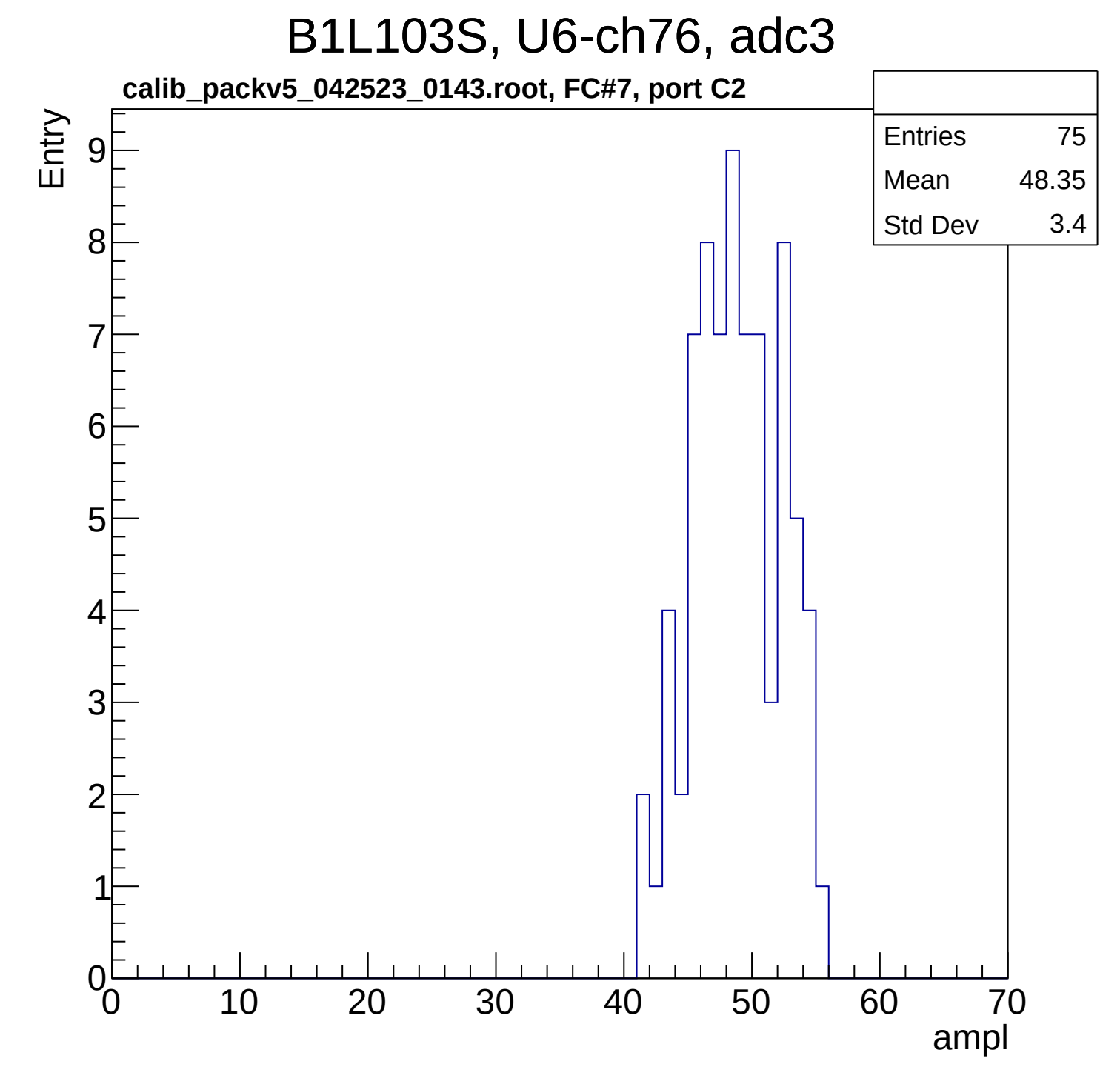
B1L103S, U6-ch76, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 48.35
Std Dev 3.4
Entry
ampl
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8
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0
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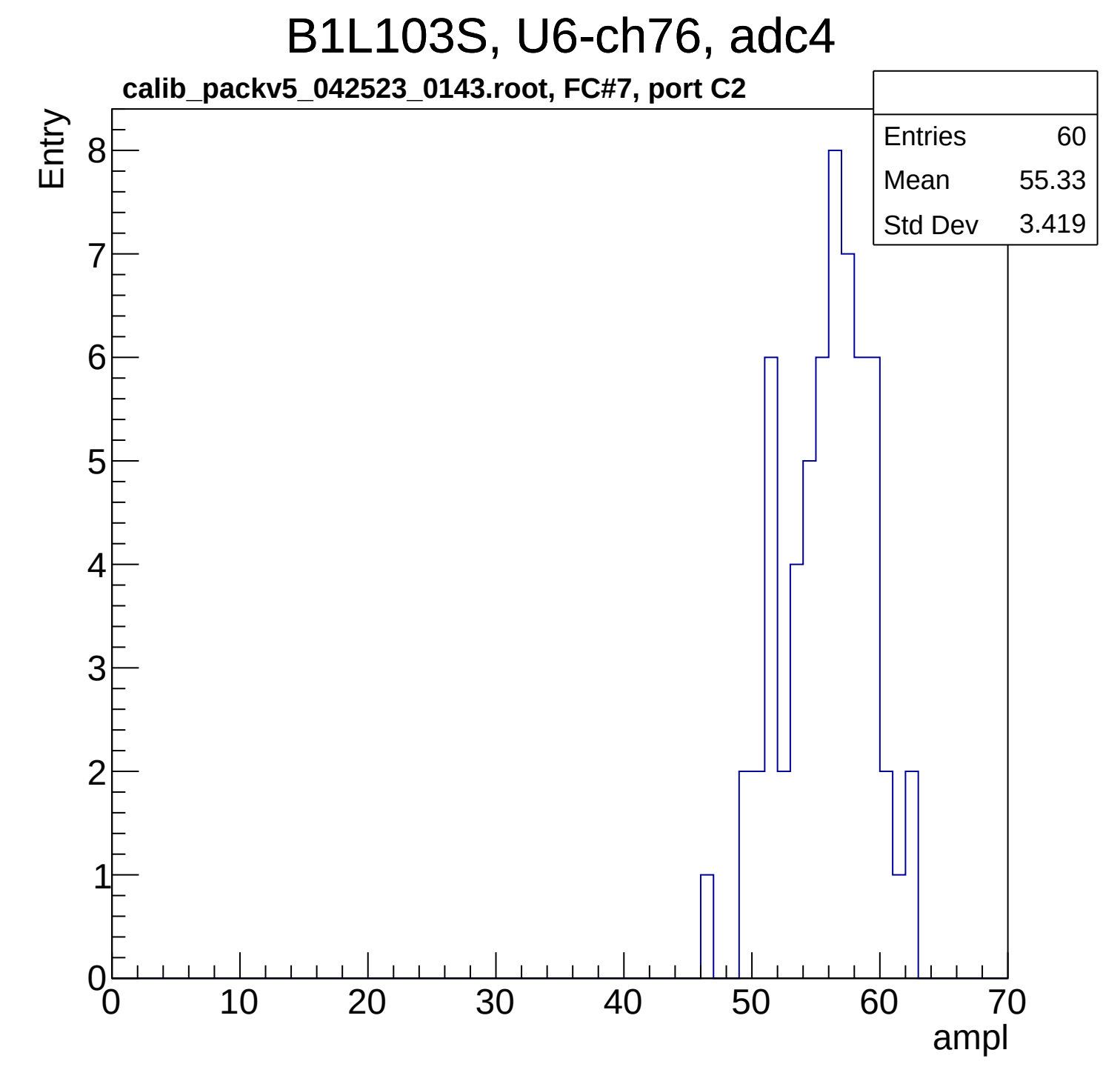
B1L103S, U6-ch76, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 55.33
Std Dev 3.419
Entry
ampl
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1
2
3
4
5
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0
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60
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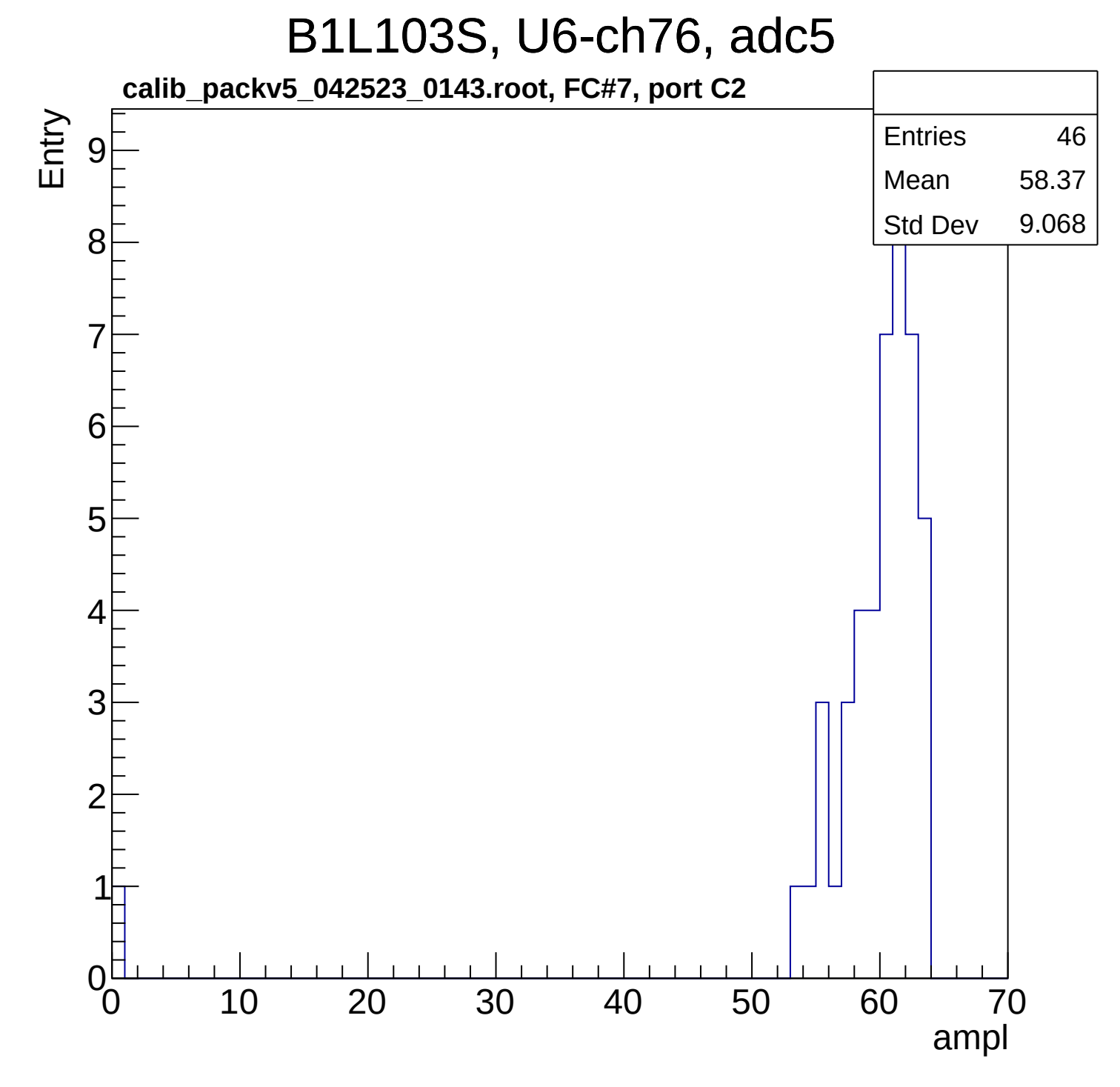
B1L103S, U6-ch76, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.37
Std Dev 9.068
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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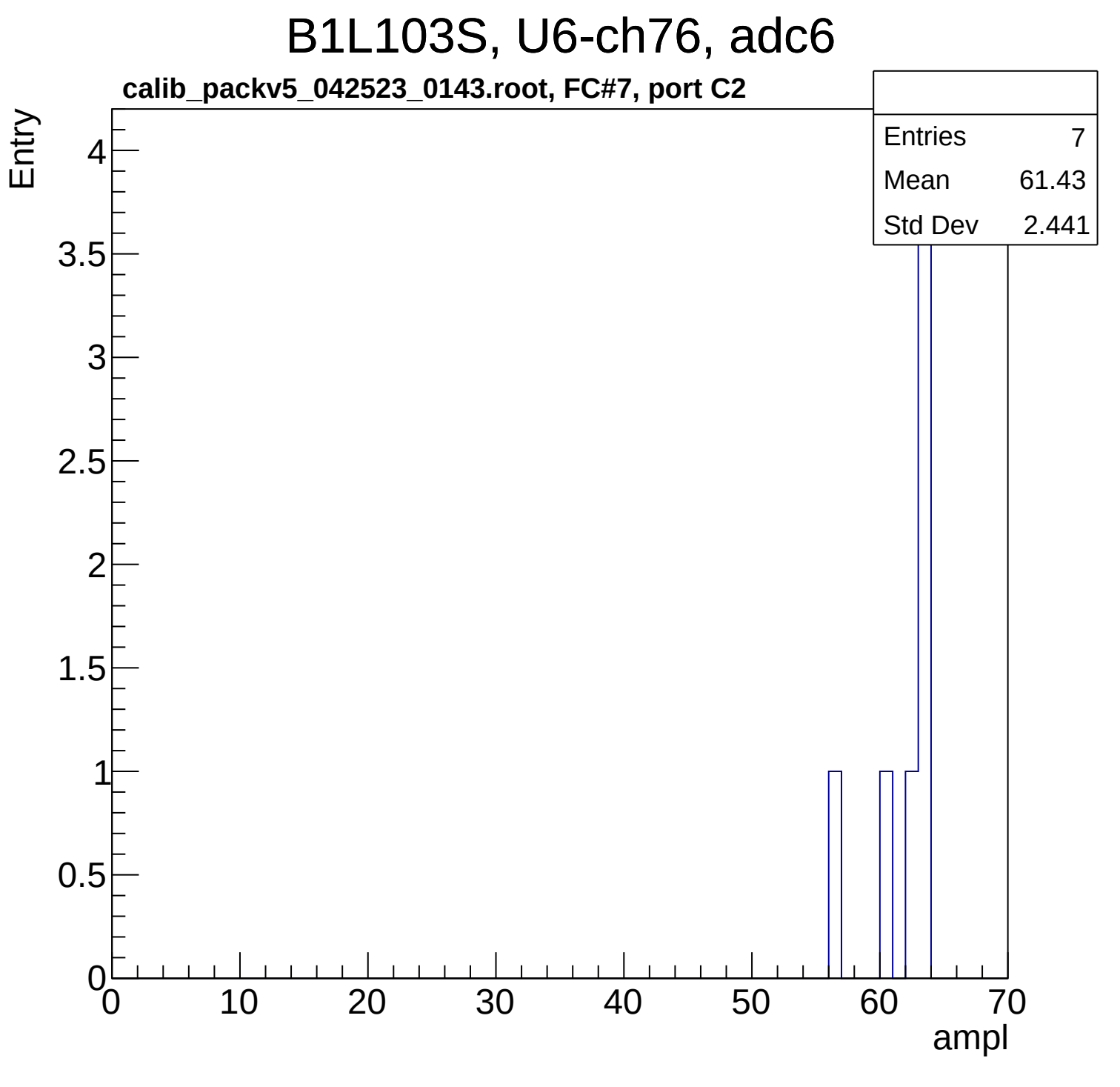
B1L103S, U6-ch76, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.43
Std Dev 2.441
Entry
ampl
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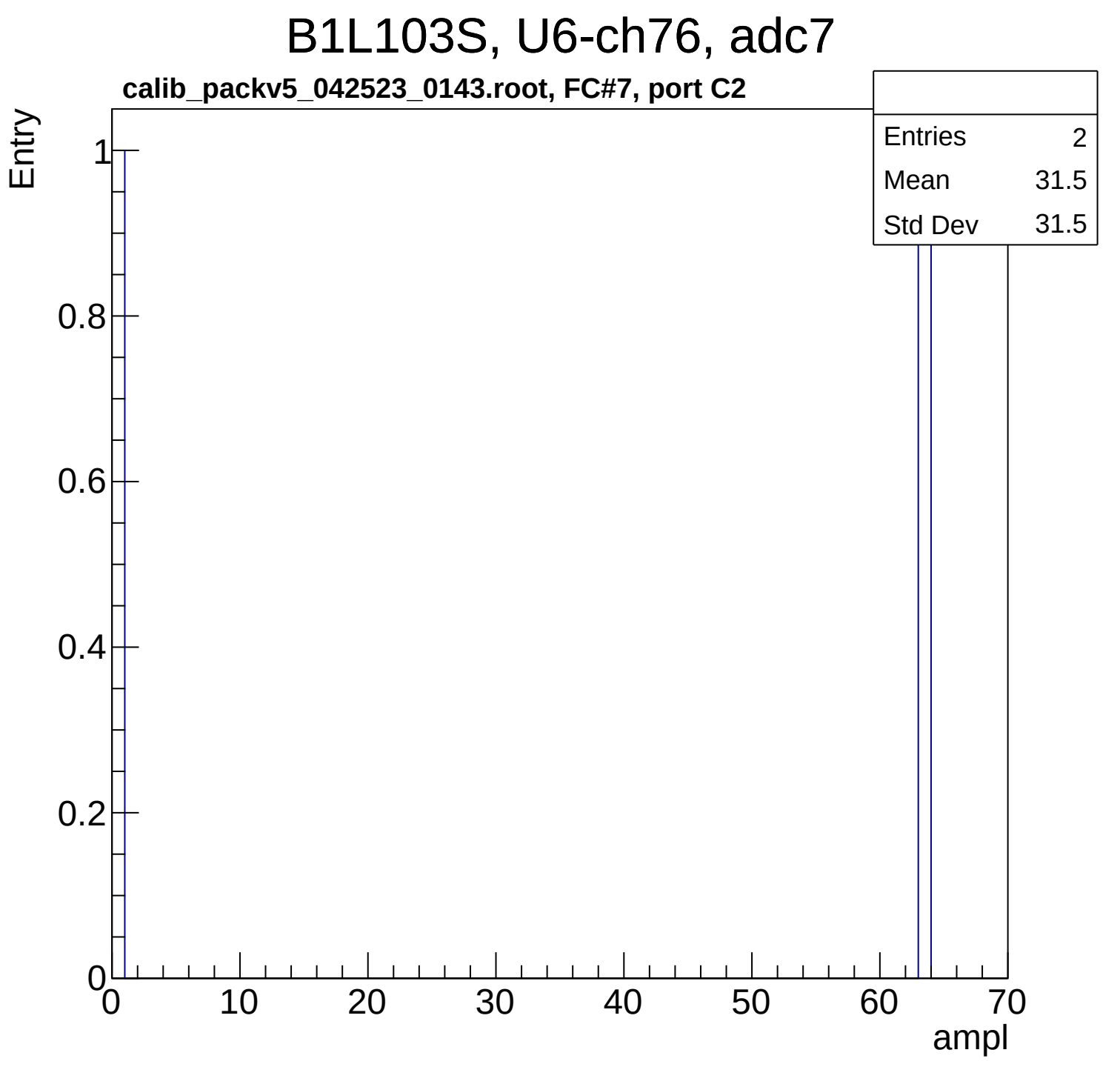
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U6-ch76, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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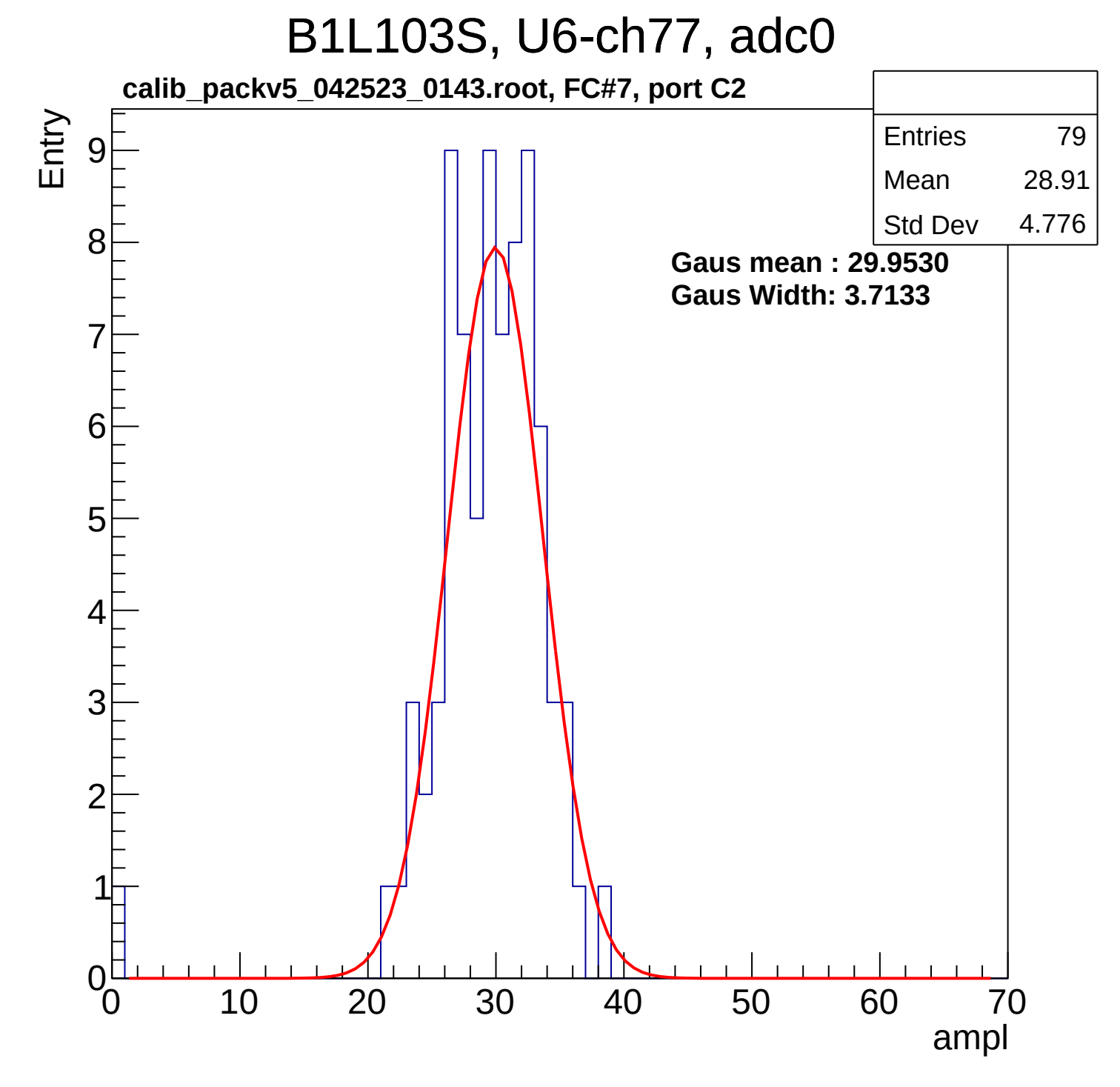
50
60
70
ampl


B1L103S, U6-ch77, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 28.91
Std Dev 4.776
Gaus mean : 29.9530
Gaus Width: 3.7133
Entry
ampl
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2
3
4
5
6
7
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0
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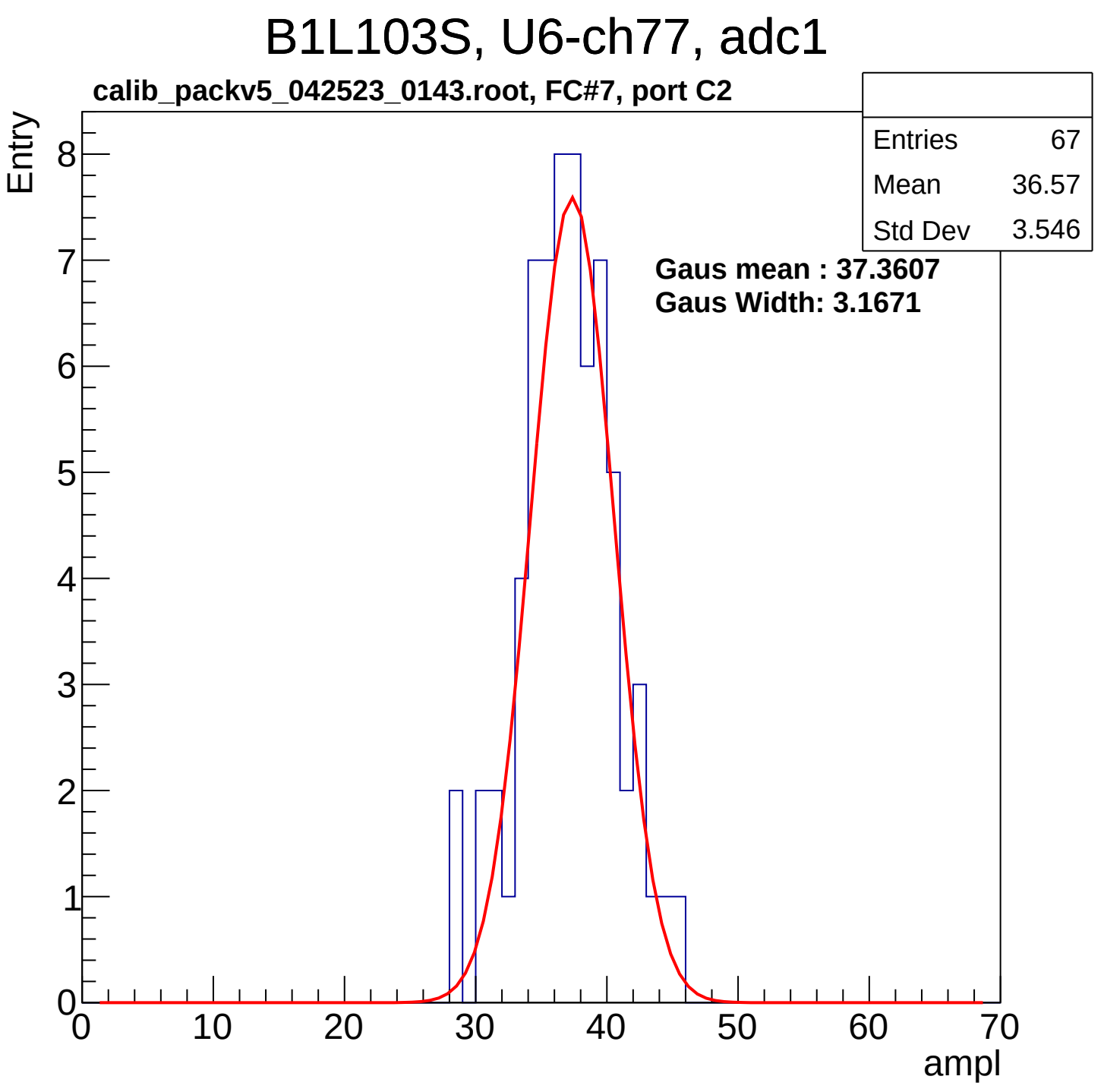
B1L103S, U6-ch77, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.57
Std Dev 3.546
Gaus mean : 37.3607
Gaus Width: 3.1671
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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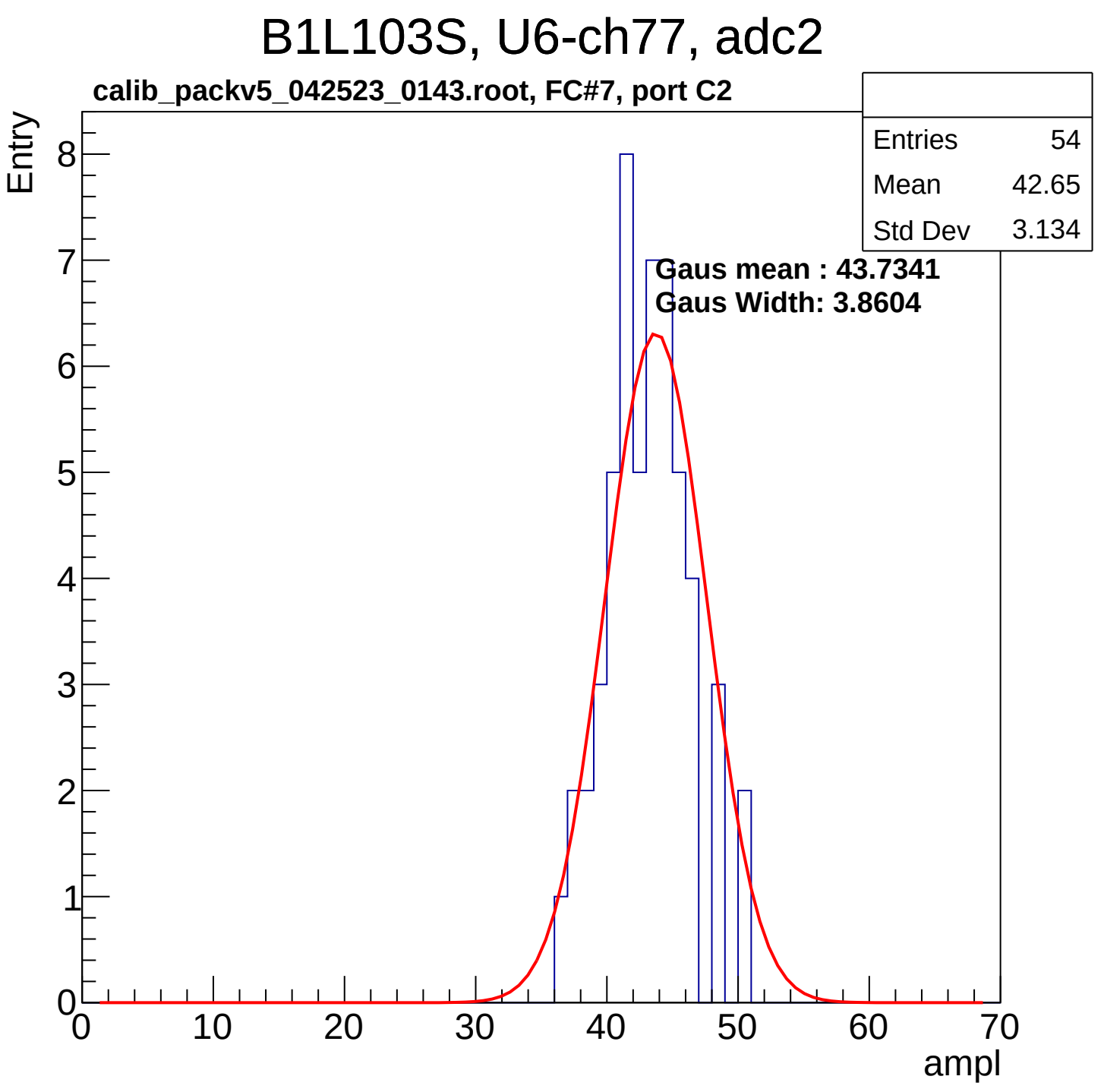
B1L103S, U6-ch77, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 42.65
Std Dev 3.134
Gaus mean : 43.7341
Gaus Width: 3.8604
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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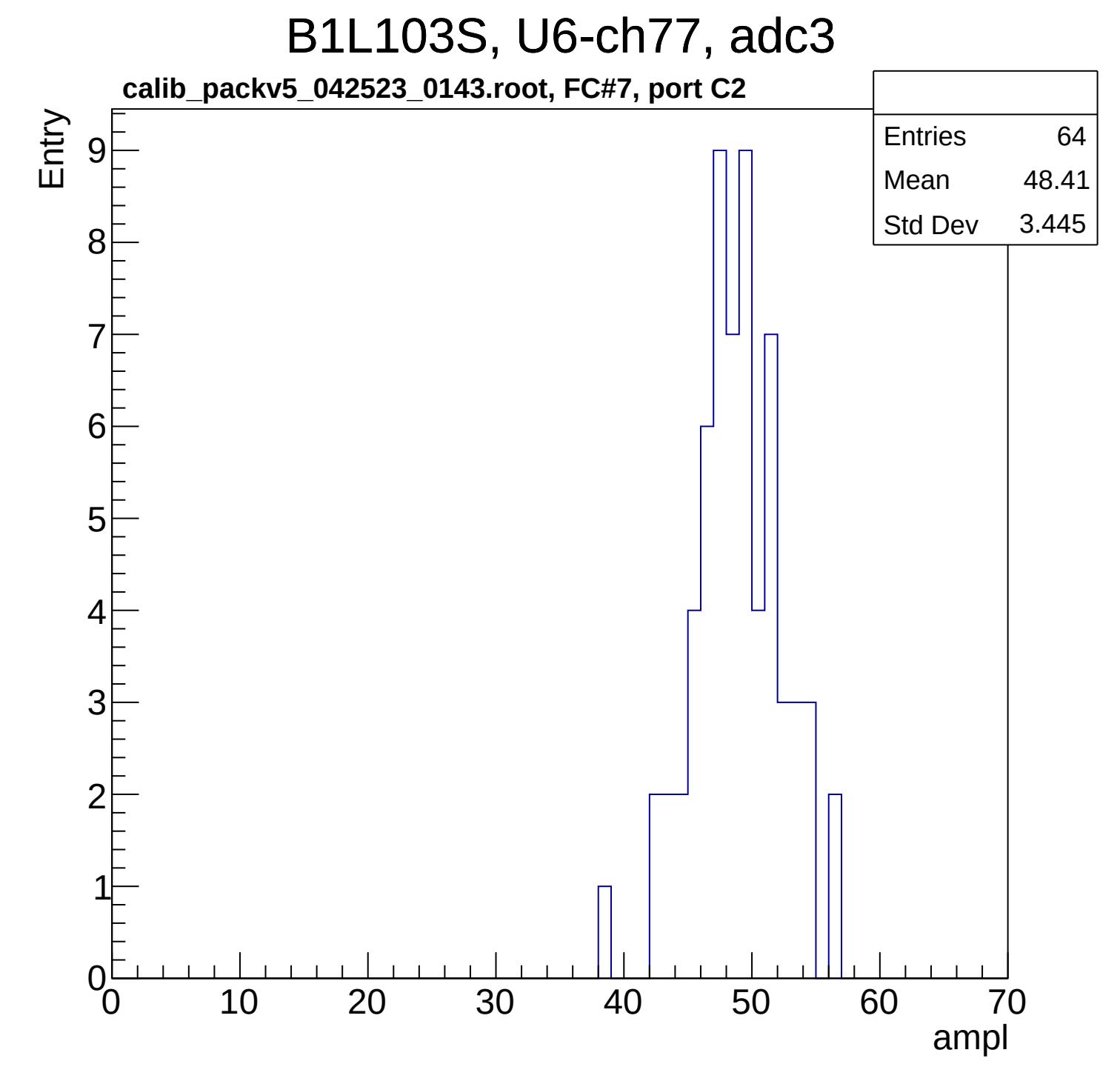
B1L103S, U6-ch77, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.41
Std Dev 3.445
Entry
ampl
0
1
2
3
4
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6
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0
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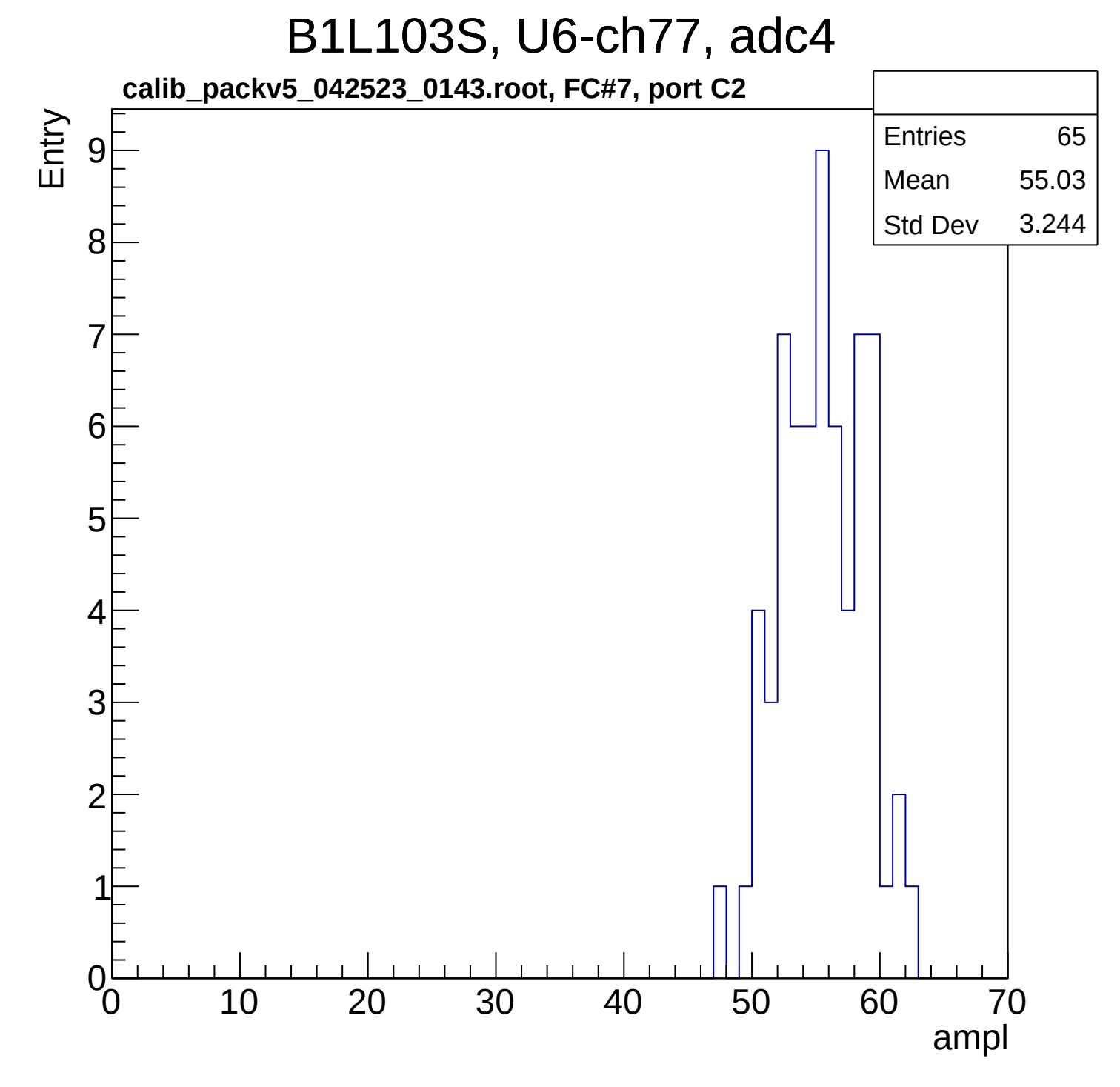
B1L103S, U6-ch77, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 55.03
Std Dev 3.244
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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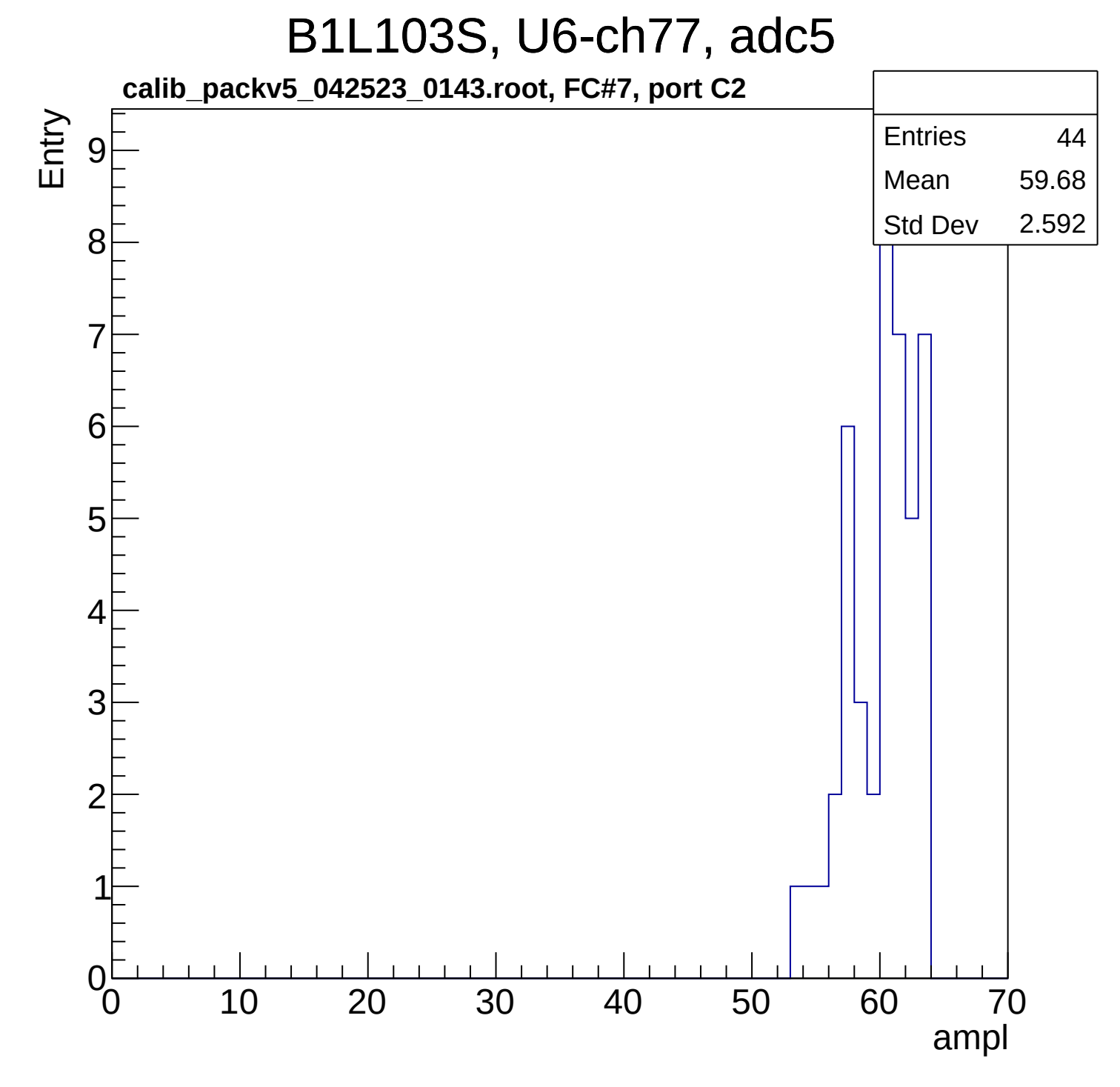
B1L103S, U6-ch77, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.68
Std Dev 2.592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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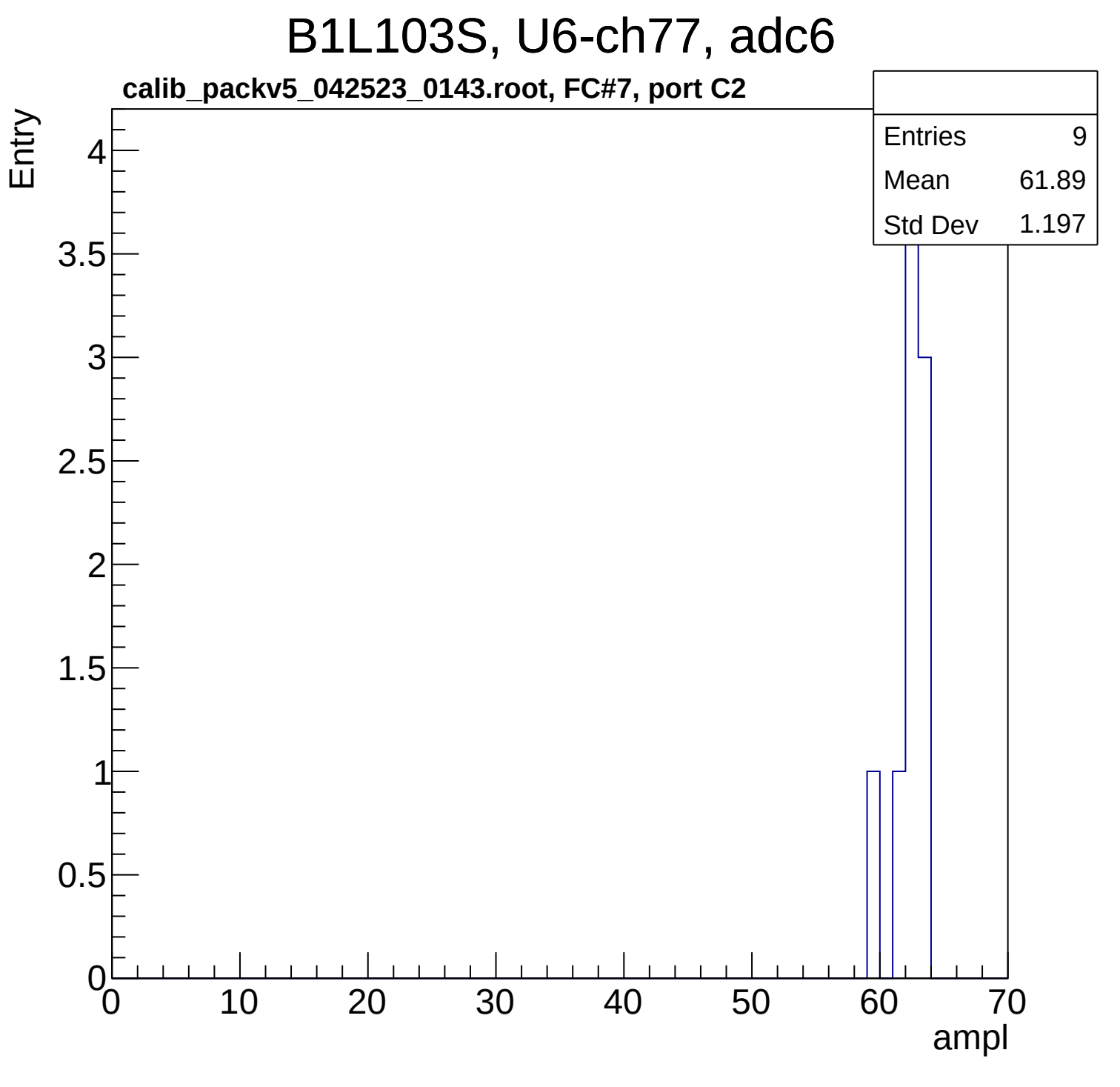
B1L103S, U6-ch77, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.89
Std Dev 1.197
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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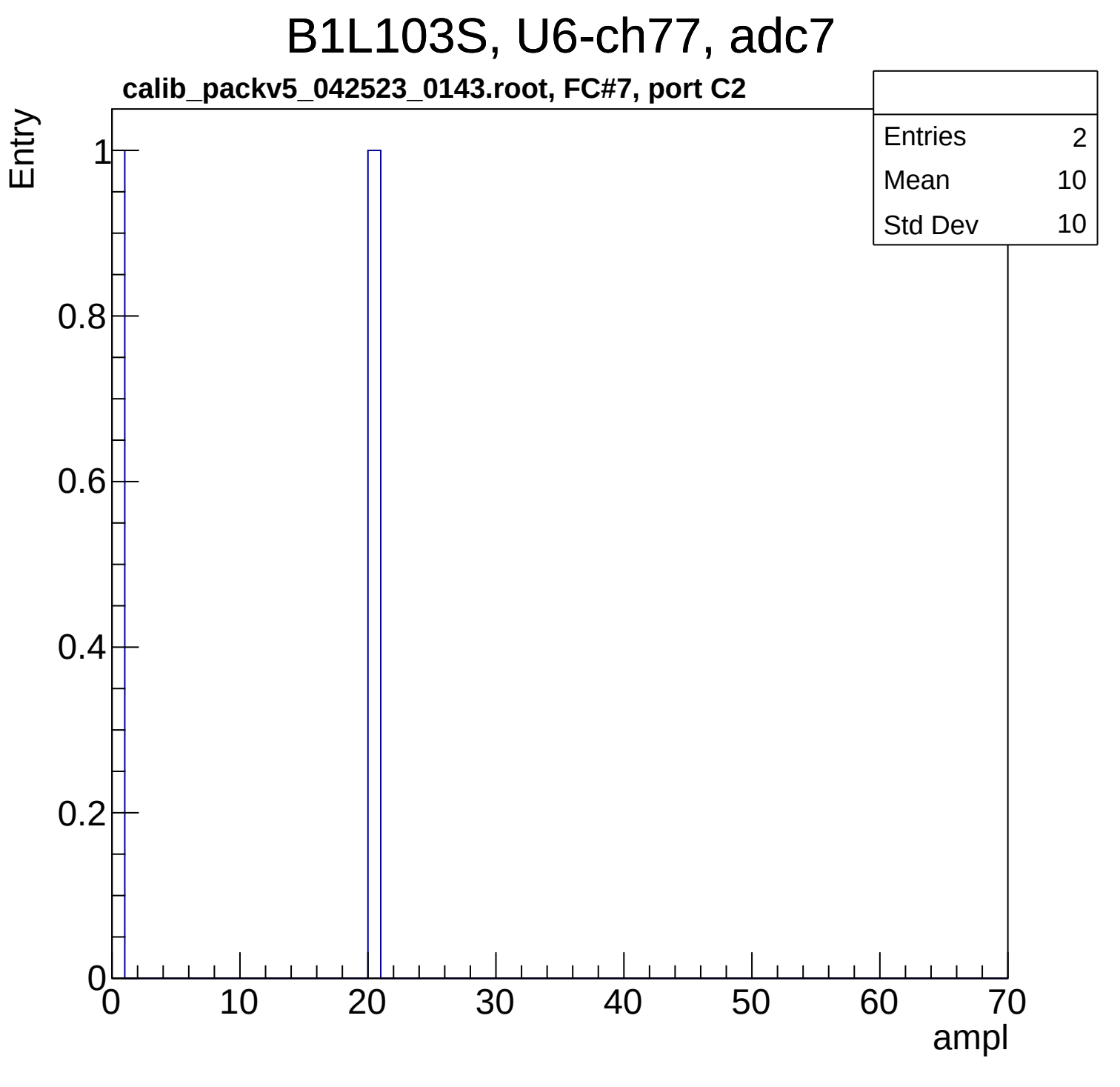
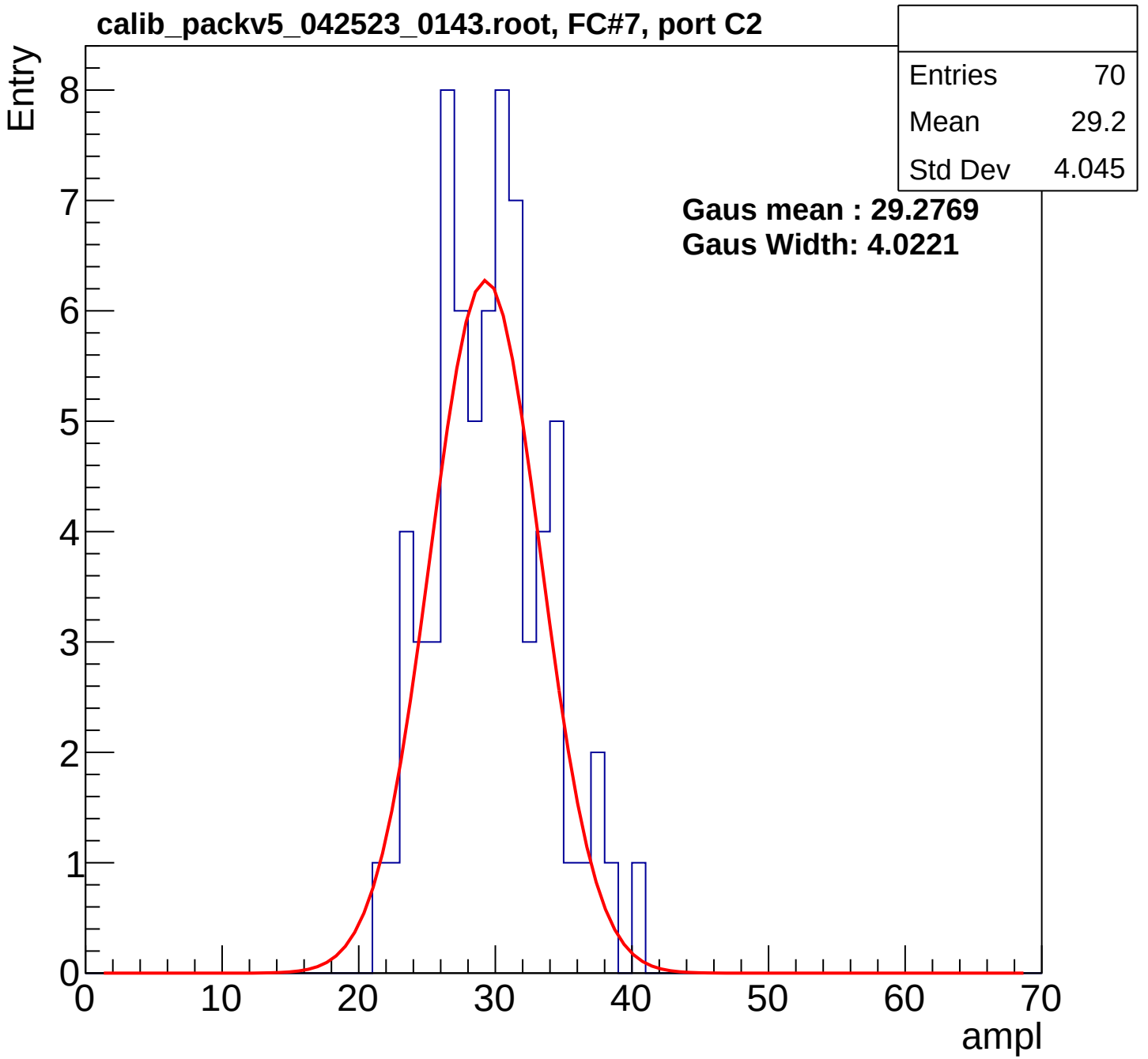
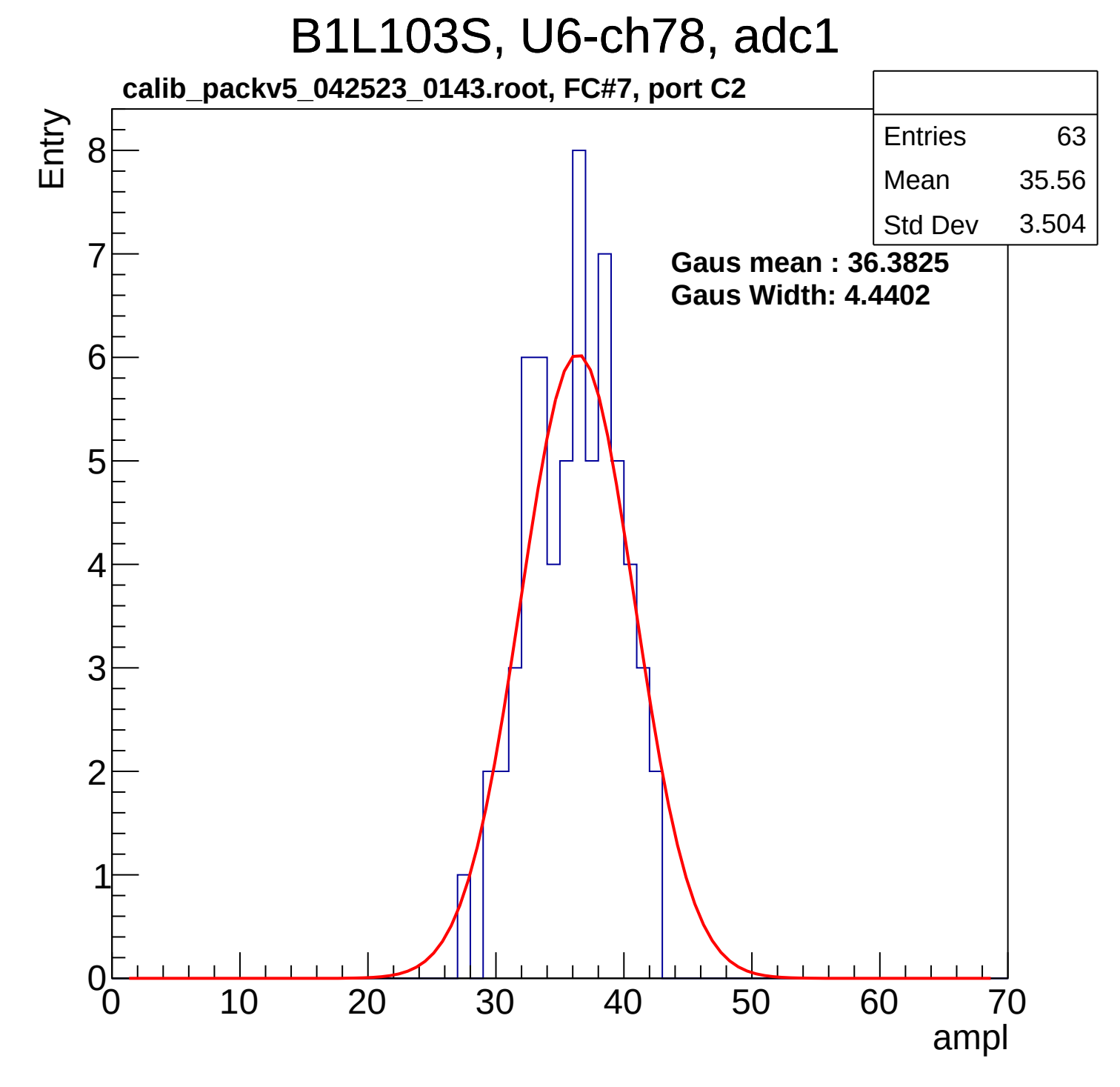

B1L103S, U6-ch77, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch78, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.2
Std Dev 4.045
Gaus mean : 29.2769
Gaus Width: 4.0221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch78, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.56
Std Dev 3.504
Gaus mean : 36.3825
Gaus Width: 4.4402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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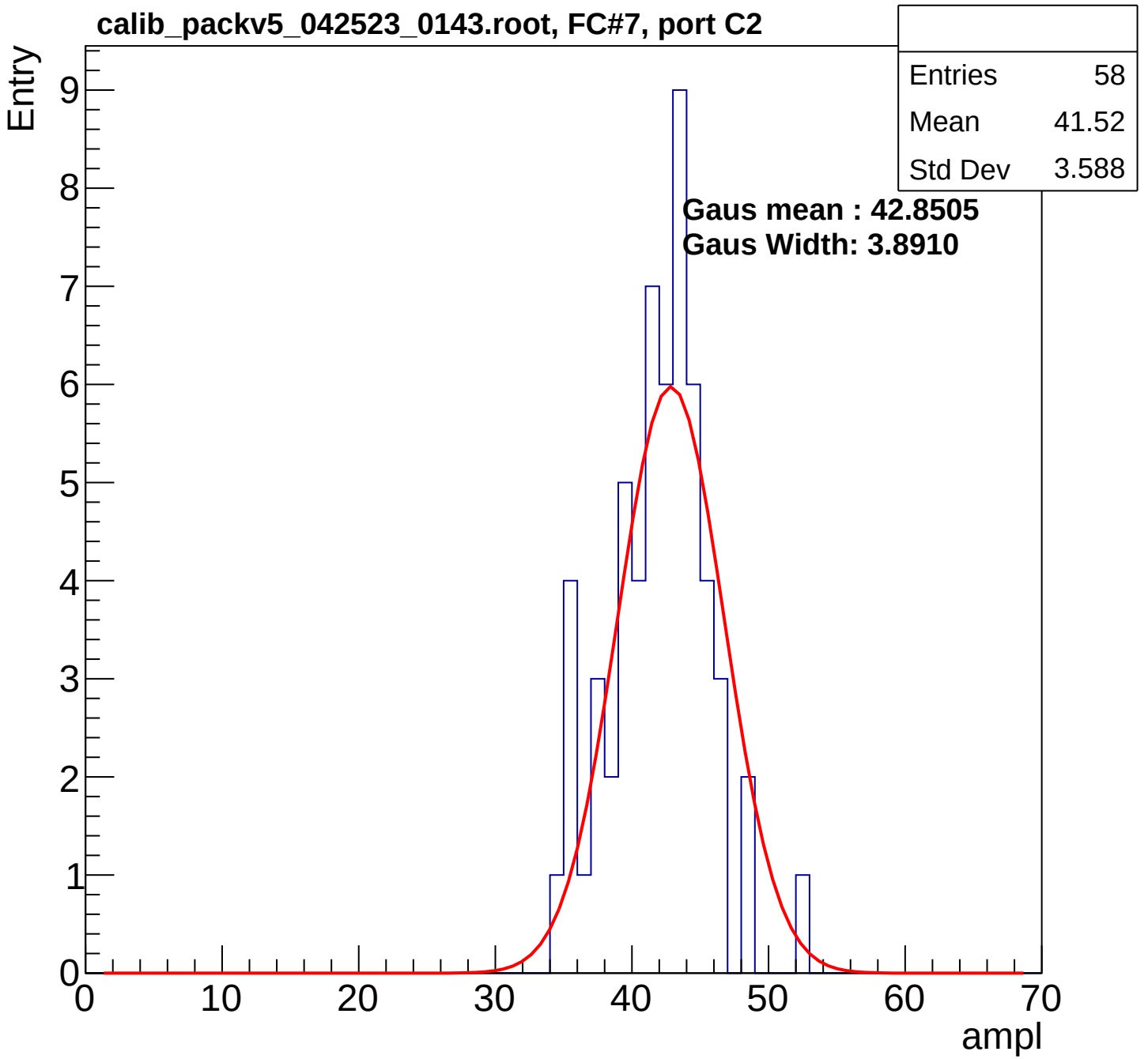
B1L103S, U6-ch78, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 41.52
Std Dev 3.588
Gaus mean : 42.8505
Gaus Width: 3.8910
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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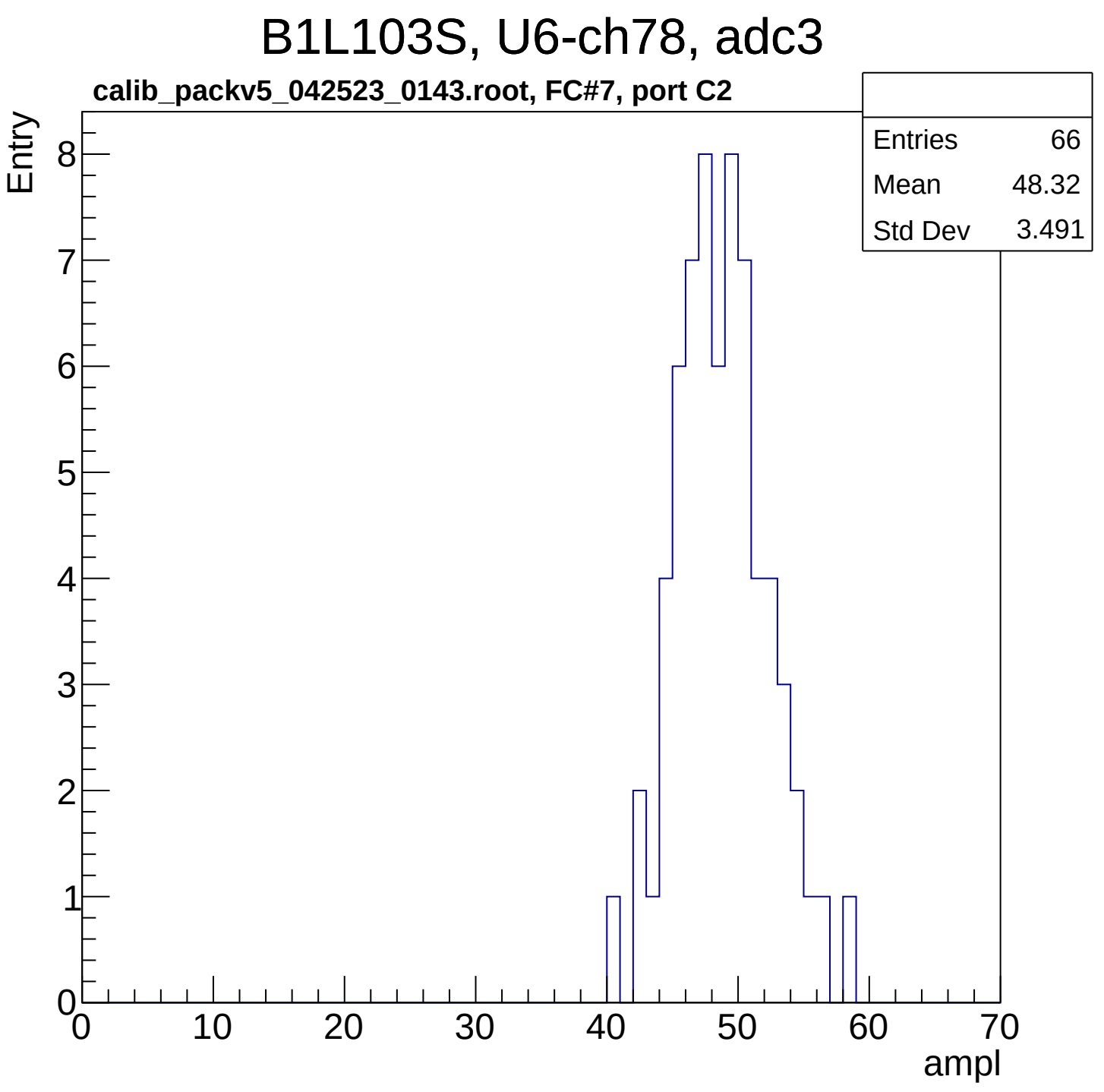
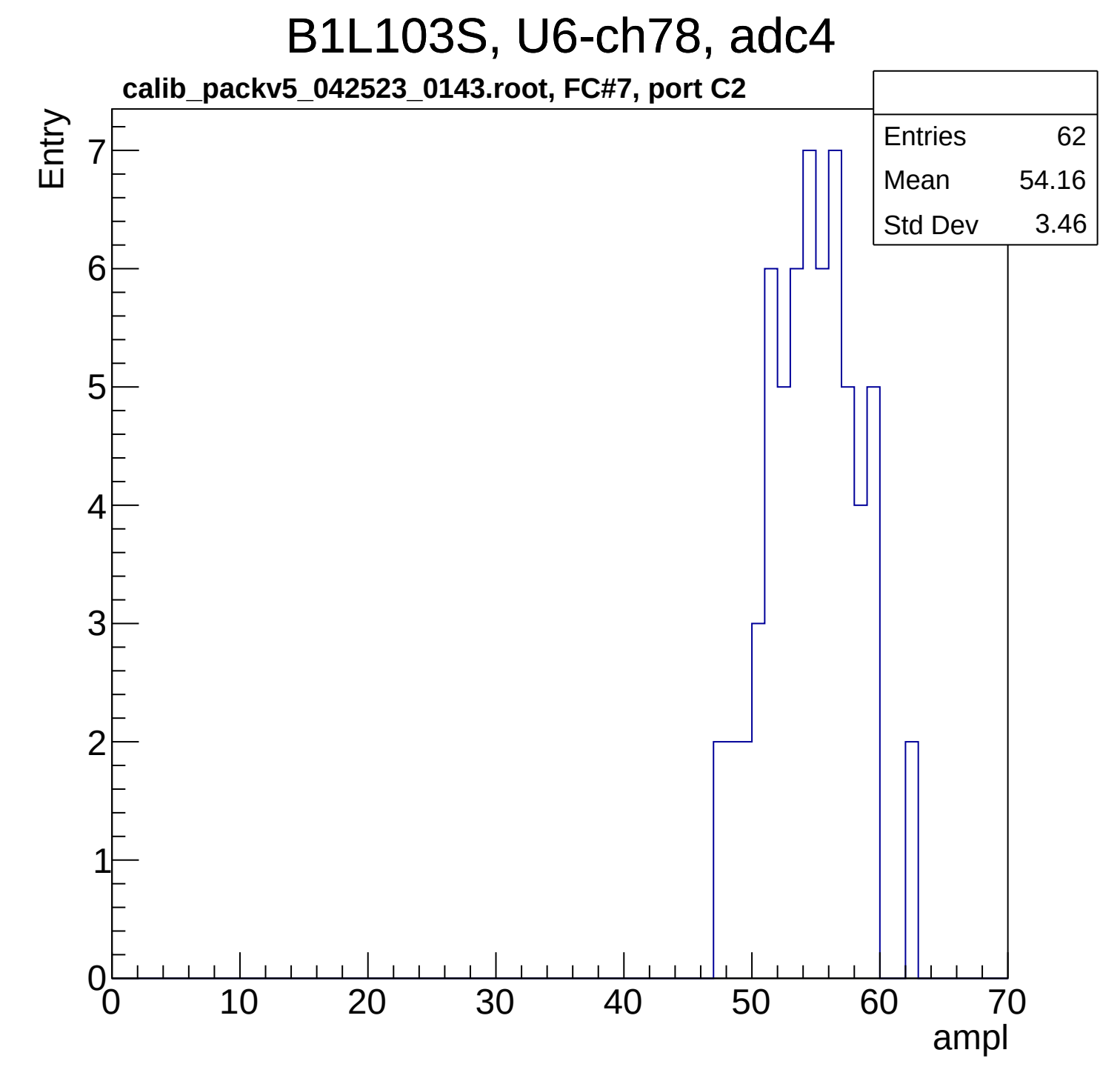

B1L103S, U6-ch78, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.32
Std Dev 3.491
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch78, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 54.16
Std Dev 3.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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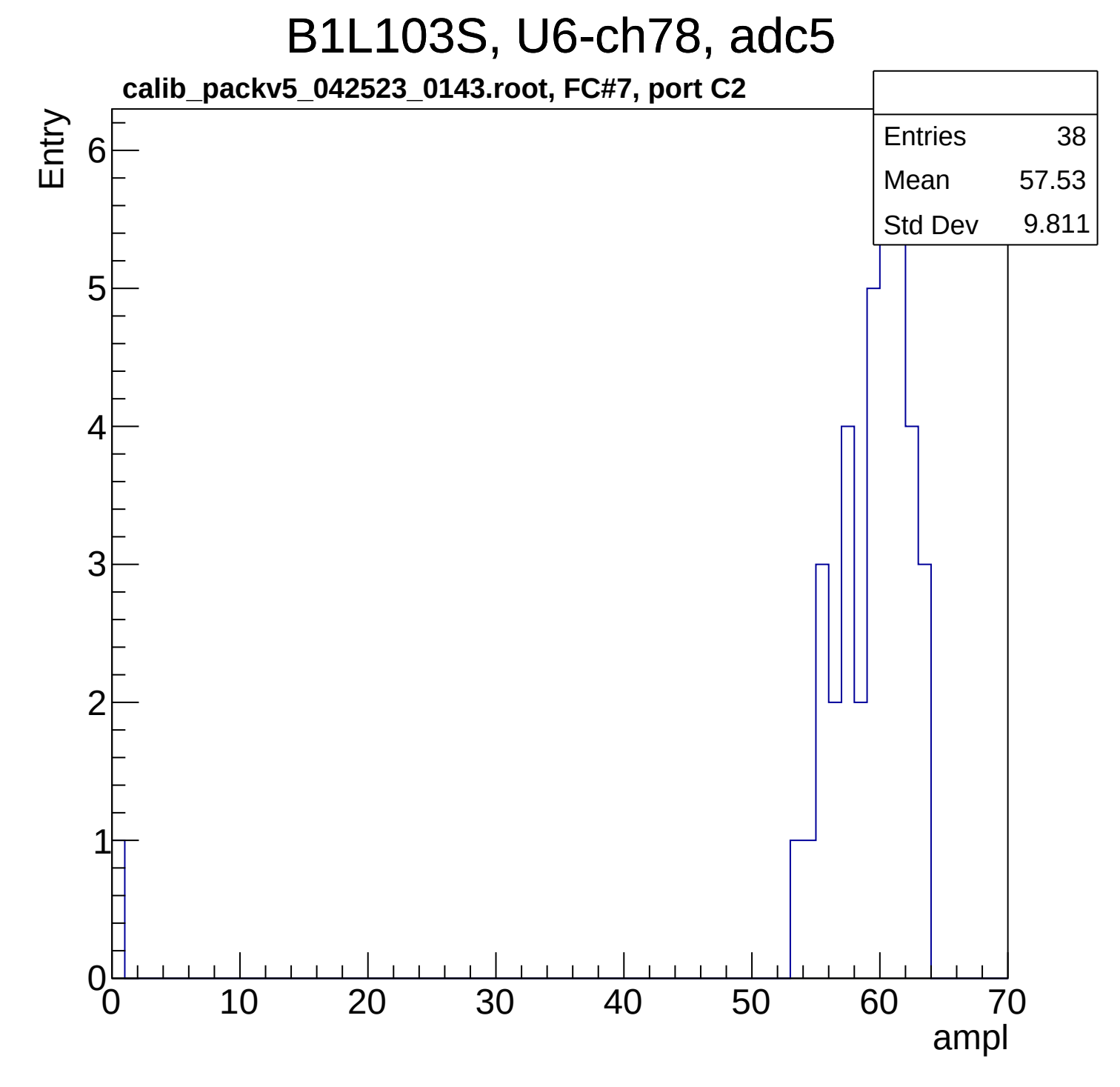
B1L103S, U6-ch78, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 57.53
Std Dev 9.811
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
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60
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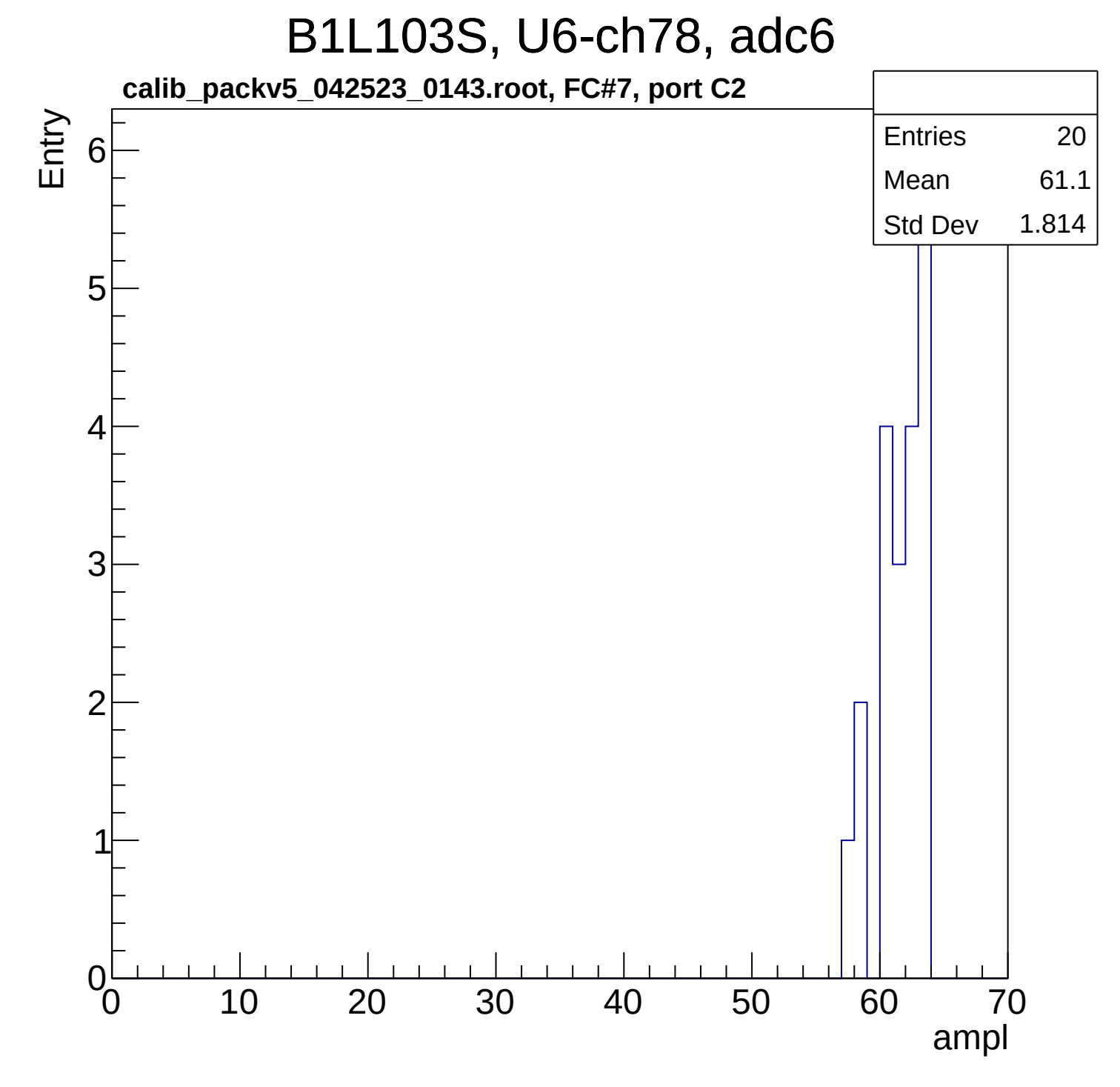
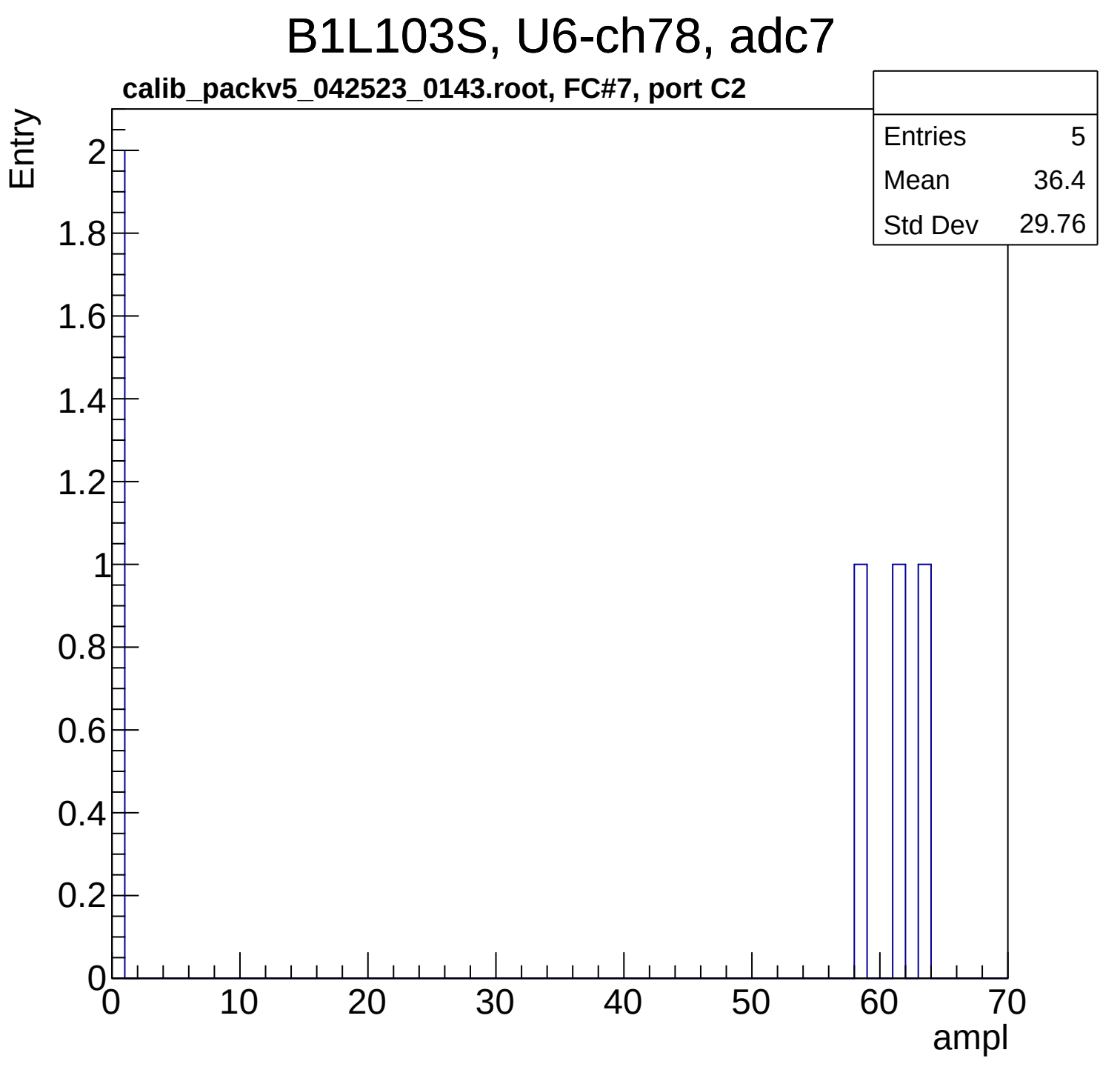

B1L103S, U6-ch78, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 61.1
Std Dev 1.814
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch78, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 36.4
Std Dev 29.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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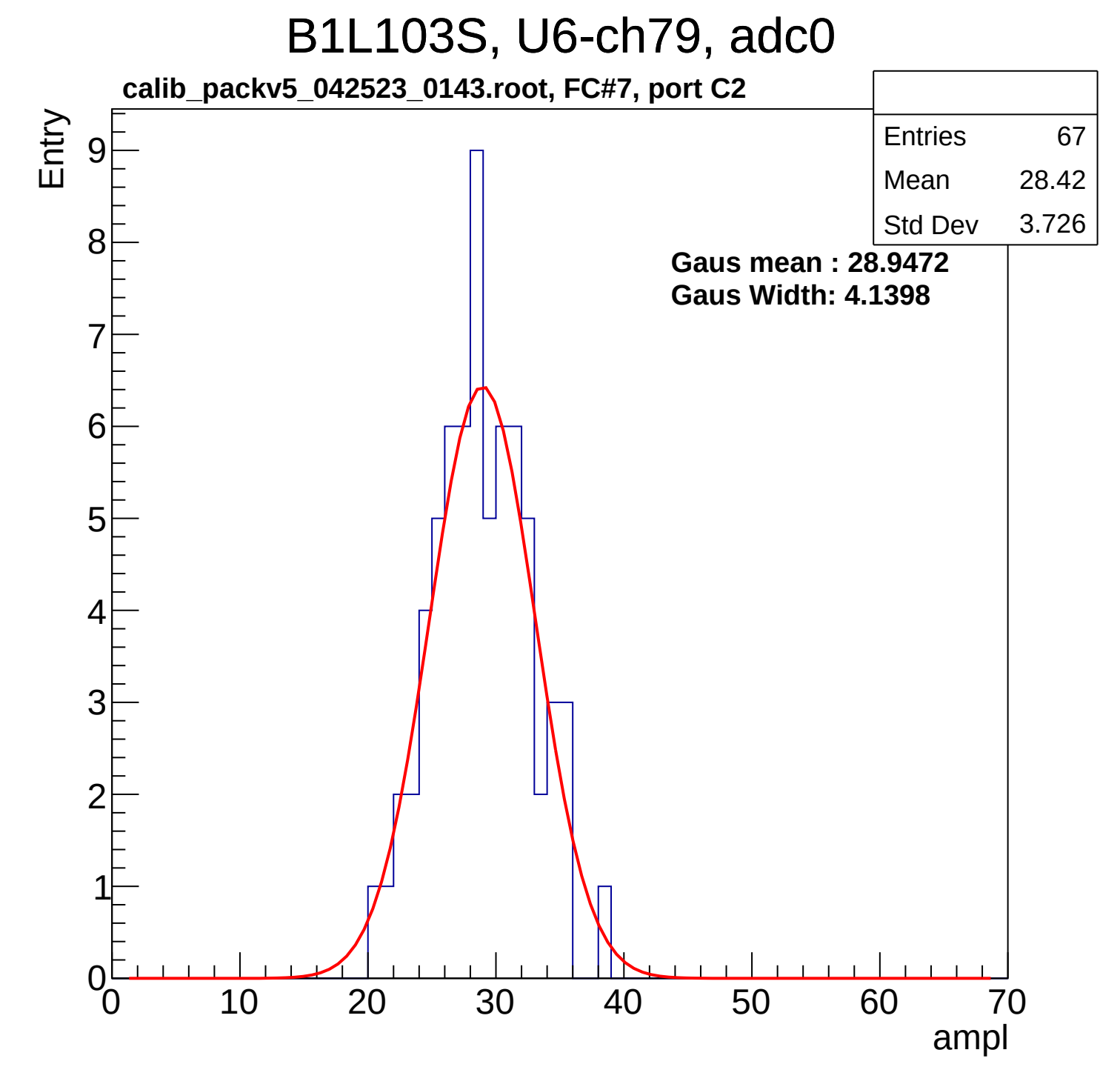
B1L103S, U6-ch79, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.42
Std Dev 3.726
Gaus mean : 28.9472
Gaus Width: 4.1398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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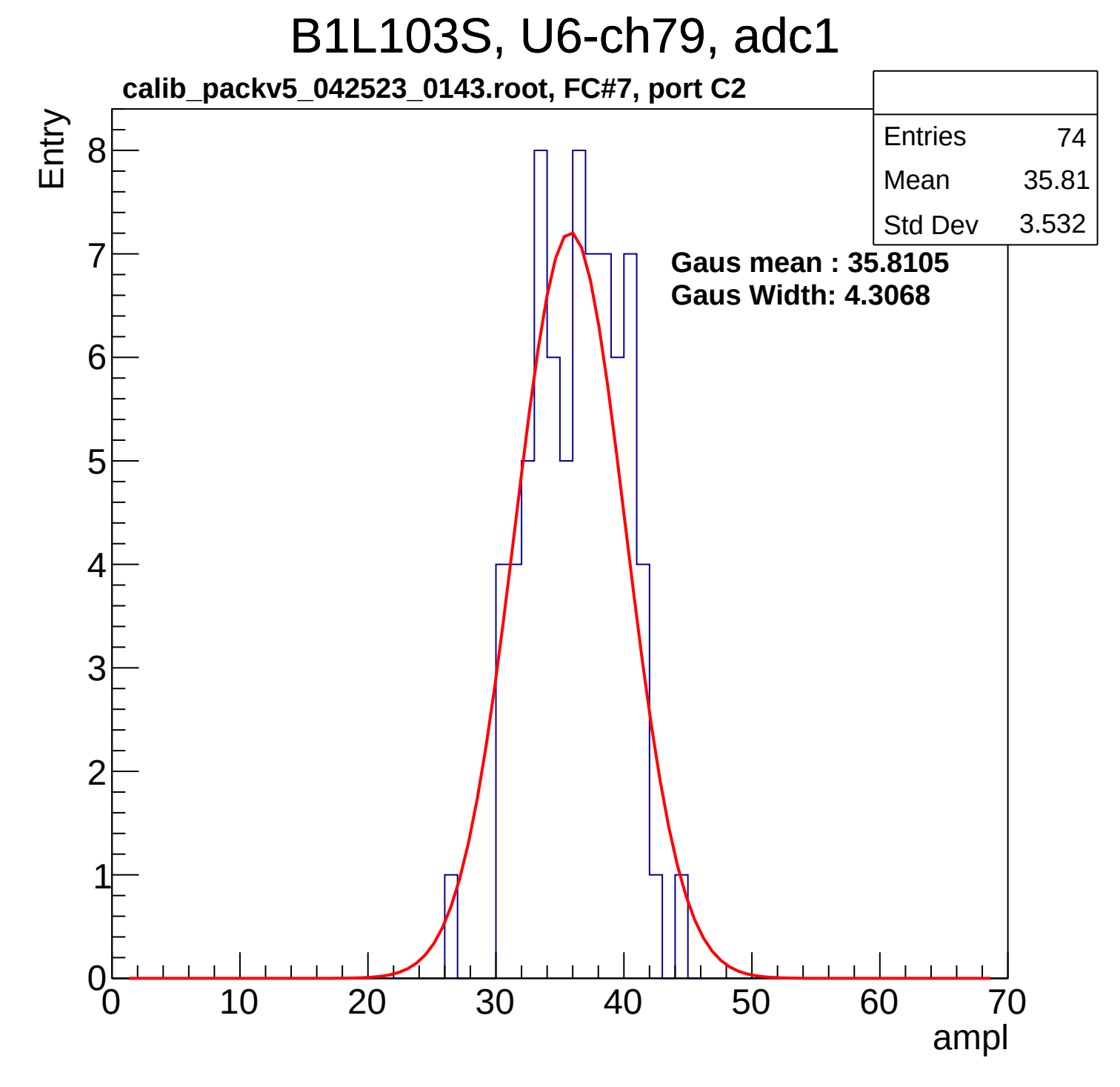
60
70


B1L103S, U6-ch79, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 35.81
Std Dev 3.532
Gaus mean : 35.8105
Gaus Width: 4.3068
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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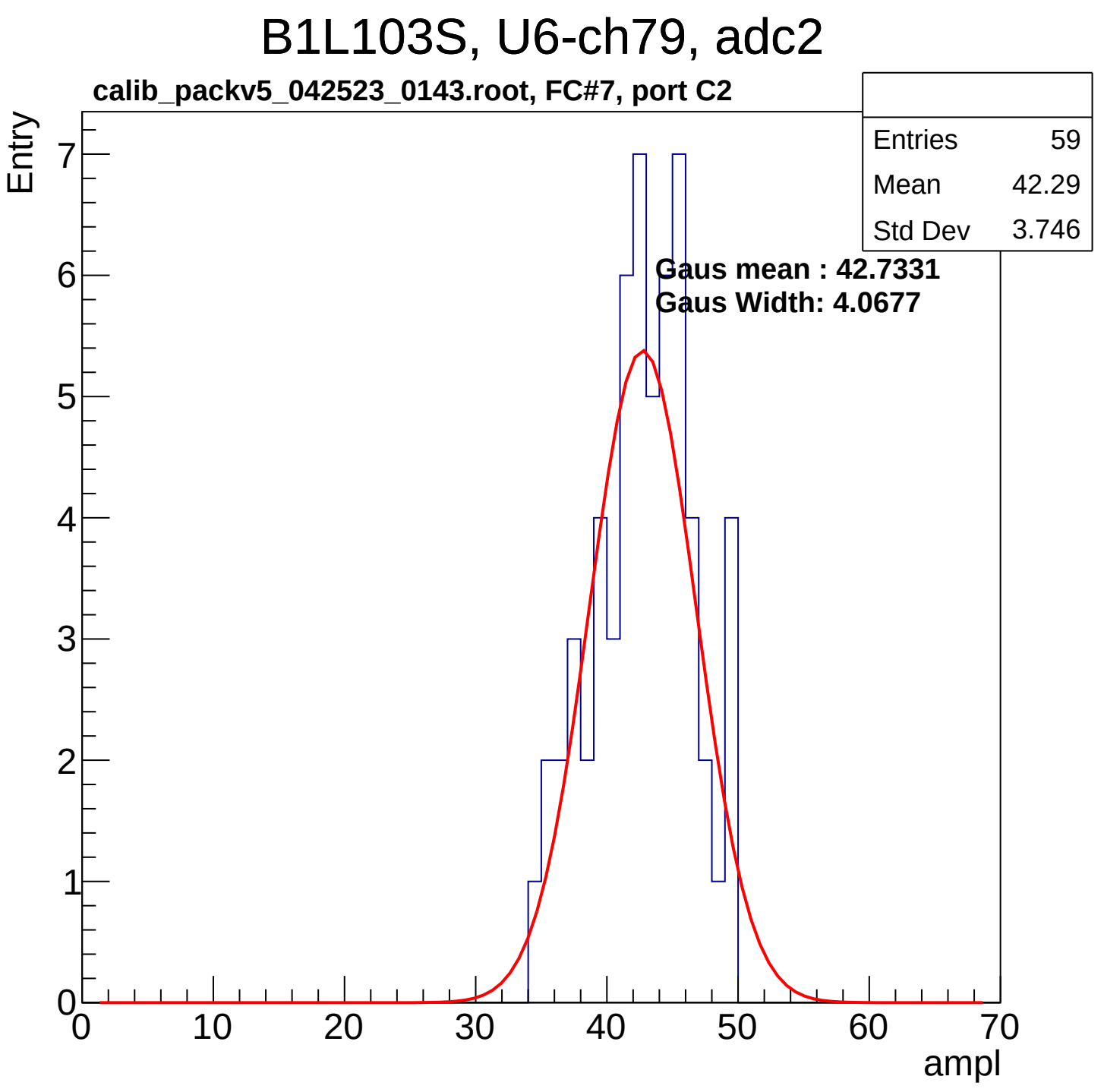
B1L103S, U6-ch79, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42.29
Std Dev 3.746
Gaus mean : 42.7331
Gaus Width: 4.0677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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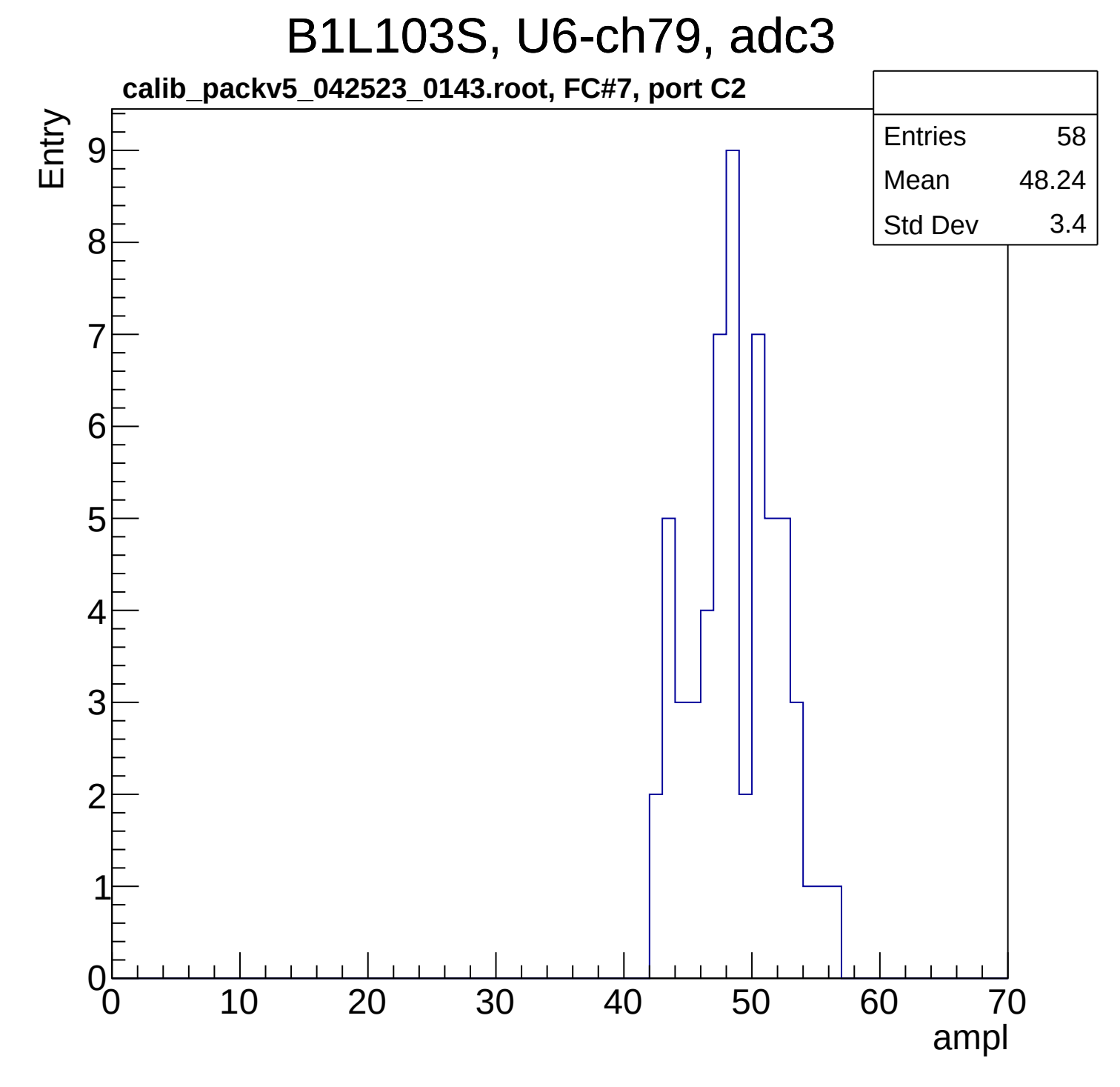
B1L103S, U6-ch79, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 48.24
Std Dev 3.4
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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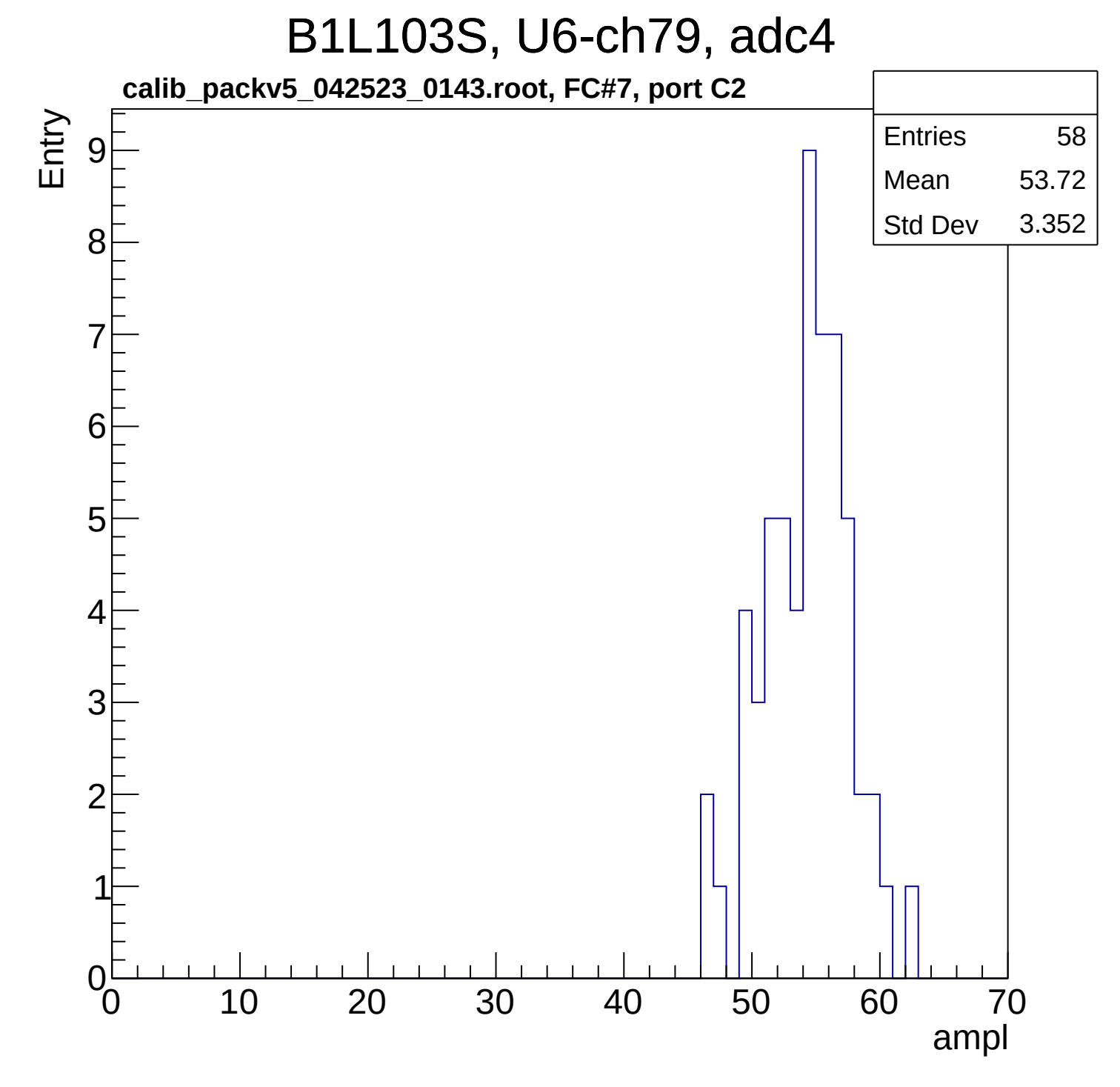
B1L103S, U6-ch79, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53.72
Std Dev 3.352
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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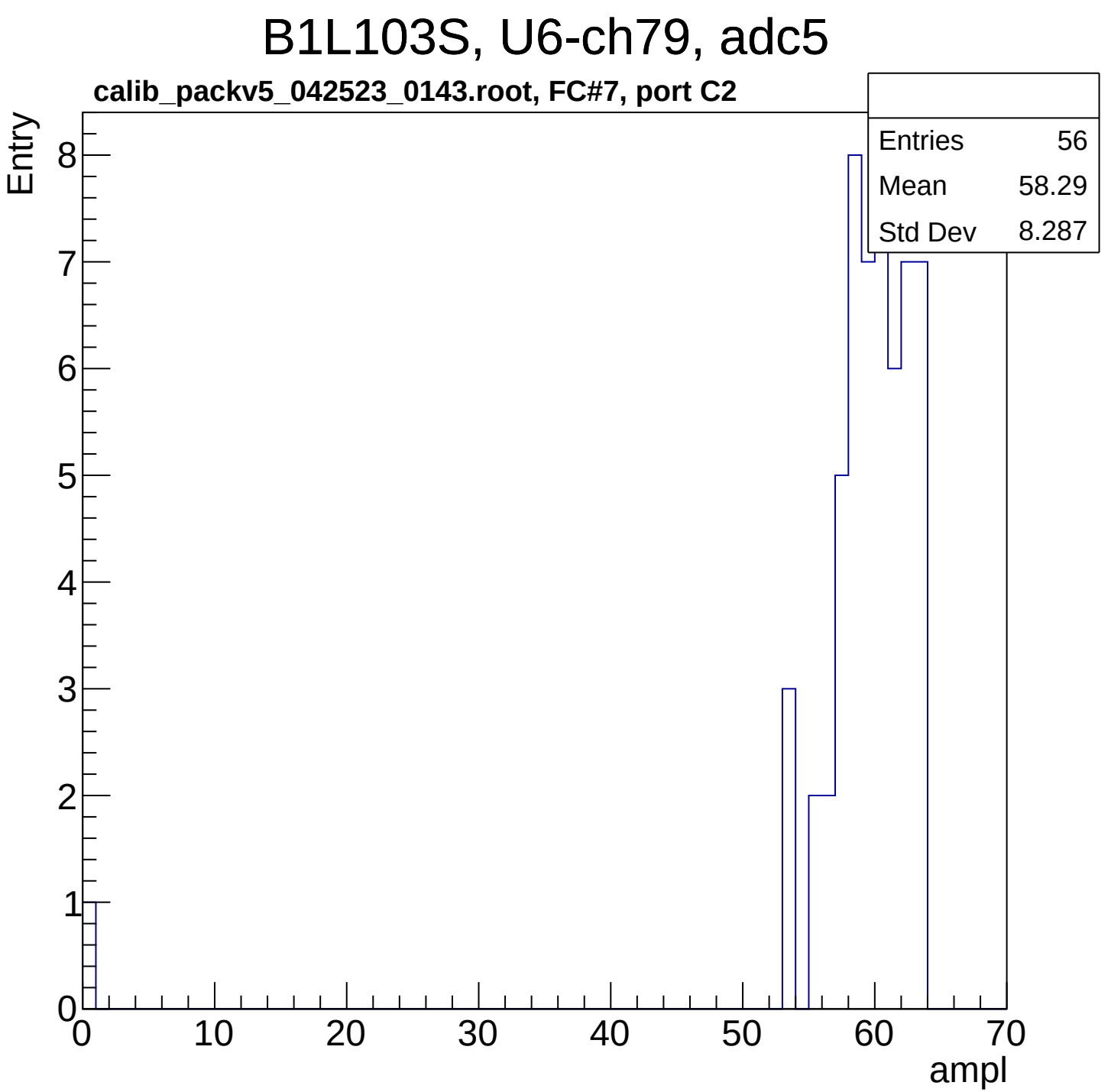
B1L103S, U6-ch79, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.29
Std Dev 8.287
Entry
ampl
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2
3
4
5
6
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0
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20
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60
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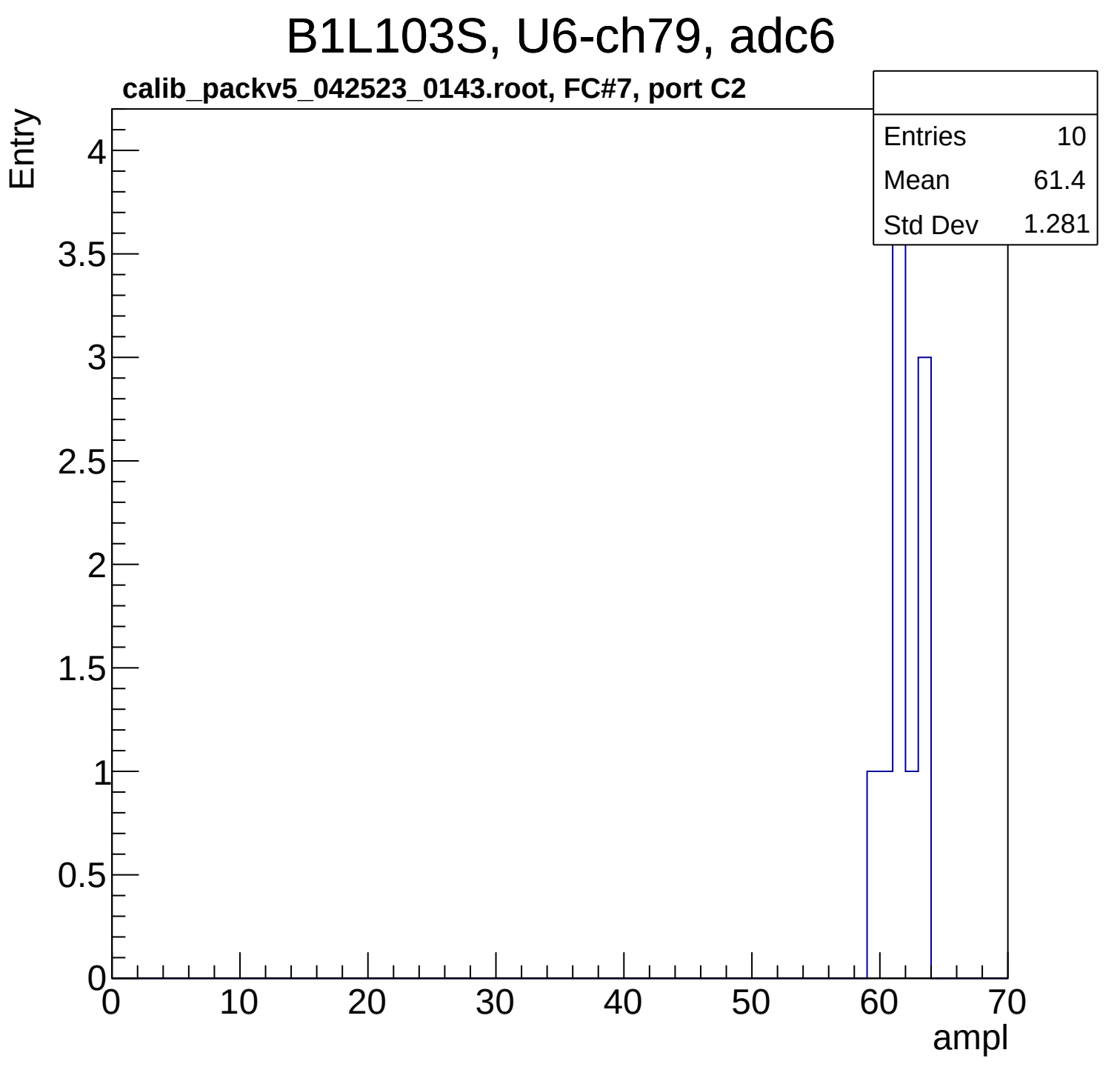
B1L103S, U6-ch79, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.4
Std Dev 1.281
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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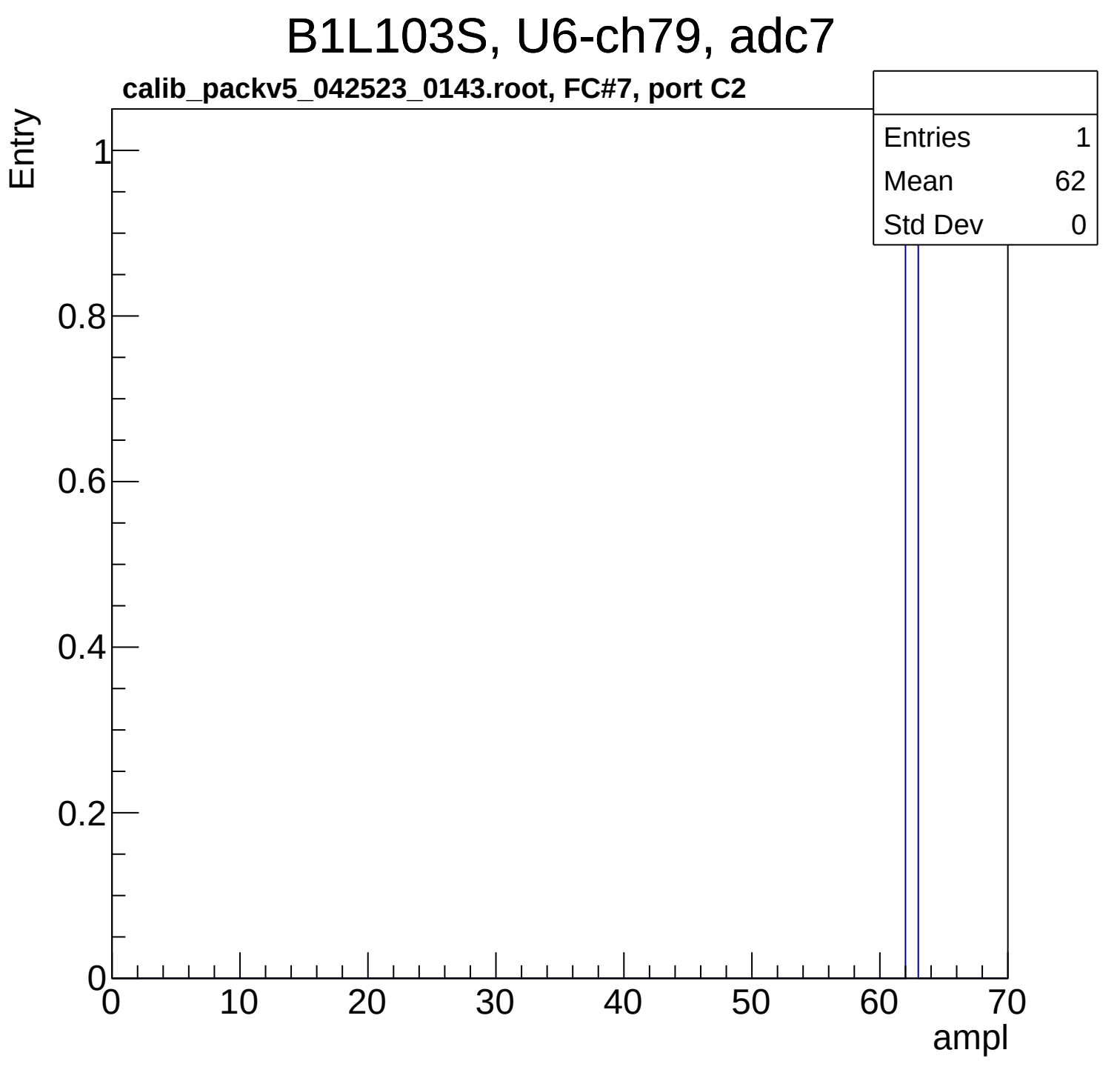
B1L103S, U6-ch79, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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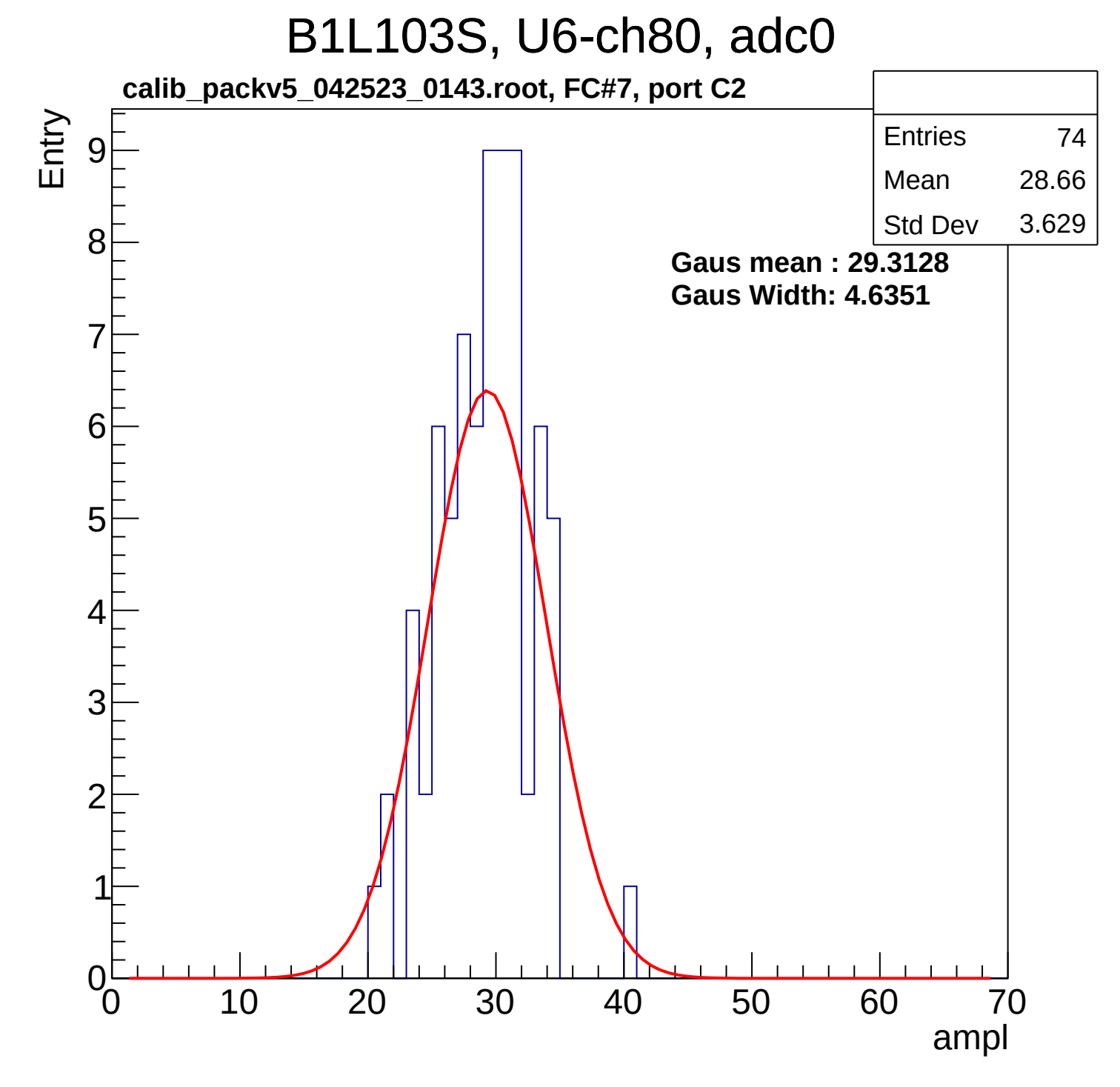
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch80, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 28.66
Std Dev 3.629
Gaus mean : 29.3128
Gaus Width: 4.6351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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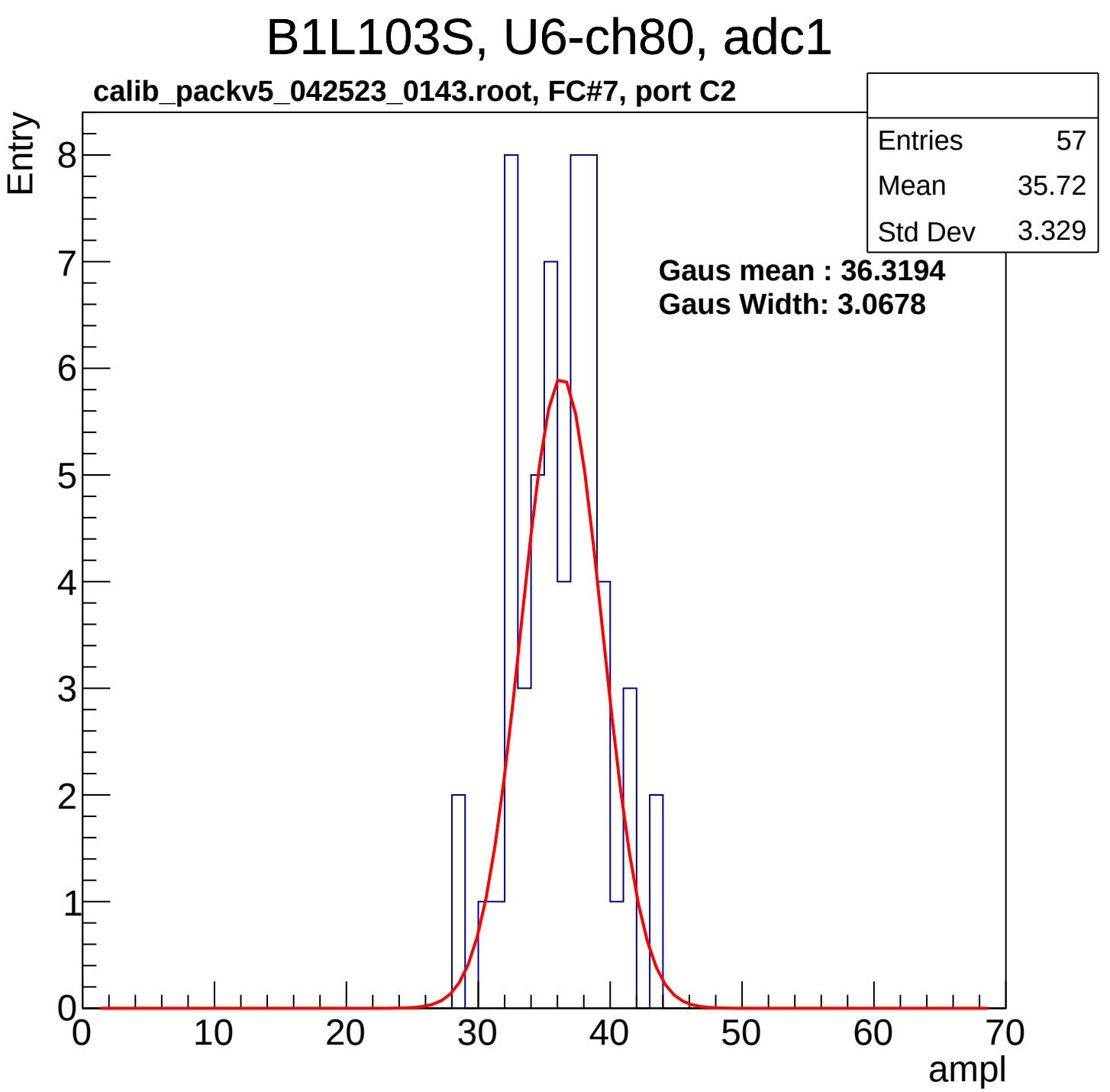
B1L103S, U6-ch80, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 35.72
Std Dev 3.329
Gaus mean : 36.3194
Gaus Width: 3.0678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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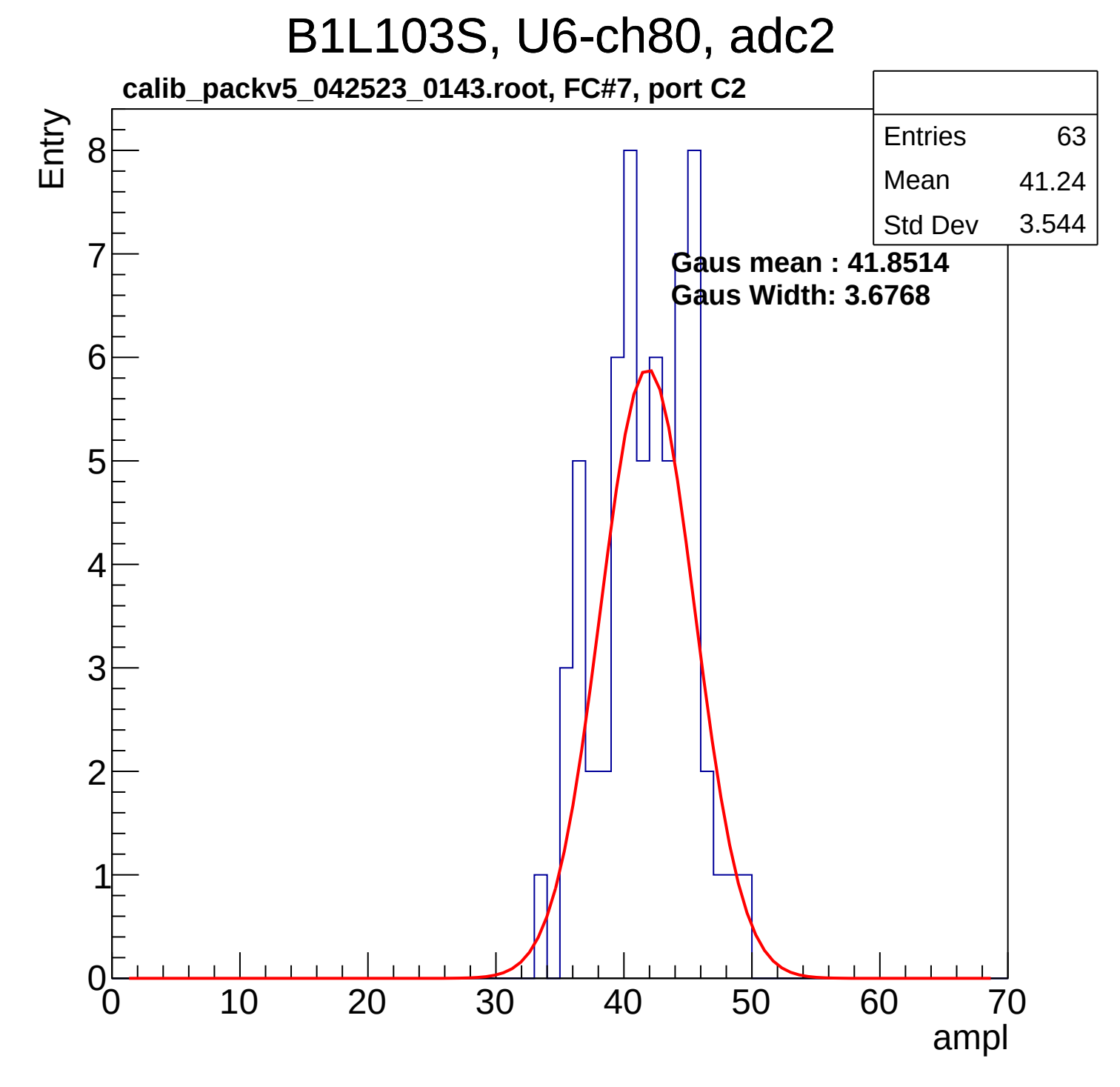
B1L103S, U6-ch80, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 41.24
Std Dev 3.544
Gaus mean : 41.8514
Gaus Width: 3.6768
Entry
ampl
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3
4
5
6
7
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0
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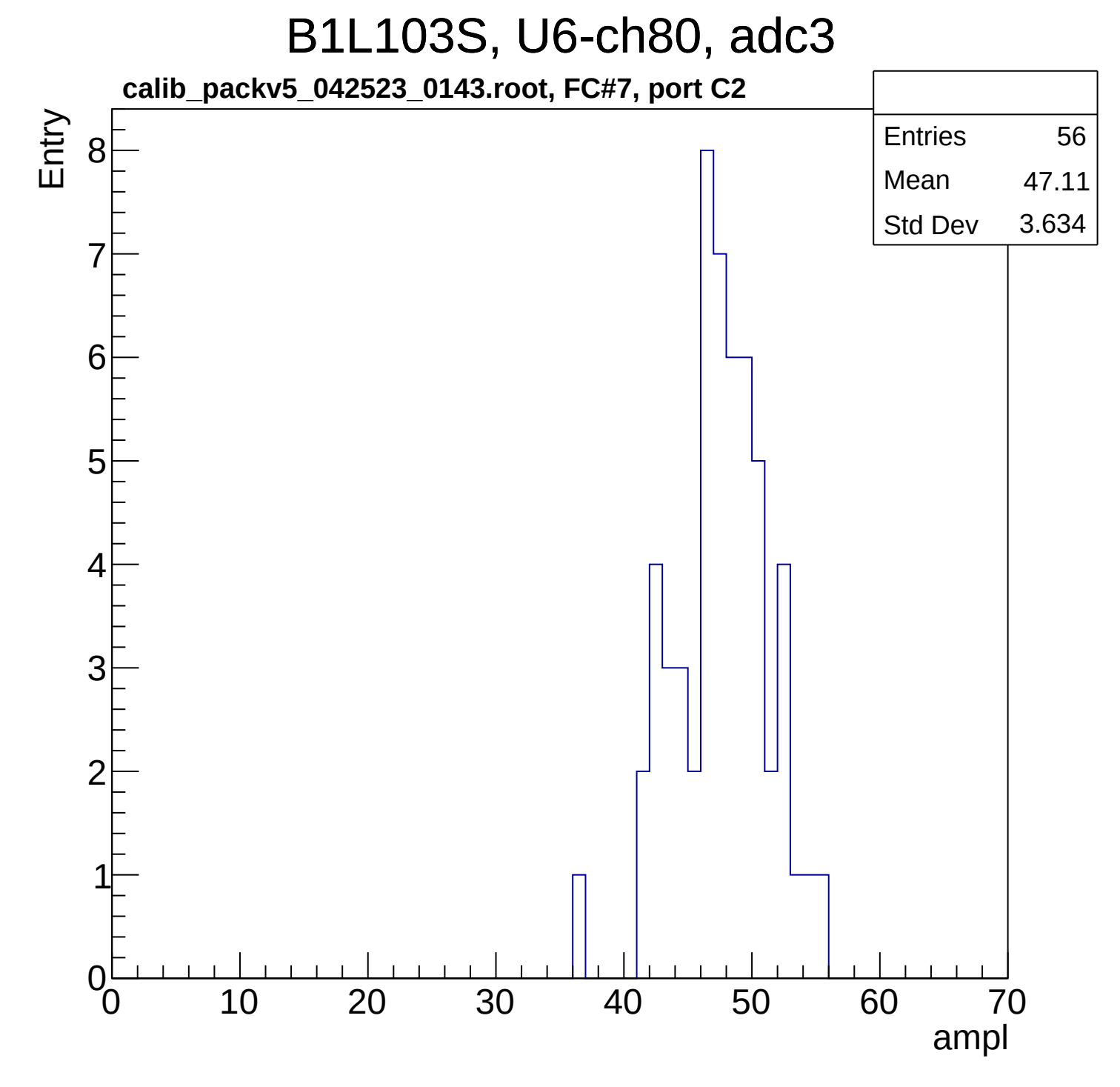
B1L103S, U6-ch80, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 47.11
Std Dev 3.634
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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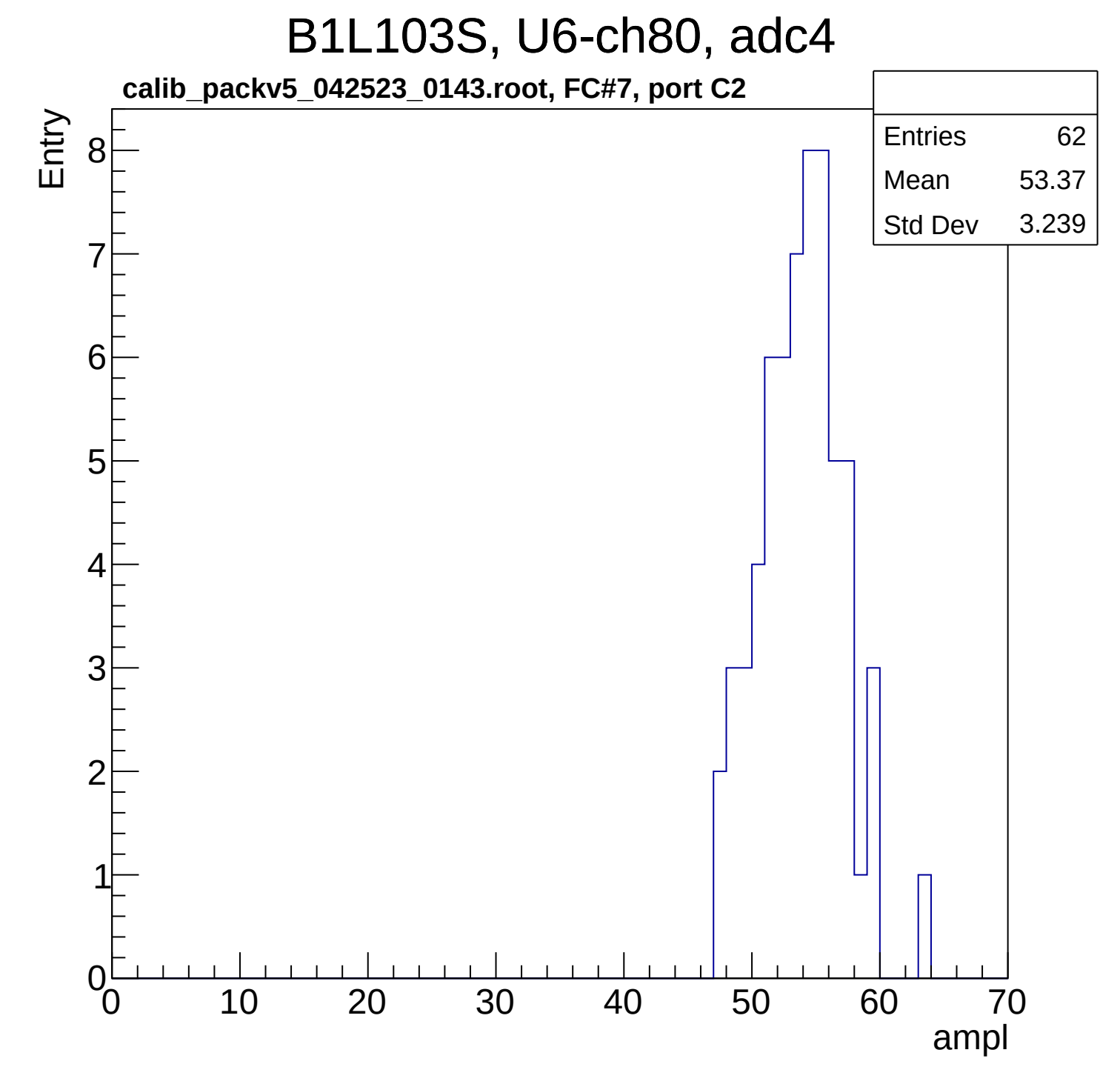
B1L103S, U6-ch80, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 53.37
Std Dev 3.239
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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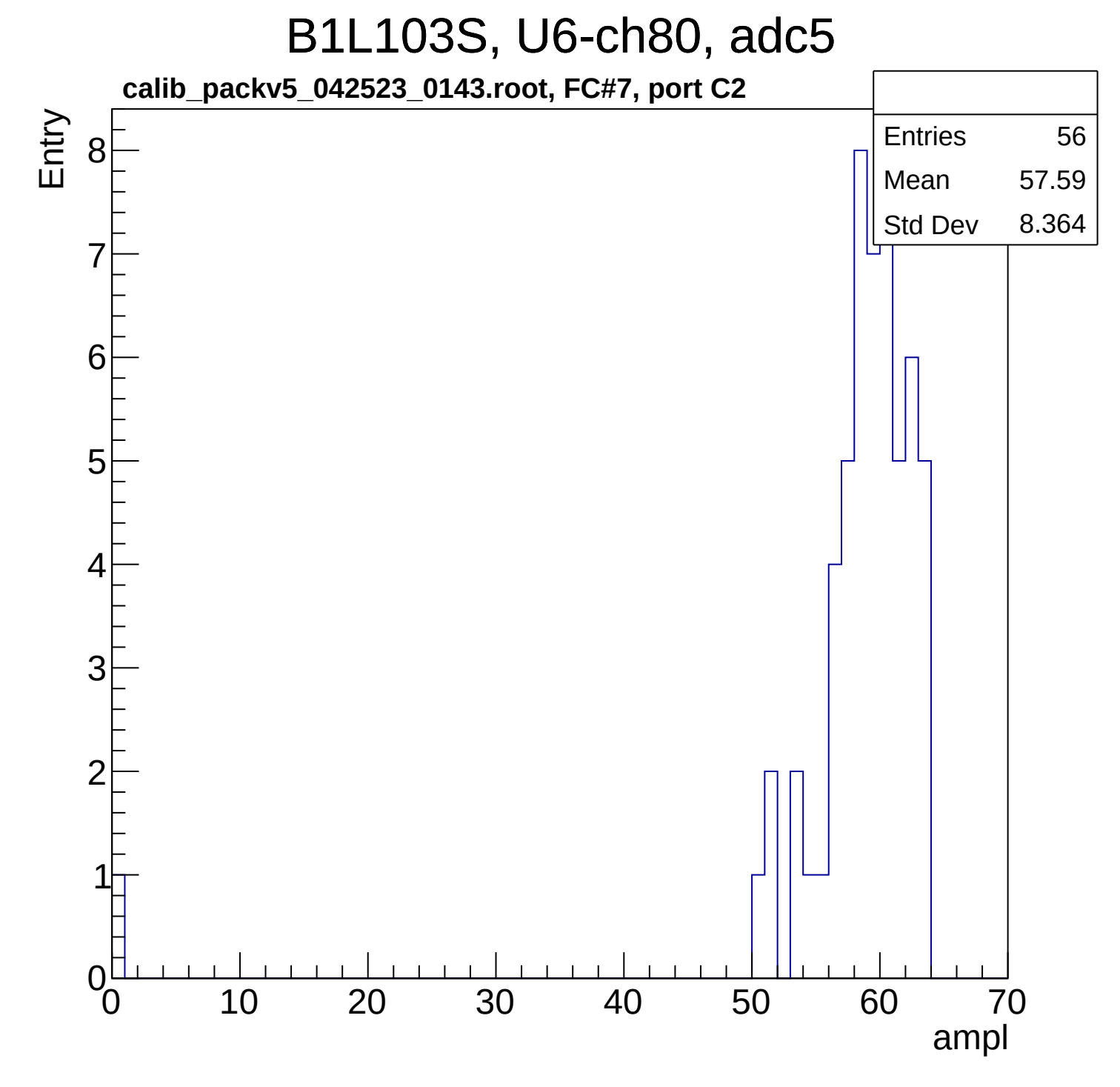
B1L103S, U6-ch80, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.59
Std Dev 8.364
Entry
ampl
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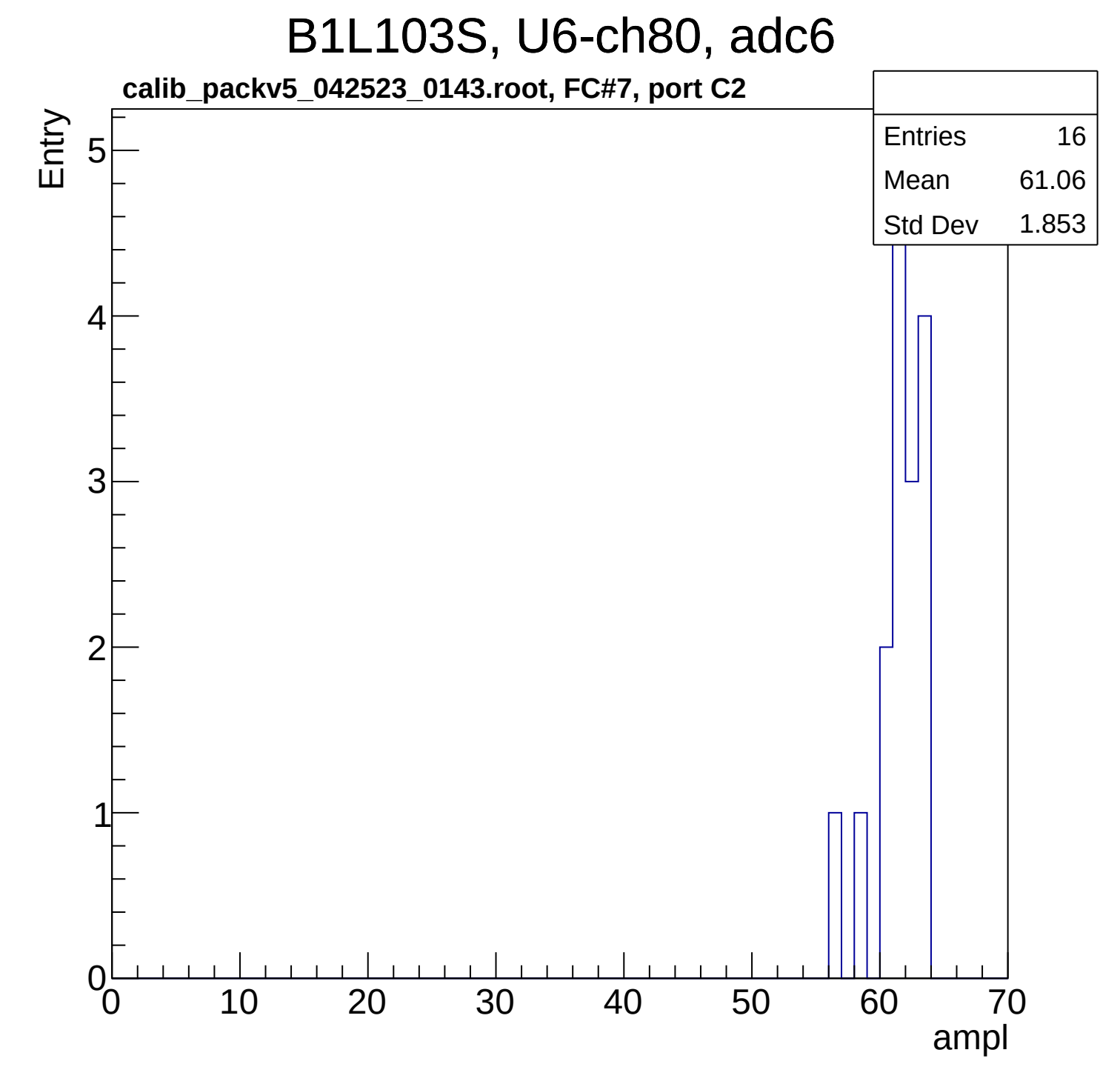
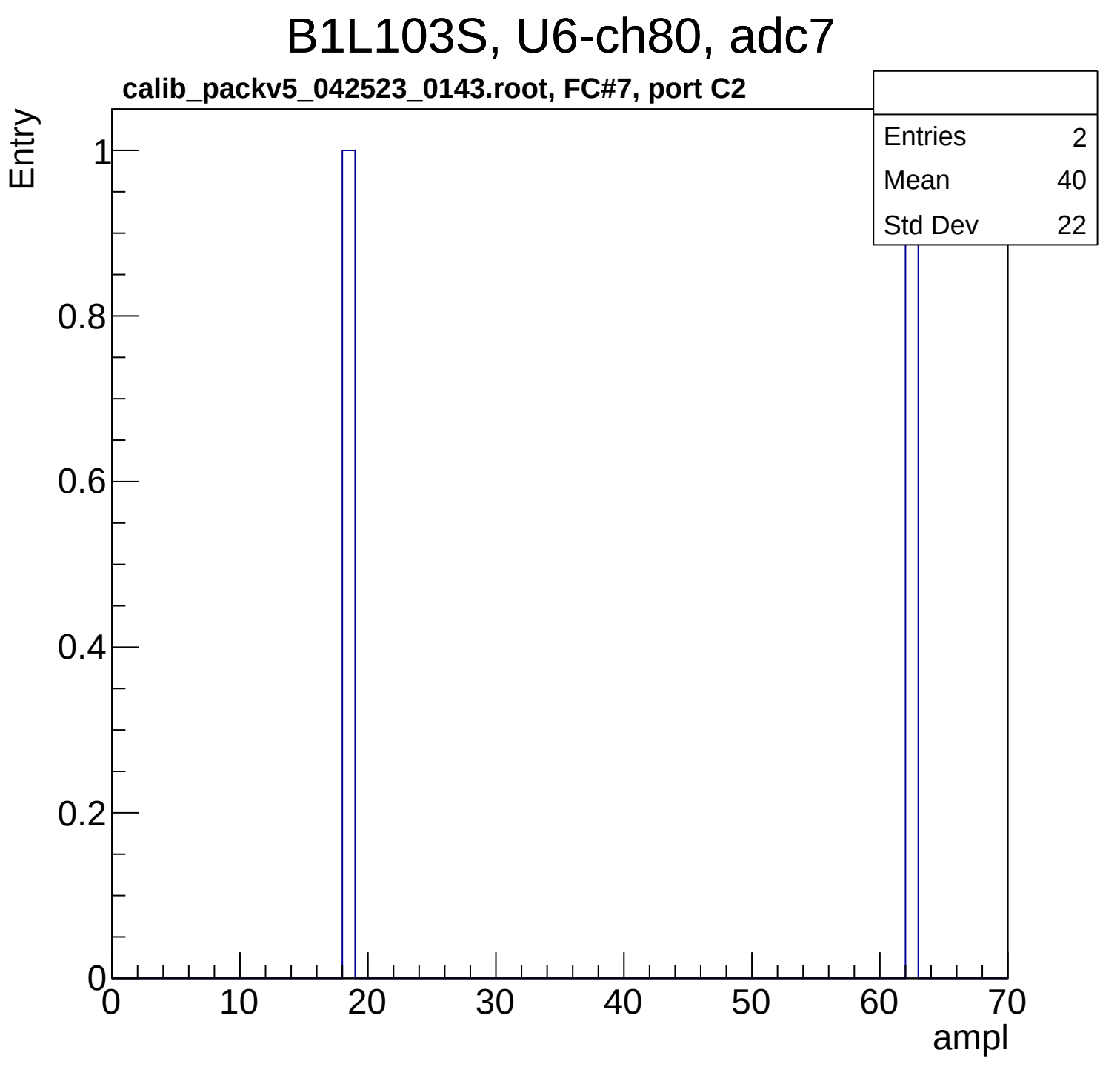
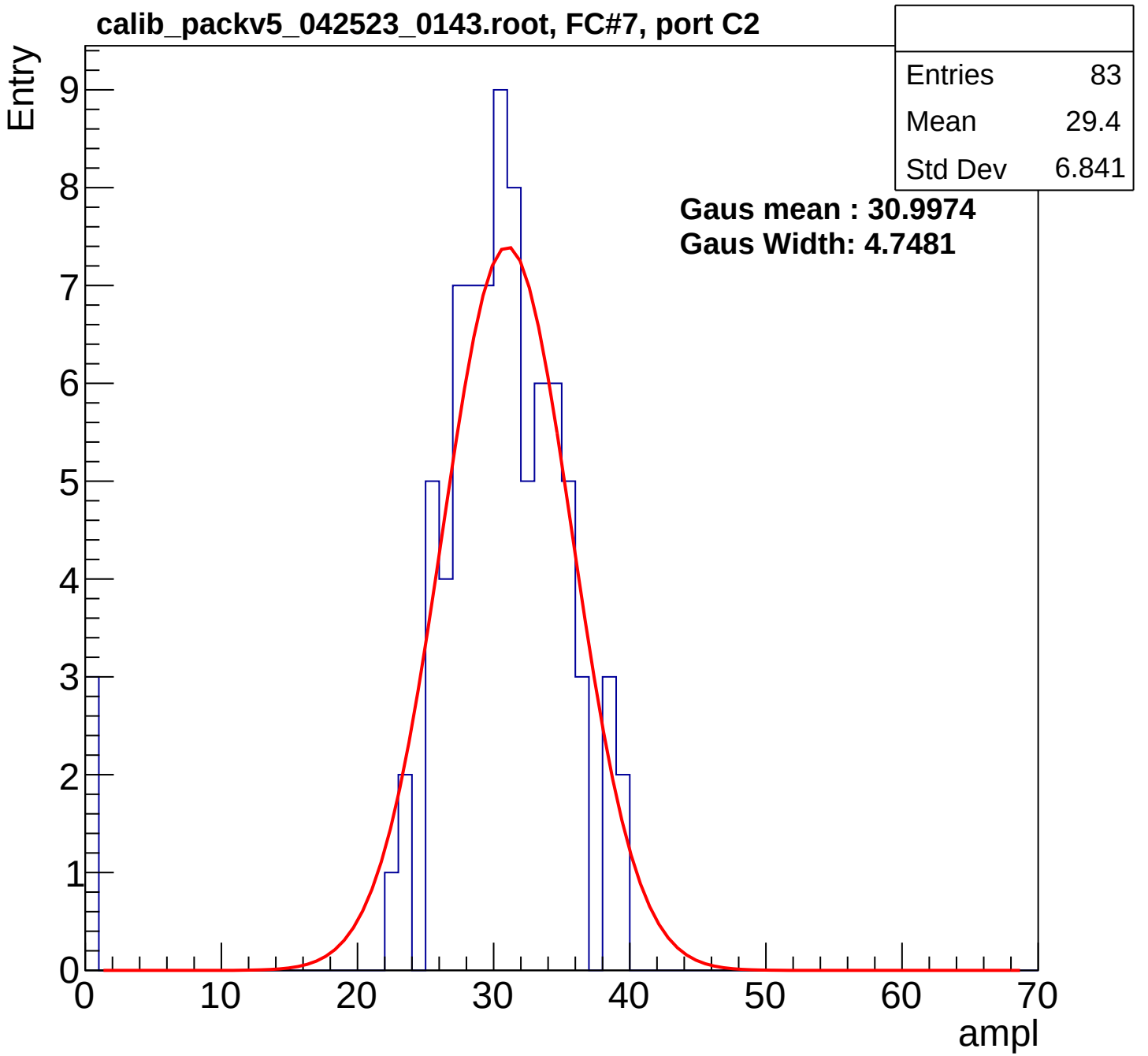
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch80, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.06
Std Dev 1.853
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch80, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 40
Std Dev 22
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U6-ch81, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 29.4
Std Dev 6.841
Gaus mean : 30.9974
Gaus Width: 4.7481
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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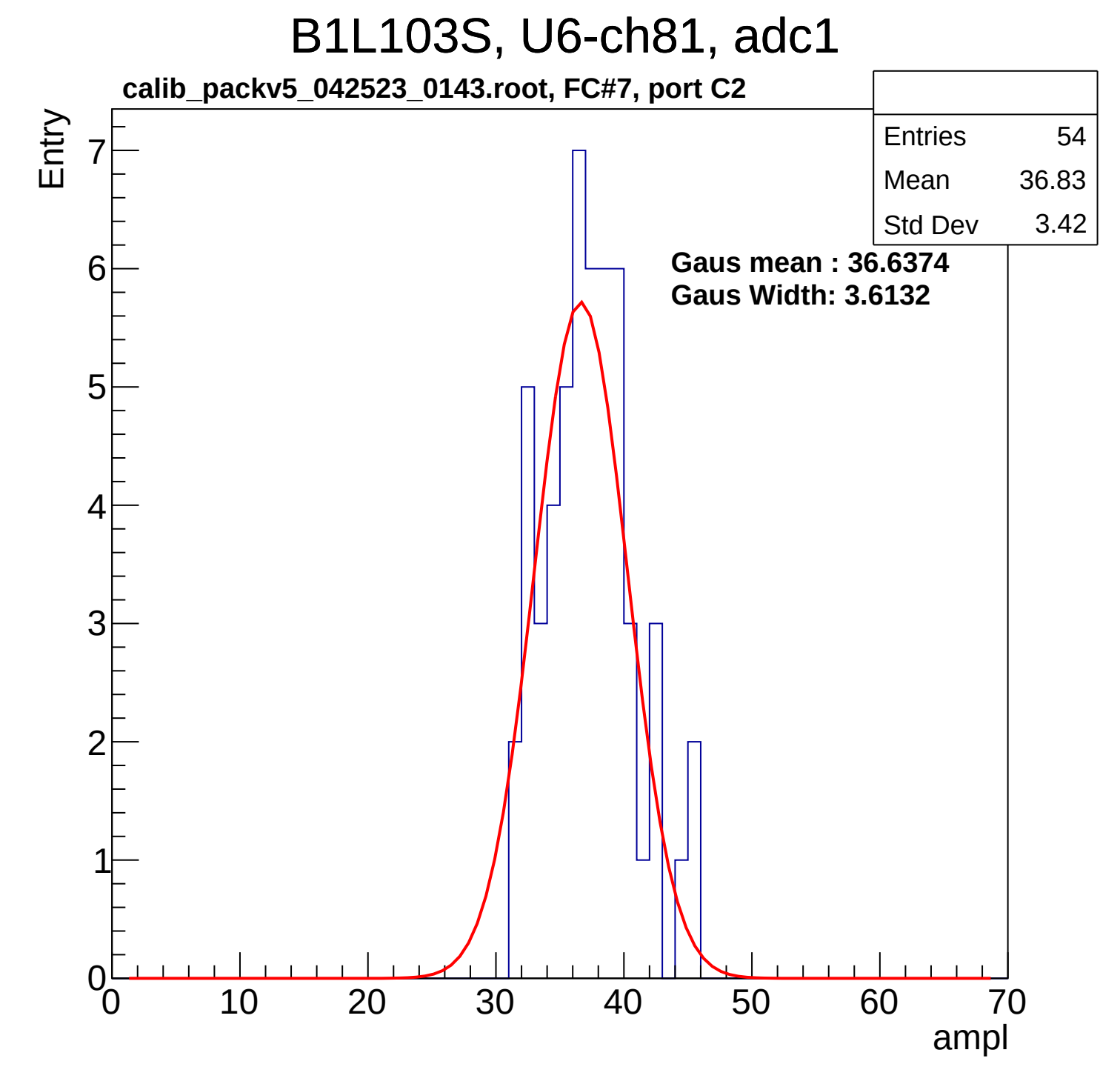
B1L103S, U6-ch81, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 36.83
Std Dev 3.42
Gaus mean : 36.6374
Gaus Width: 3.6132
Entry
ampl
0
1
2
3
4
5
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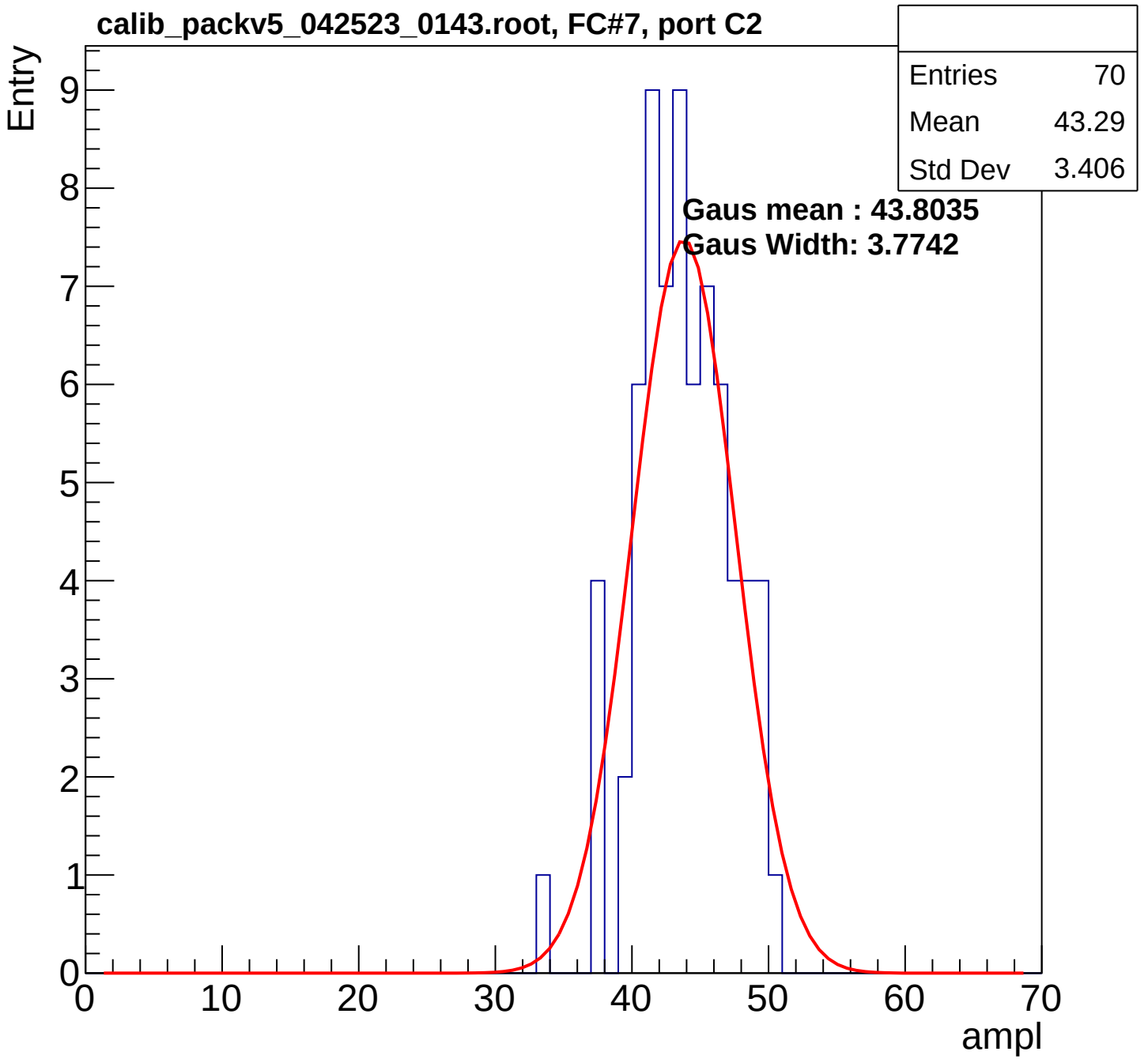
B1L103S, U6-ch81, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 43.29
Std Dev 3.406
Gaus mean : 43.8035
Gaus Width: 3.7742
Entry
ampl
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2
3
4
5
6
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8
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0
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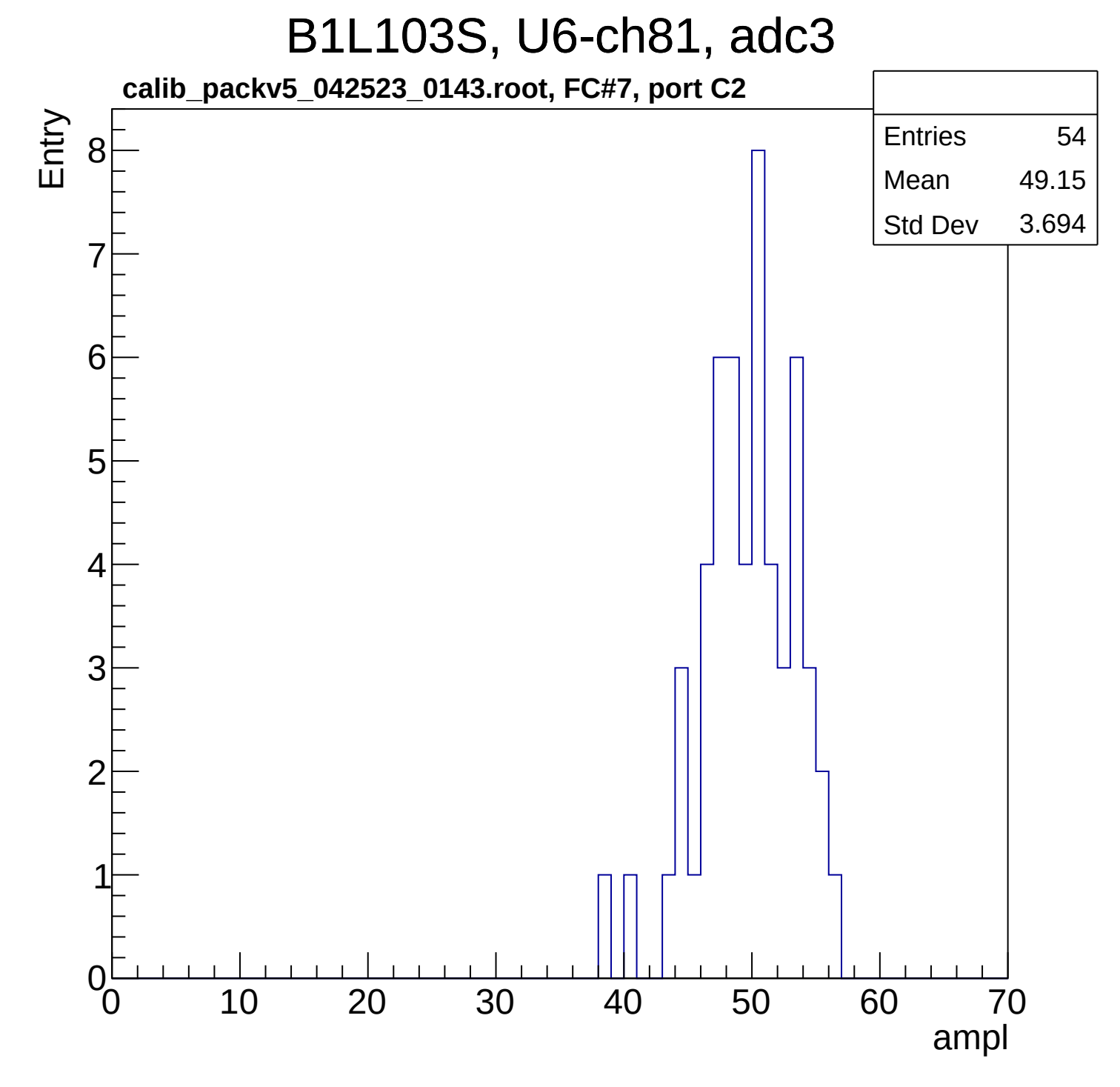
B1L103S, U6-ch81, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.15
Std Dev 3.694
Entry
ampl
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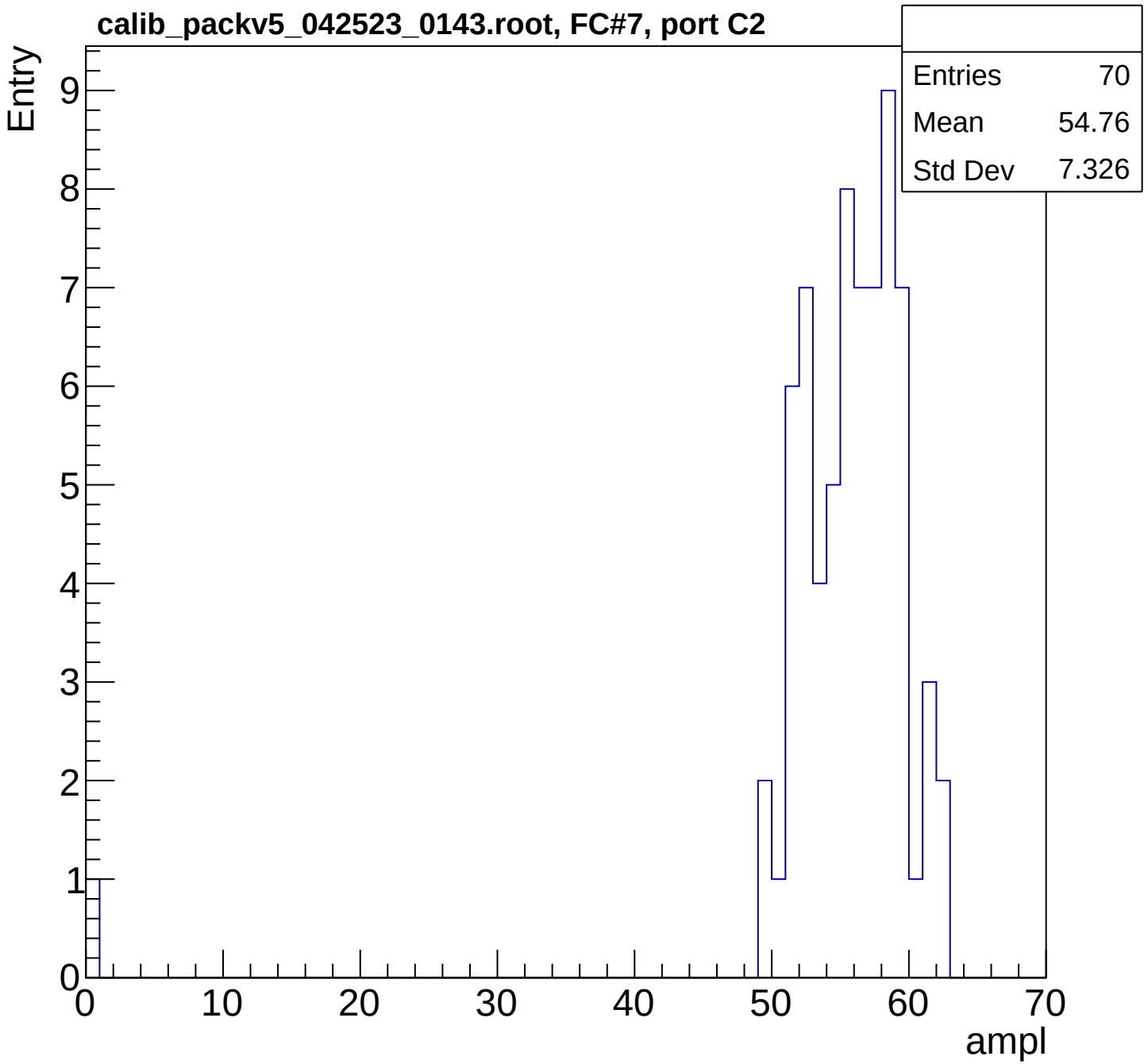
B1L103S, U6-ch81, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 54.76
Std Dev 7.326
Entry
ampl
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2
3
4
5
6
7
8
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0
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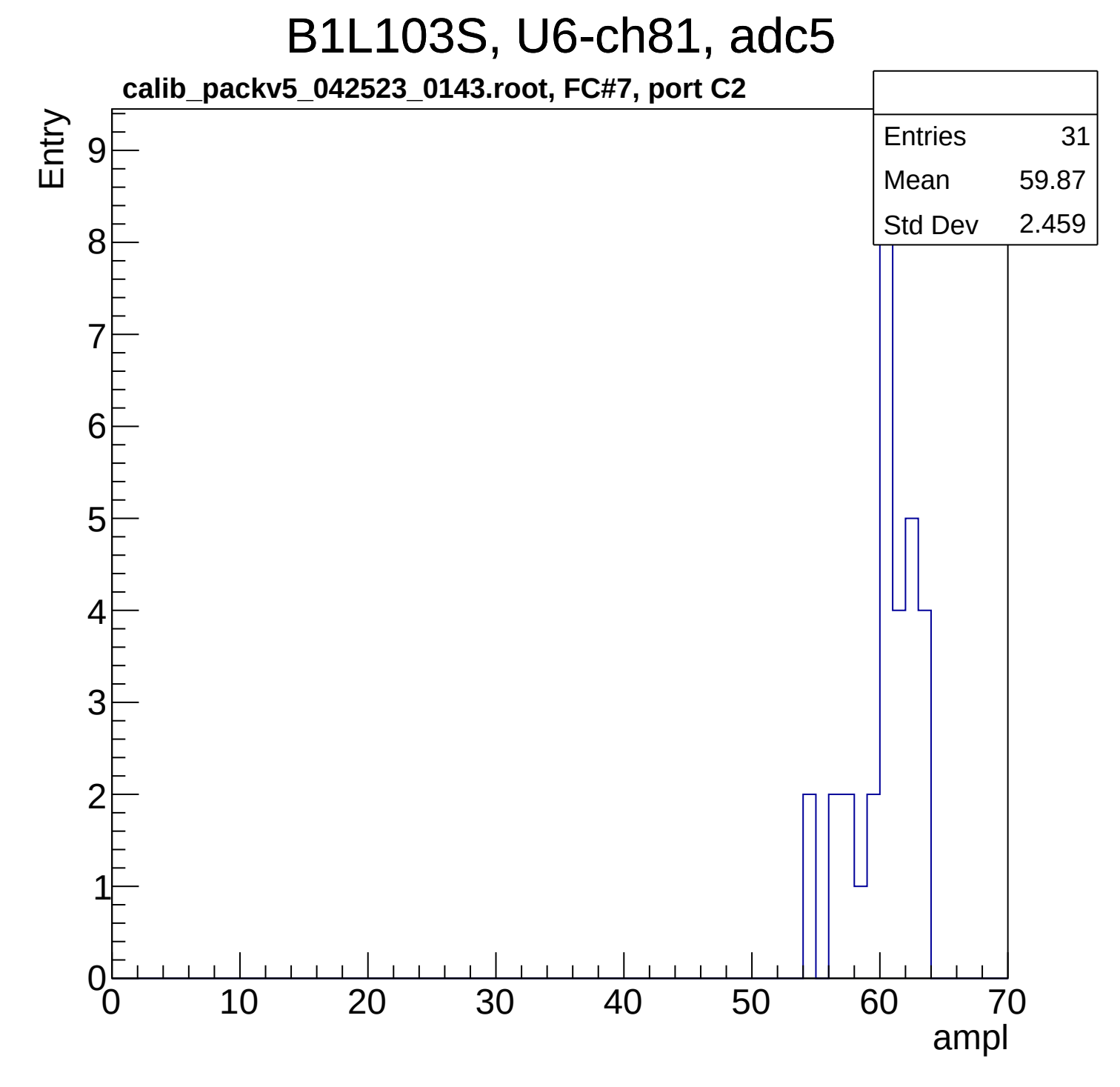
B1L103S, U6-ch81, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 59.87
Std Dev 2.459
Entry
ampl
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6
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0
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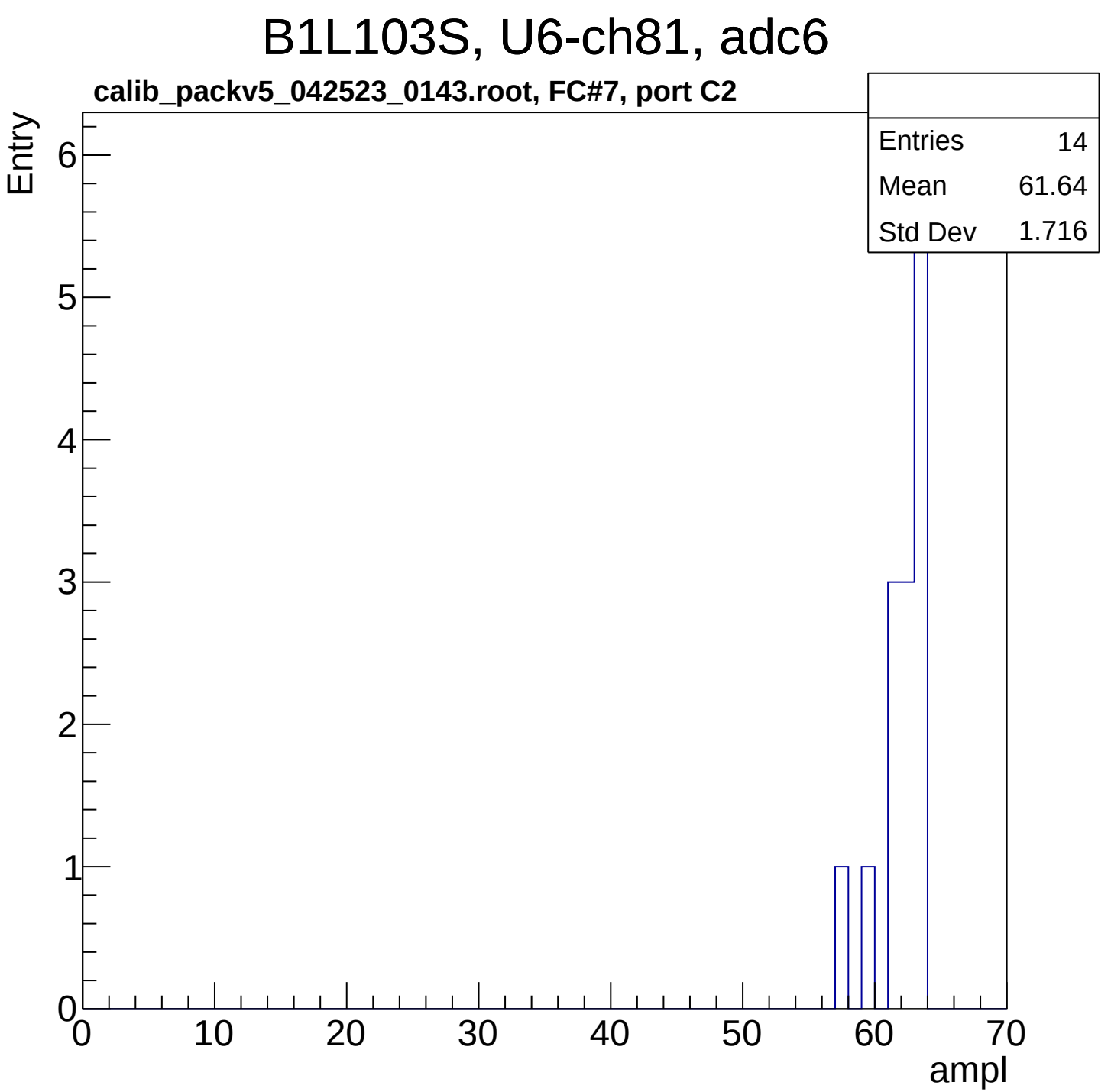
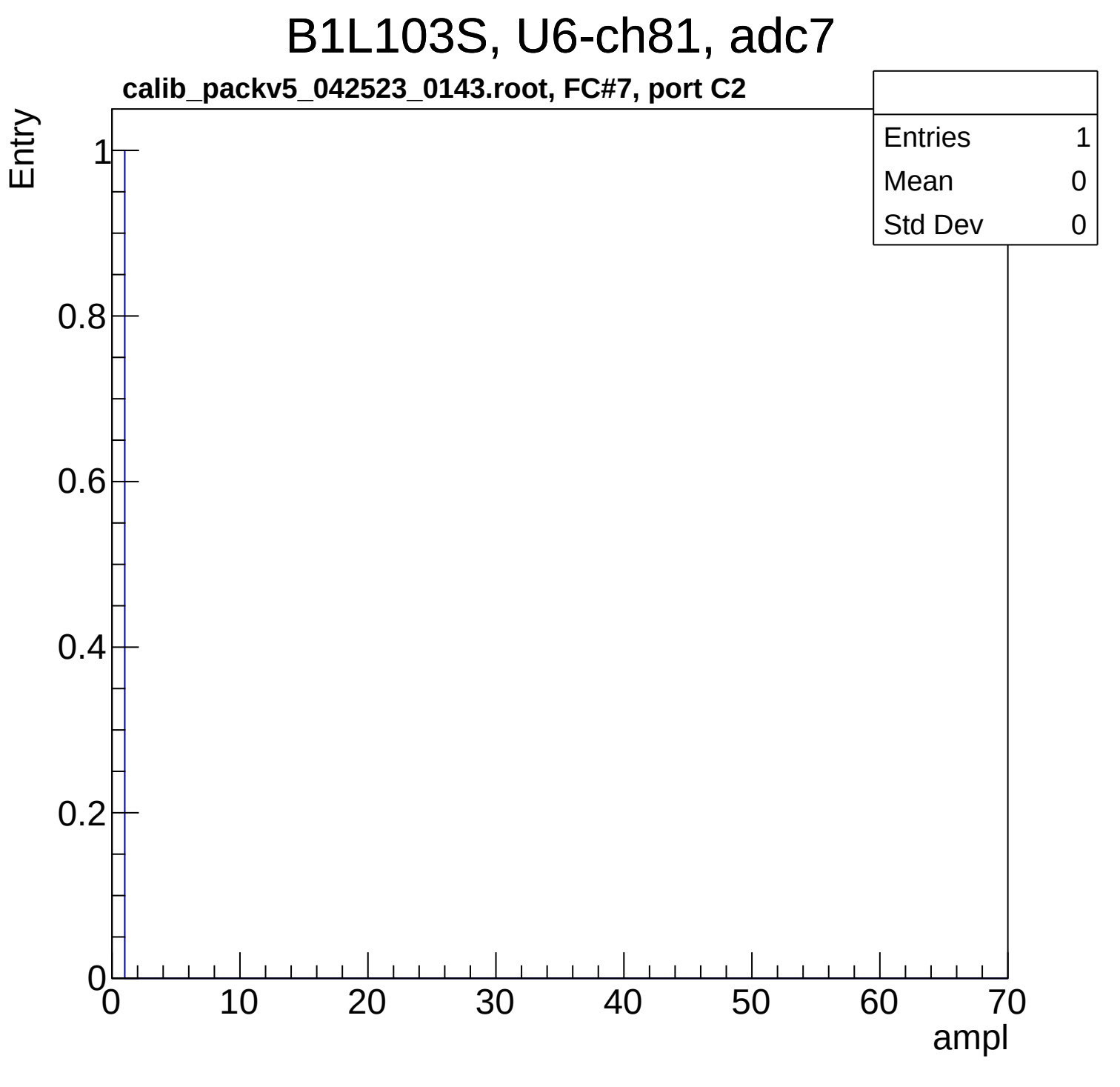
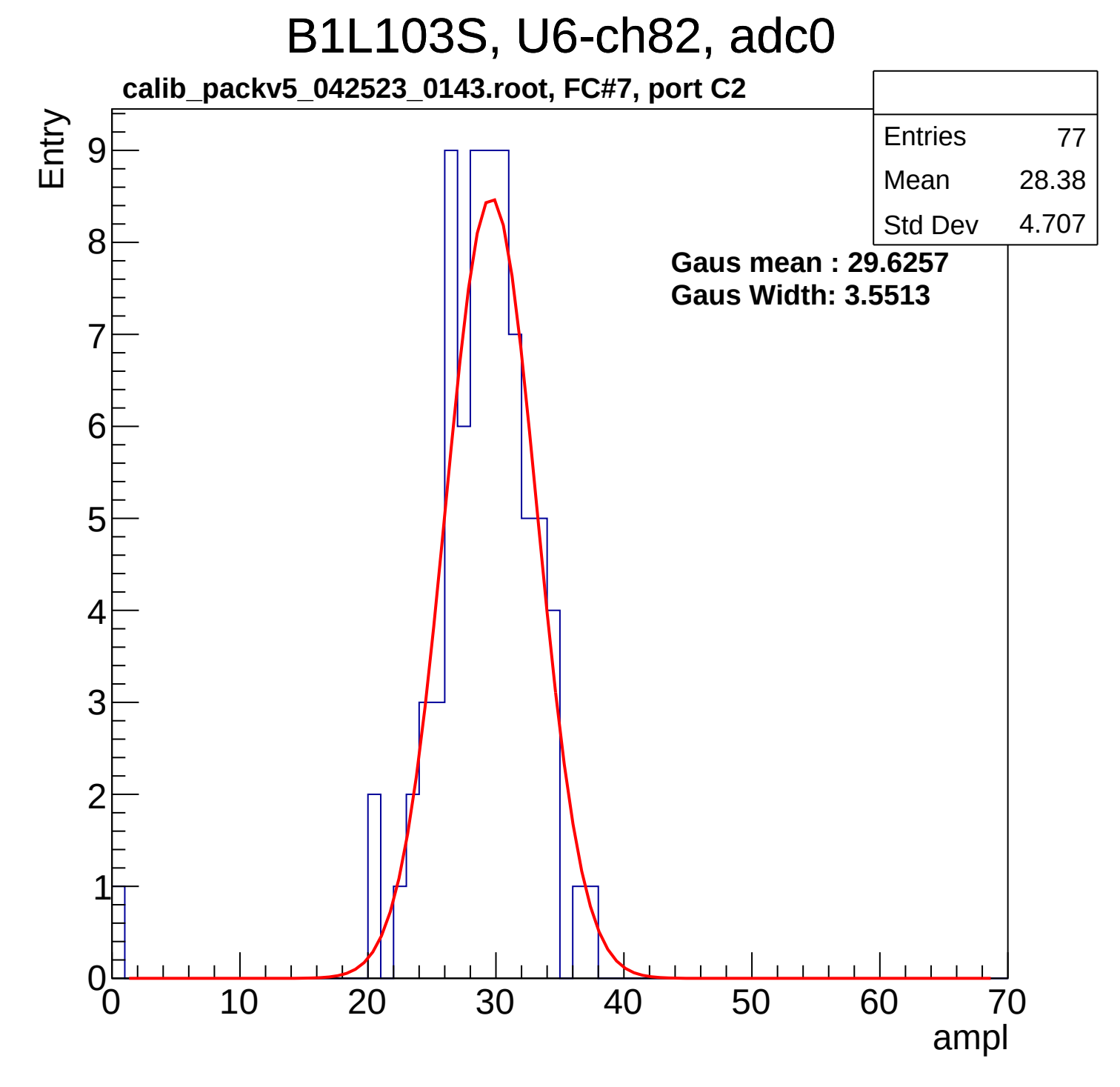

B1L103S, U6-ch81, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 61.64
Std Dev 1.716
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch81, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch82, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 28.38
Std Dev 4.707
Gaus mean : 29.6257
Gaus Width: 3.5513
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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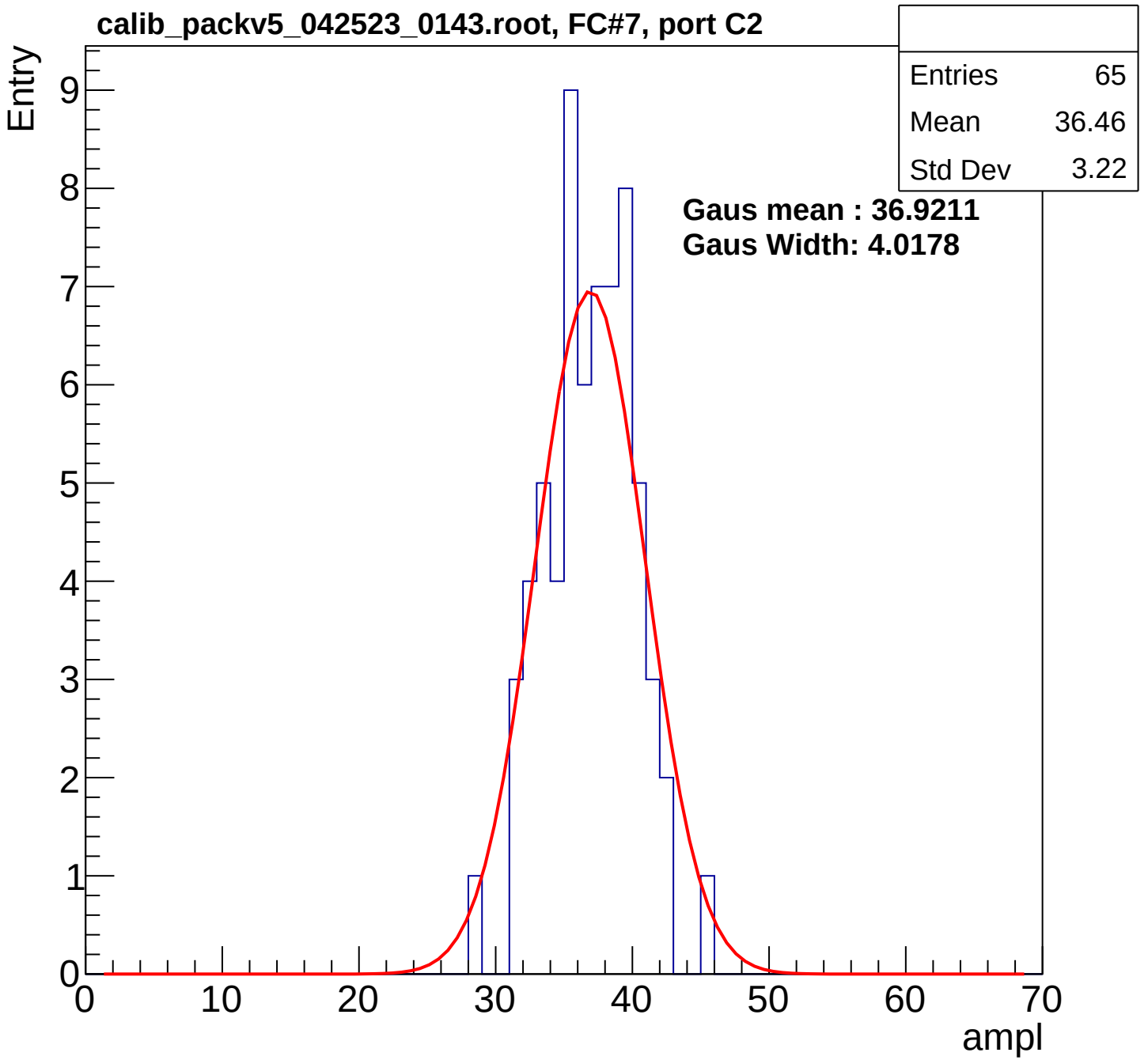
B1L103S, U6-ch82, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 36.46
Std Dev 3.22
Gaus mean : 36.9211
Gaus Width: 4.0178
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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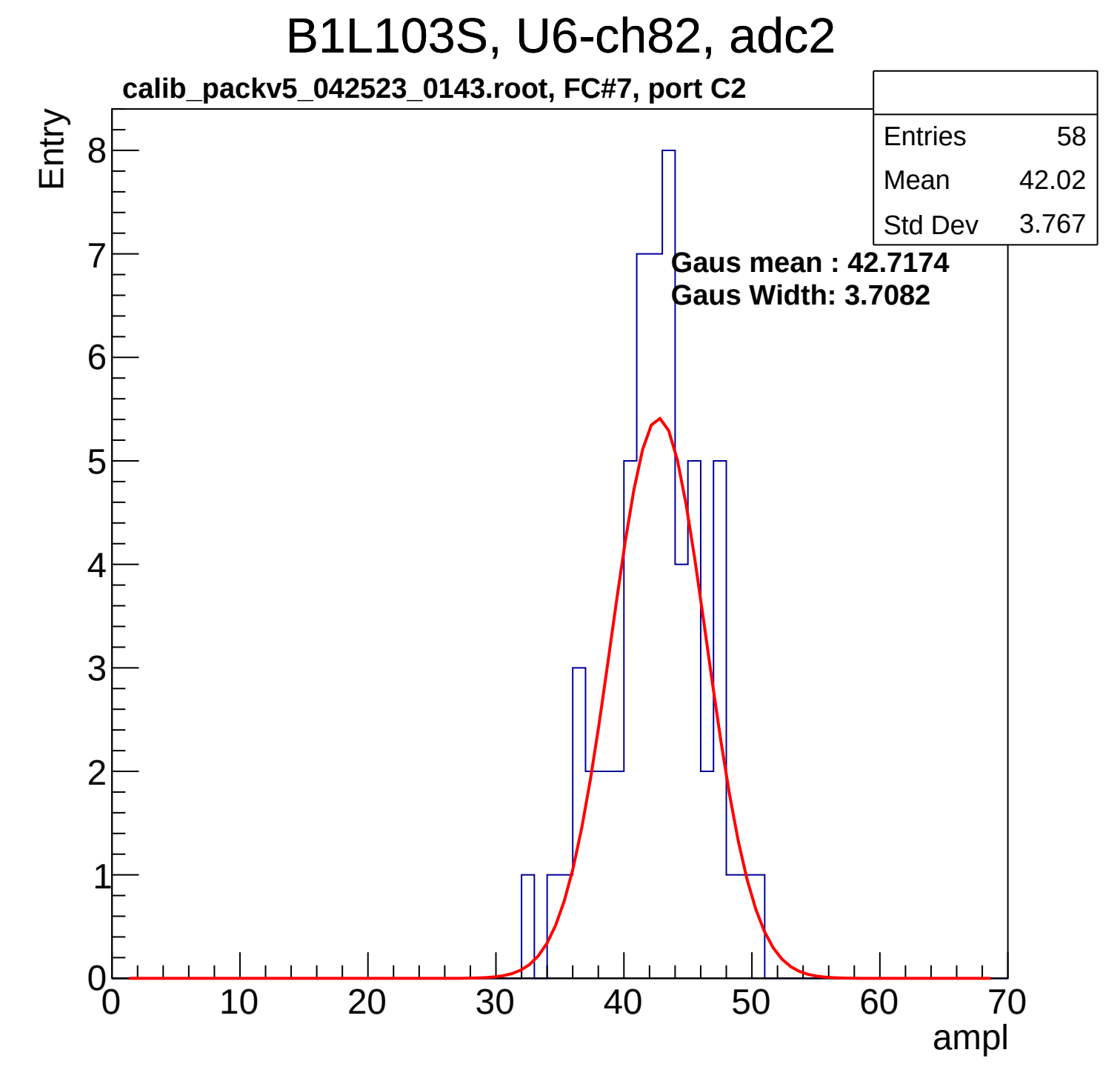
B1L103S, U6-ch82, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 42.02
Std Dev 3.767
Gaus mean : 42.7174
Gaus Width: 3.7082
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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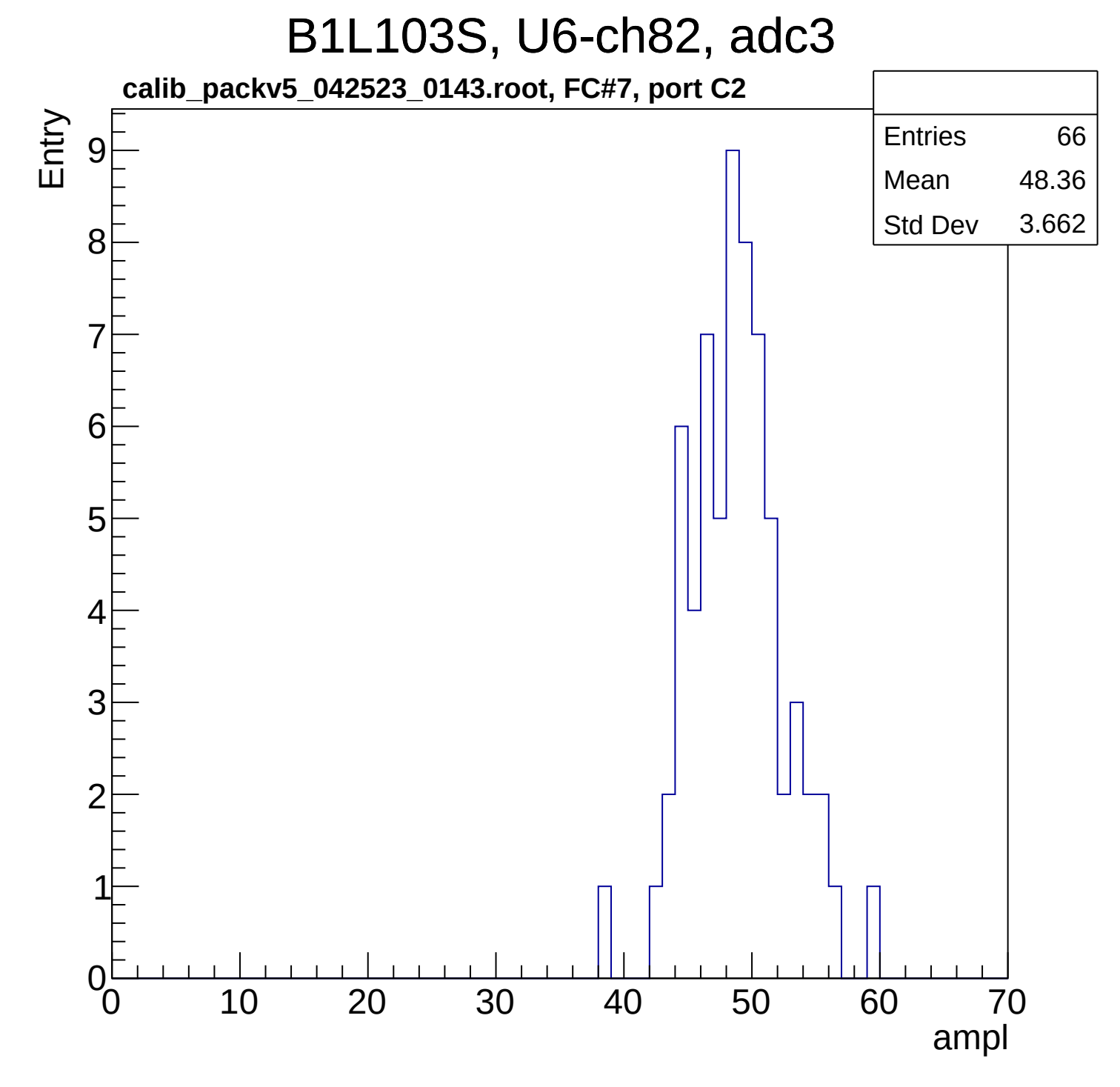
B1L103S, U6-ch82, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.36
Std Dev 3.662
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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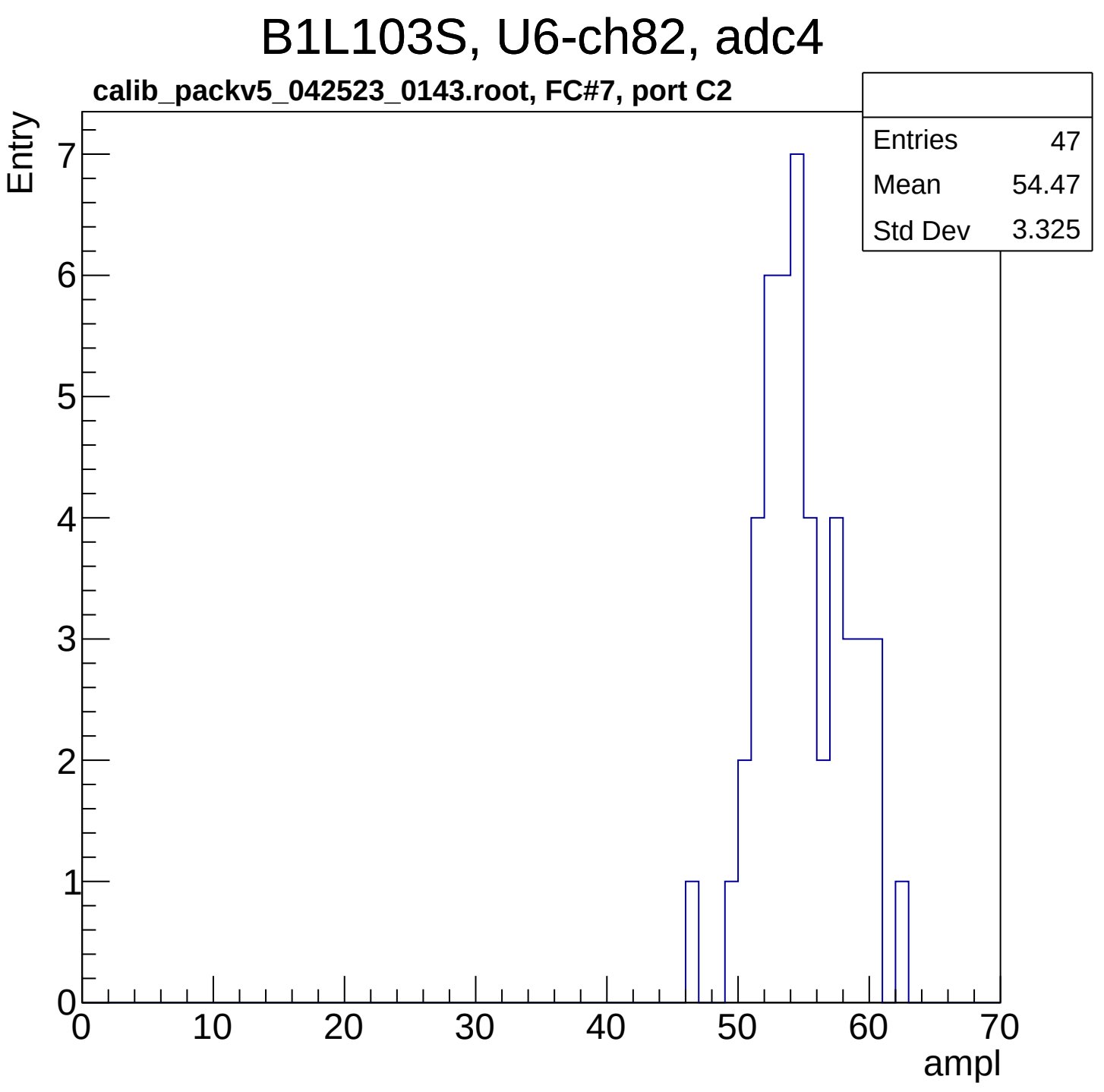
B1L103S, U6-ch82, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 54.47
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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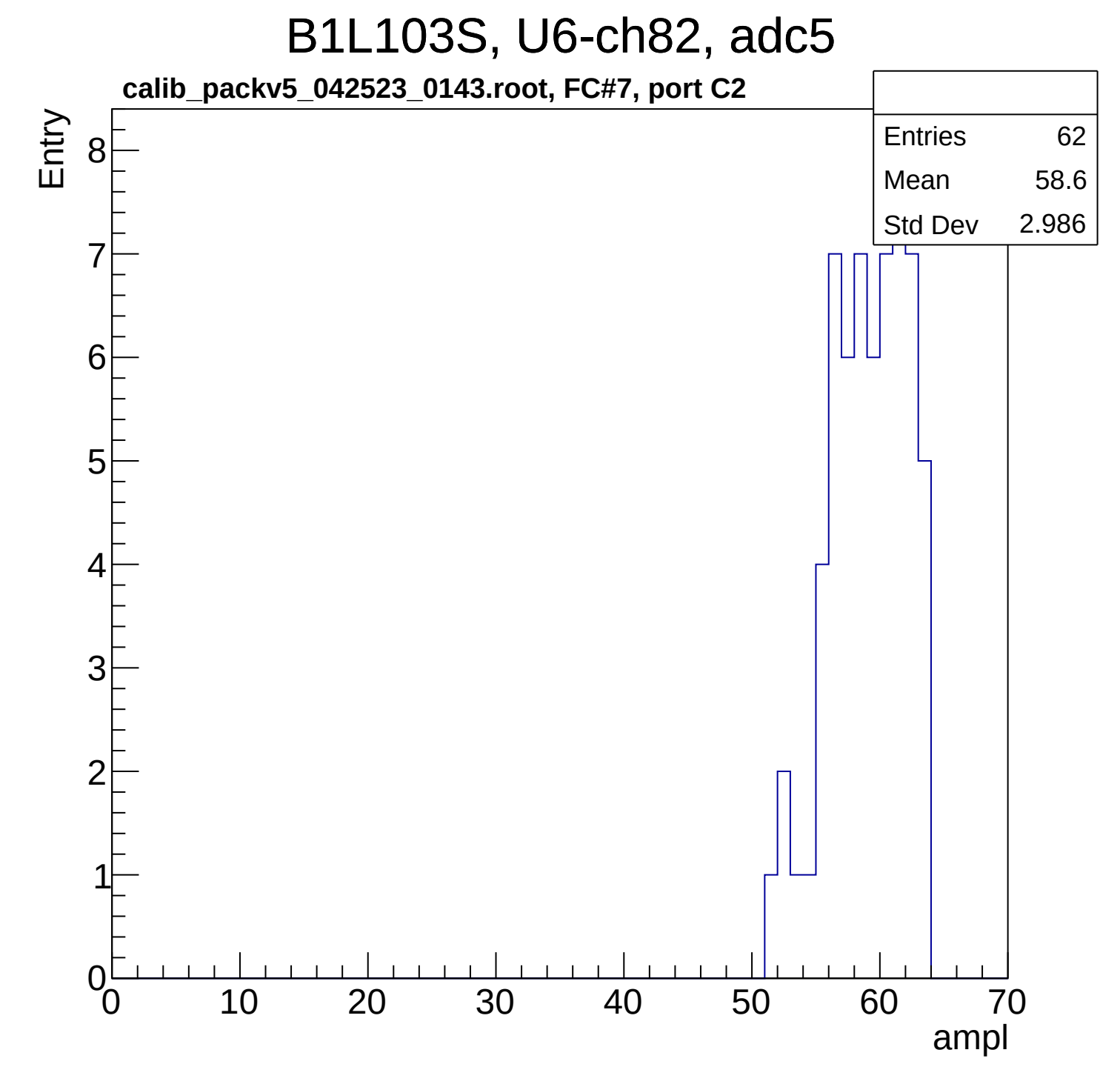
B1L103S, U6-ch82, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 58.6
Std Dev 2.986
Entry
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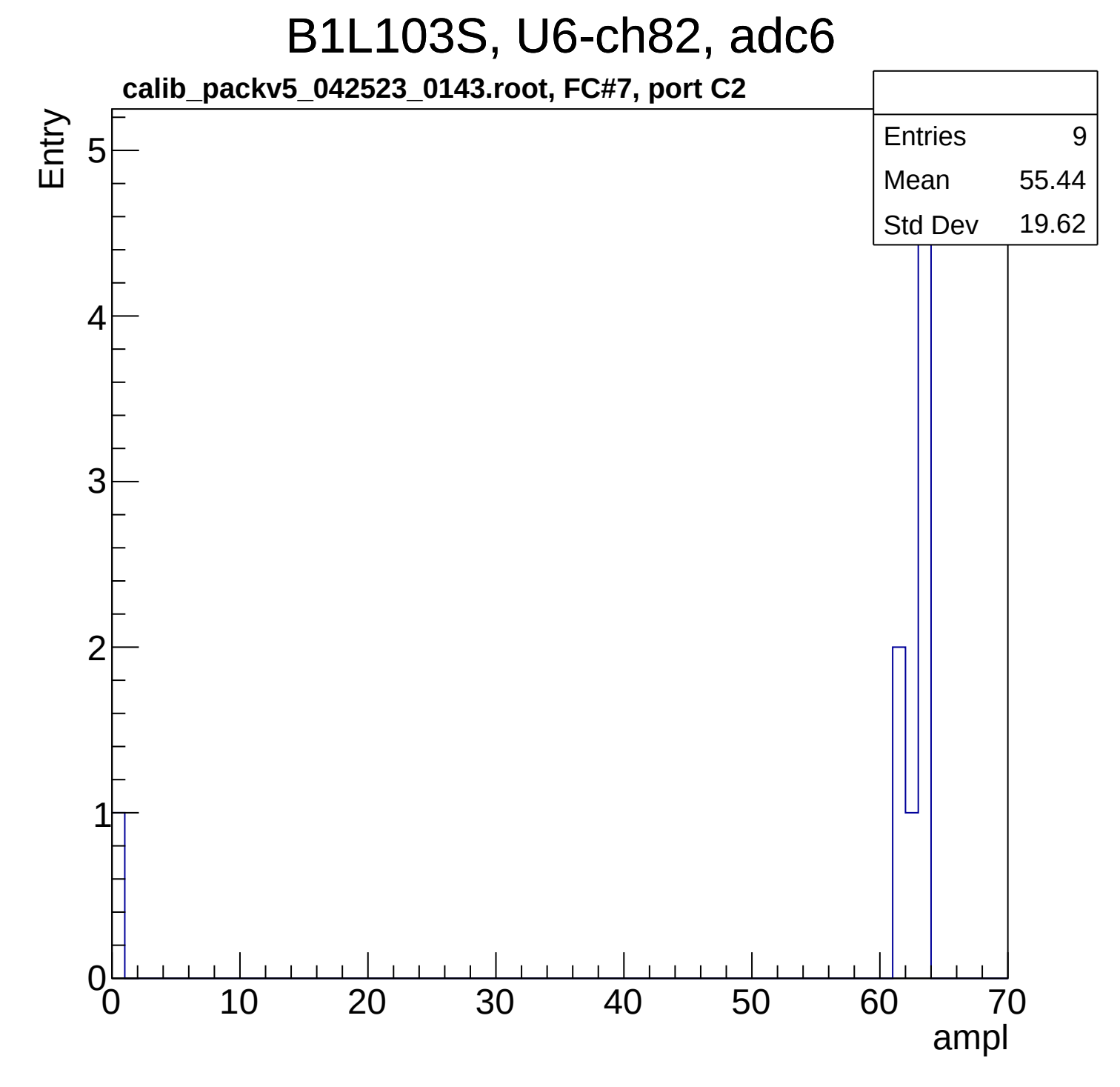
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch82, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 55.44
Std Dev 19.62
Entry
ampl
0
1
2
3
4
5
0
10
20
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60
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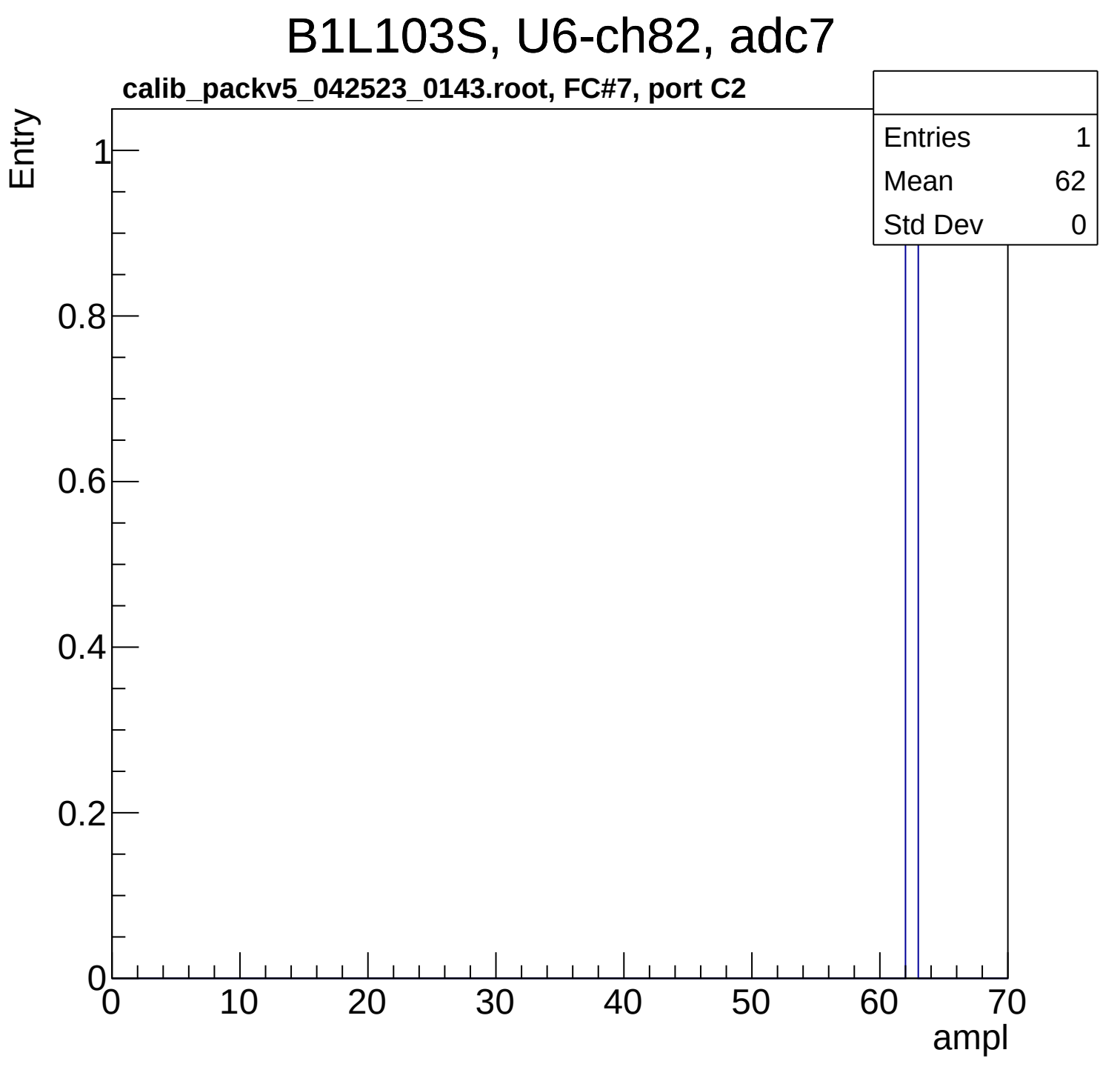
B1L103S, U6-ch82, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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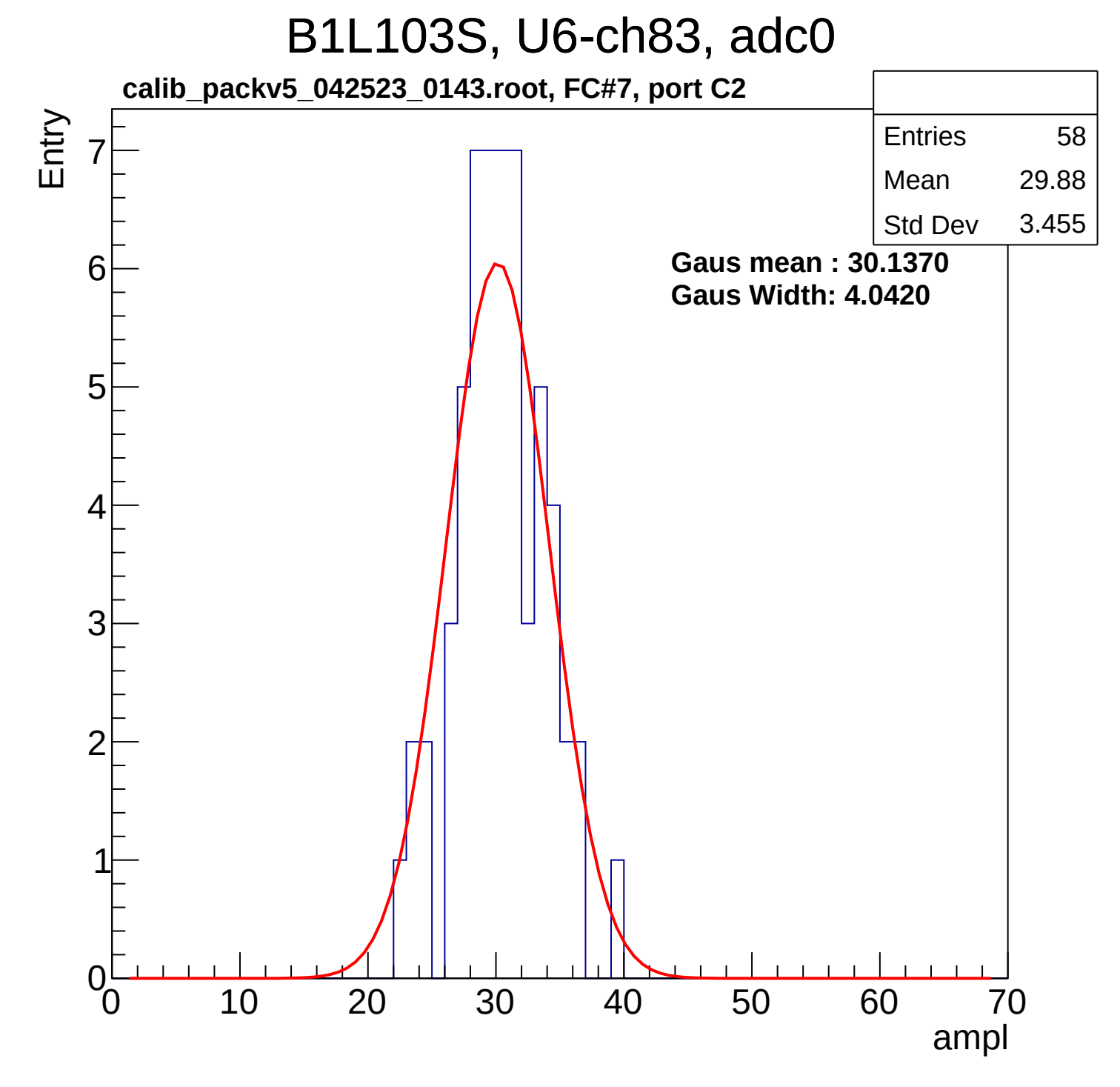
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch83, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 29.88
Std Dev 3.455
Gaus mean : 30.1370
Gaus Width: 4.0420
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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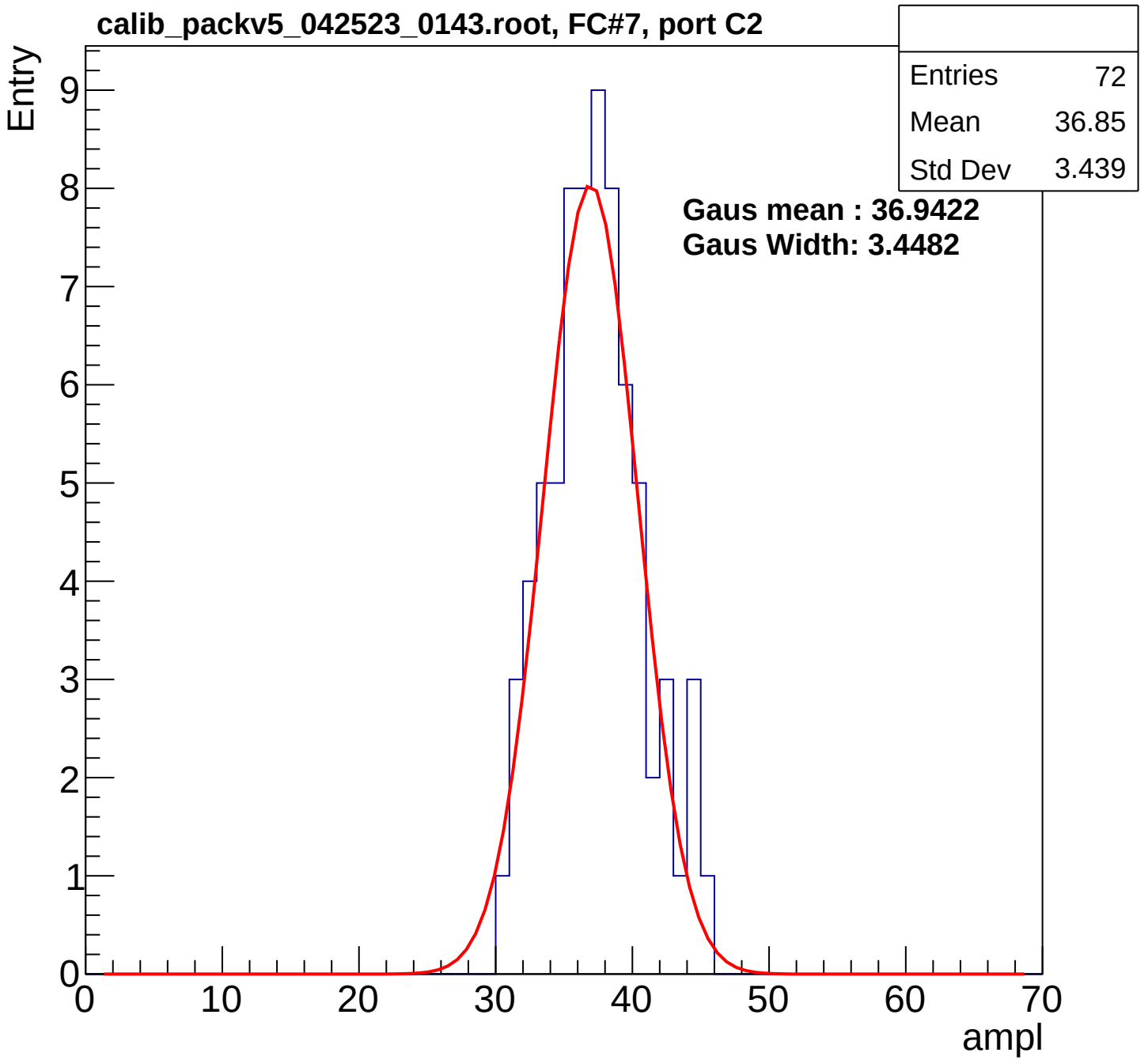
B1L103S, U6-ch83, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.85
Std Dev 3.439
Gaus mean : 36.9422
Gaus Width: 3.4482
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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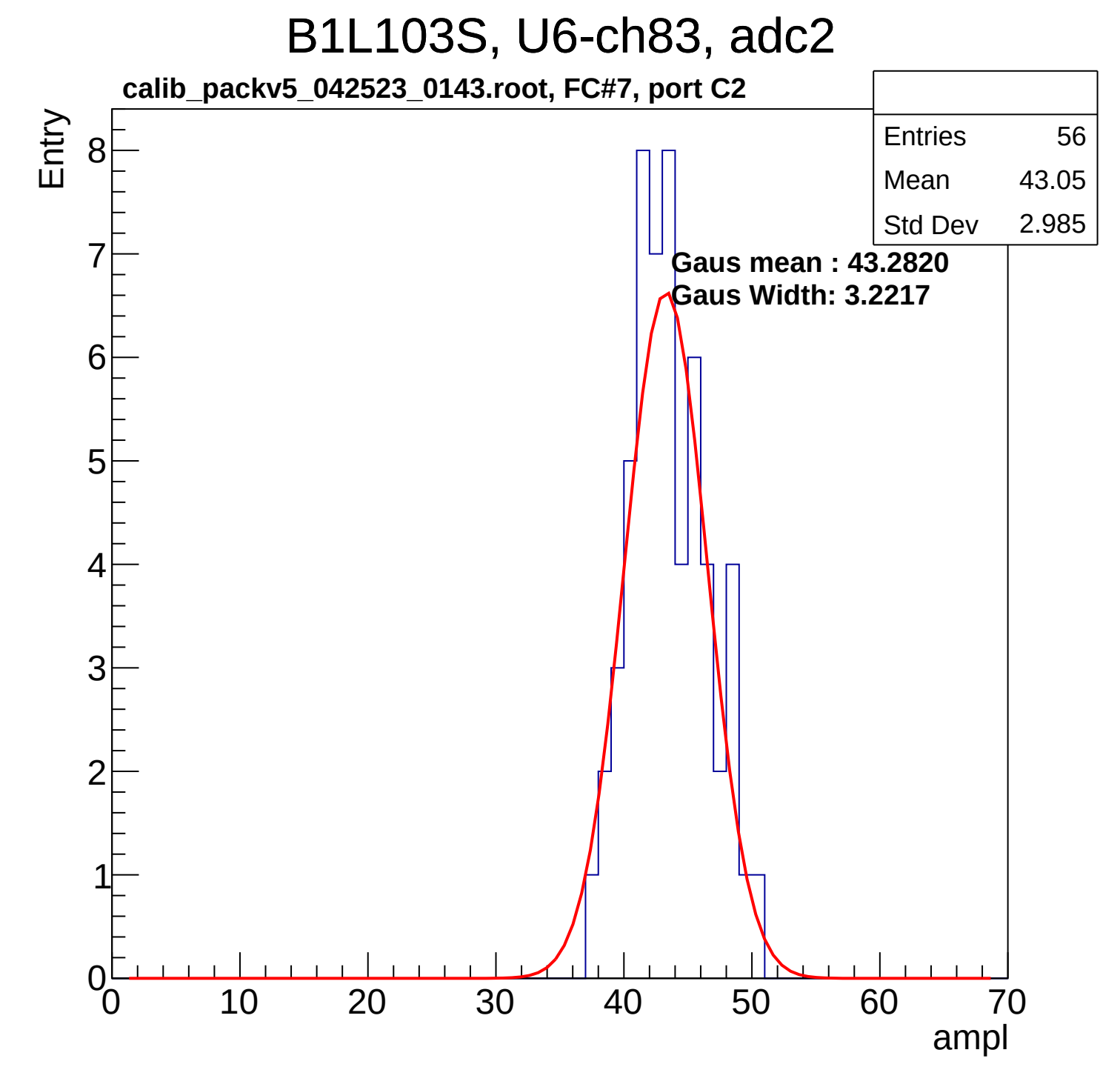
B1L103S, U6-ch83, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.05
Std Dev 2.985
Gaus mean : 43.2820
Gaus Width: 3.2217
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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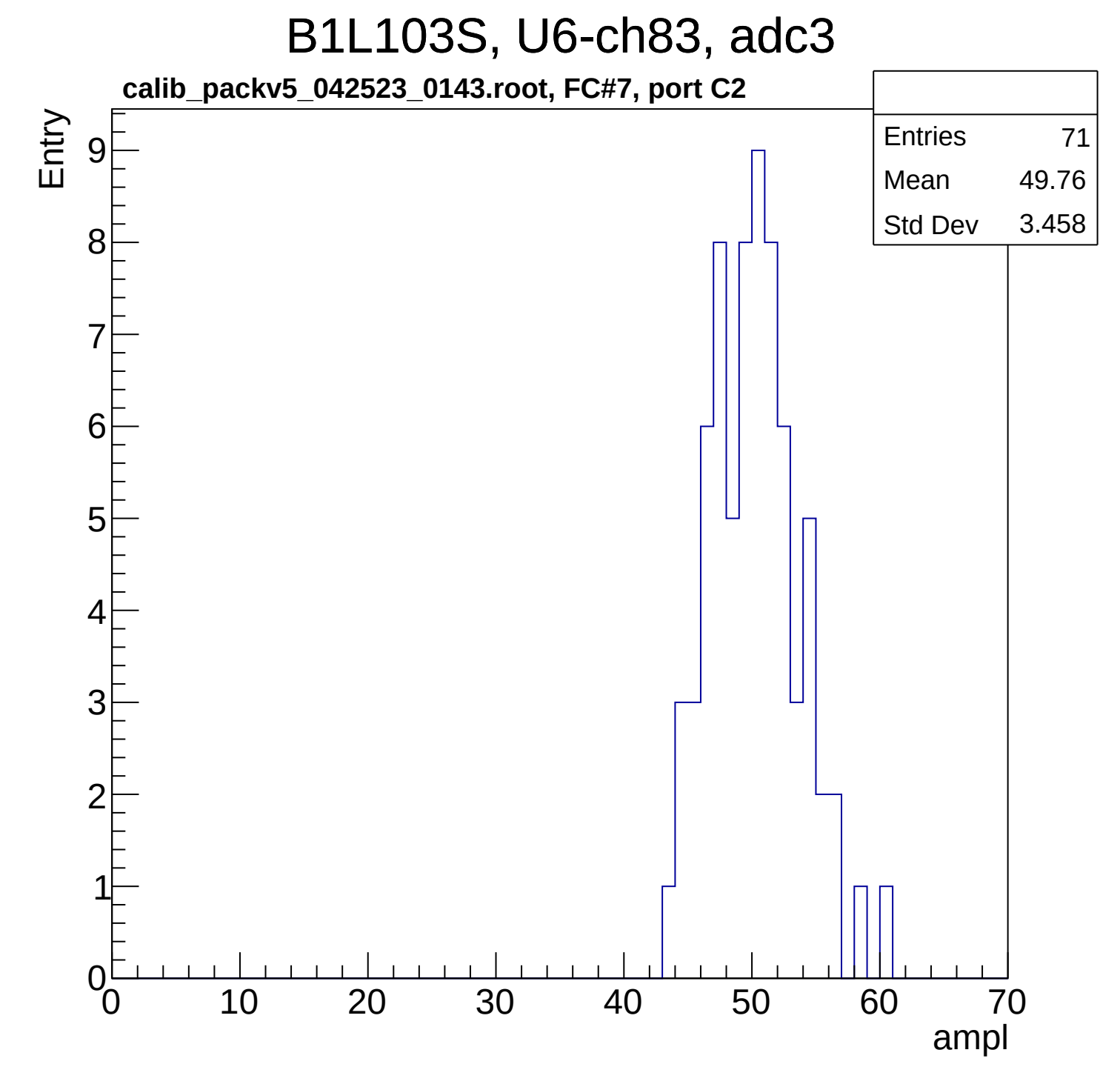
B1L103S, U6-ch83, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.76
Std Dev 3.458
Entry
ampl
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0
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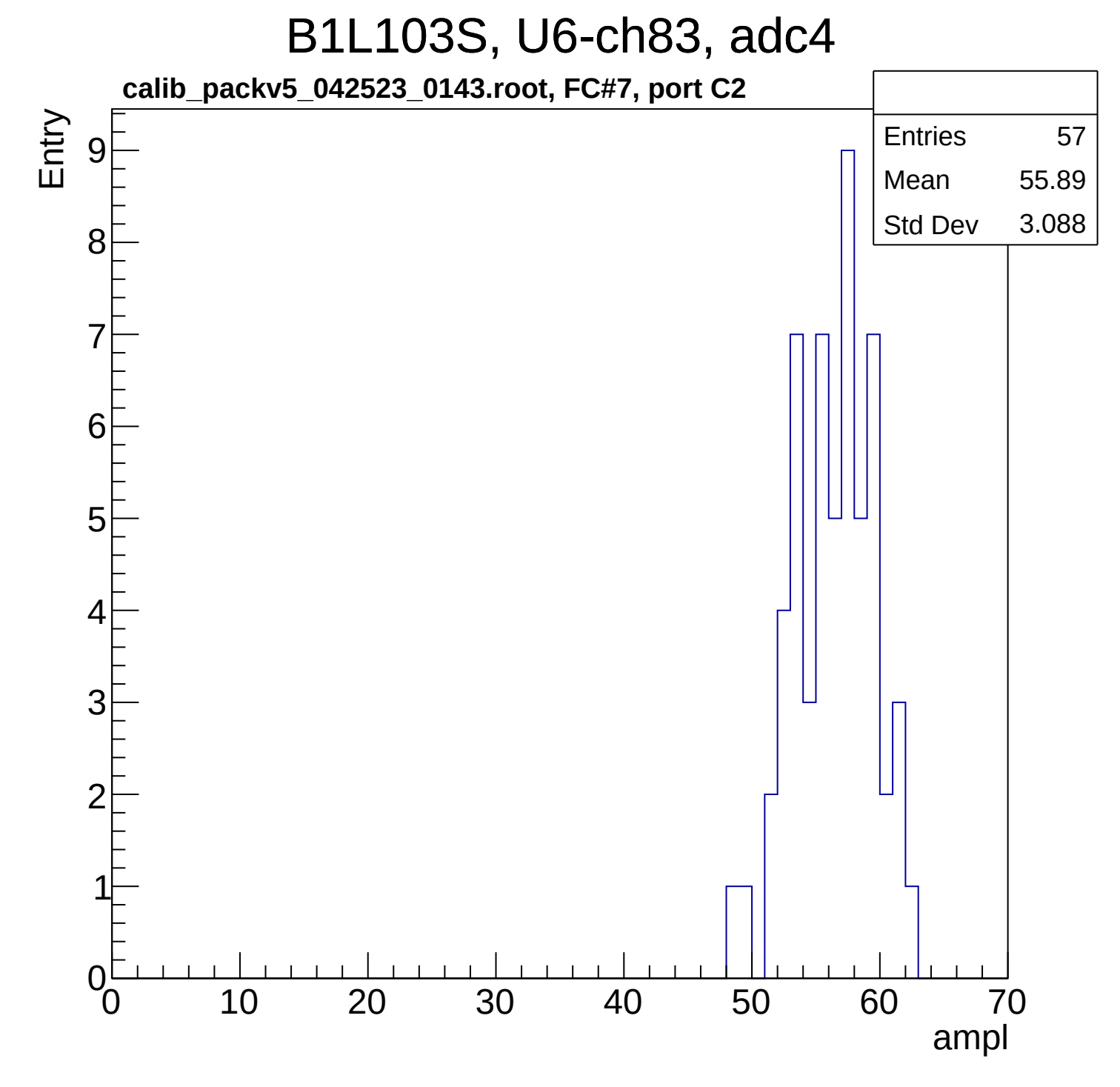
B1L103S, U6-ch83, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.89
Std Dev 3.088
Entry
ampl
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5
6
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0
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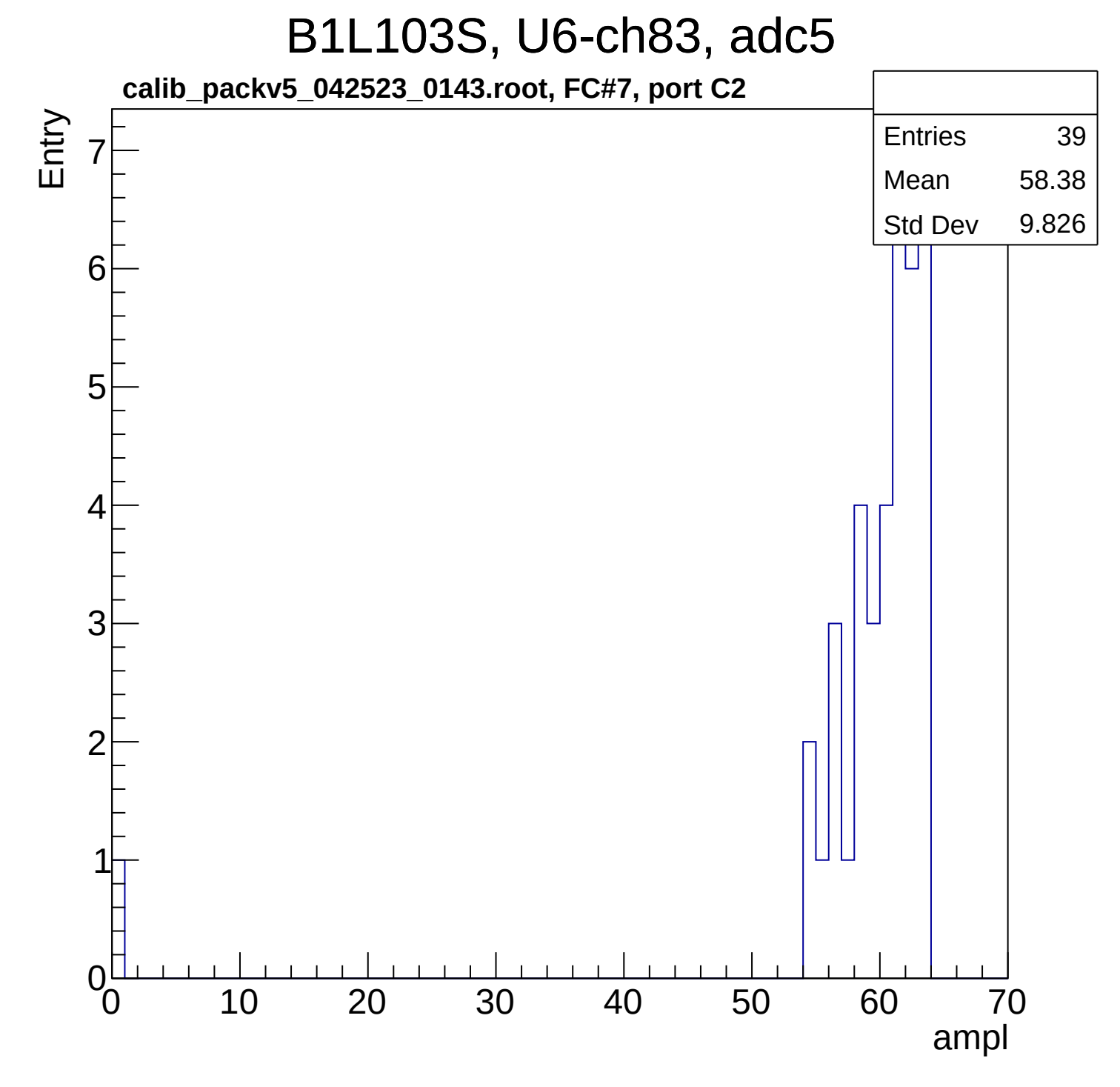
B1L103S, U6-ch83, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.38
Std Dev 9.826
Entry
ampl
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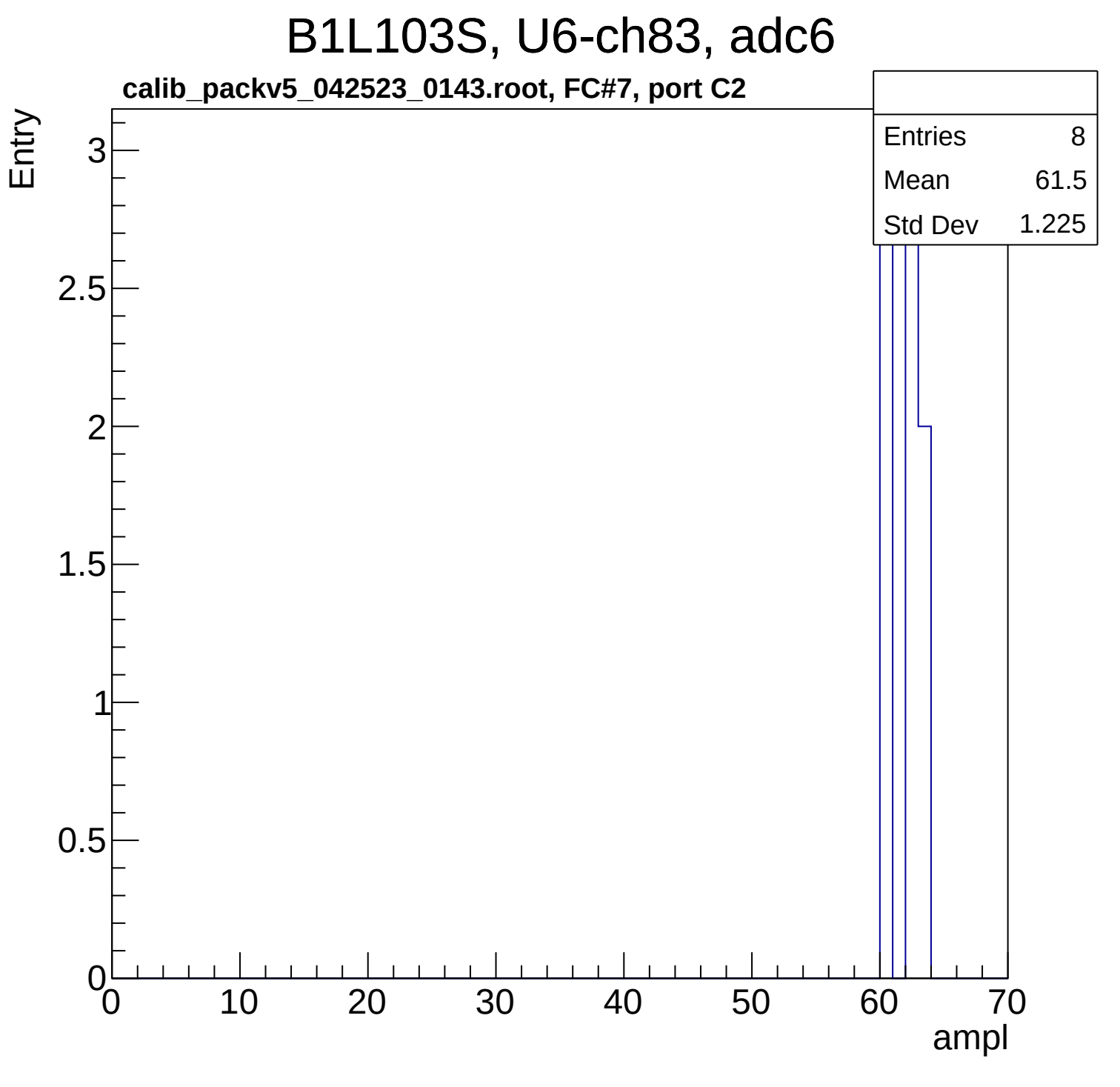
B1L103S, U6-ch83, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.5
Std Dev 1.225
Entry
ampl
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1
1.5
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2.5
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0
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60
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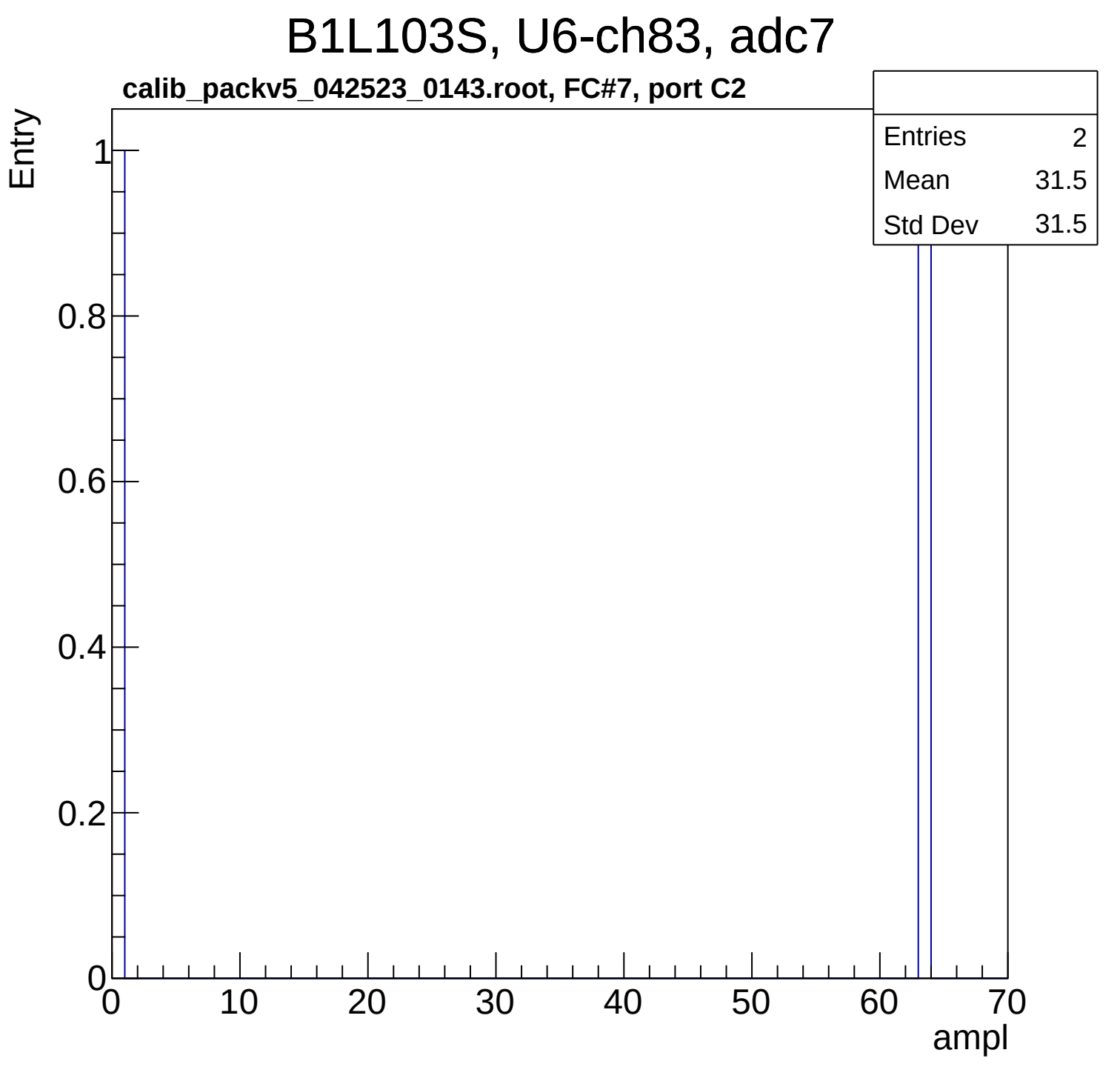
B1L103S, U6-ch83, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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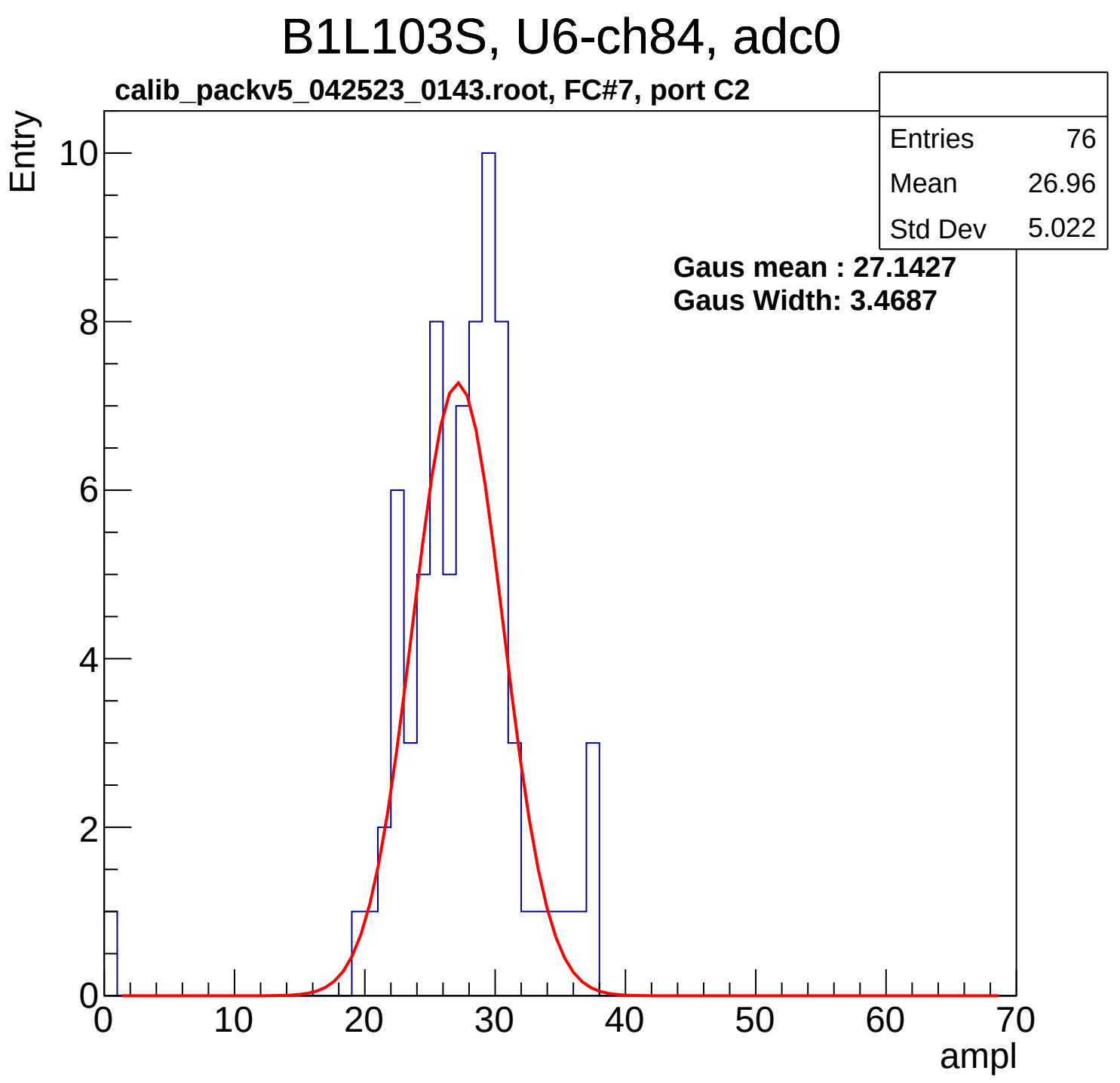
60
70
ampl


B1L103S, U6-ch84, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 26.96
Std Dev 5.022
Gaus mean : 27.1427
Gaus Width: 3.4687
Entry
ampl
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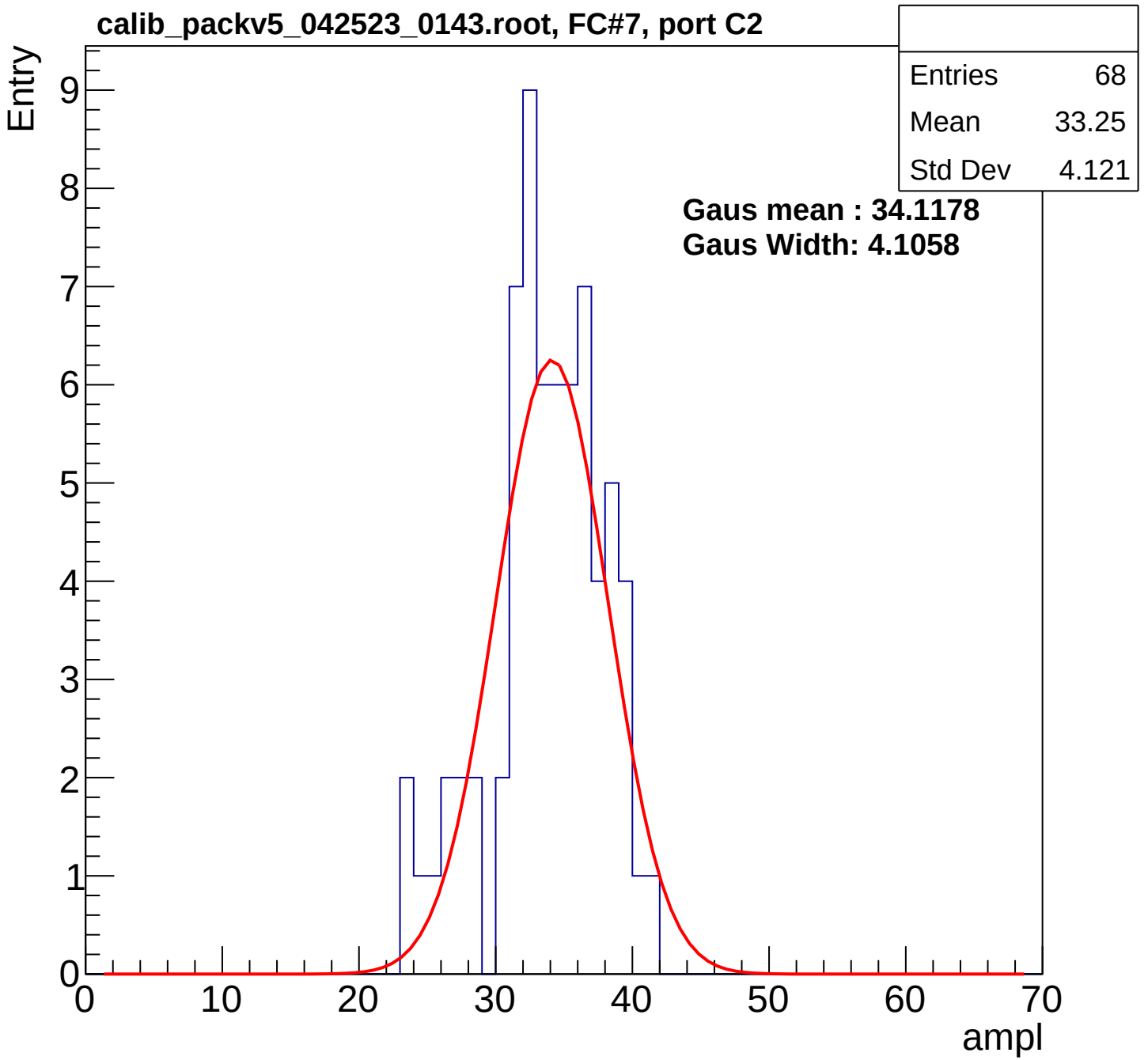
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch84, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 33.25
Std Dev 4.121
Gaus mean : 34.1178
Gaus Width: 4.1058
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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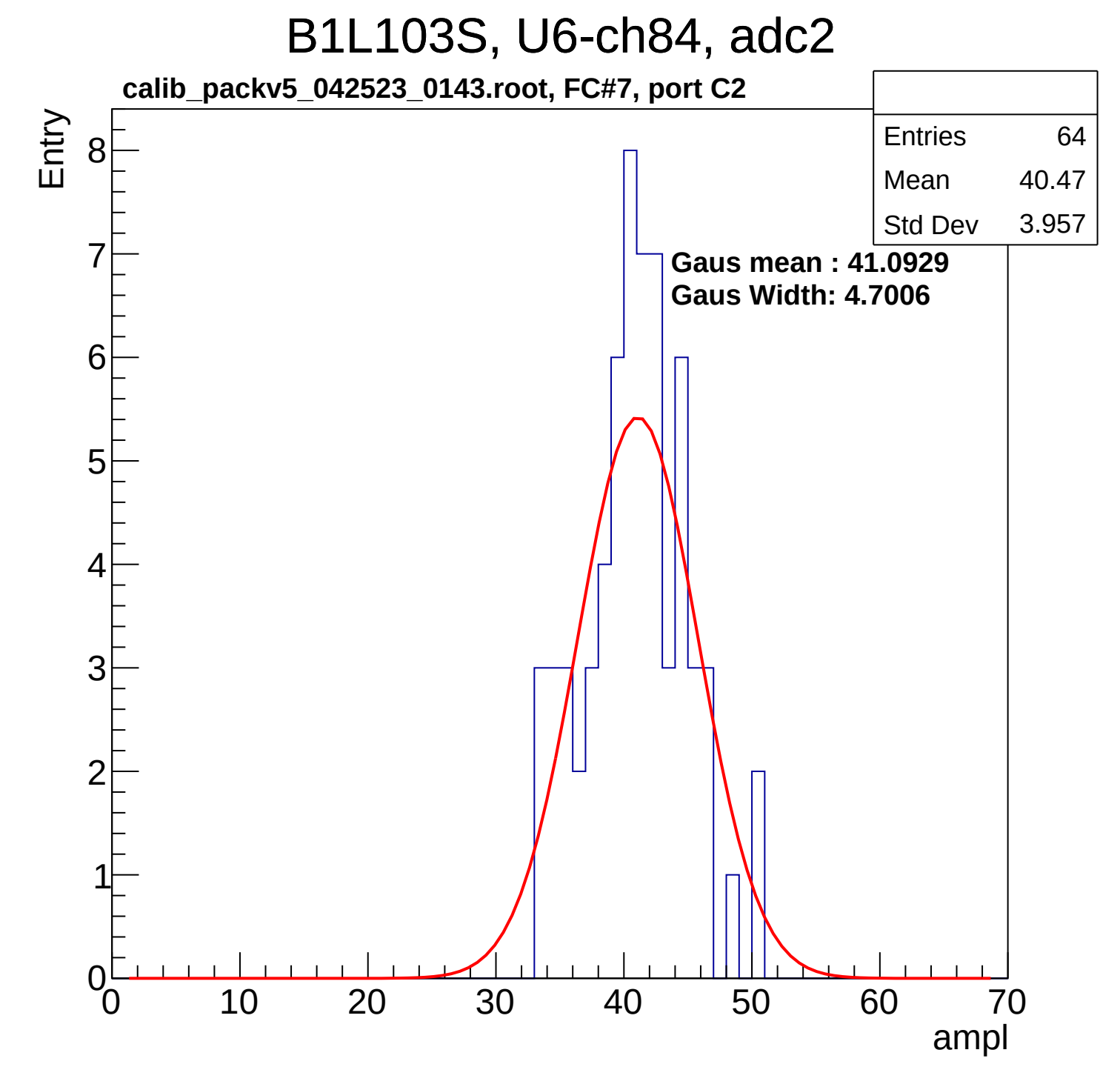
B1L103S, U6-ch84, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.47
Std Dev 3.957
Gaus mean : 41.0929
Gaus Width: 4.7006
Entry
ampl
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2
3
4
5
6
7
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0
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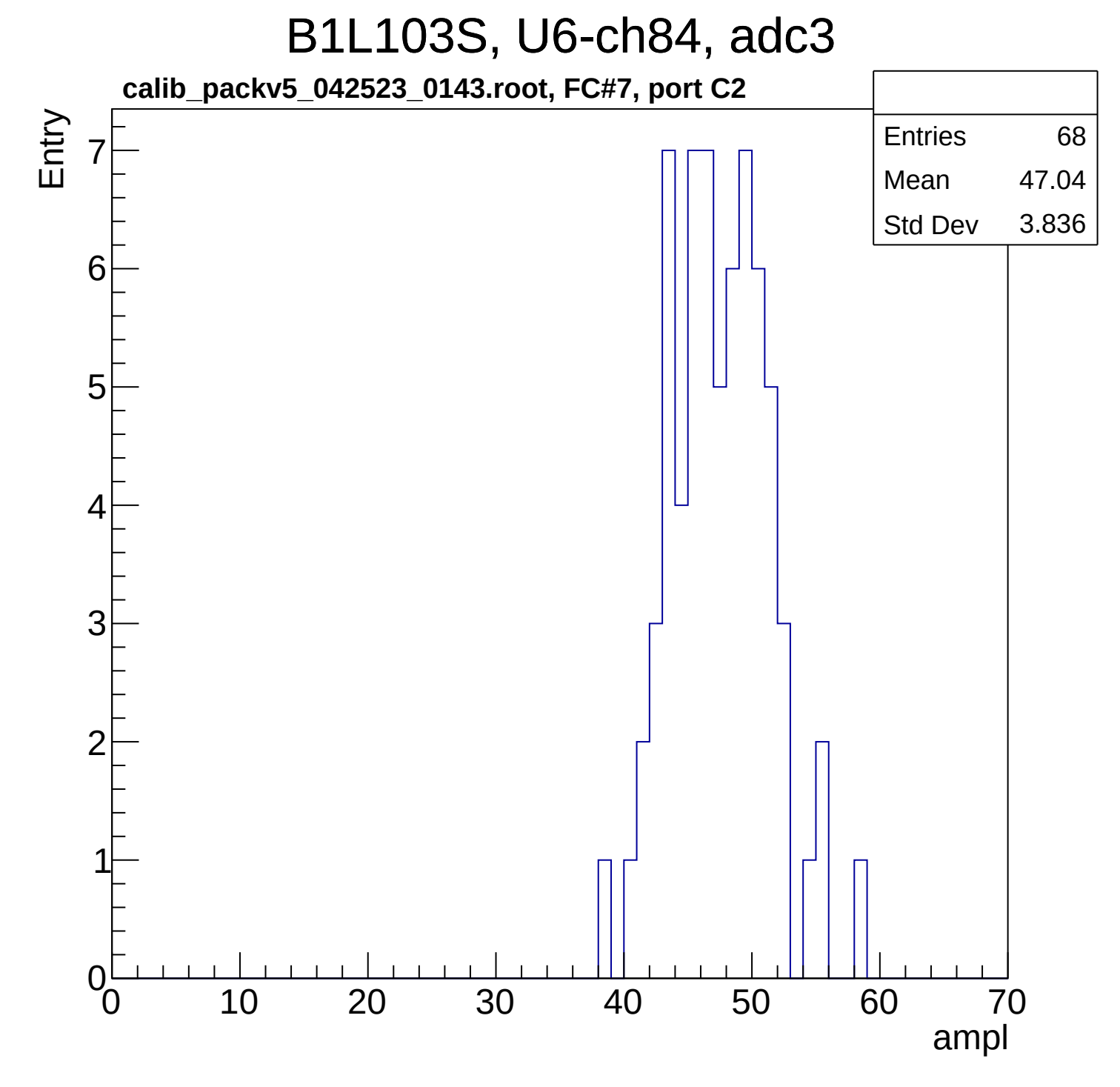
B1L103S, U6-ch84, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 47.04
Std Dev 3.836
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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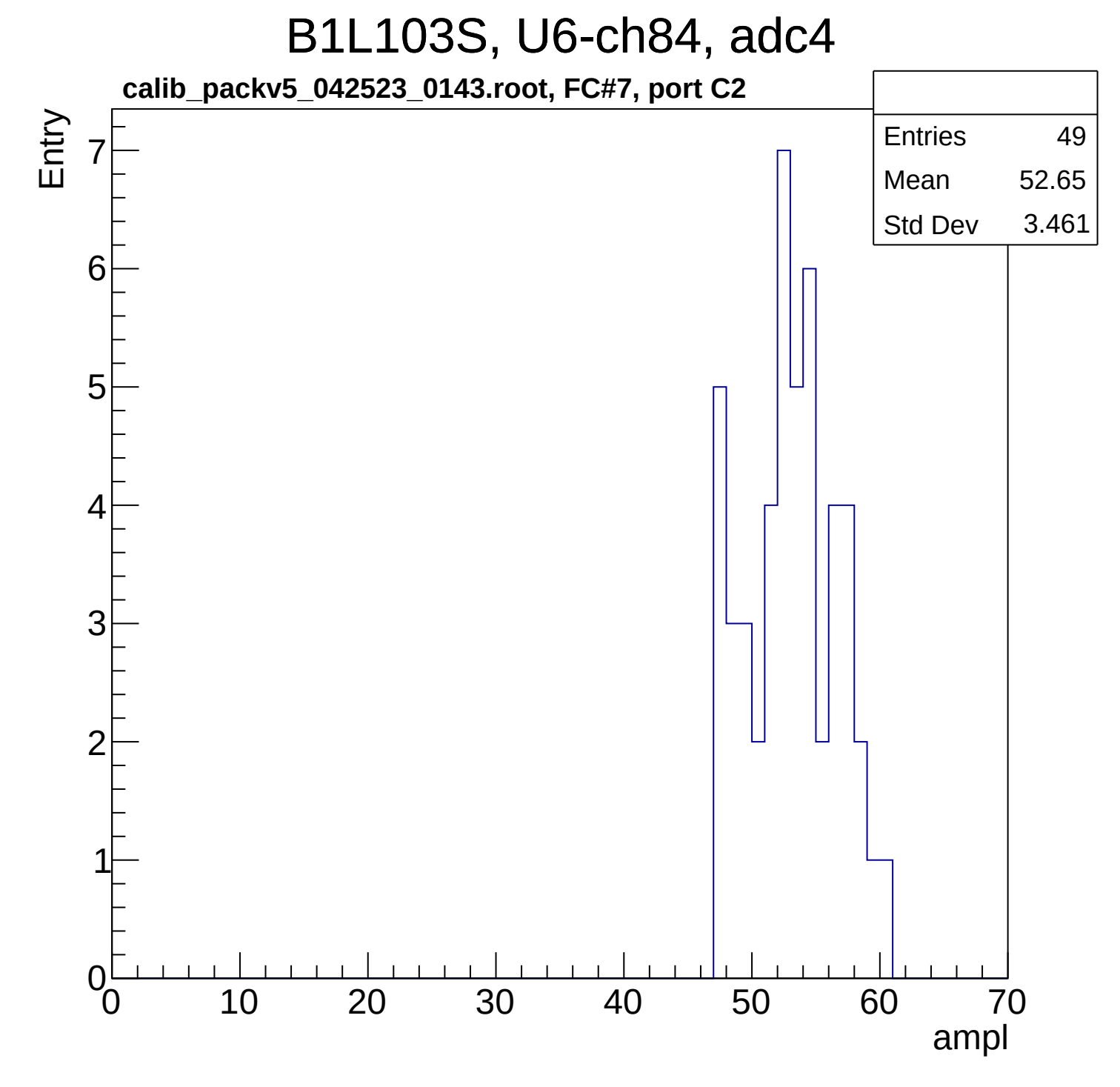
B1L103S, U6-ch84, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 52.65
Std Dev 3.461
Entry
ampl
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7
0
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60
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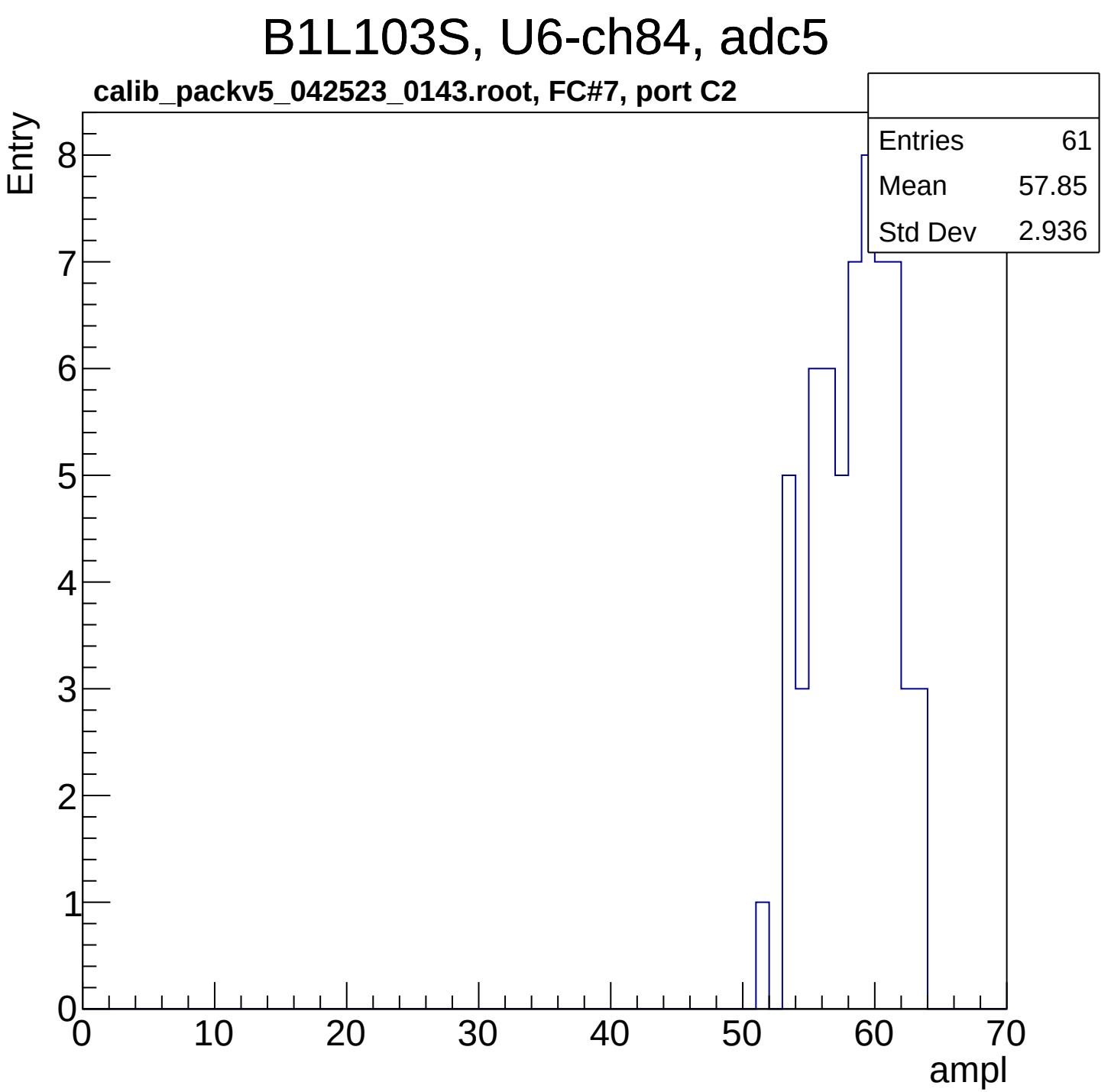
B1L103S, U6-ch84, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 57.85
Std Dev 2.936
Entry
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2
3
4
5
6
7
8
0
10
20
30
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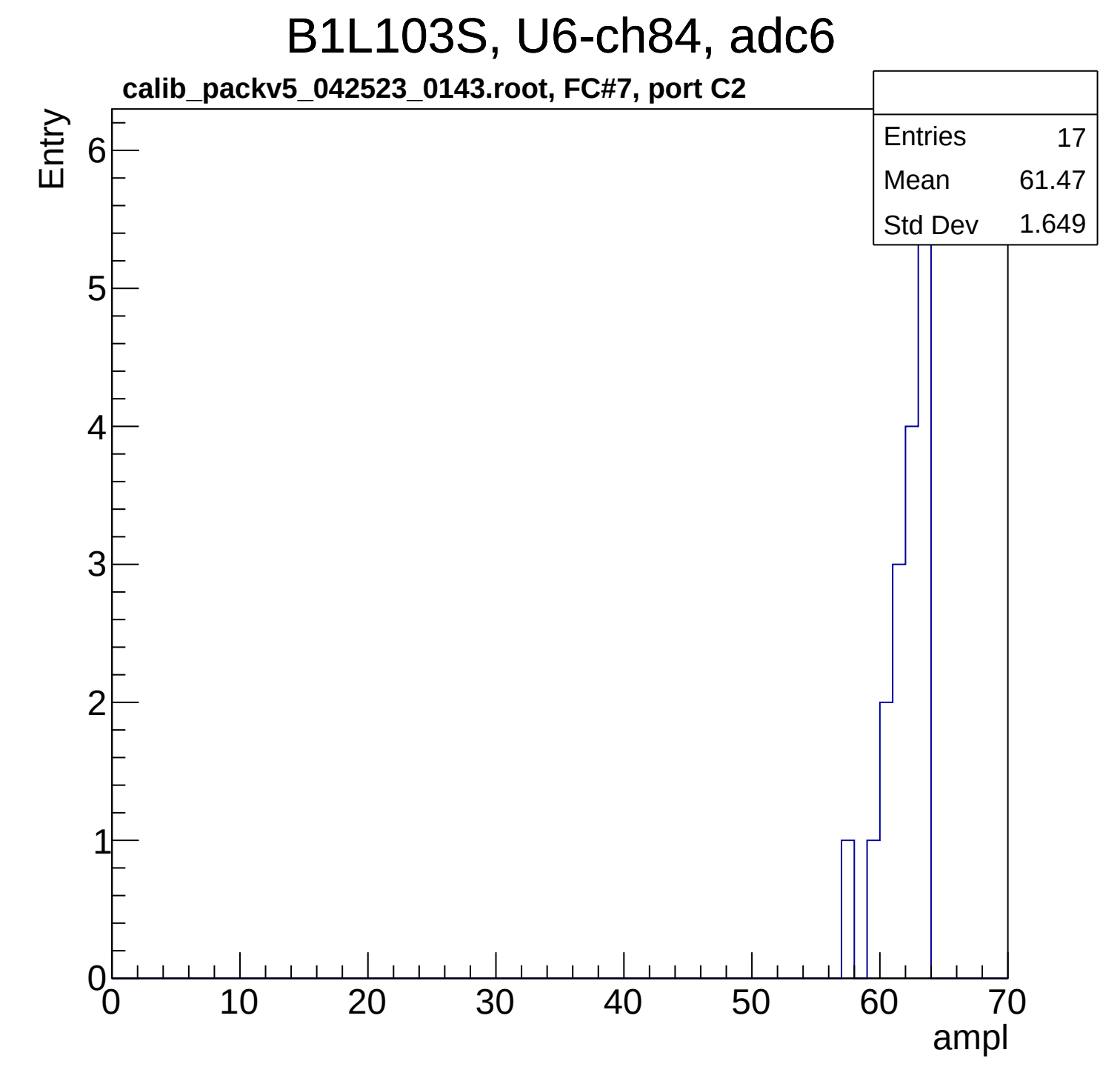
50
60
70
ampl


B1L103S, U6-ch84, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 61.47
Std Dev 1.649
Entry
ampl
0
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2
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5
6
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20
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40
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60
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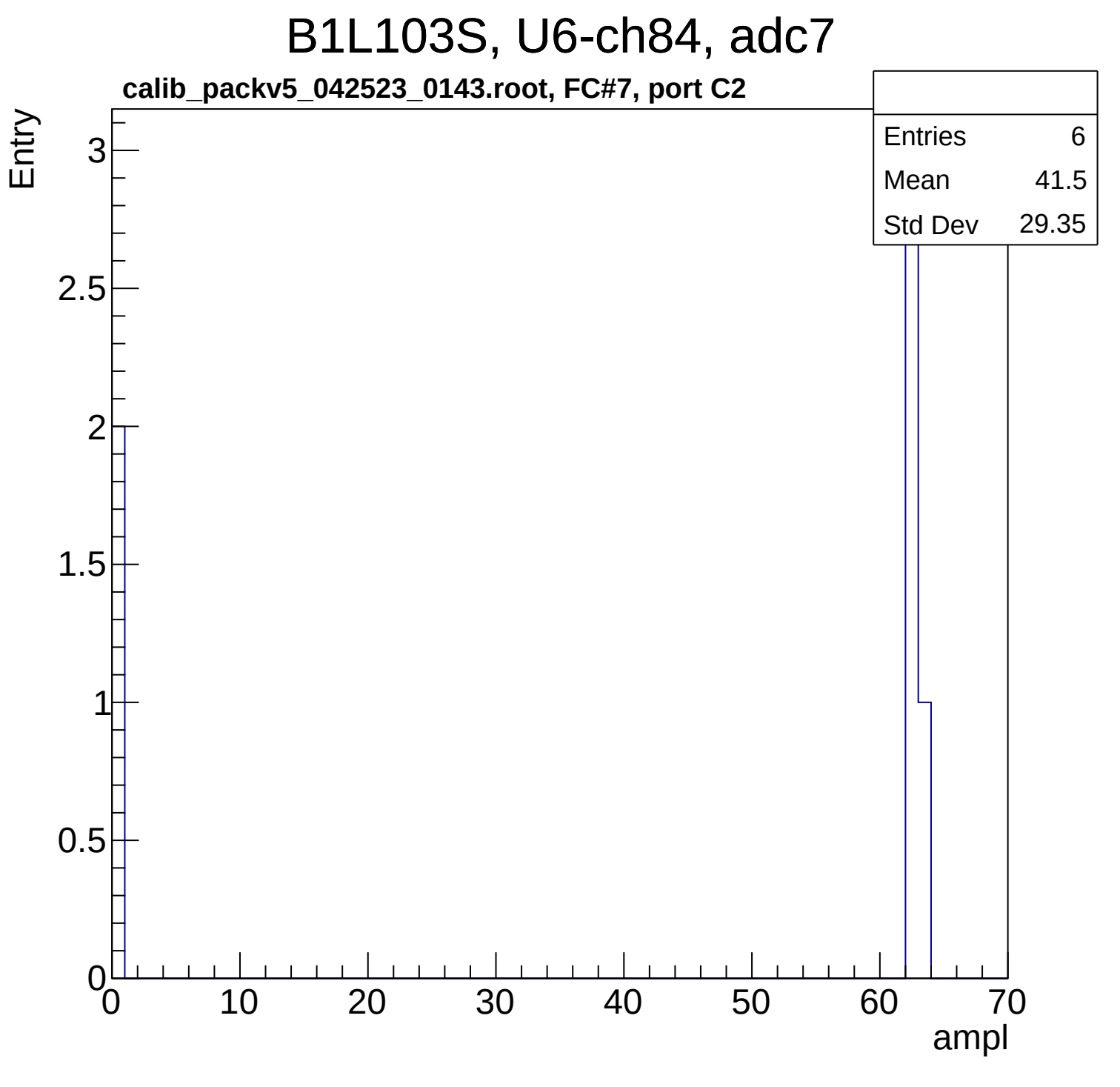
B1L103S, U6-ch84, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 41.5
Std Dev 29.35
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
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50
60
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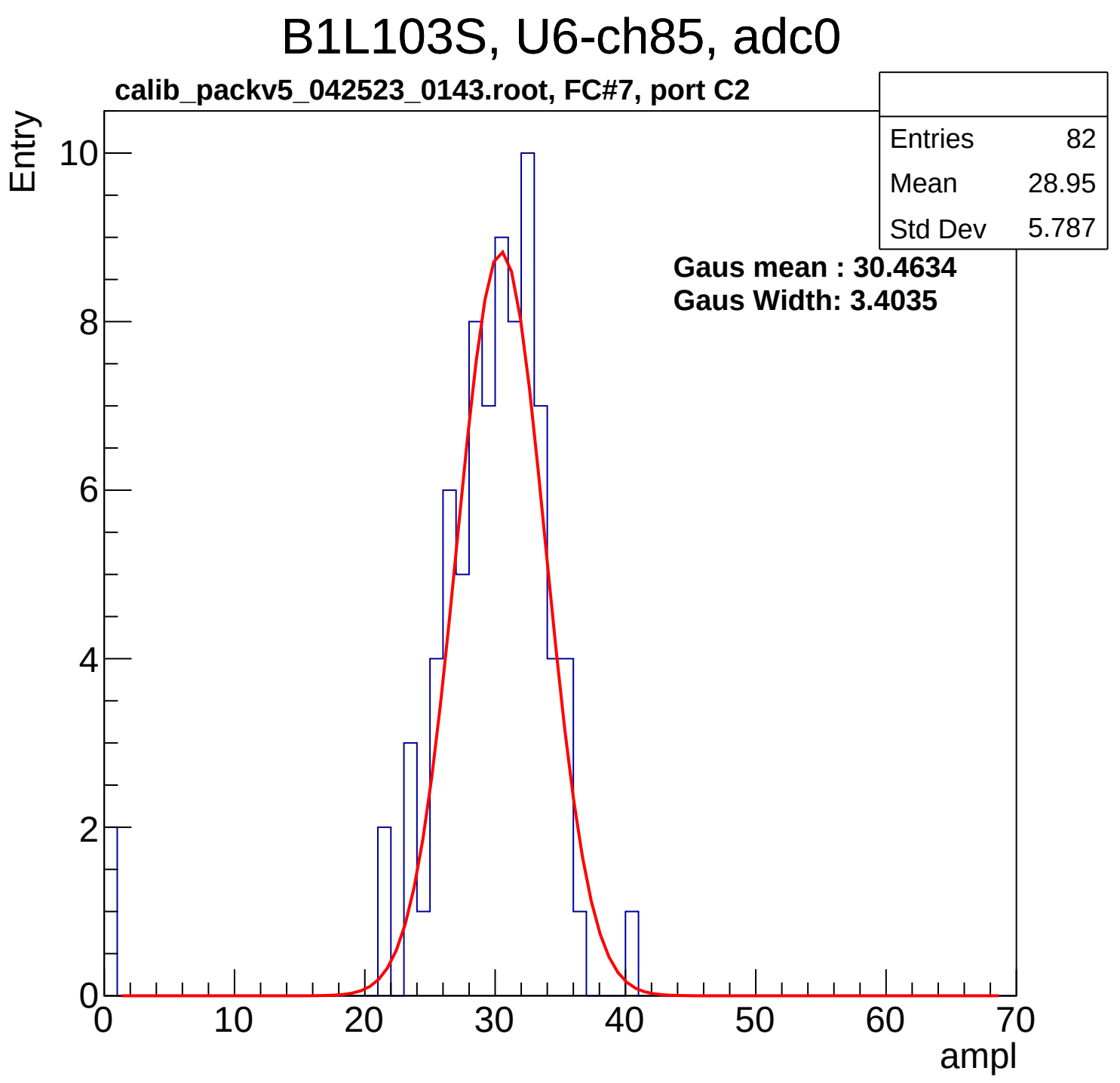
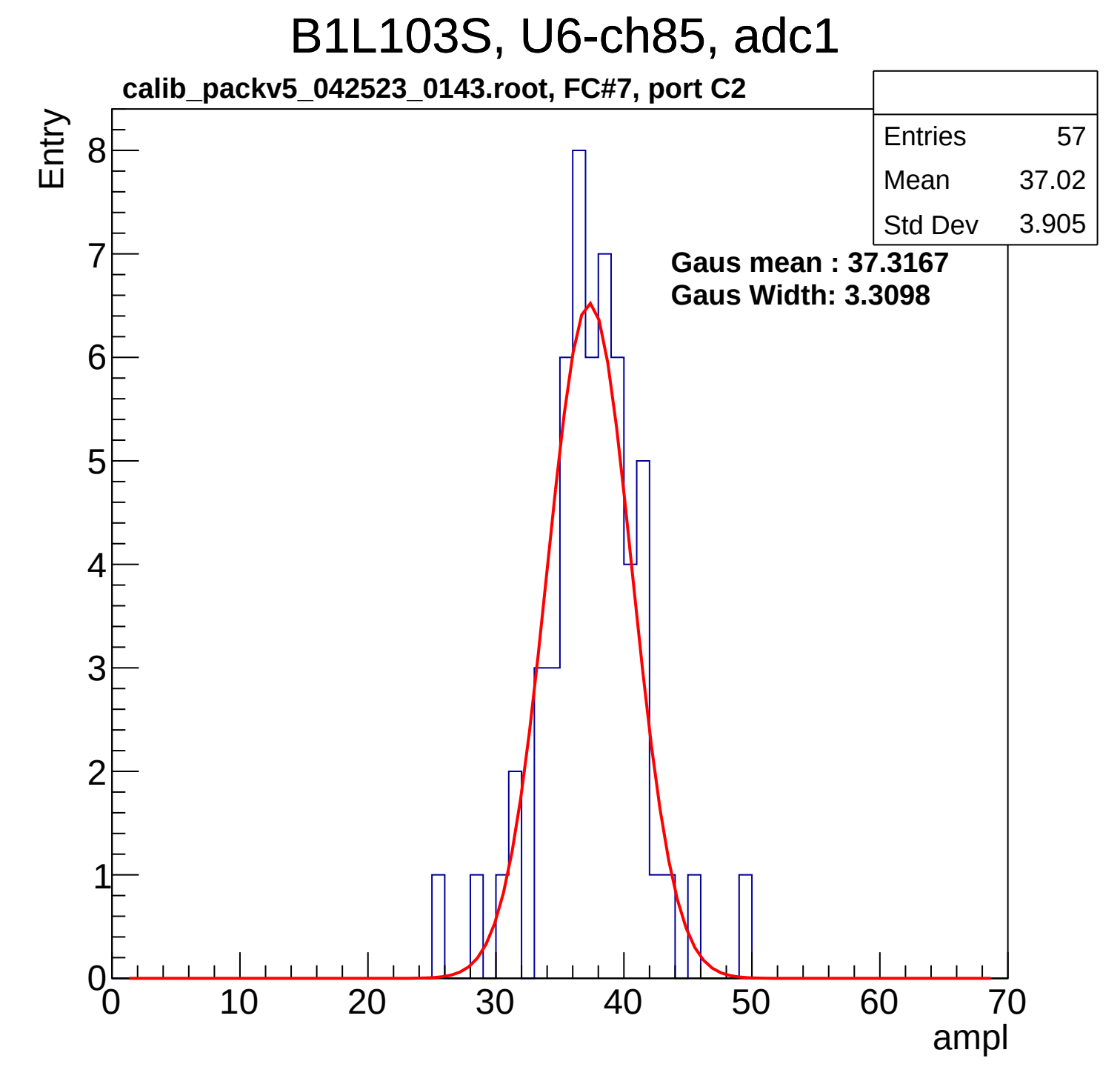

B1L103S, U6-ch85, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 28.95
Std Dev 5.787
Gaus mean : 30.4634
Gaus Width: 3.4035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch85, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 37.02
Std Dev 3.905
Gaus mean : 37.3167
Gaus Width: 3.3098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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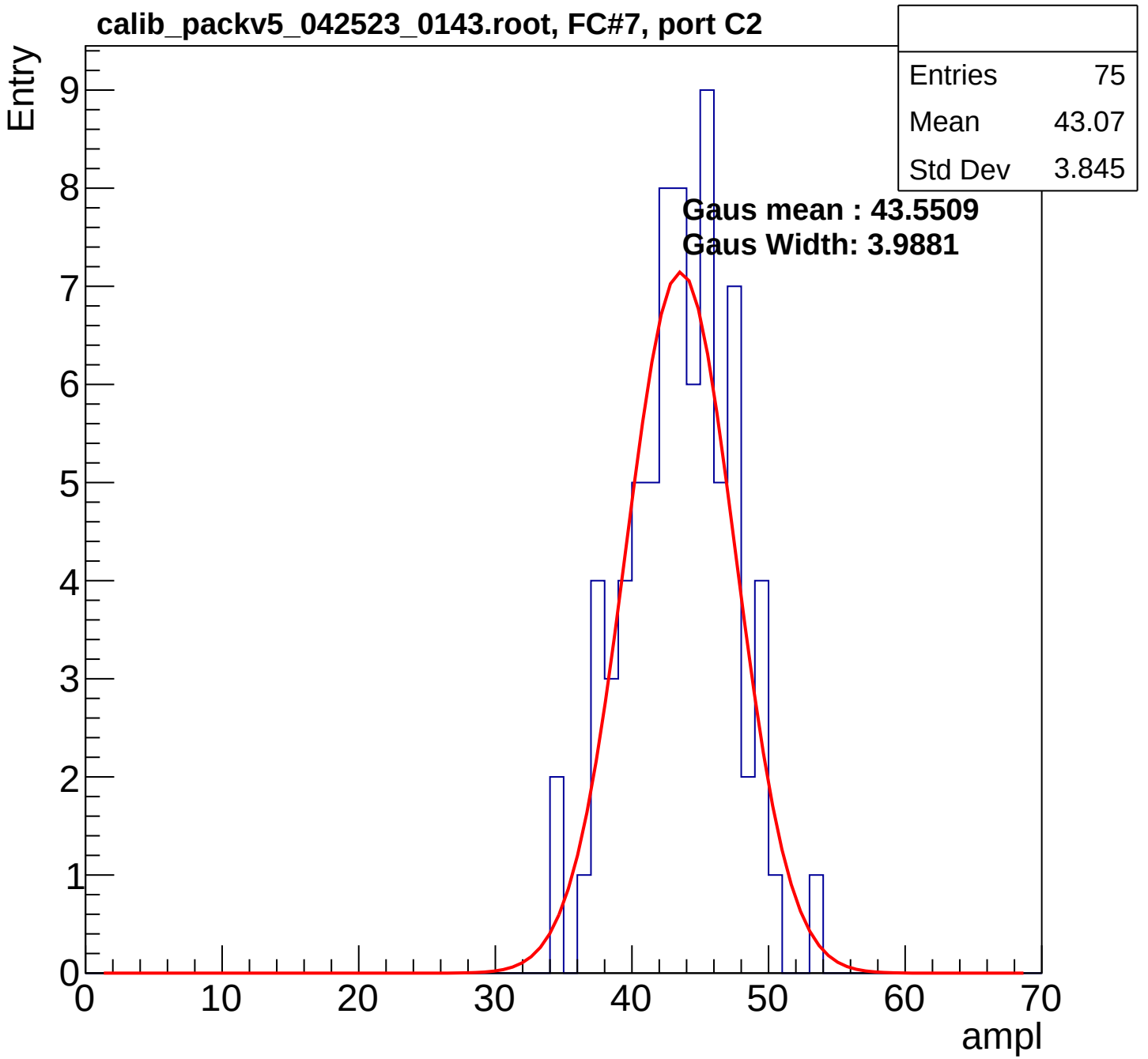
B1L103S, U6-ch85, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 43.07
Std Dev 3.845
Gaus mean : 43.5509
Gaus Width: 3.9881
Entry
ampl
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2
3
4
5
6
7
8
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0
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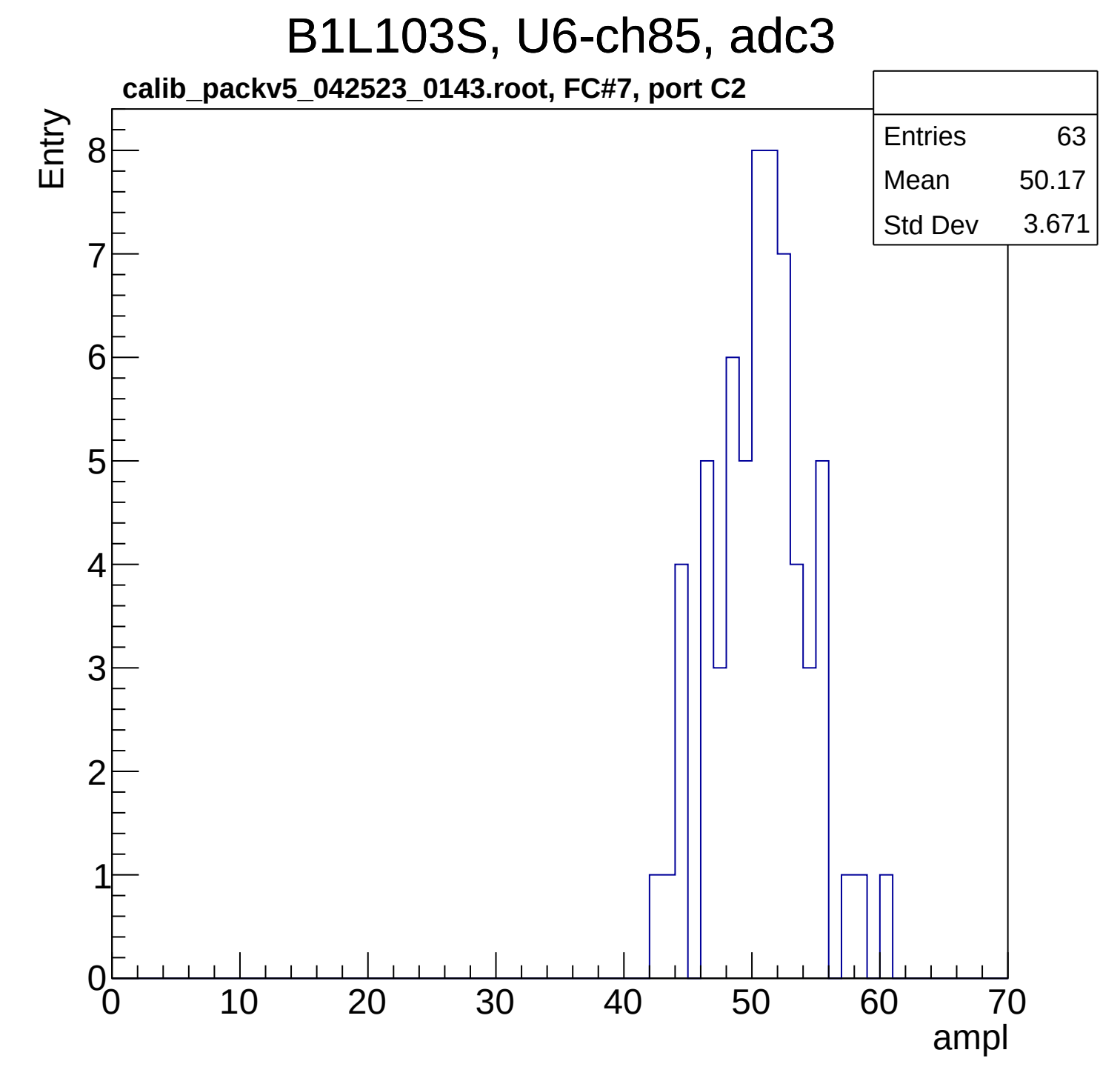
B1L103S, U6-ch85, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 50.17
Std Dev 3.671
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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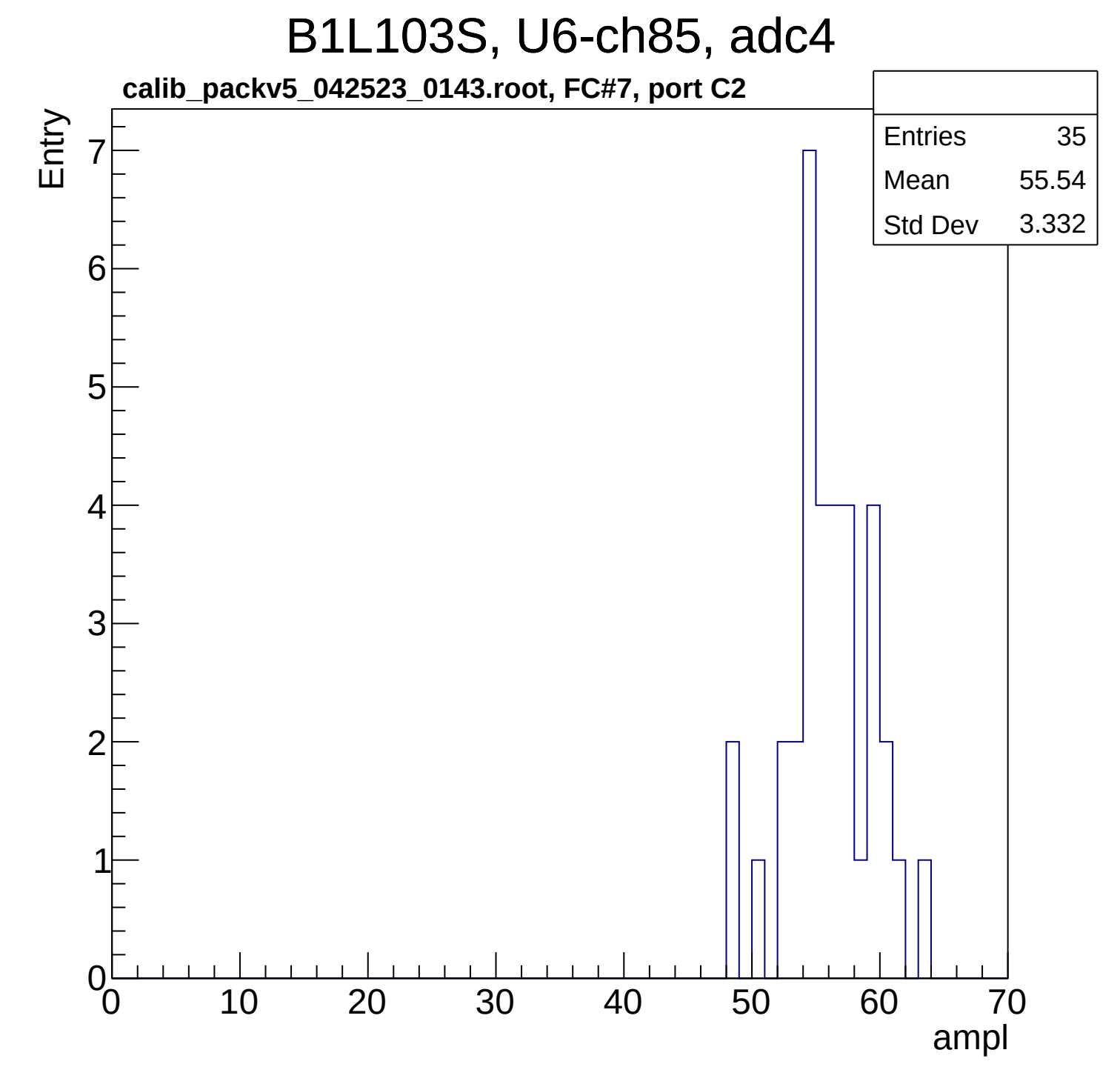
B1L103S, U6-ch85, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 55.54
Std Dev 3.332
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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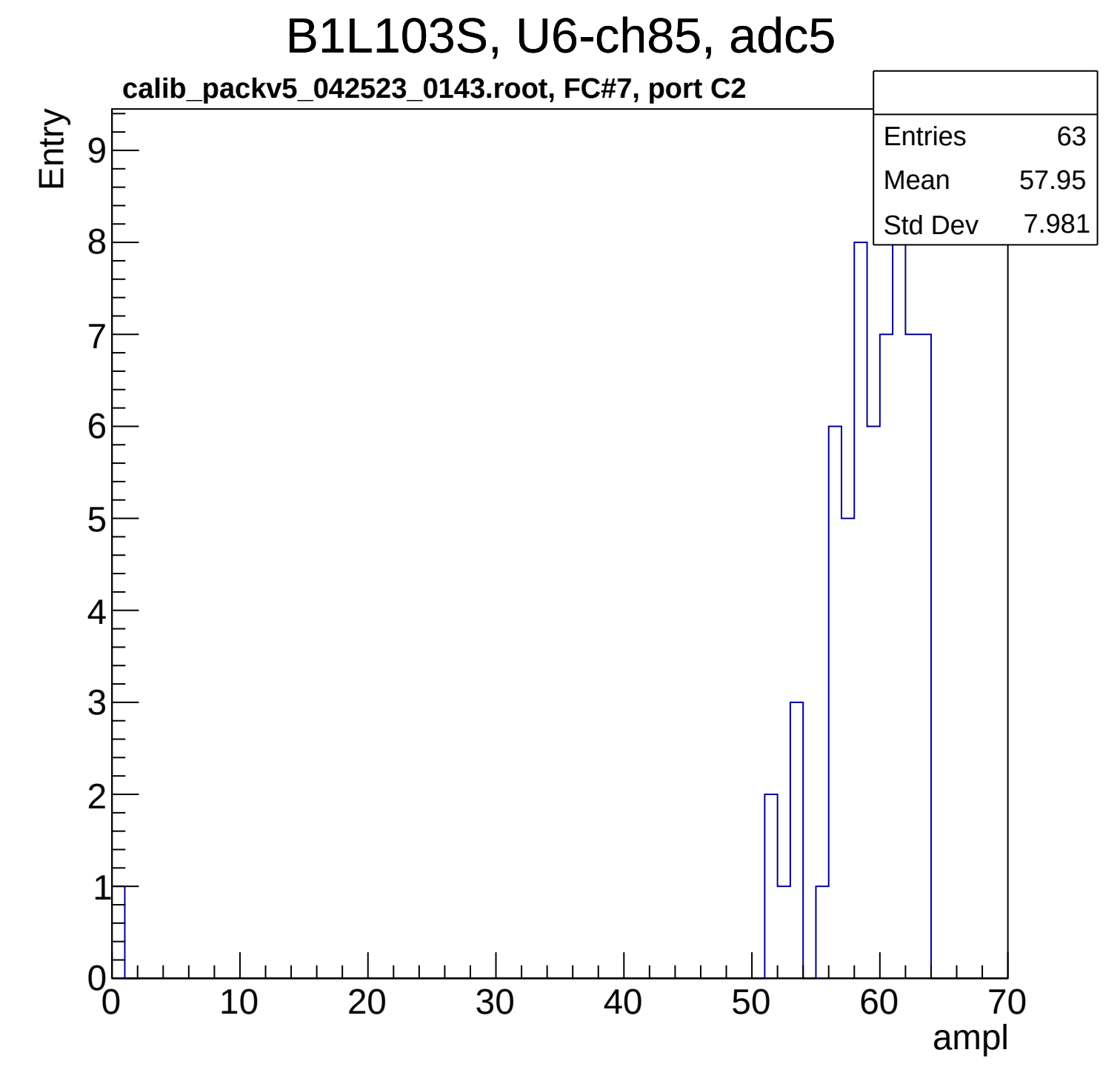
B1L103S, U6-ch85, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 57.95
Std Dev 7.981
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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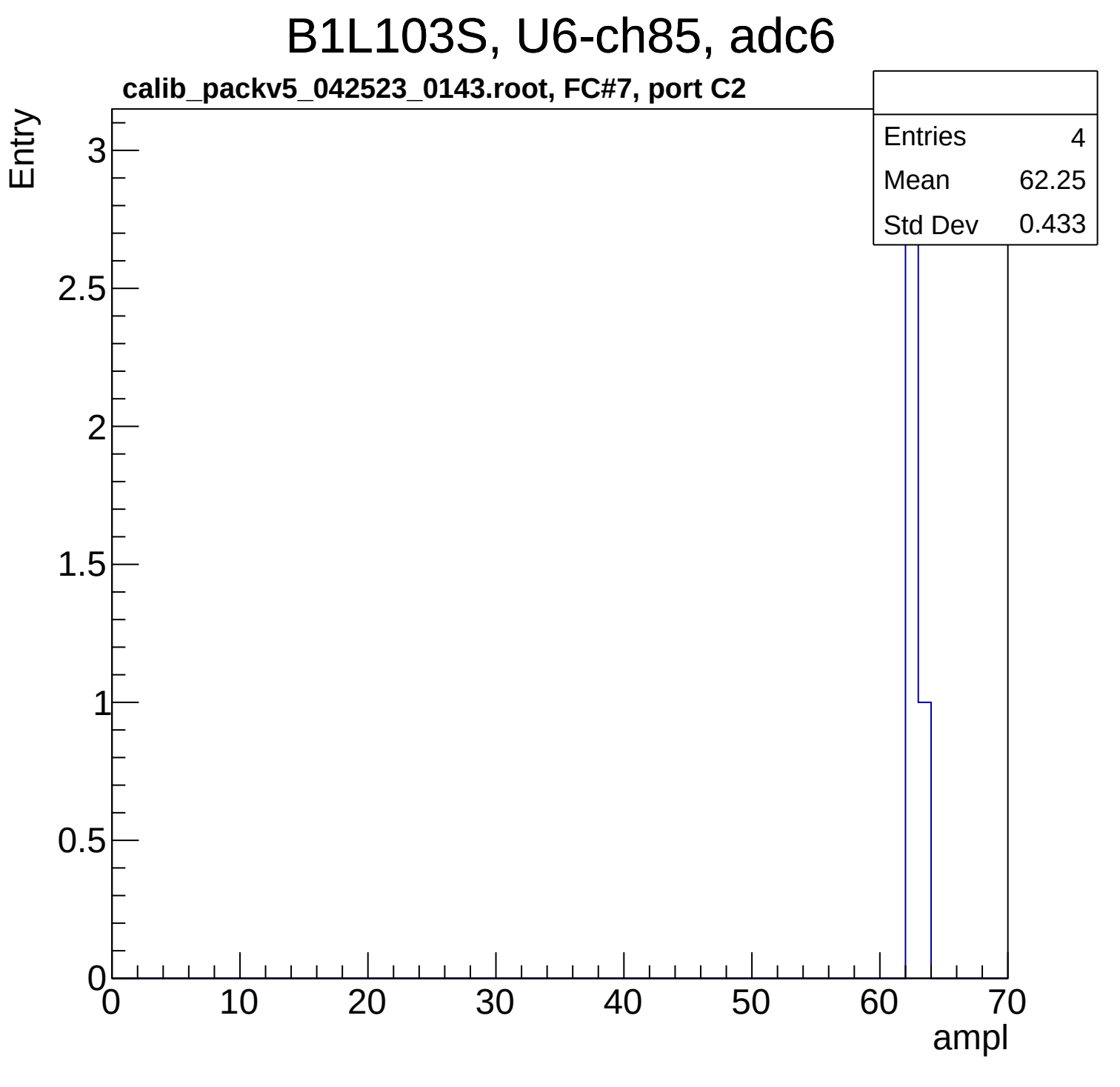
B1L103S, U6-ch85, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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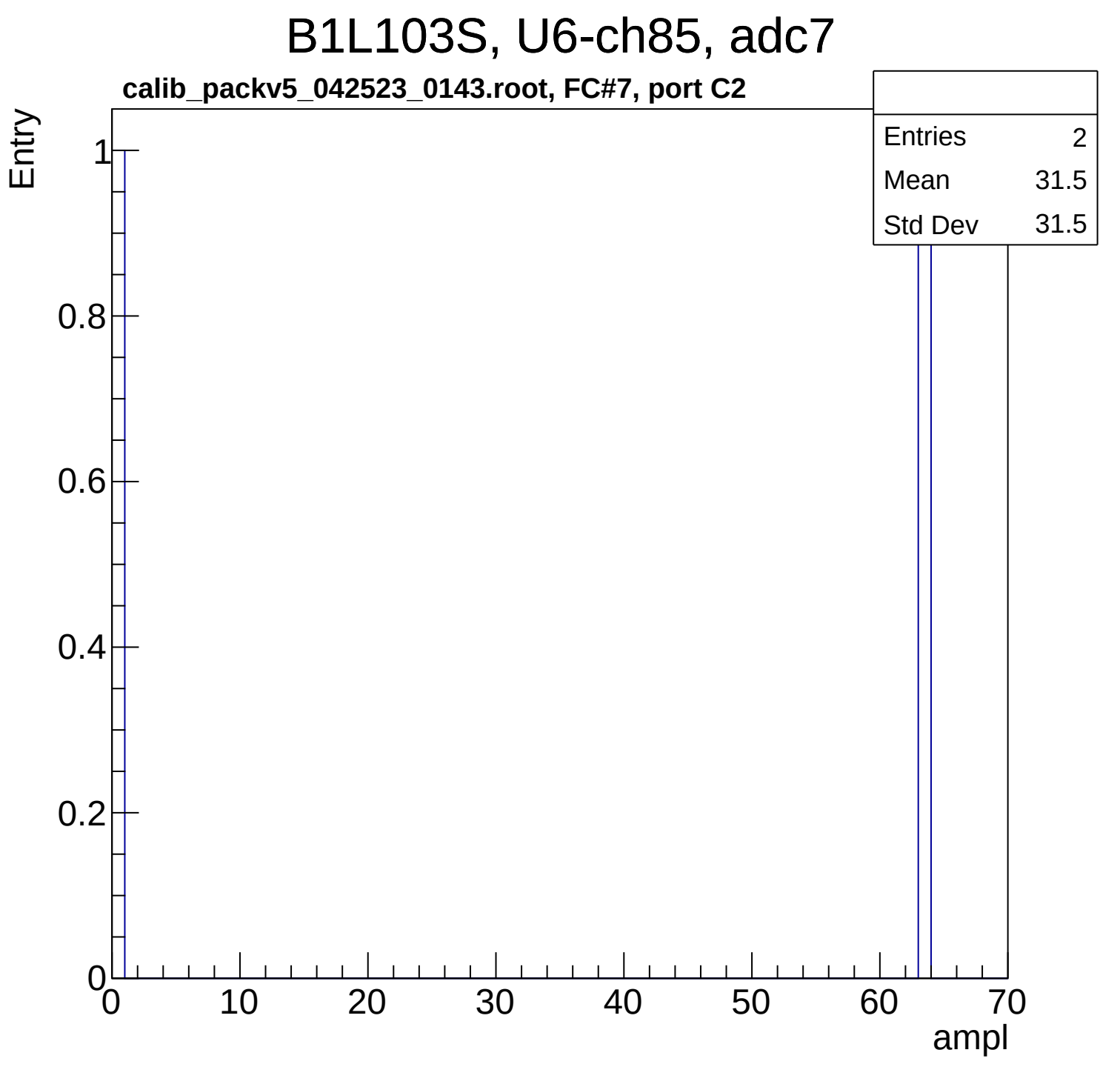
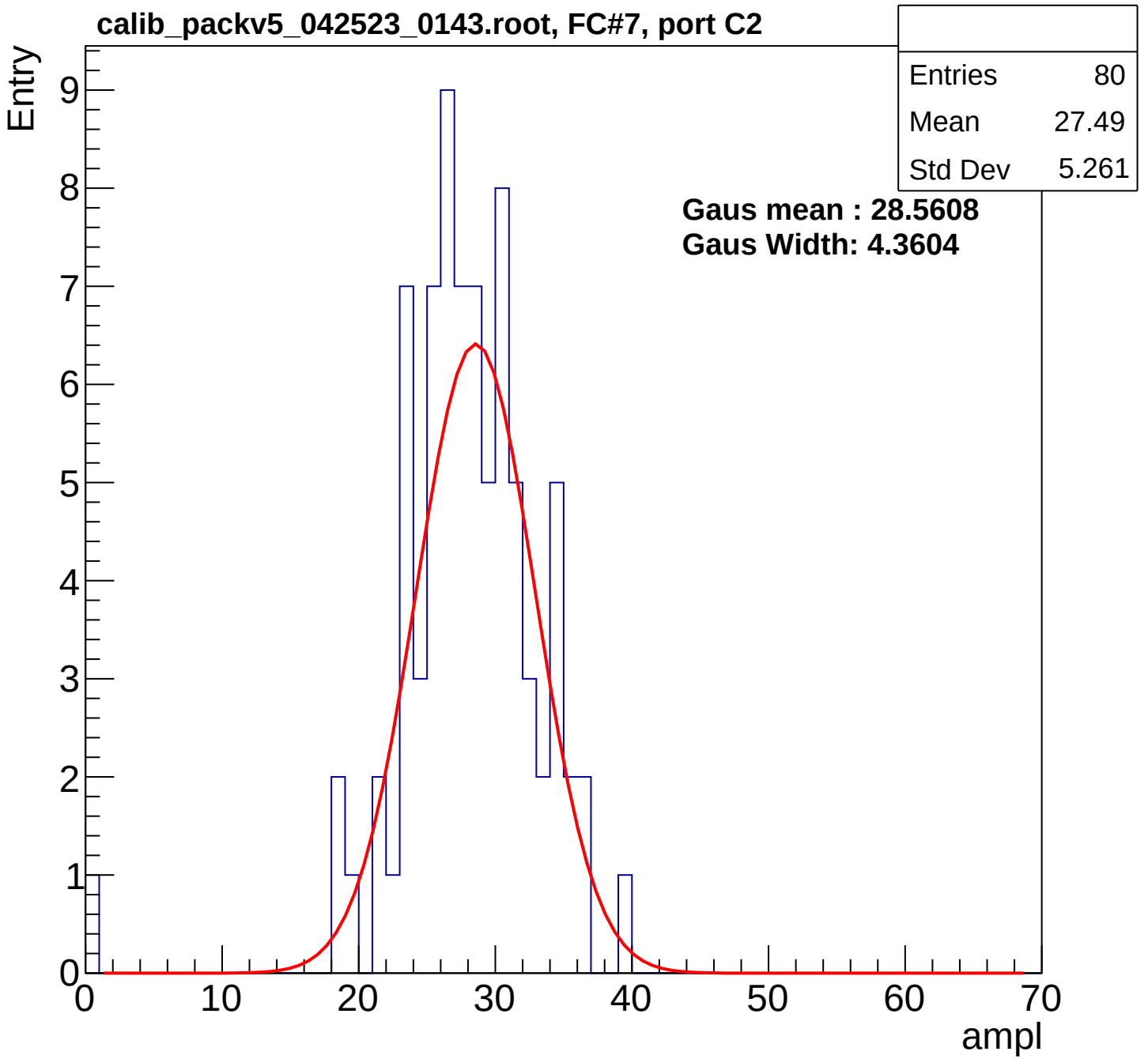

B1L103S, U6-ch85, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch86, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 27.49
Std Dev 5.261
Gaus mean : 28.5608
Gaus Width: 4.3604
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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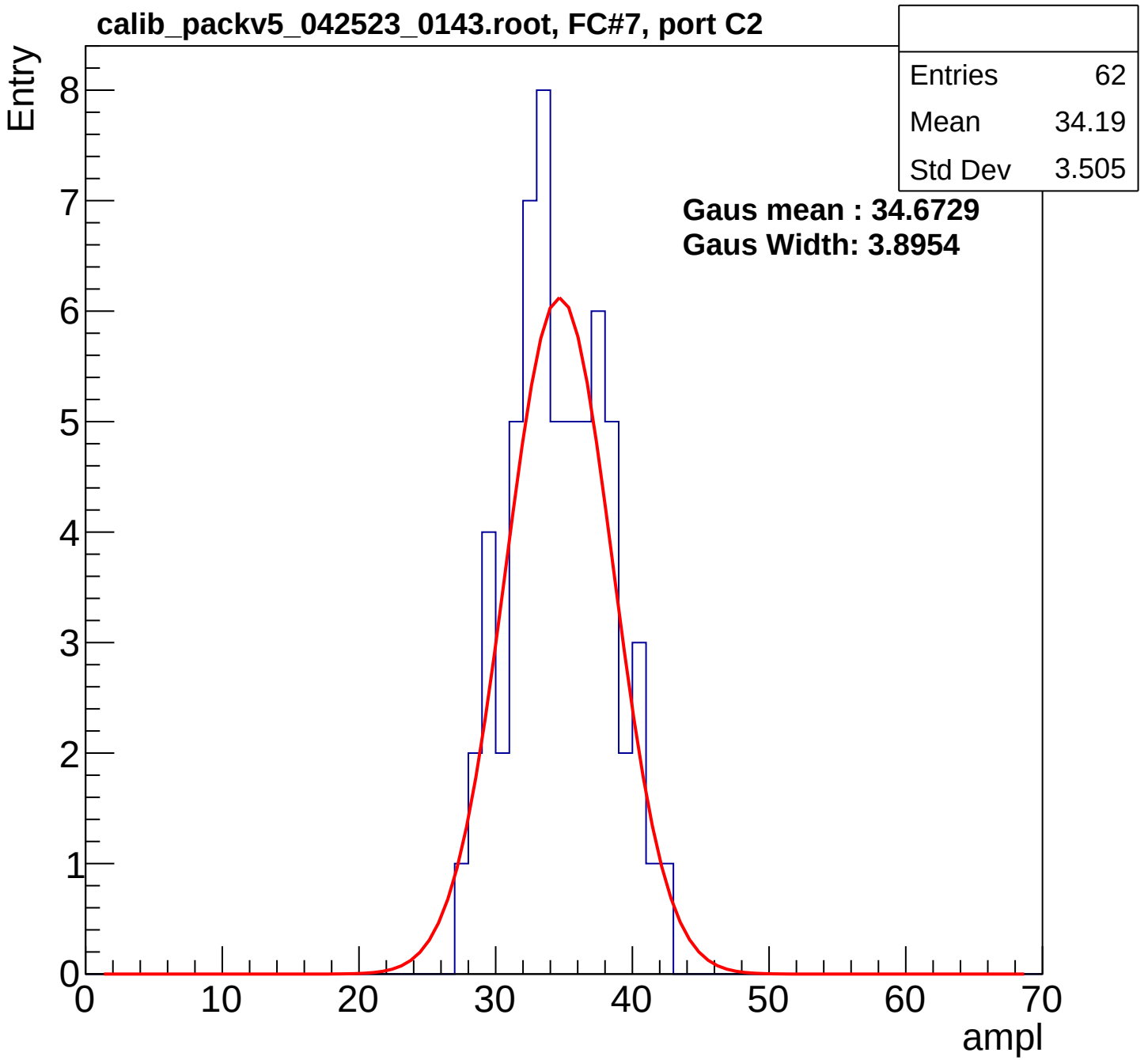
B1L103S, U6-ch86, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 34.19
Std Dev 3.505
Gaus mean : 34.6729
Gaus Width: 3.8954
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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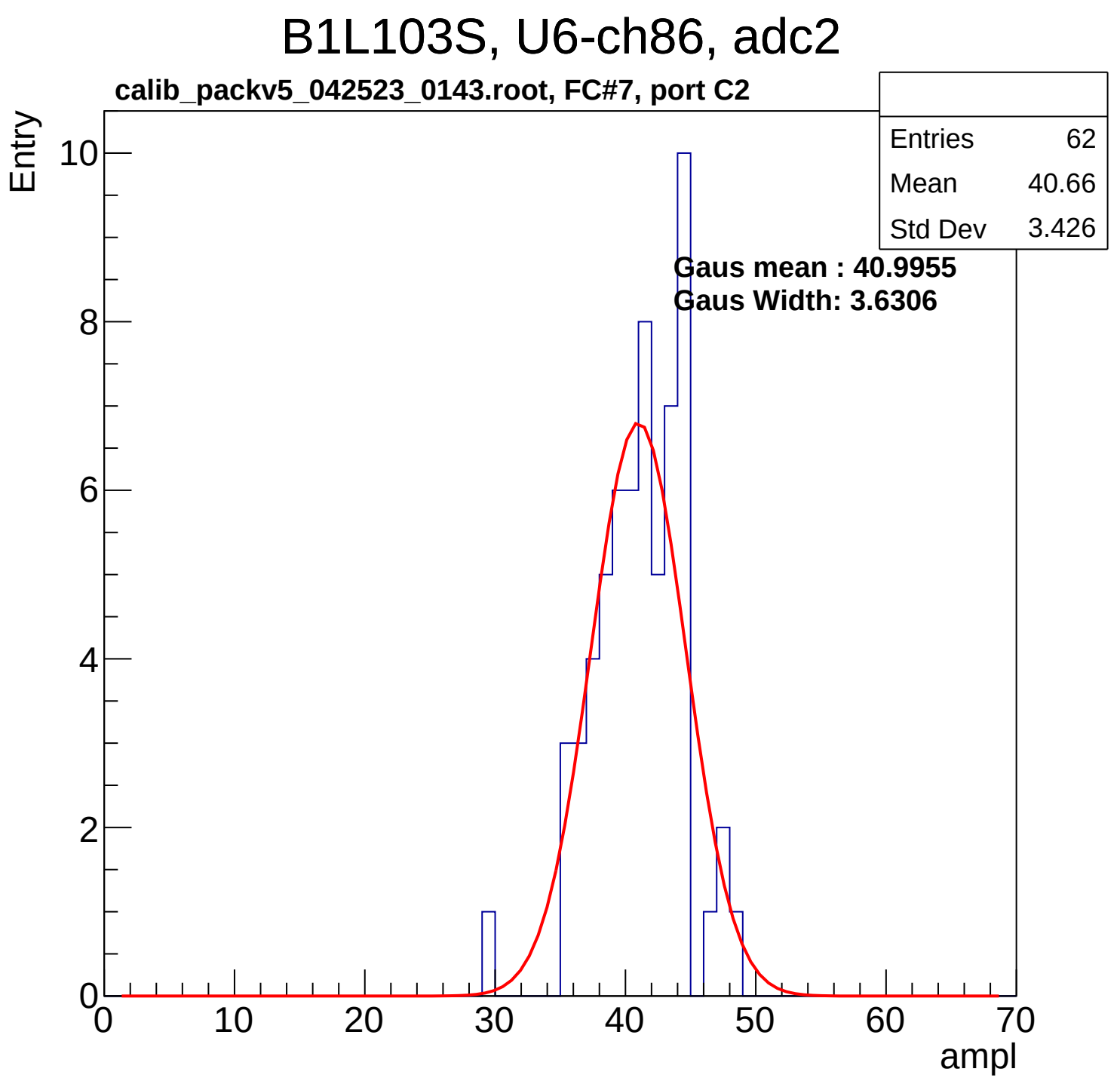
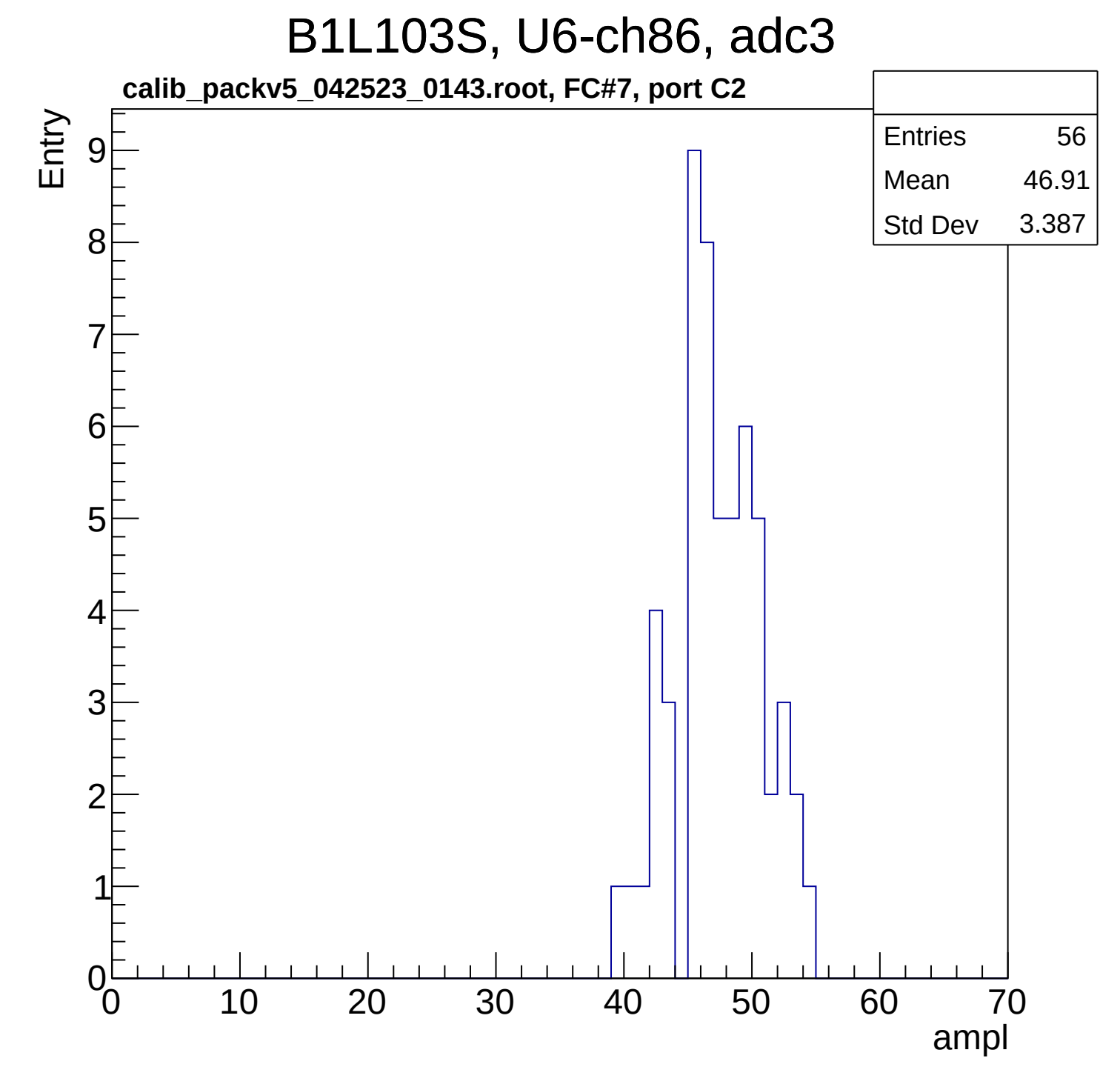

B1L103S, U6-ch86, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 40.66
Std Dev 3.426
Gaus mean : 40.9955
Gaus Width: 3.6306
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch86, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 46.91
Std Dev 3.387
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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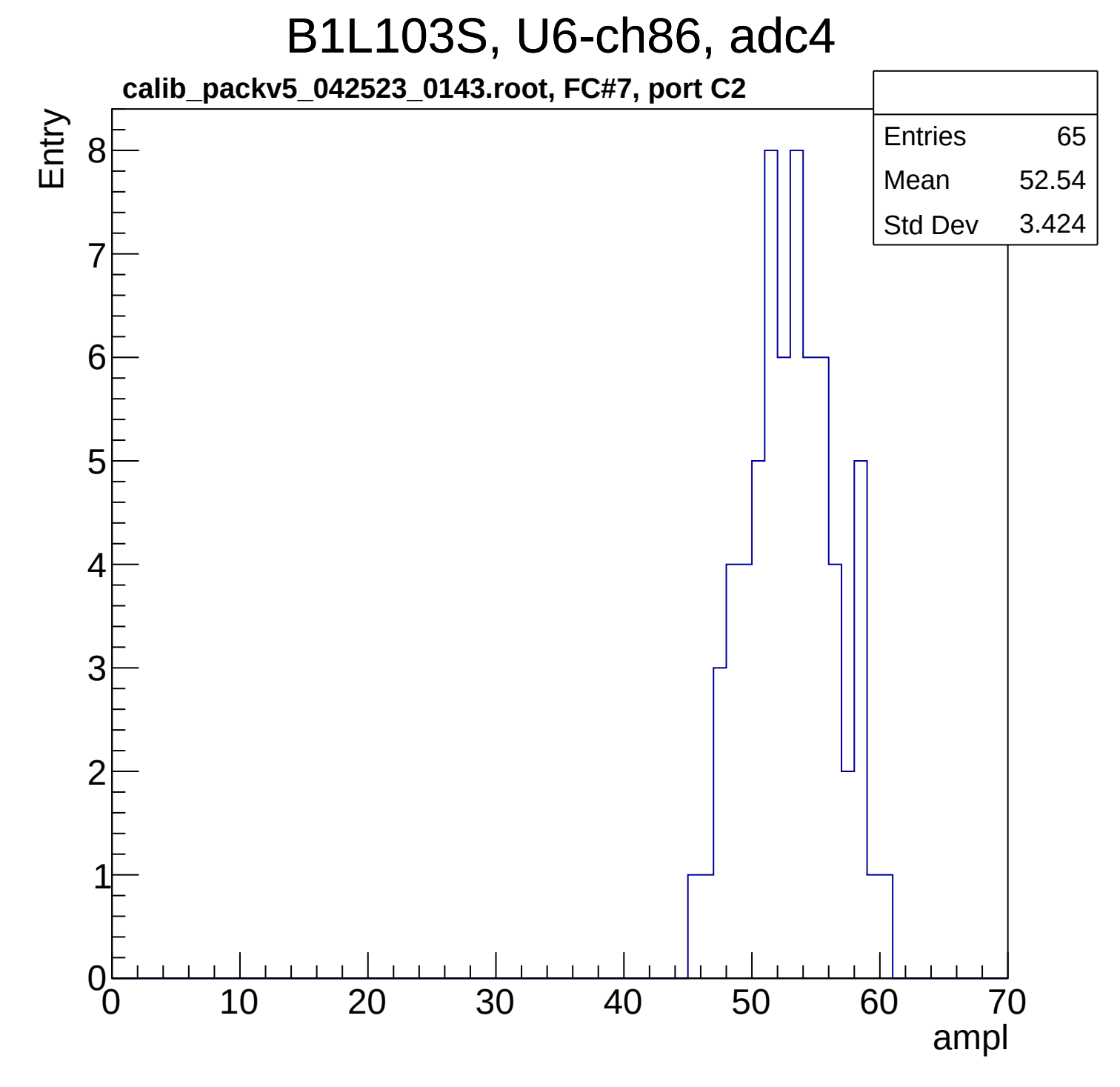
B1L103S, U6-ch86, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 52.54
Std Dev 3.424
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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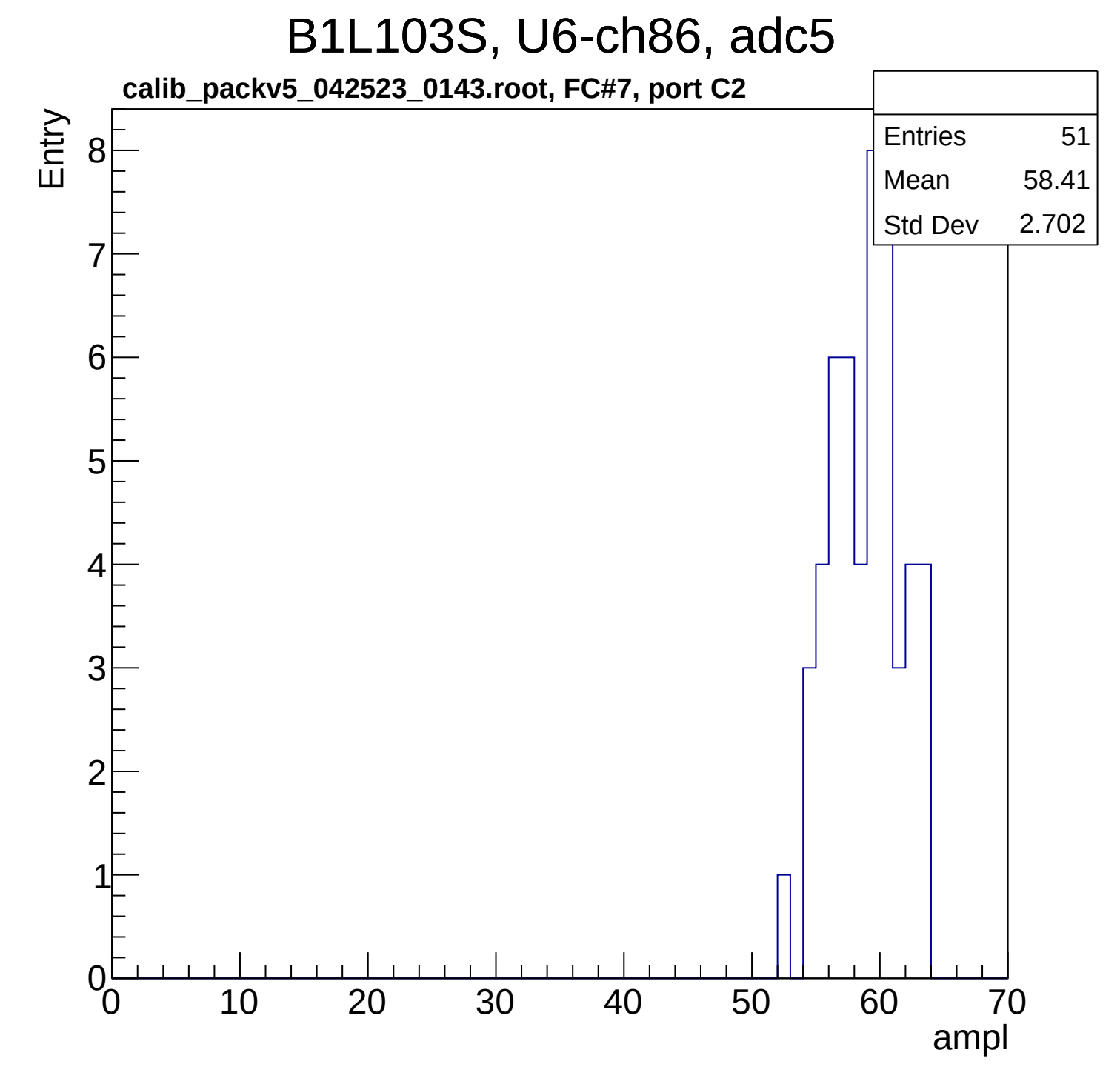
B1L103S, U6-ch86, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.41
Std Dev 2.702
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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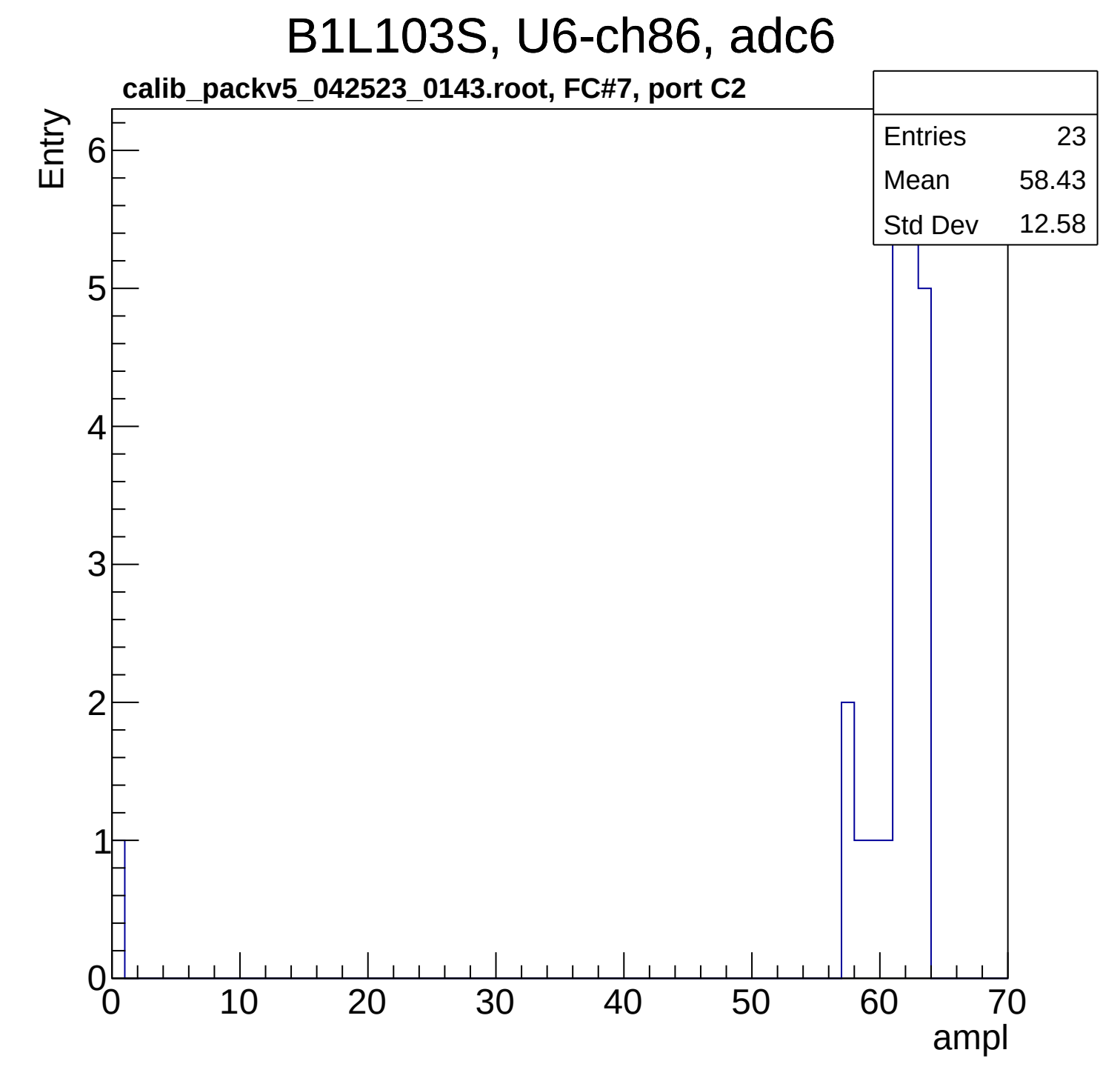
B1L103S, U6-ch86, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 23
Mean 58.43
Std Dev 12.58
Entry
ampl
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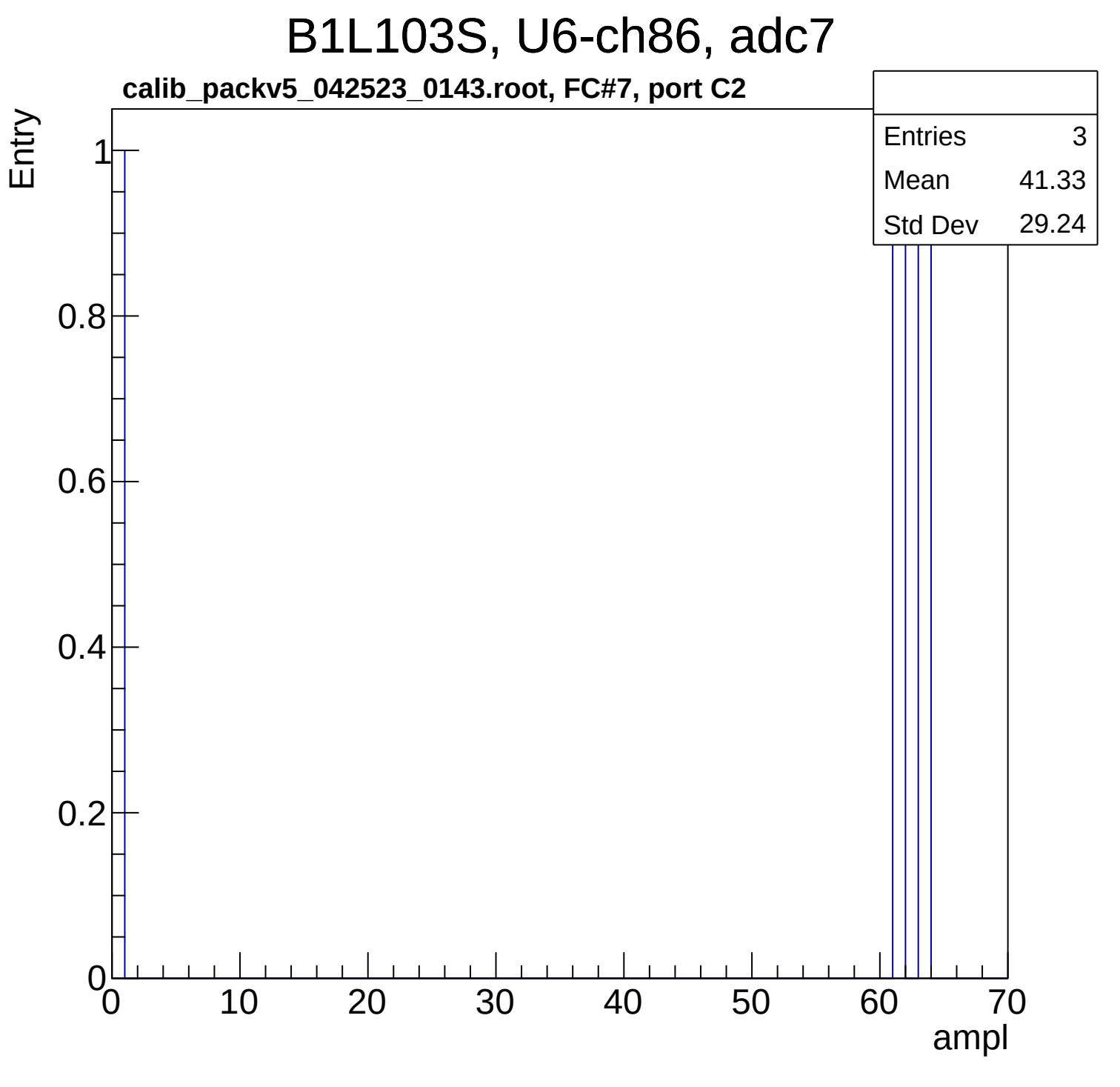
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch86, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
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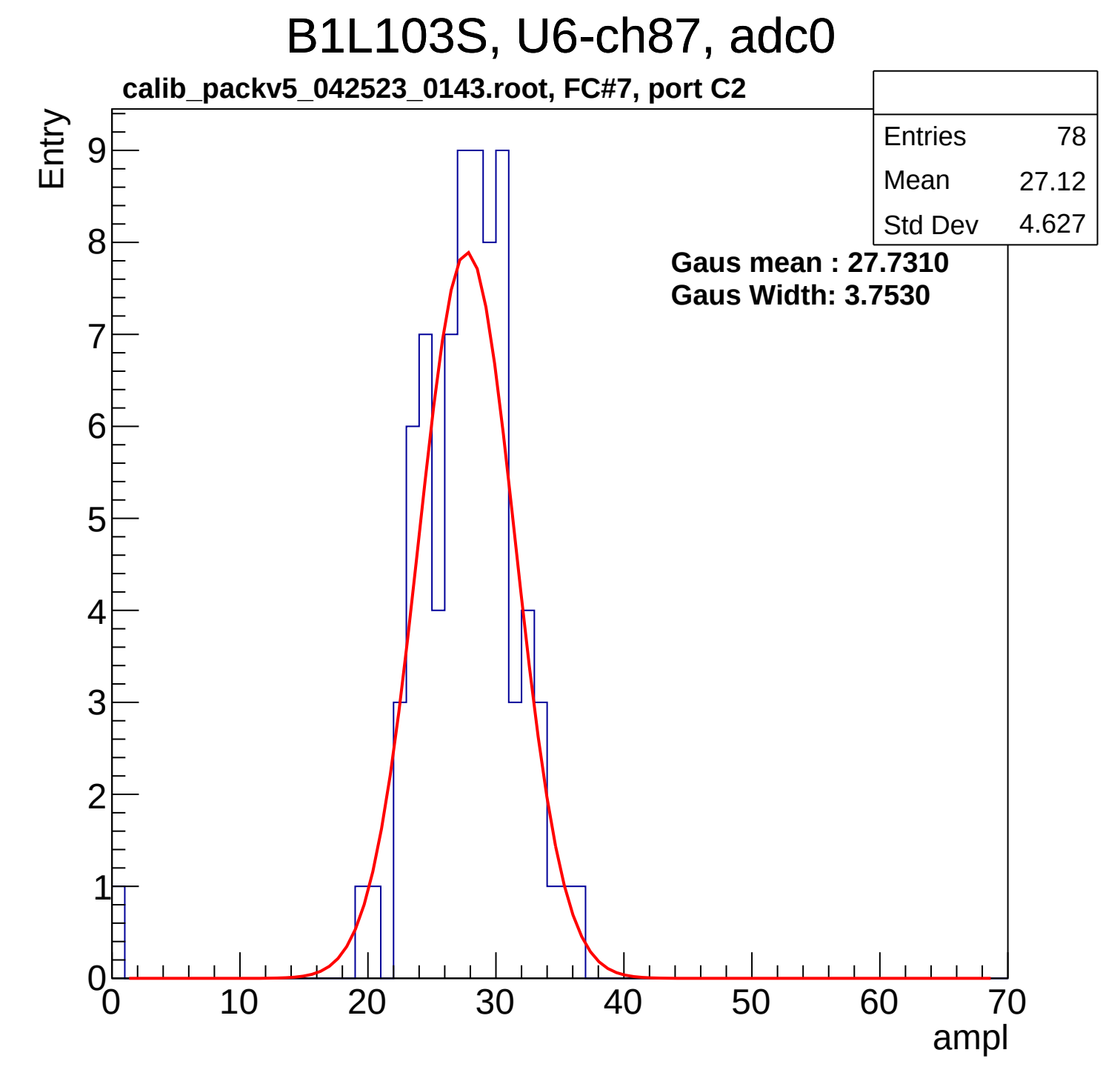
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch87, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 27.12
Std Dev 4.627
Gaus mean : 27.7310
Gaus Width: 3.7530
Entry
ampl
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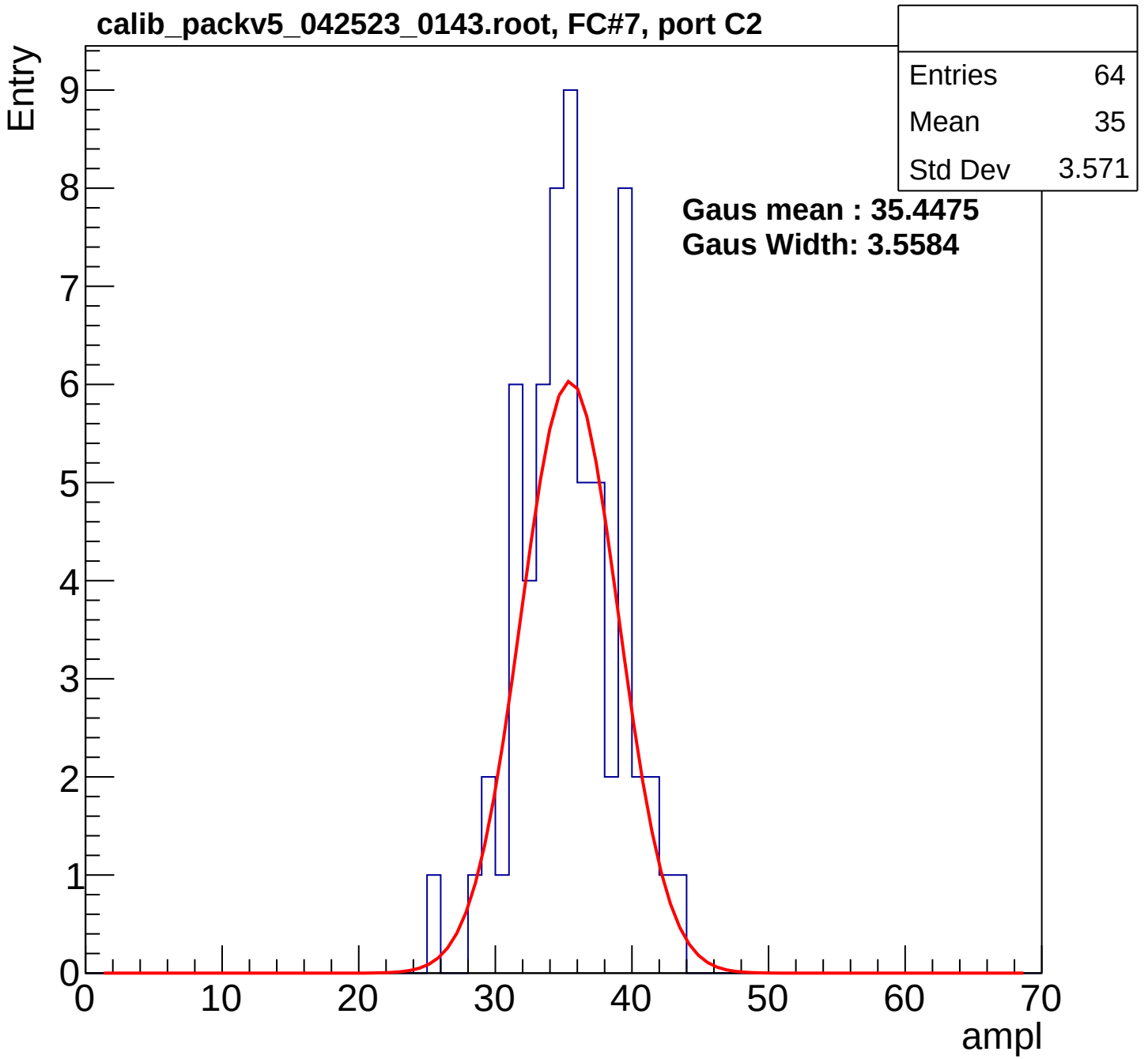
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch87, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 35
Std Dev 3.571
Gaus mean : 35.4475
Gaus Width: 3.5584
Entry
ampl
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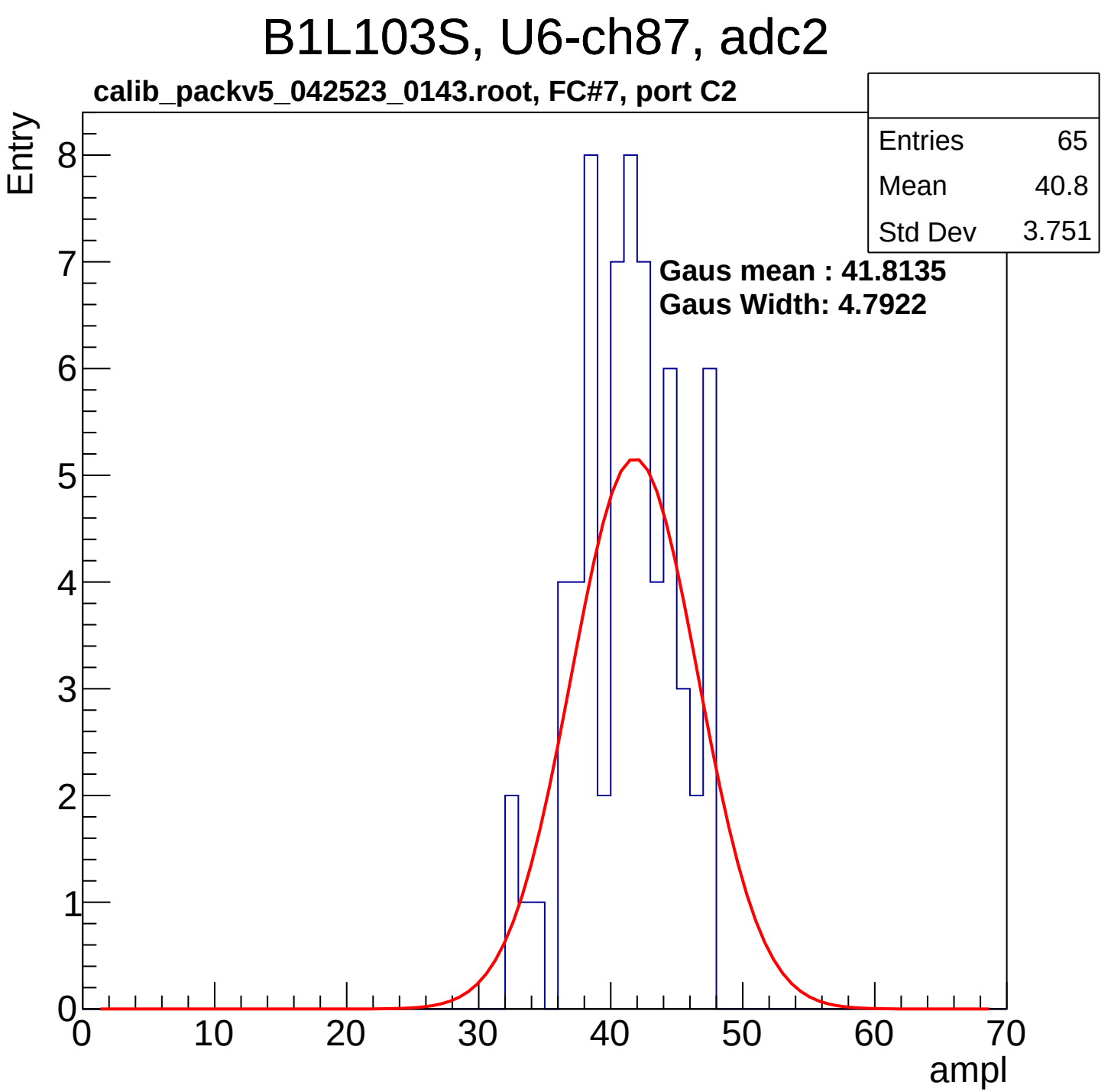
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch87, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 40.8
Std Dev 3.751
Gaus mean : 41.8135
Gaus Width: 4.7922
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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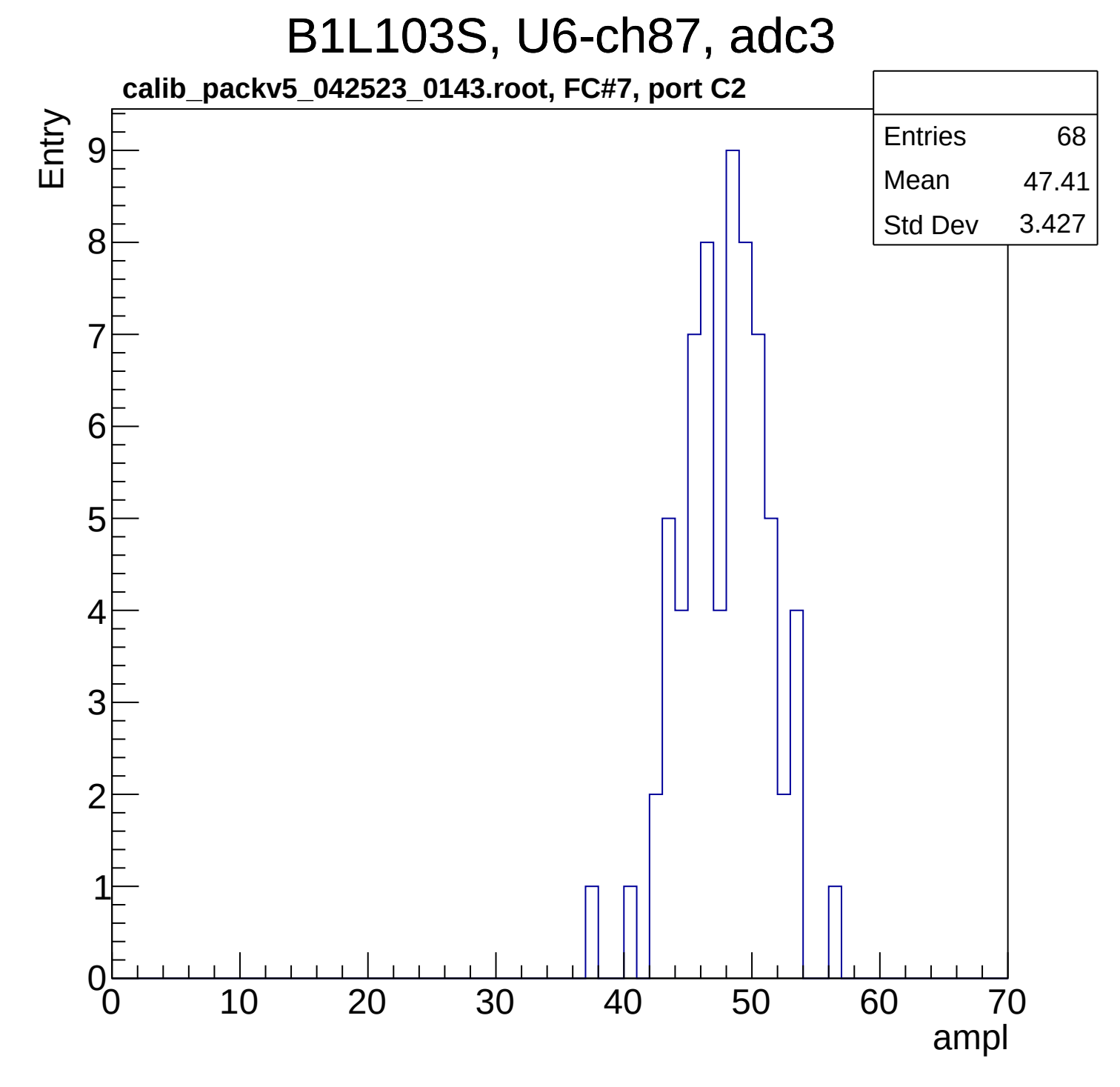
B1L103S, U6-ch87, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 47.41
Std Dev 3.427
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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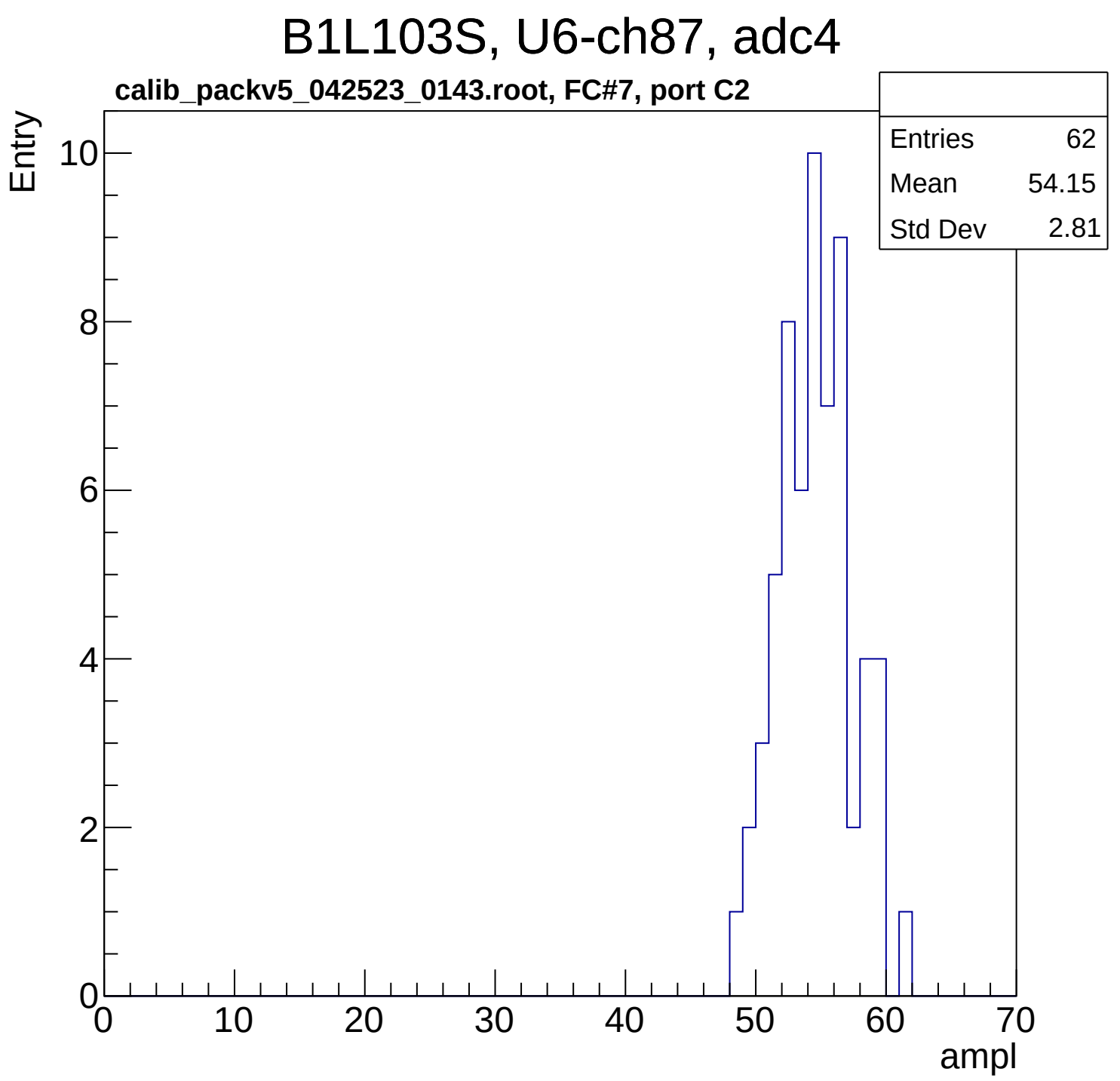
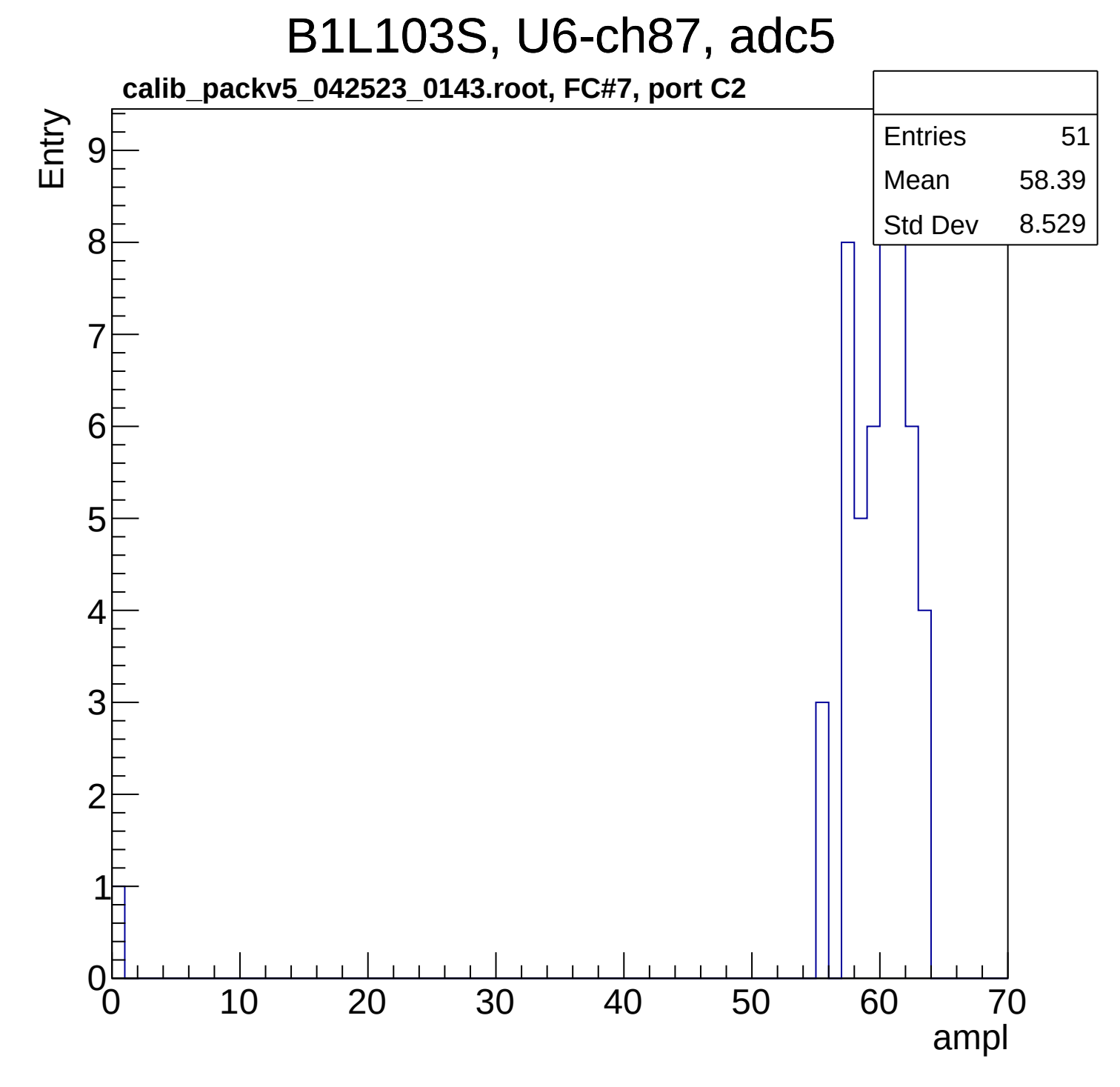

B1L103S, U6-ch87, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 54.15
Std Dev 2.81
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch87, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.39
Std Dev 8.529
Entry
ampl
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2
3
4
5
6
7
8
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0
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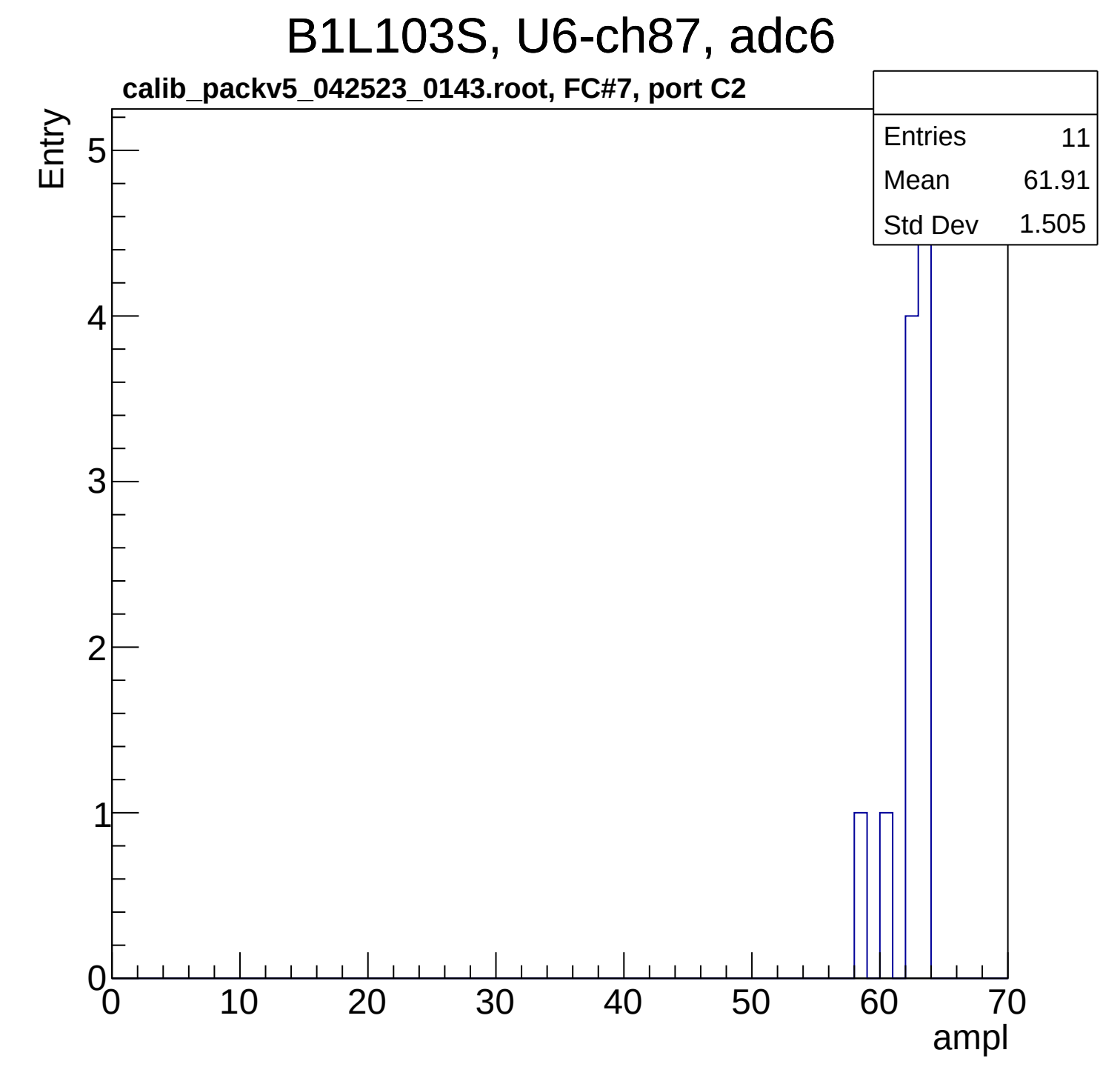
B1L103S, U6-ch87, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61.91
Std Dev 1.505
Entry
ampl
0
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2
3
4
5
0
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20
30
40
50
60
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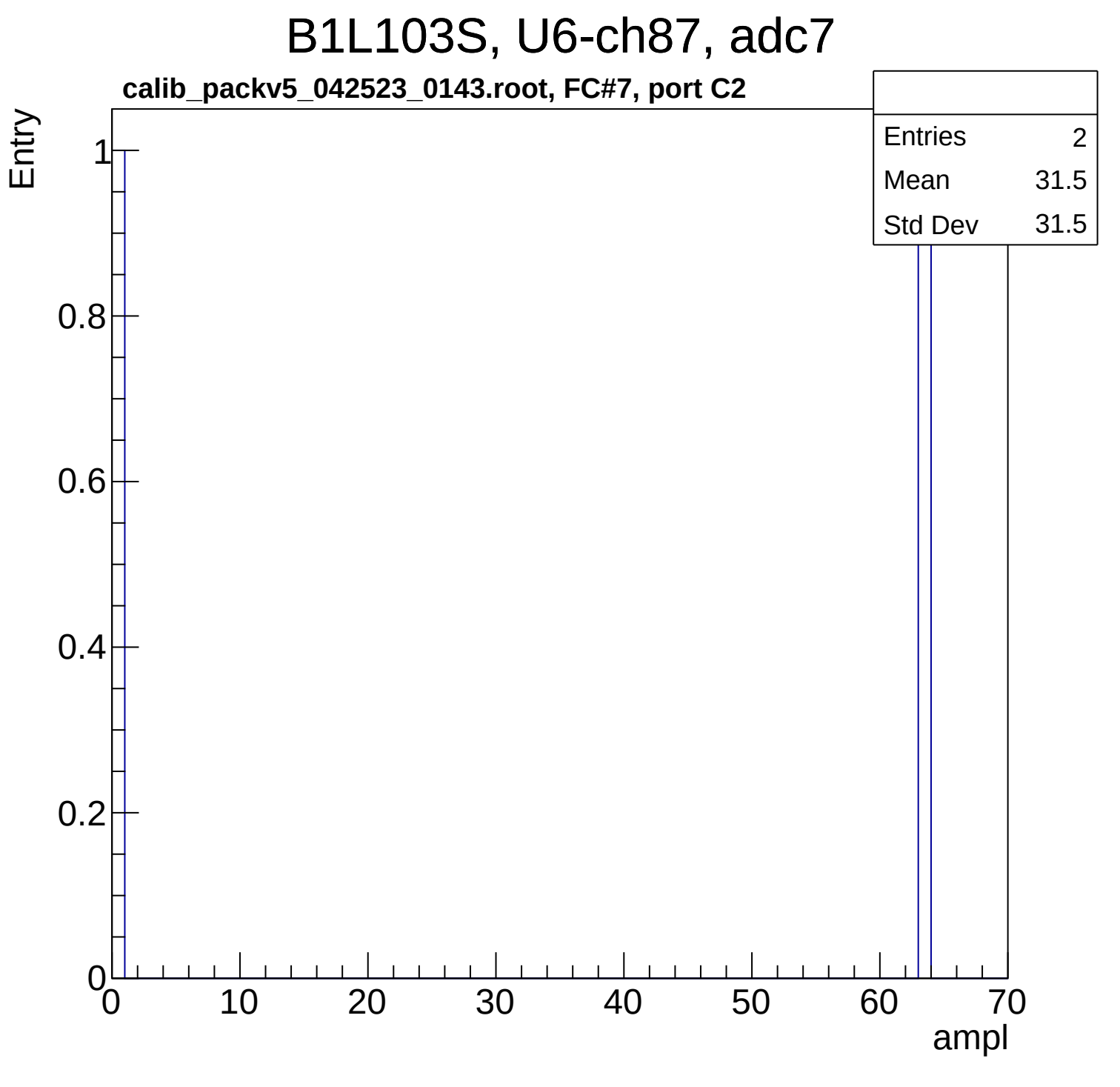
B1L103S, U6-ch87, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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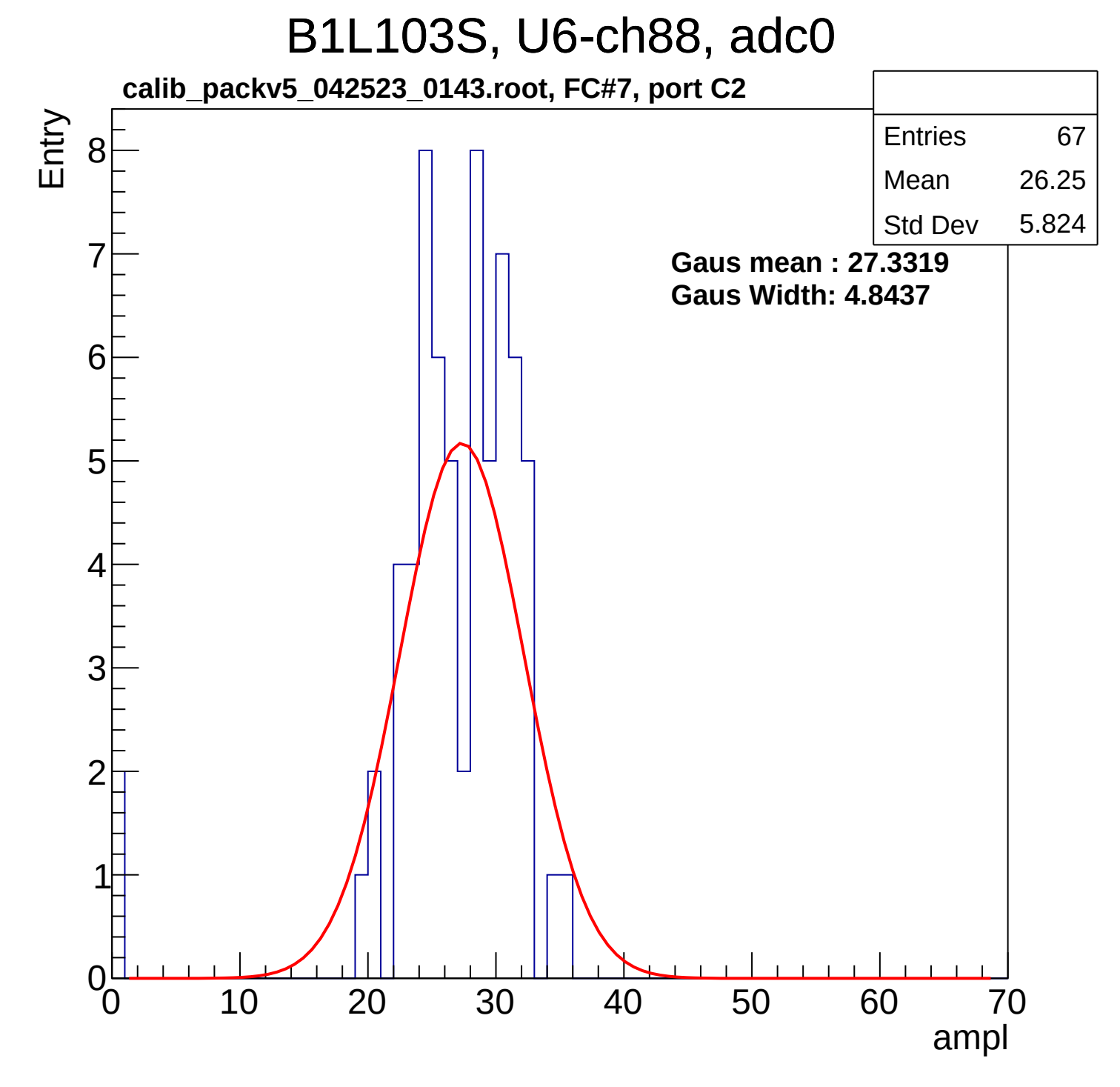
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch88, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 26.25
Std Dev 5.824
Gaus mean : 27.3319
Gaus Width: 4.8437
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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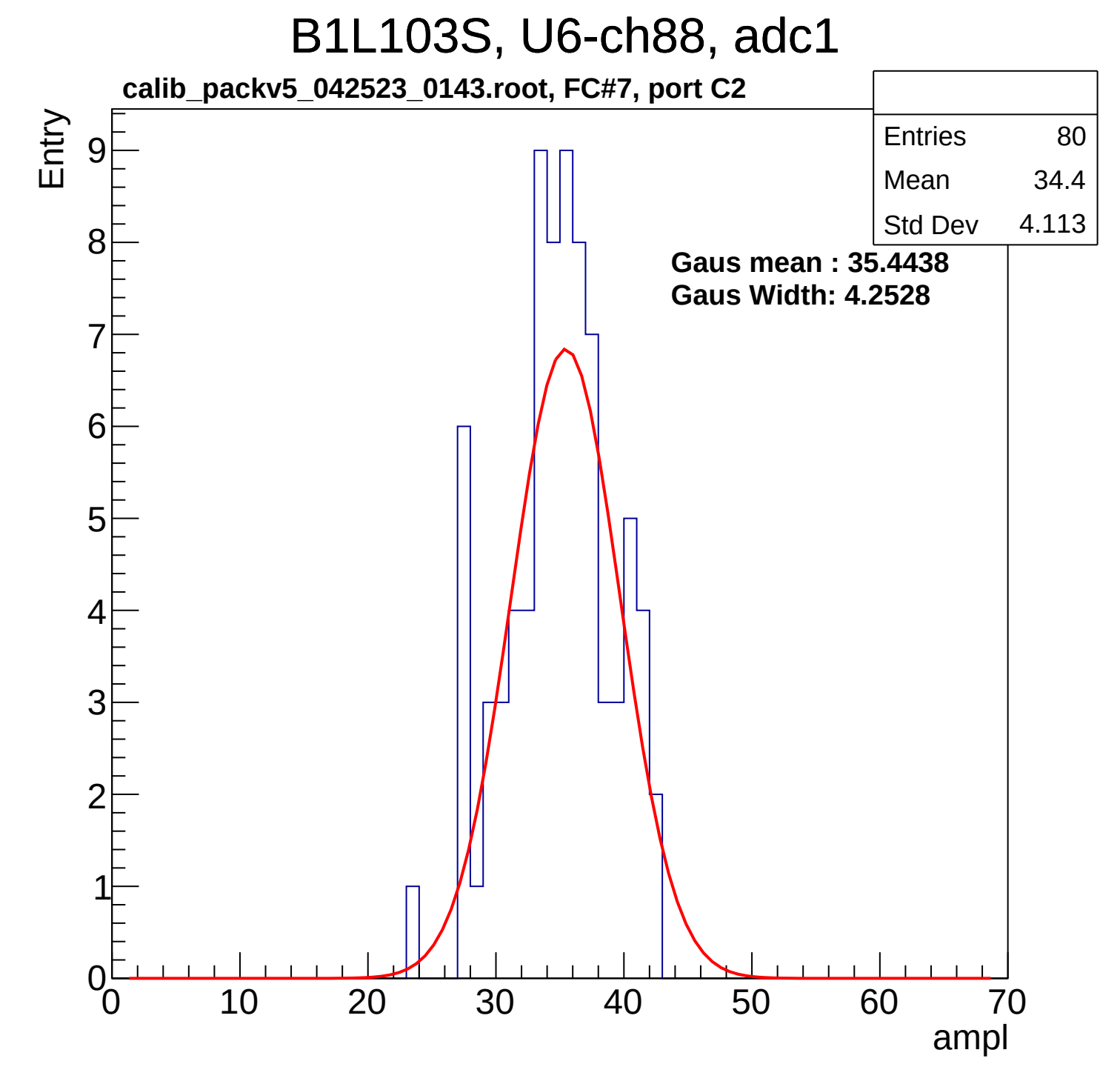
B1L103S, U6-ch88, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 34.4
Std Dev 4.113
Gaus mean : 35.4438
Gaus Width: 4.2528
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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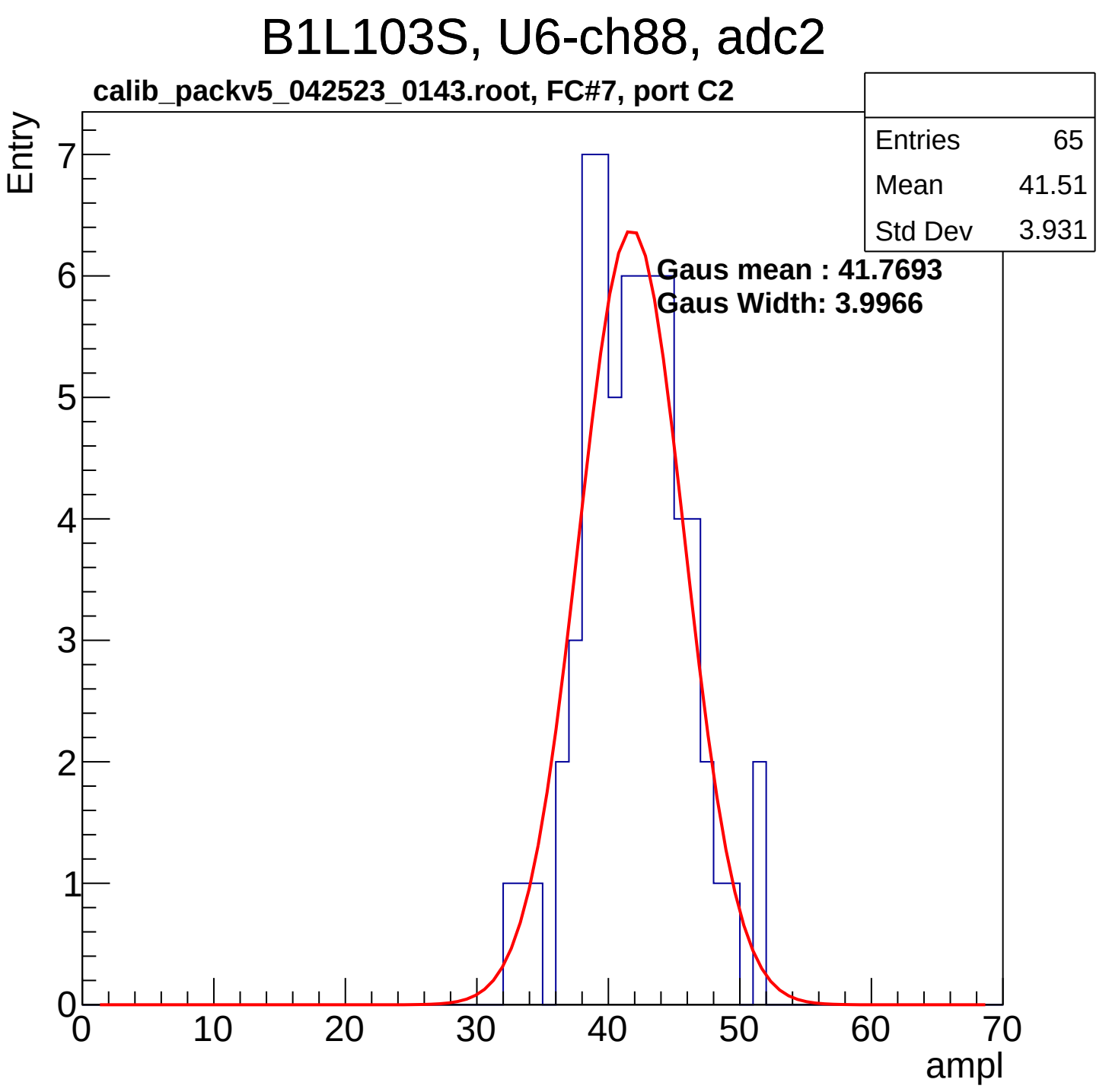
B1L103S, U6-ch88, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.51
Std Dev 3.931
Gaus mean : 41.7693
Gaus Width: 3.9966
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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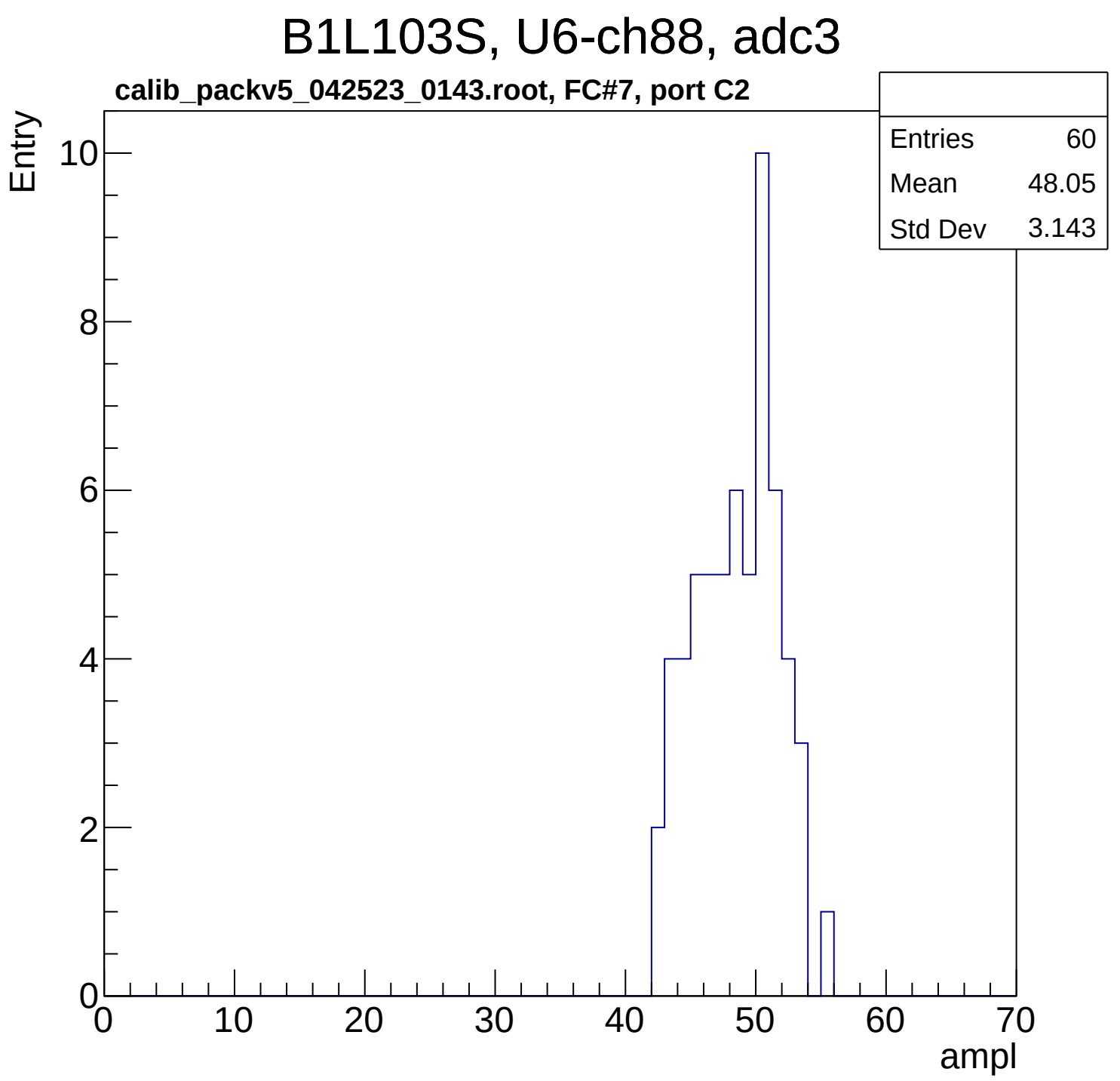
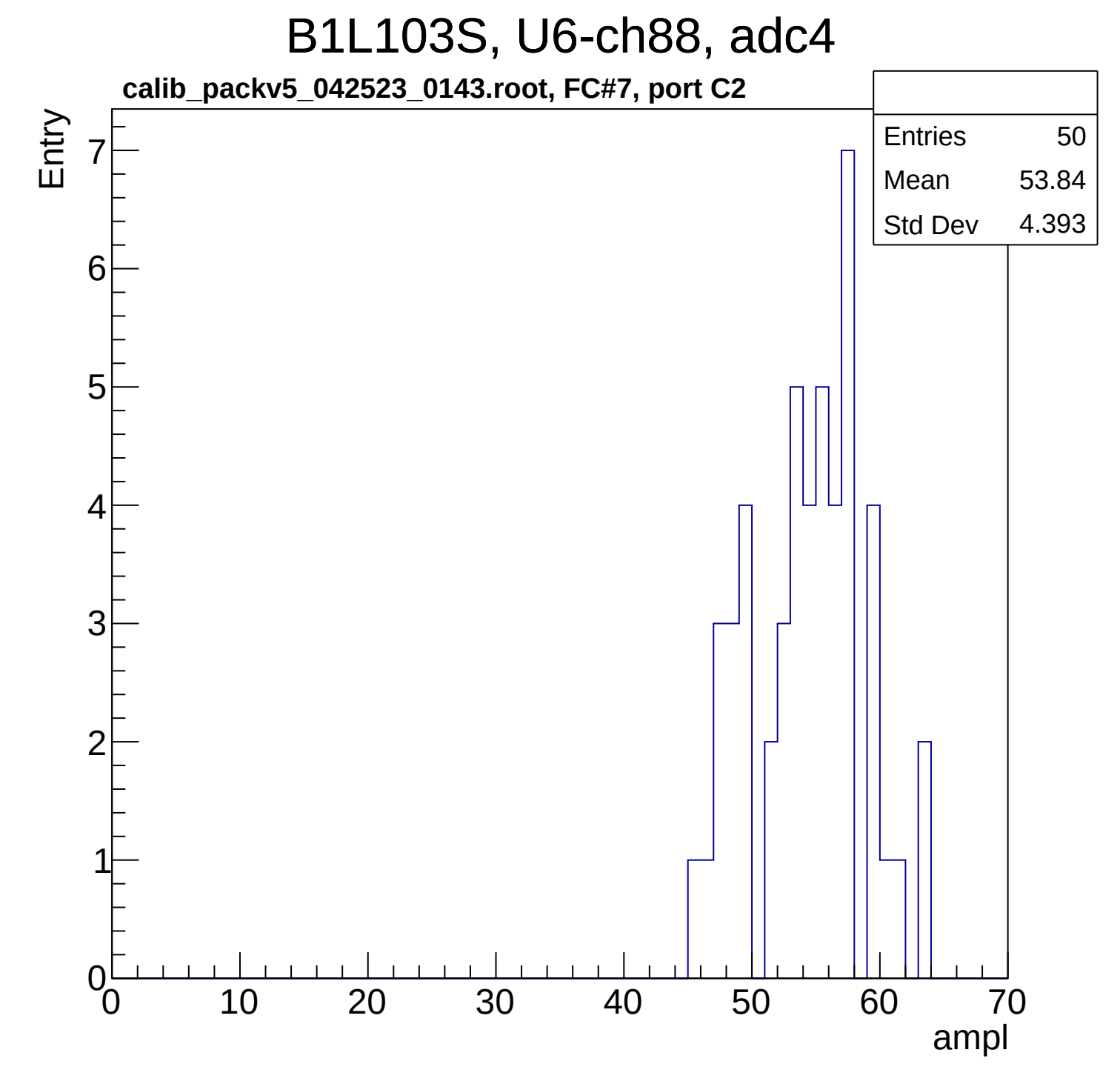

B1L103S, U6-ch88, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.05
Std Dev 3.143
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch88, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 53.84
Std Dev 4.393
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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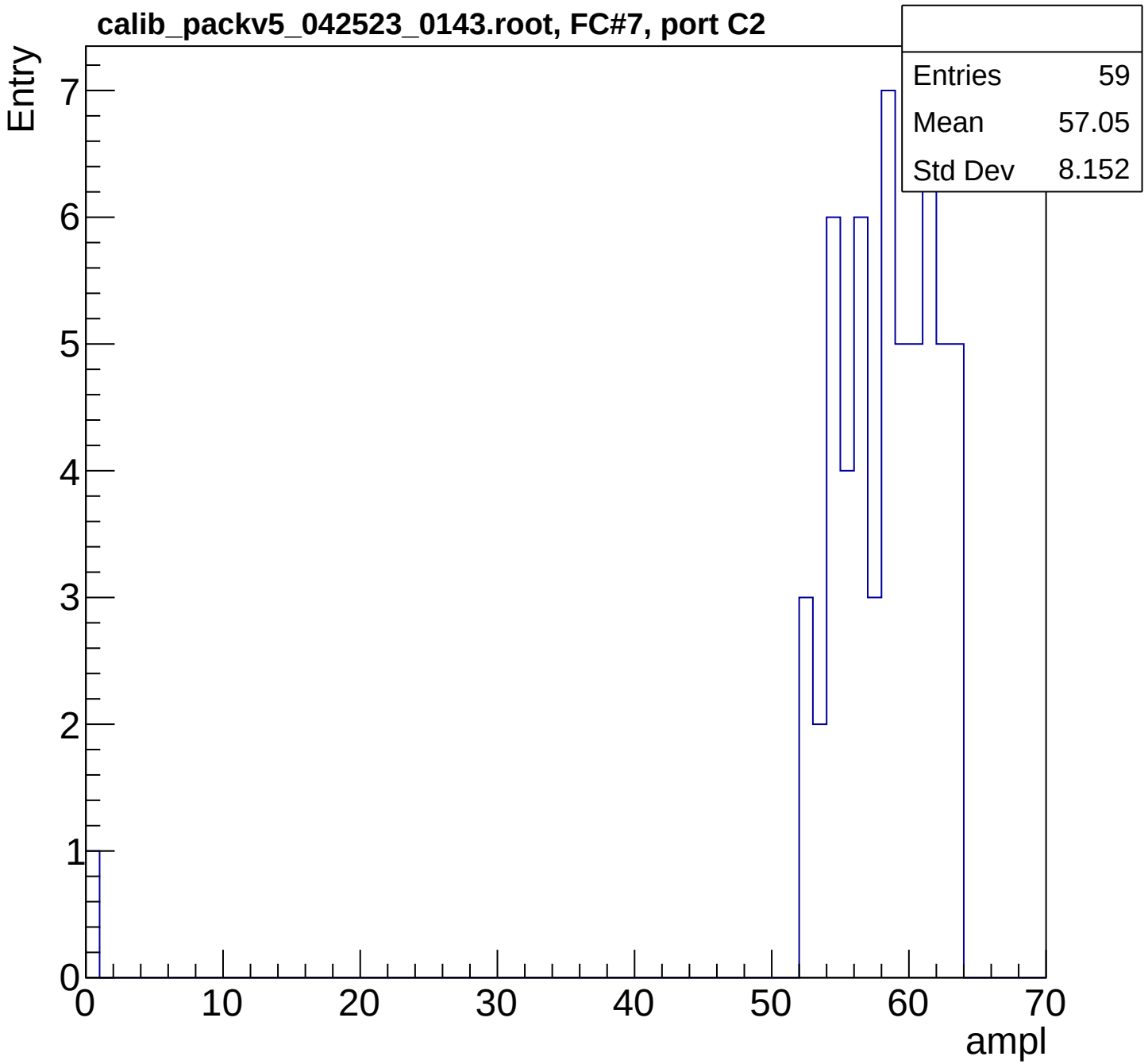
B1L103S, U6-ch88, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 57.05
Std Dev 8.152
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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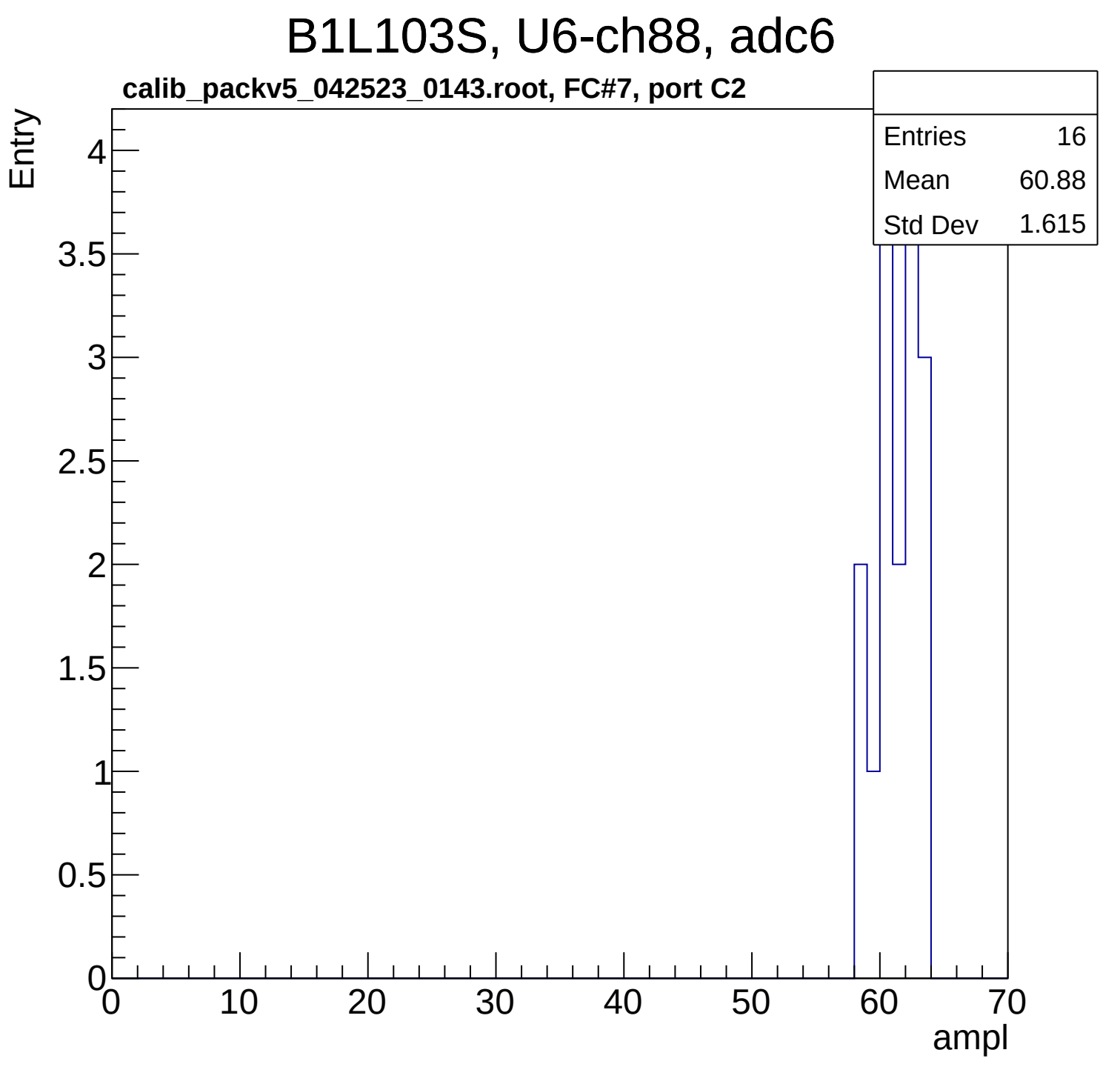
B1L103S, U6-ch88, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 60.88
Std Dev 1.615
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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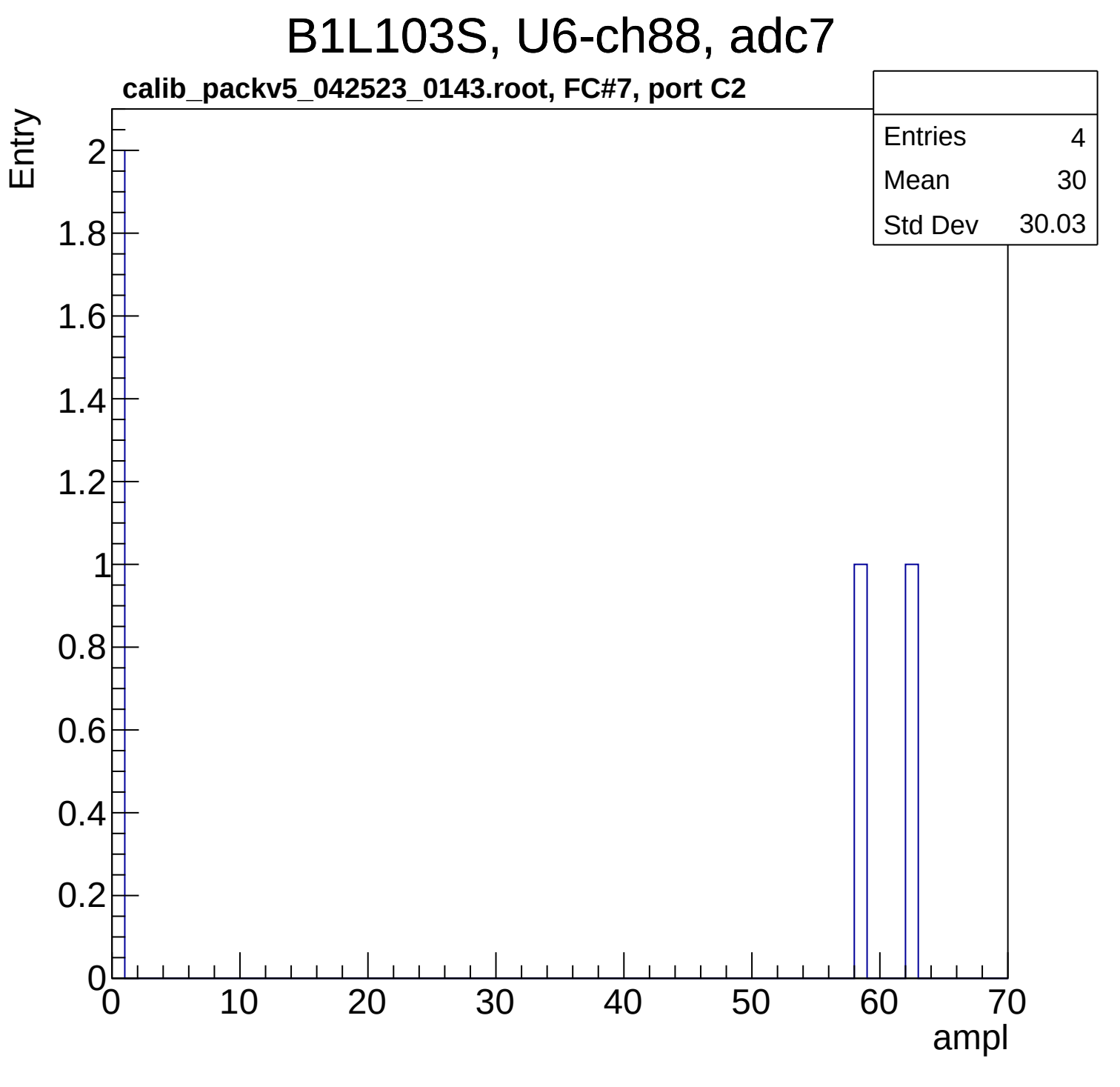
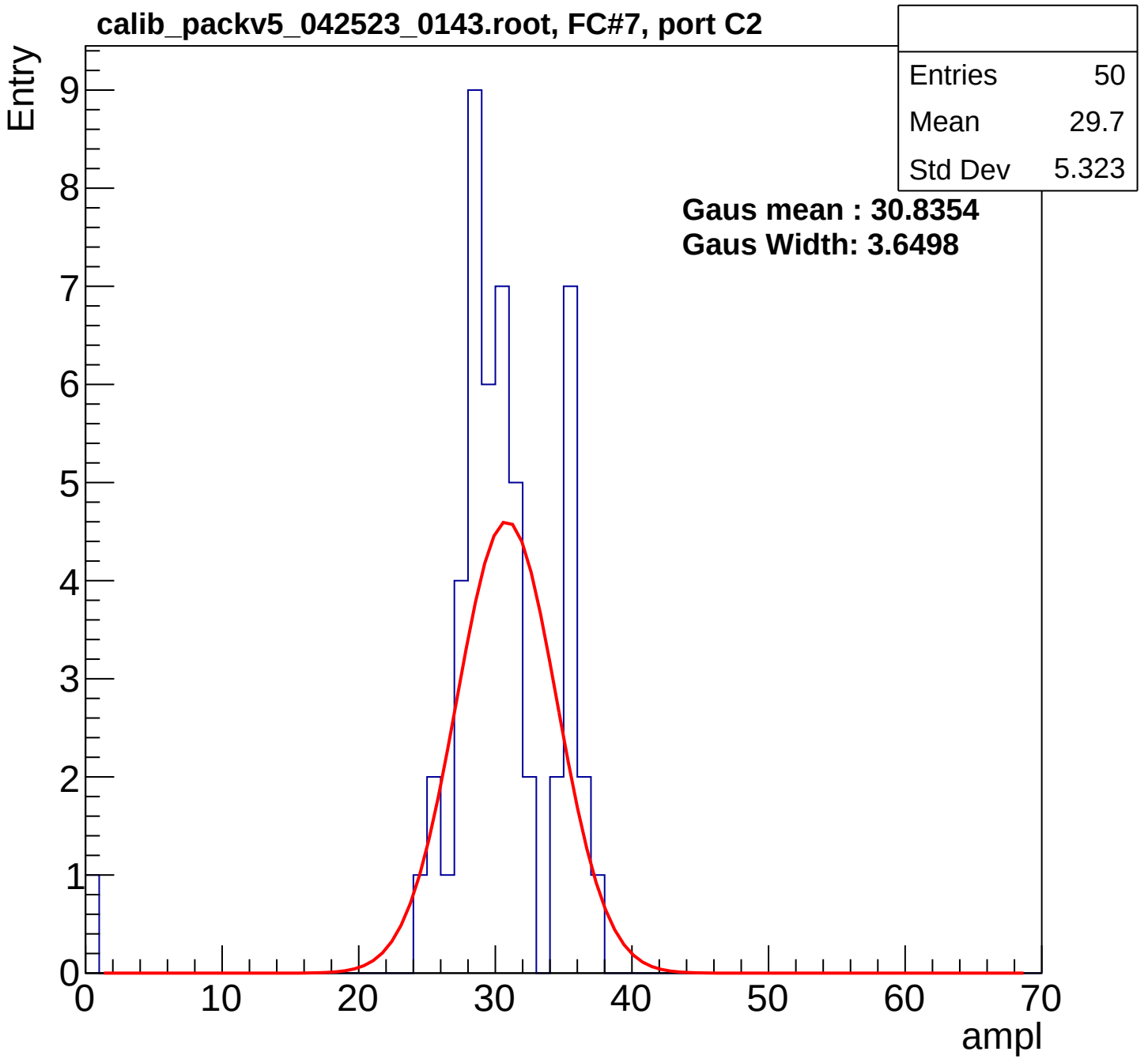

B1L103S, U6-ch88, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 30
Std Dev 30.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch89, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 29.7
Std Dev 5.323
Gaus mean : 30.8354
Gaus Width: 3.6498
Entry
ampl
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2
3
4
5
6
7
8
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0
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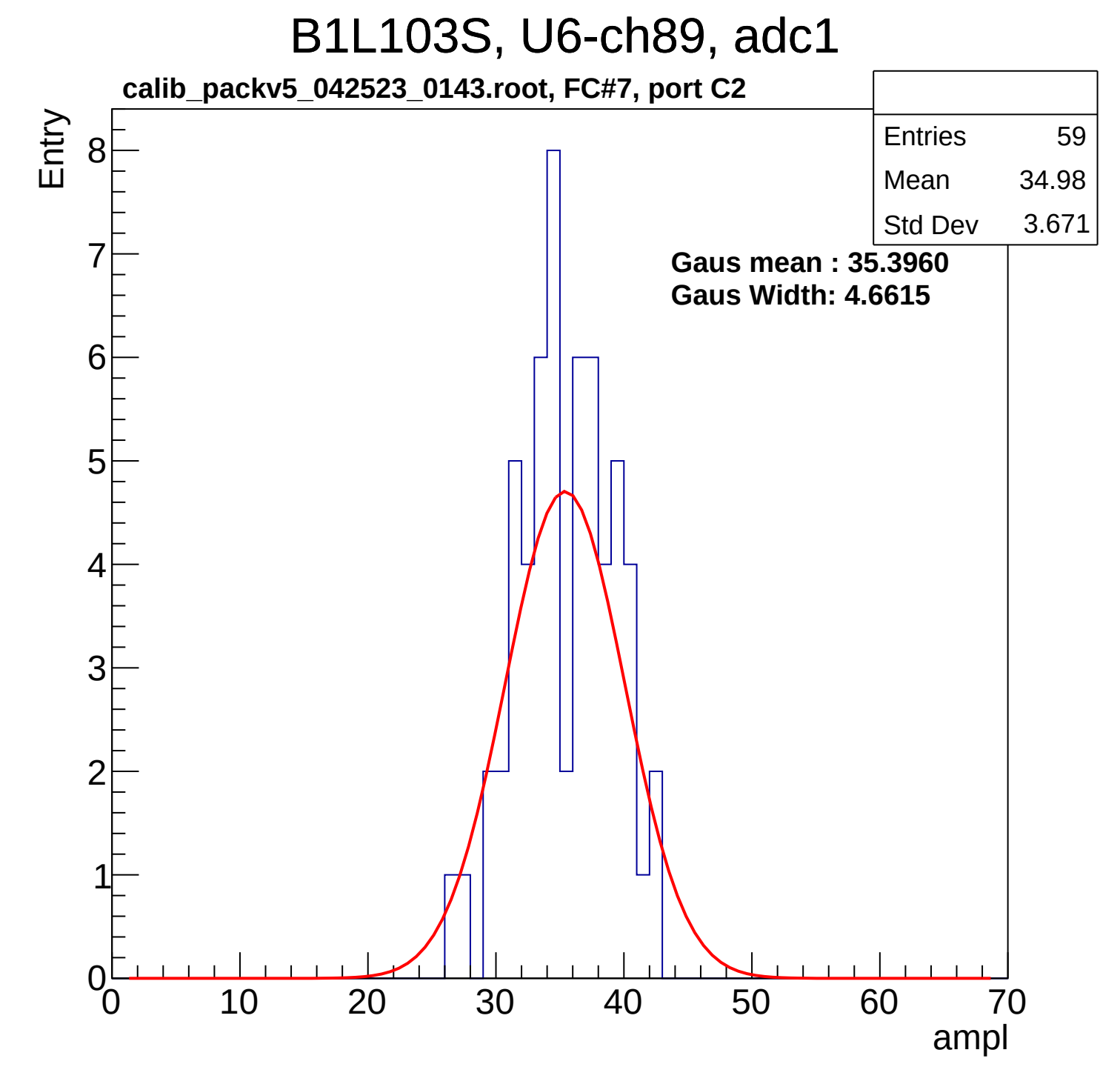
B1L103S, U6-ch89, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 34.98
Std Dev 3.671
Gaus mean : 35.3960
Gaus Width: 4.6615
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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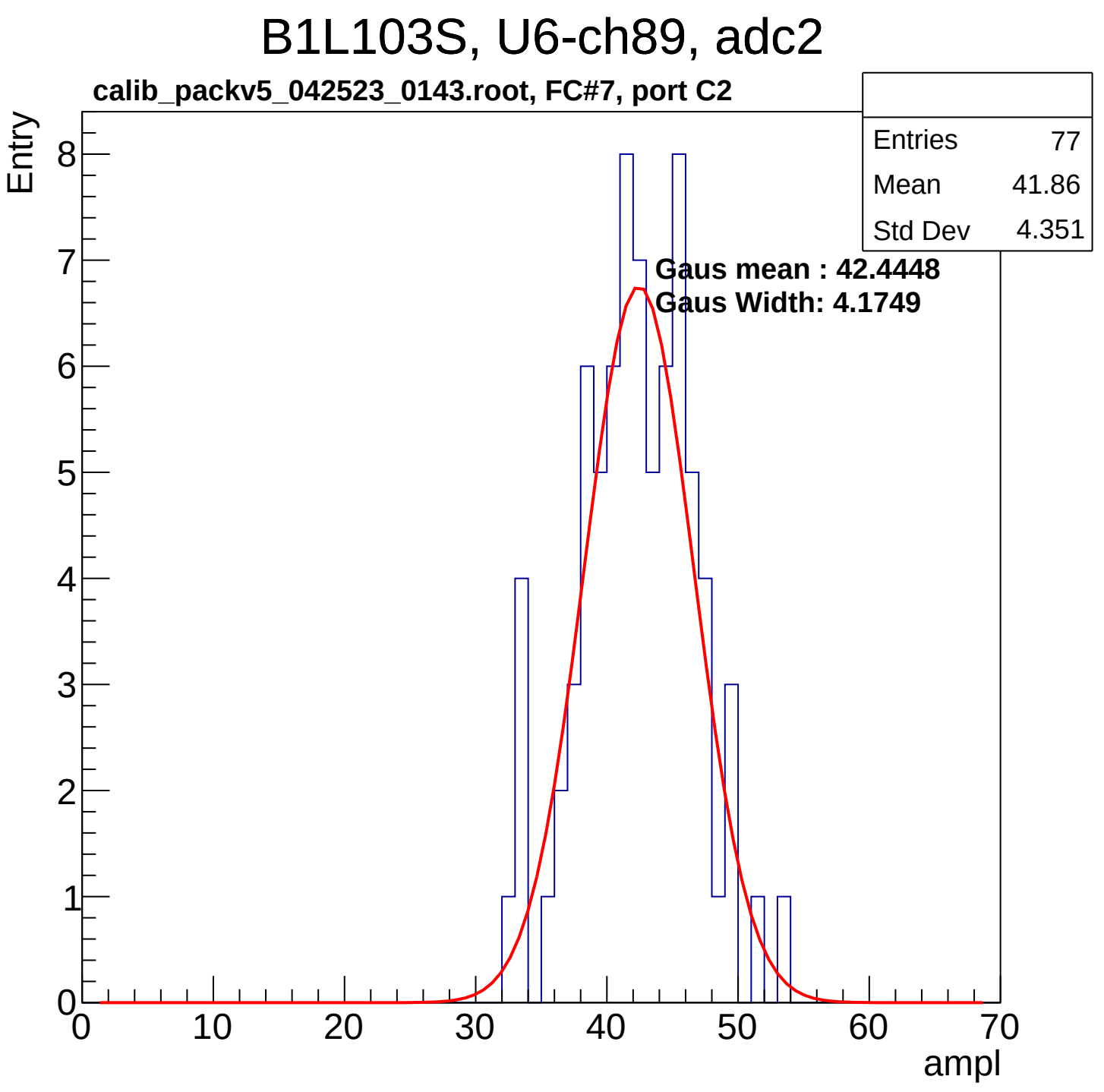
B1L103S, U6-ch89, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 41.86
Std Dev 4.351
Gaus mean : 42.4448
Gaus Width: 4.1749
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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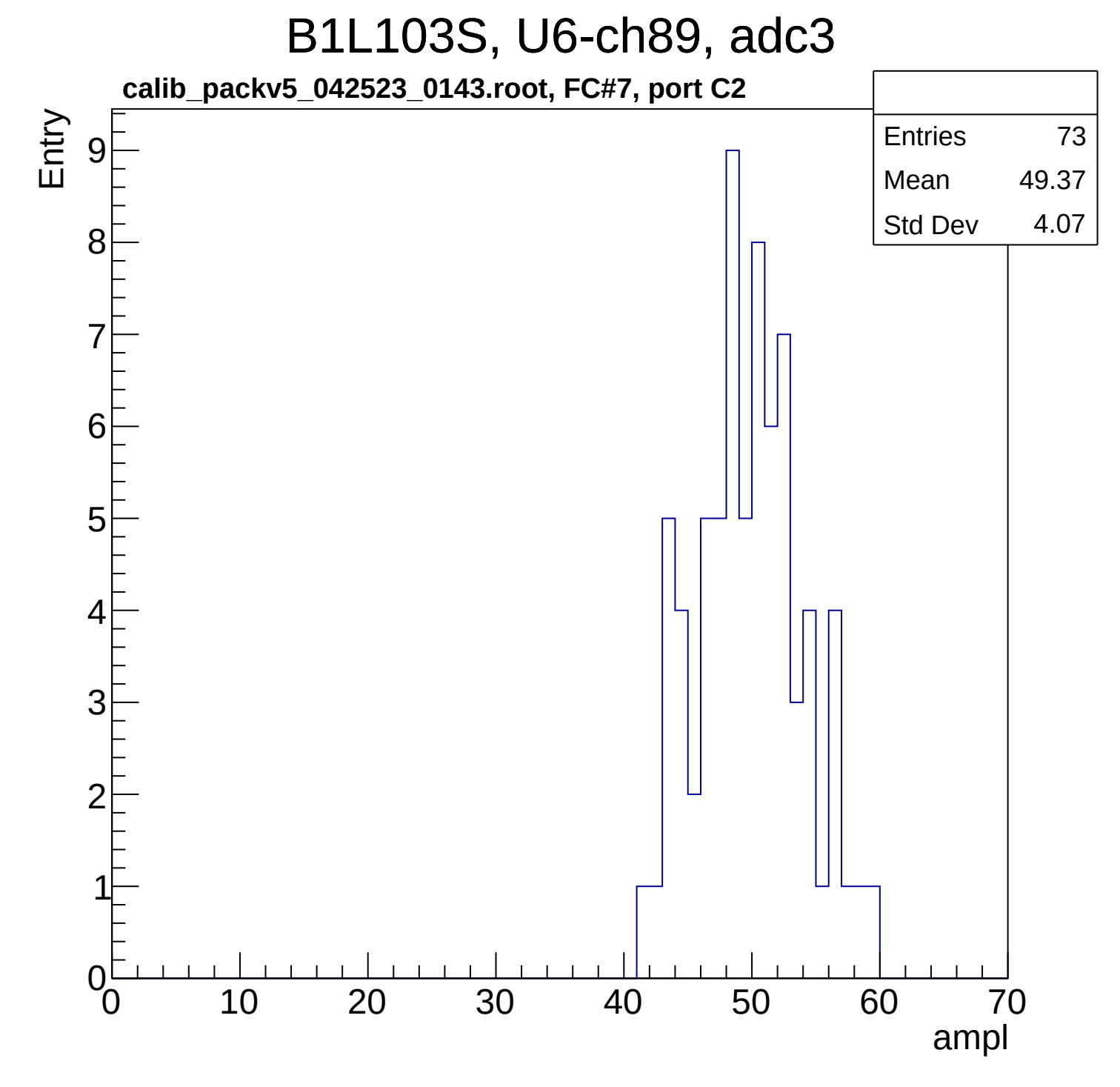
B1L103S, U6-ch89, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.37
Std Dev 4.07
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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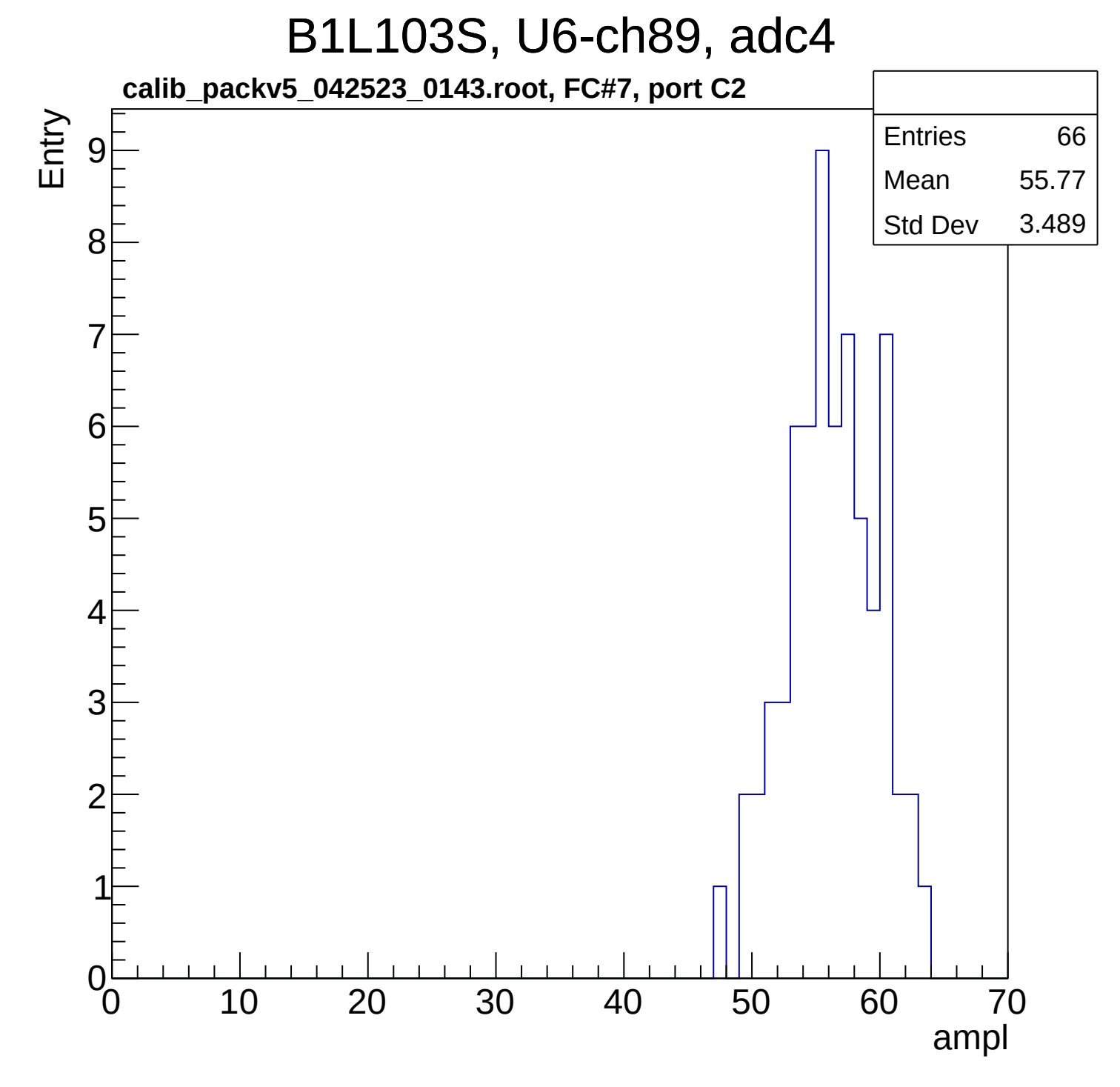
B1L103S, U6-ch89, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 55.77
Std Dev 3.489
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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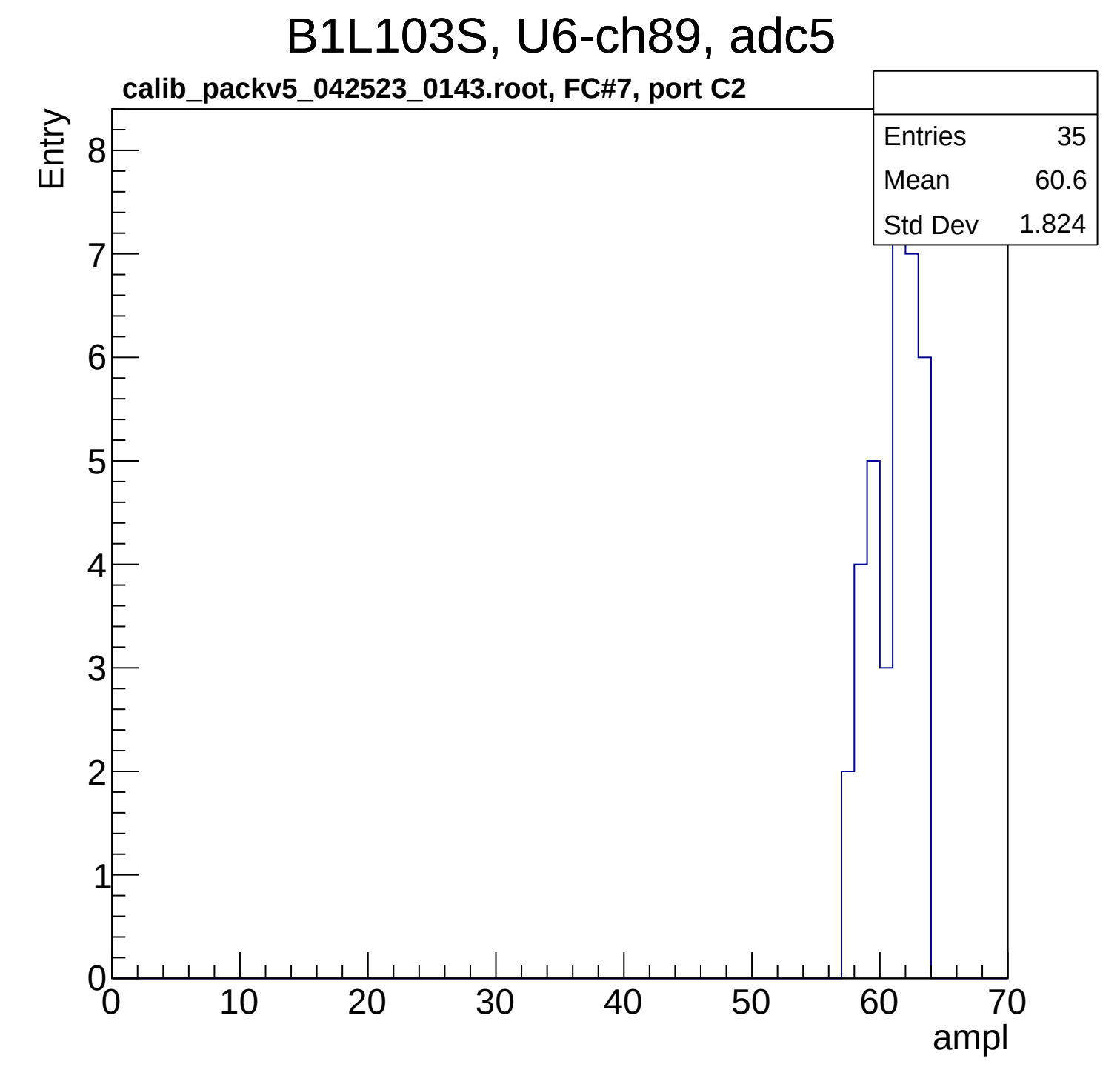
B1L103S, U6-ch89, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 60.6
Std Dev 1.824
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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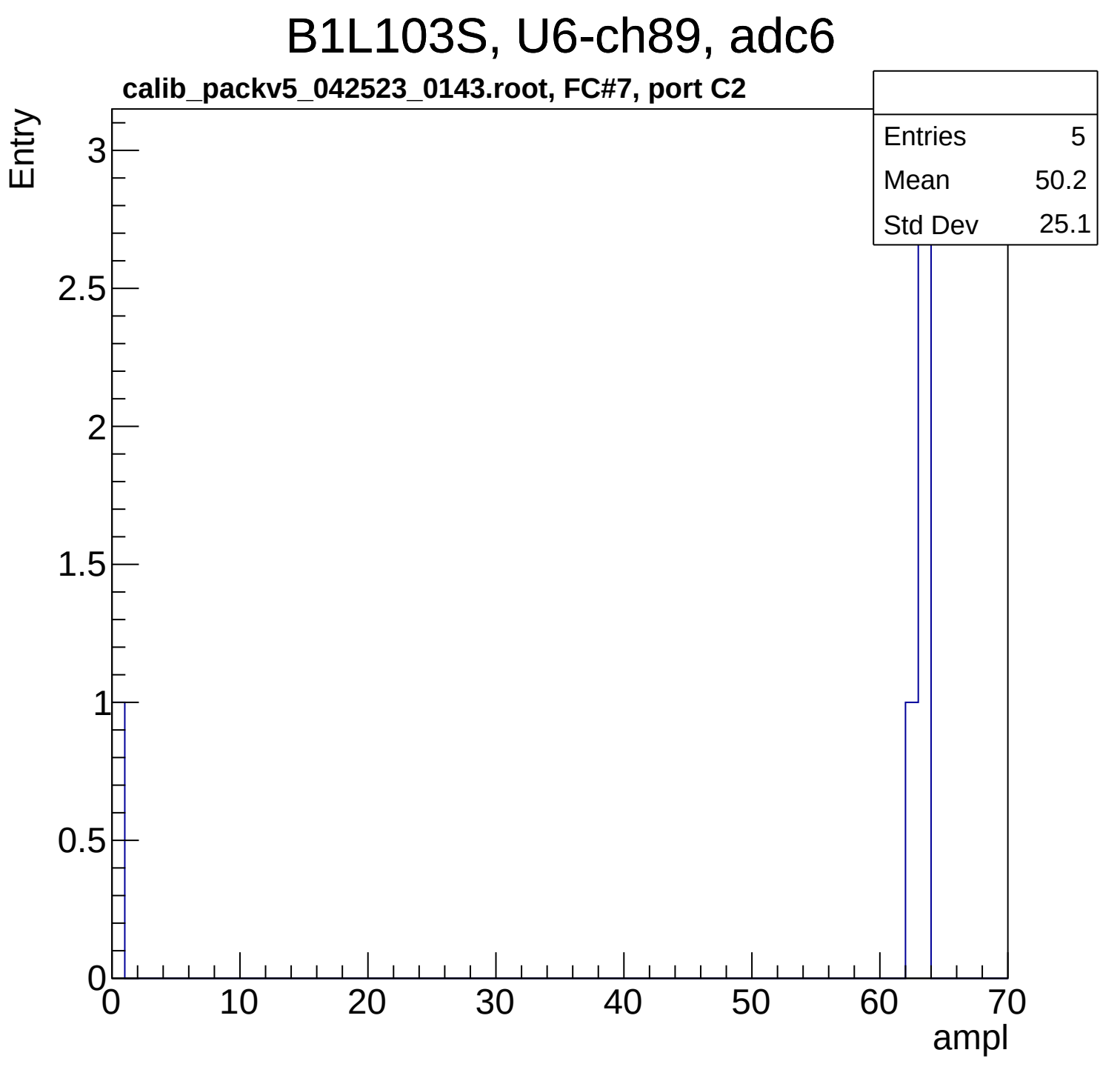
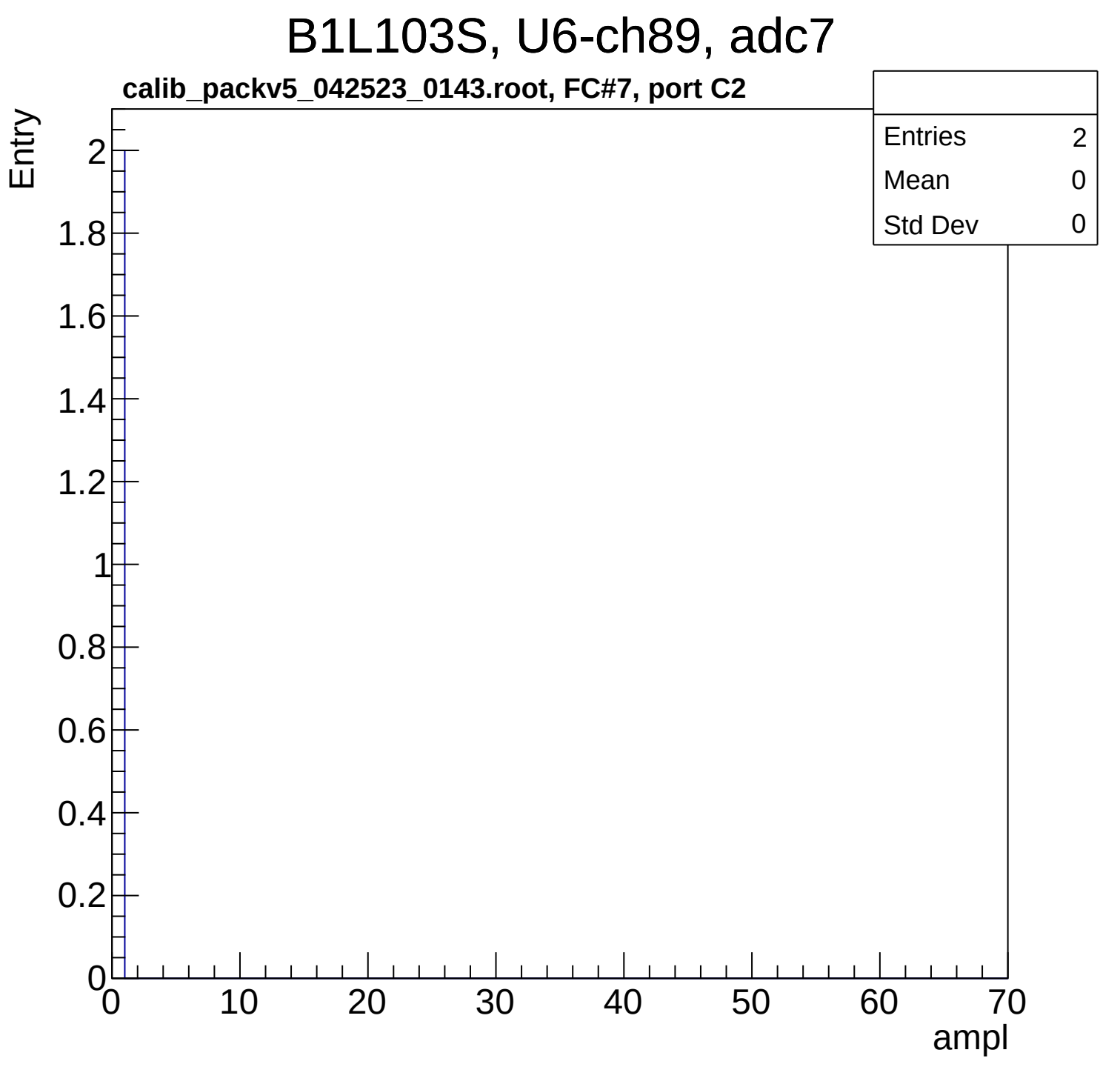

B1L103S, U6-ch89, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch89, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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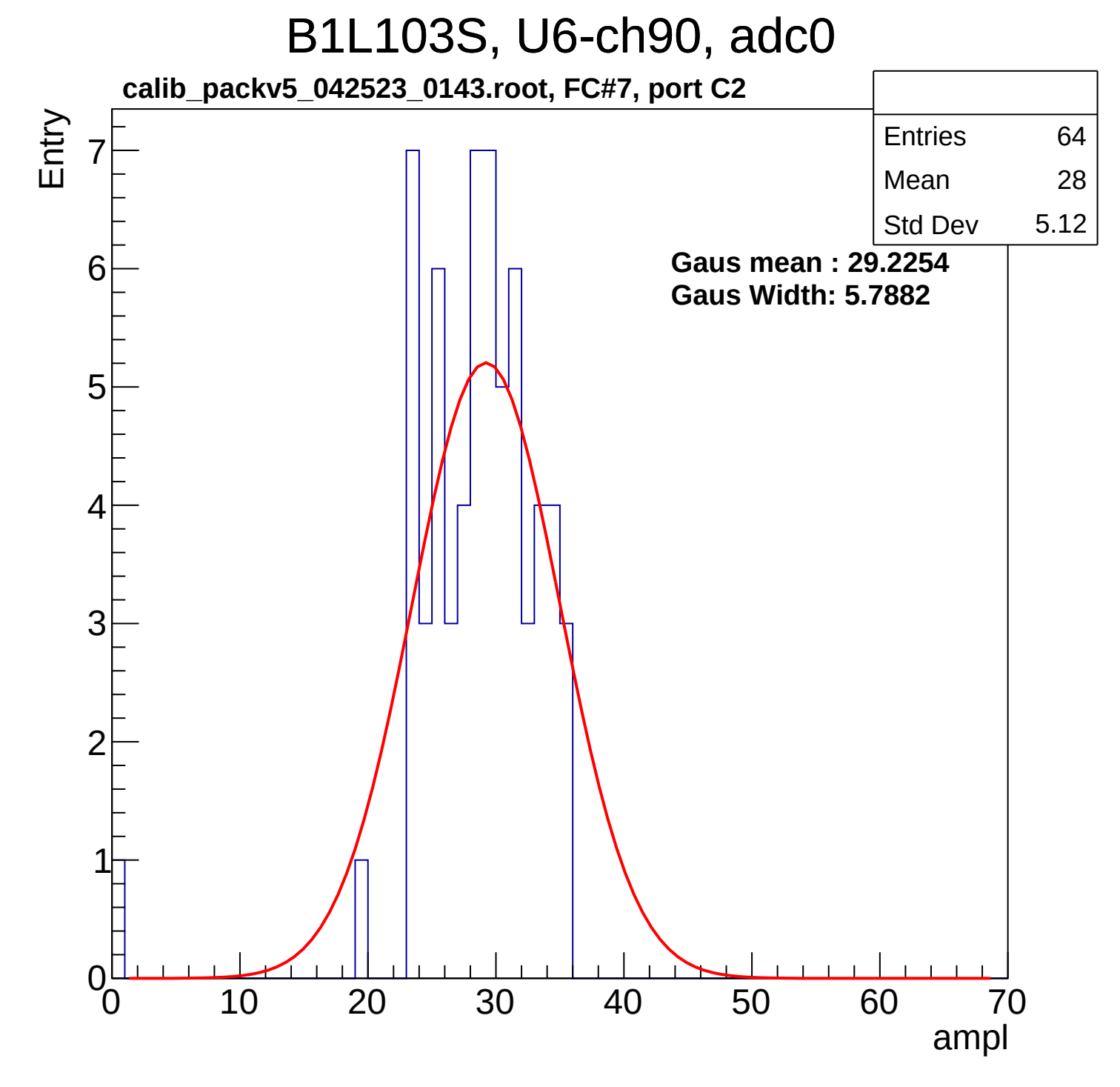
B1L103S, U6-ch90, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 28
Std Dev 5.12
Gaus mean : 29.2254
Gaus Width: 5.7882
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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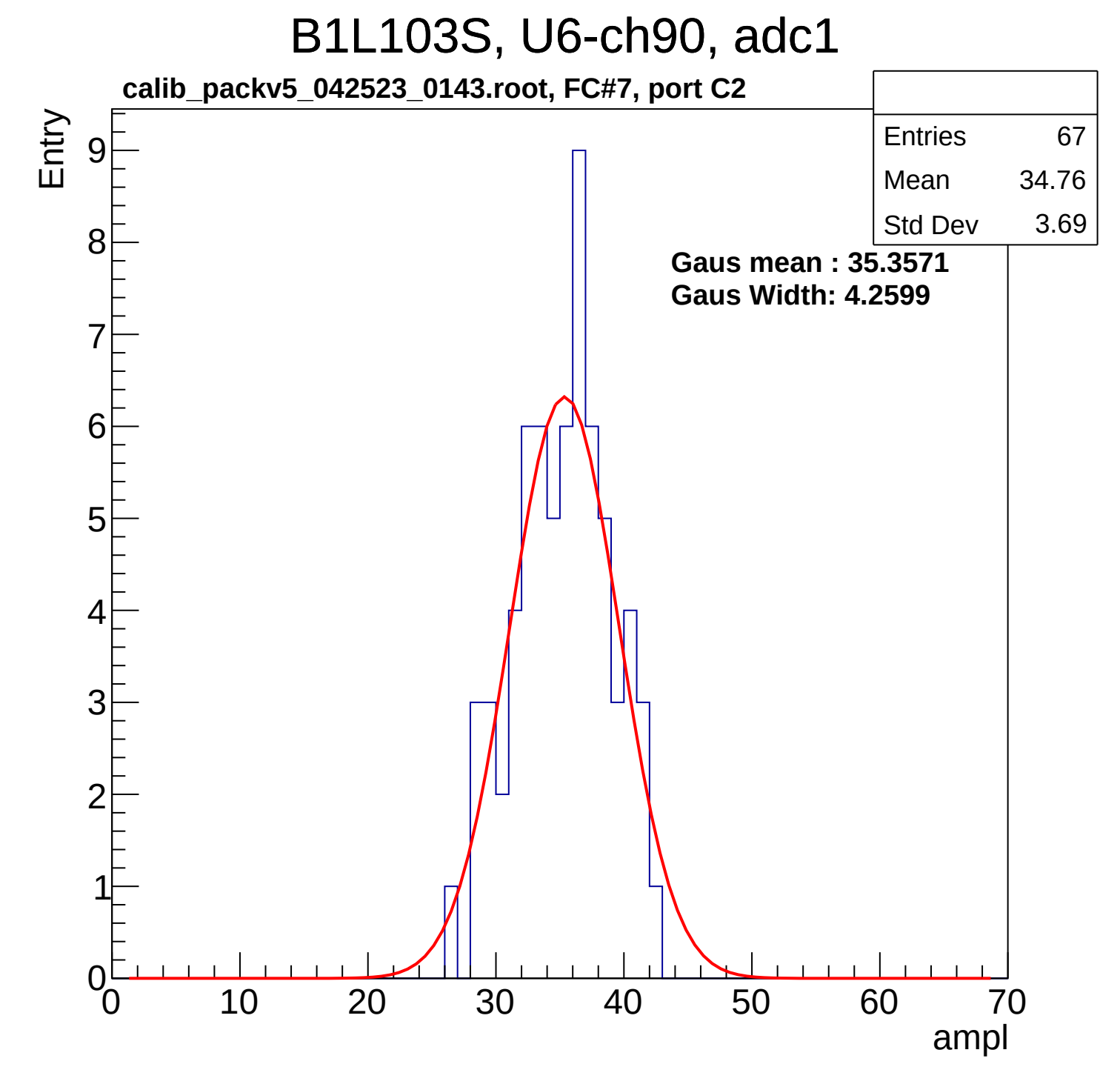
B1L103S, U6-ch90, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 34.76
Std Dev 3.69
Gaus mean : 35.3571
Gaus Width: 4.2599
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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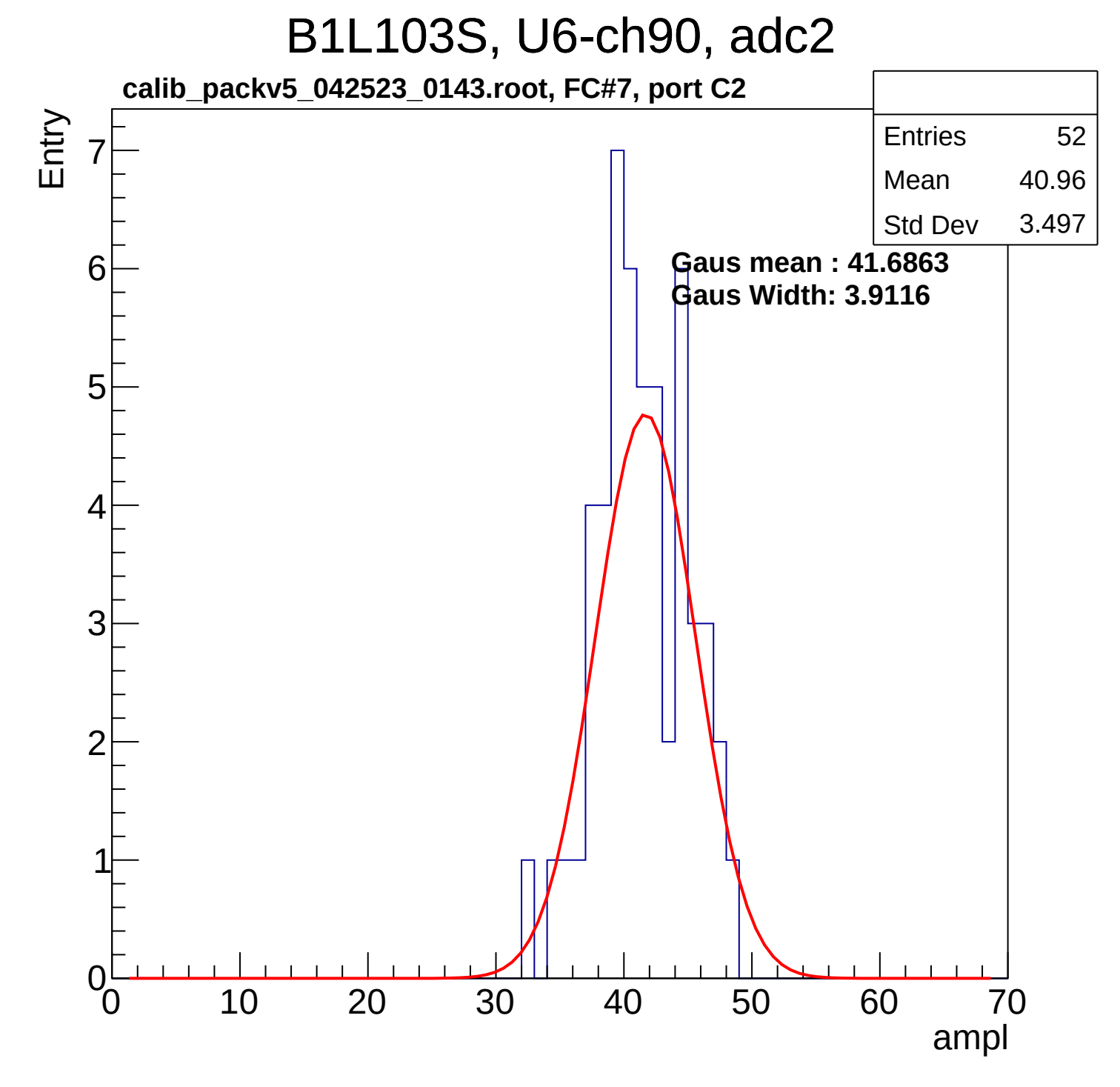
B1L103S, U6-ch90, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 40.96
Std Dev 3.497
Gaus mean : 41.6863
Gaus Width: 3.9116
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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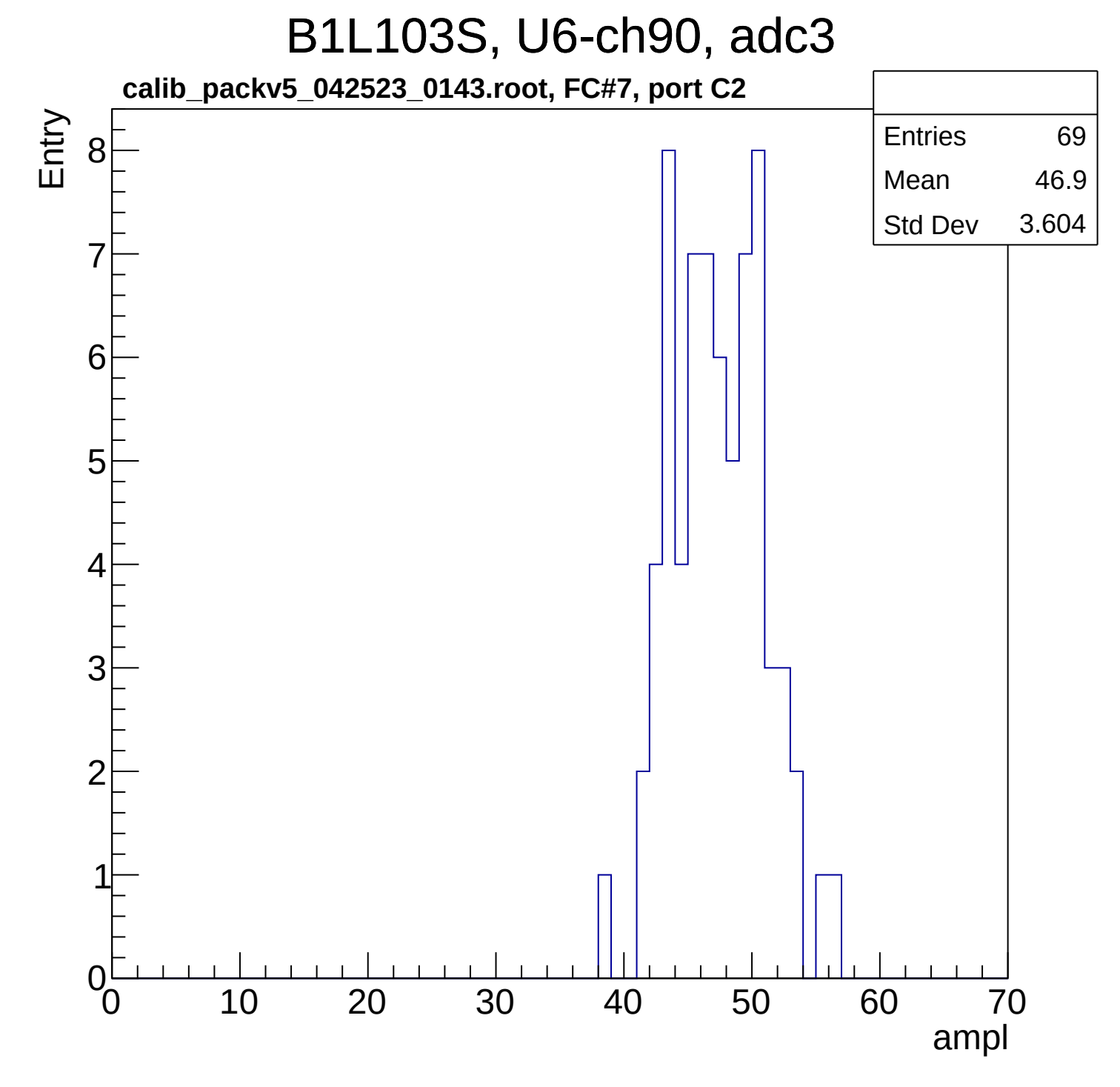
B1L103S, U6-ch90, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 46.9
Std Dev 3.604
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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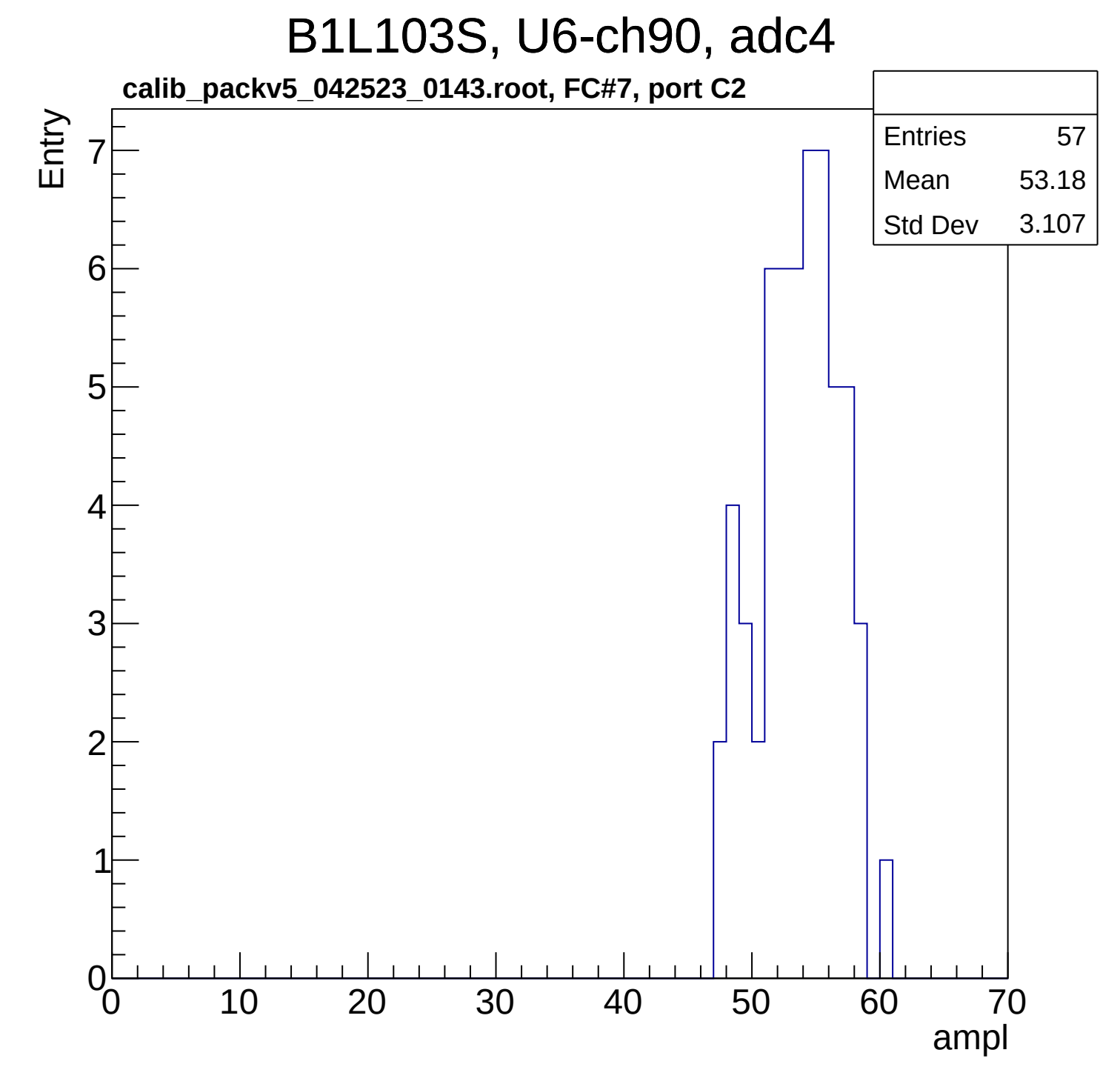
B1L103S, U6-ch90, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 53.18
Std Dev 3.107
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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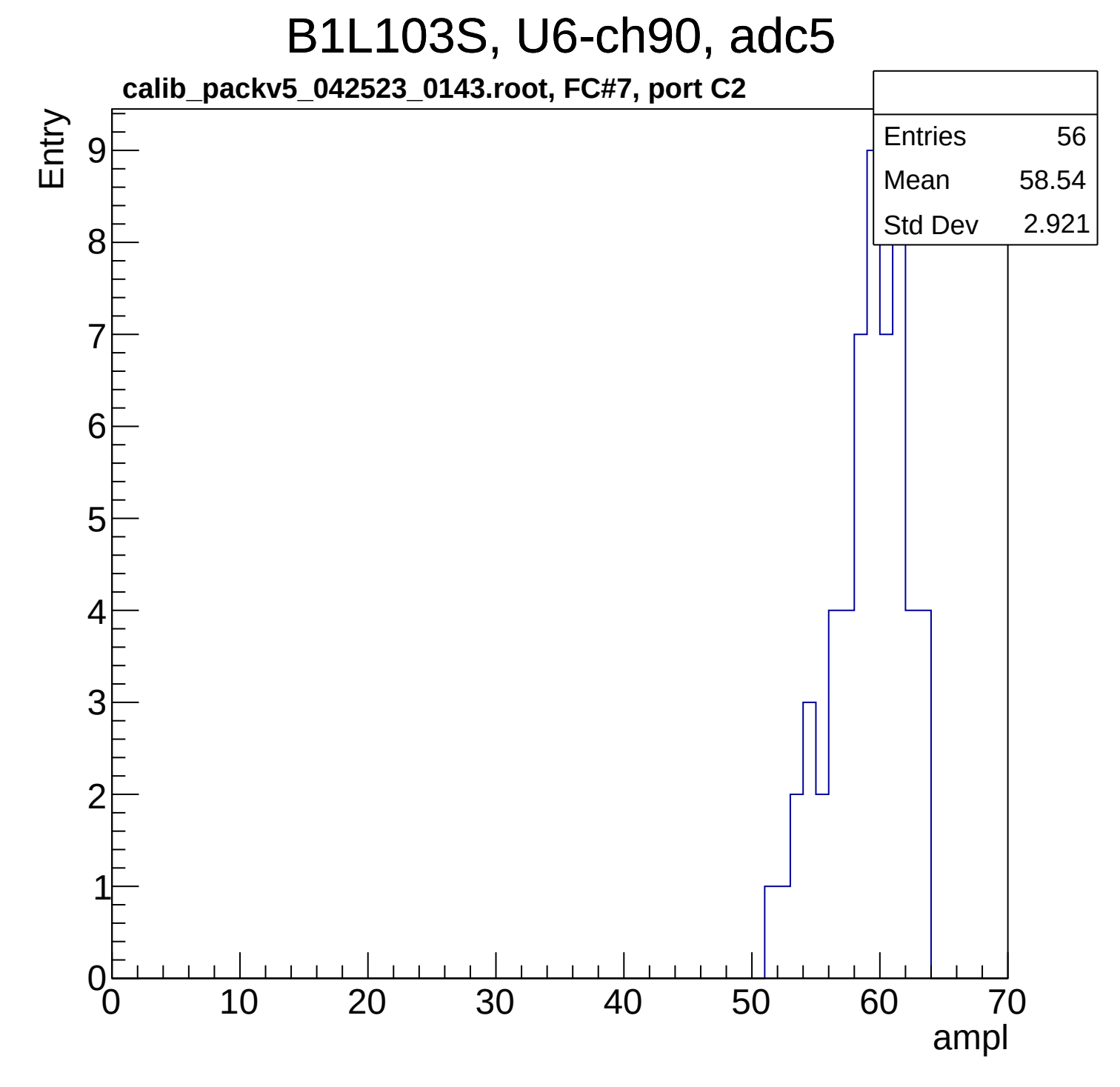
B1L103S, U6-ch90, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.54
Std Dev 2.921
Entry
ampl
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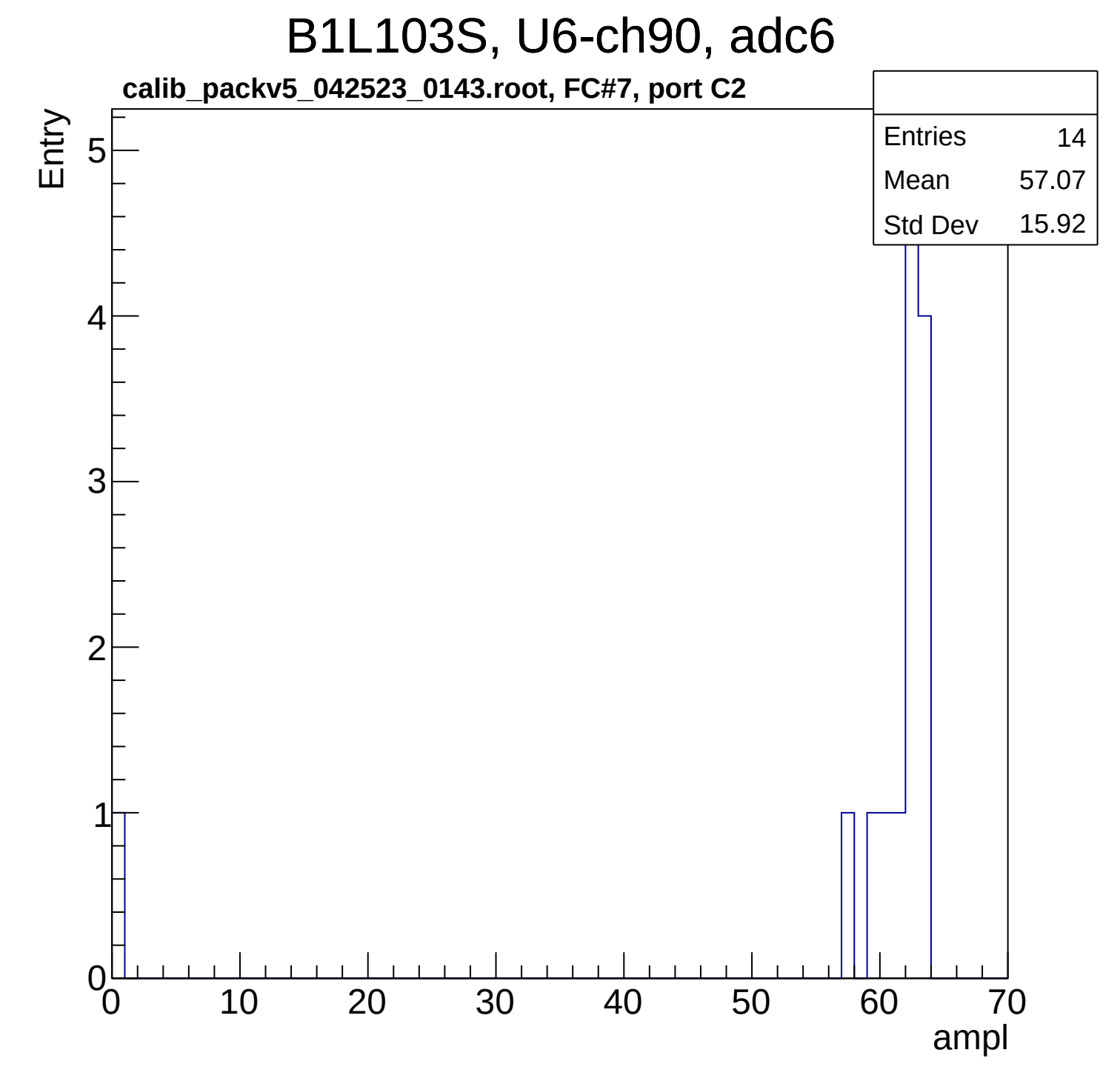
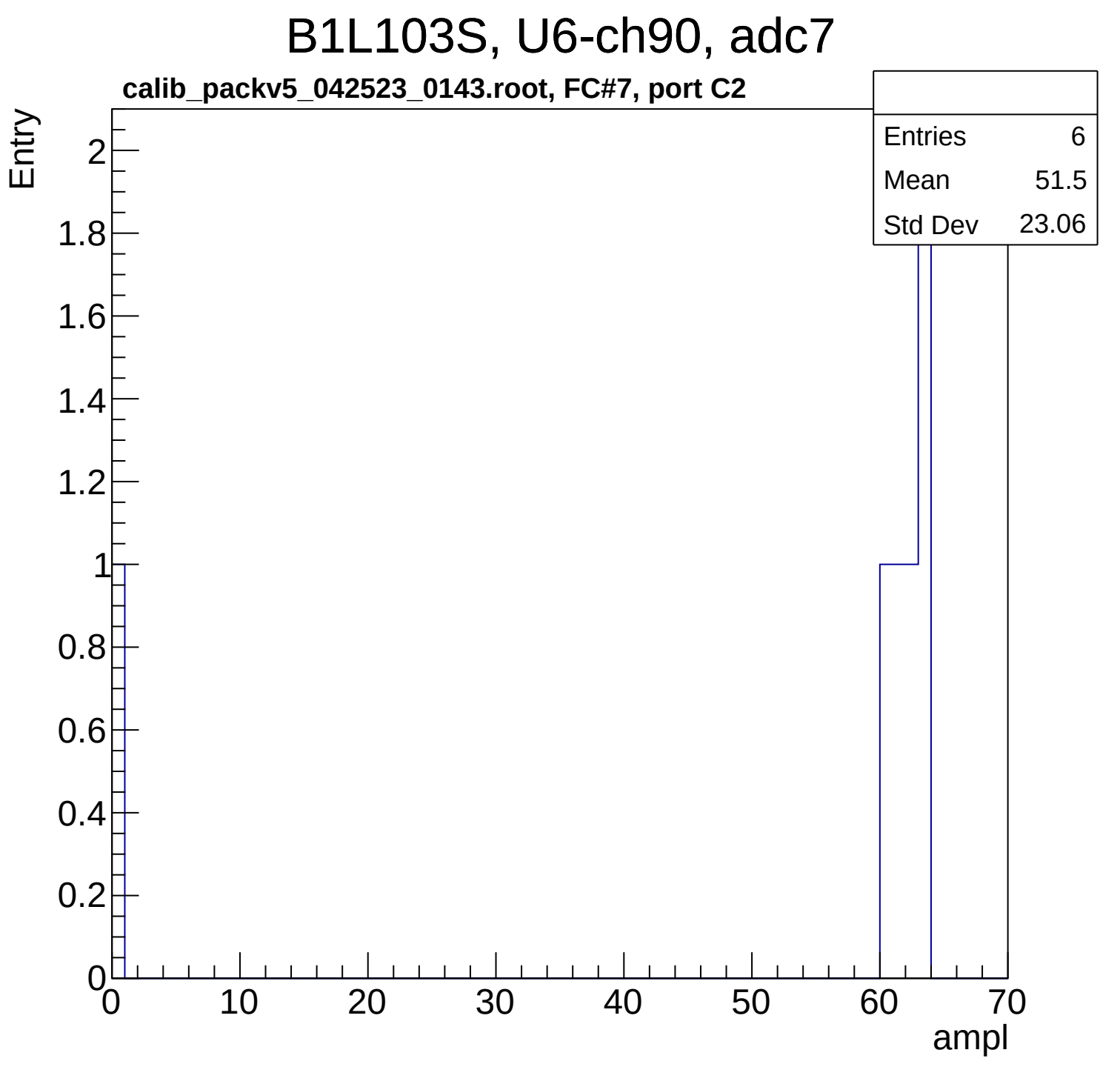
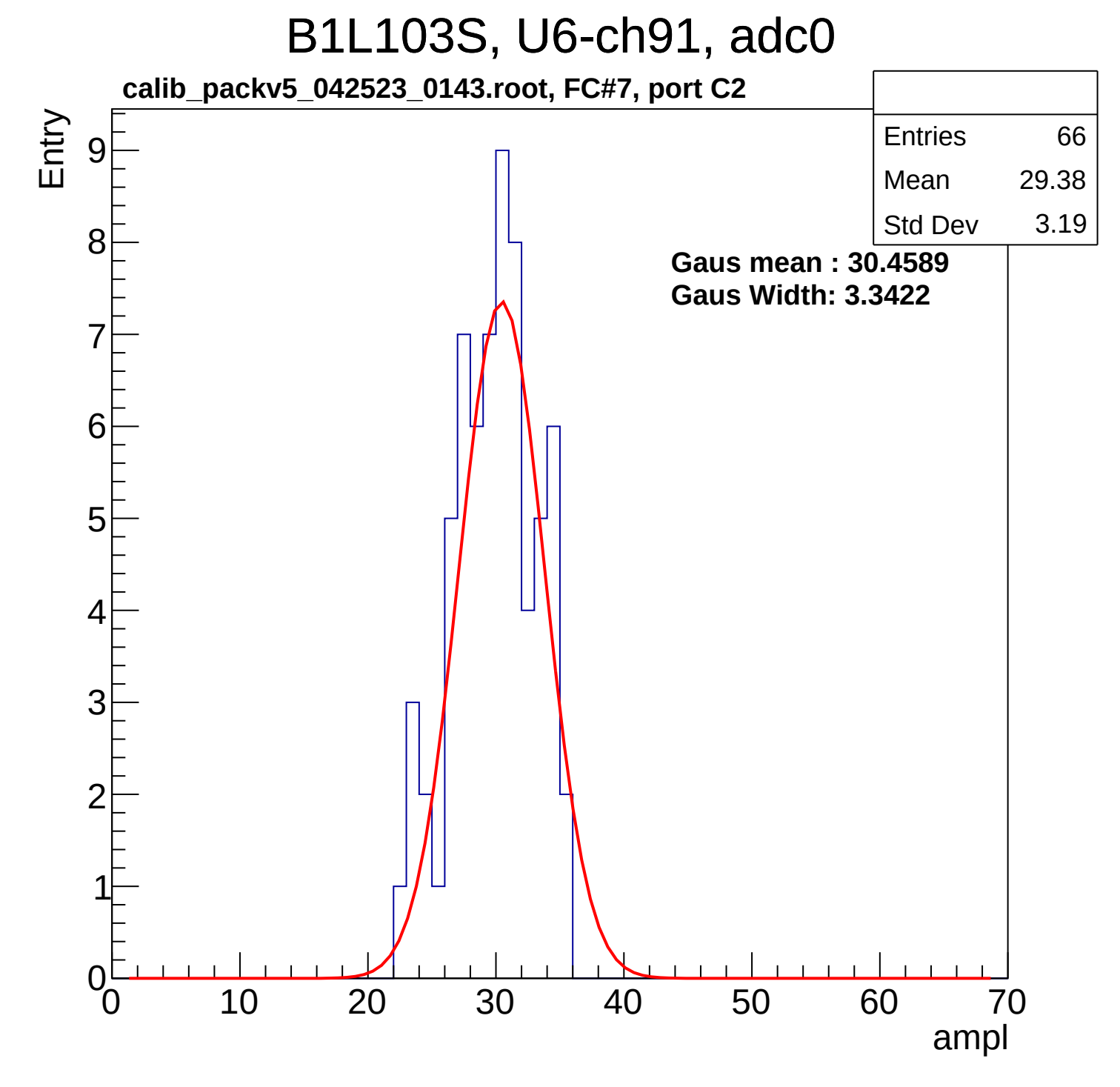
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch90, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 57.07
Std Dev 15.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch90, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch91, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.38
Std Dev 3.19
Gaus mean : 30.4589
Gaus Width: 3.3422
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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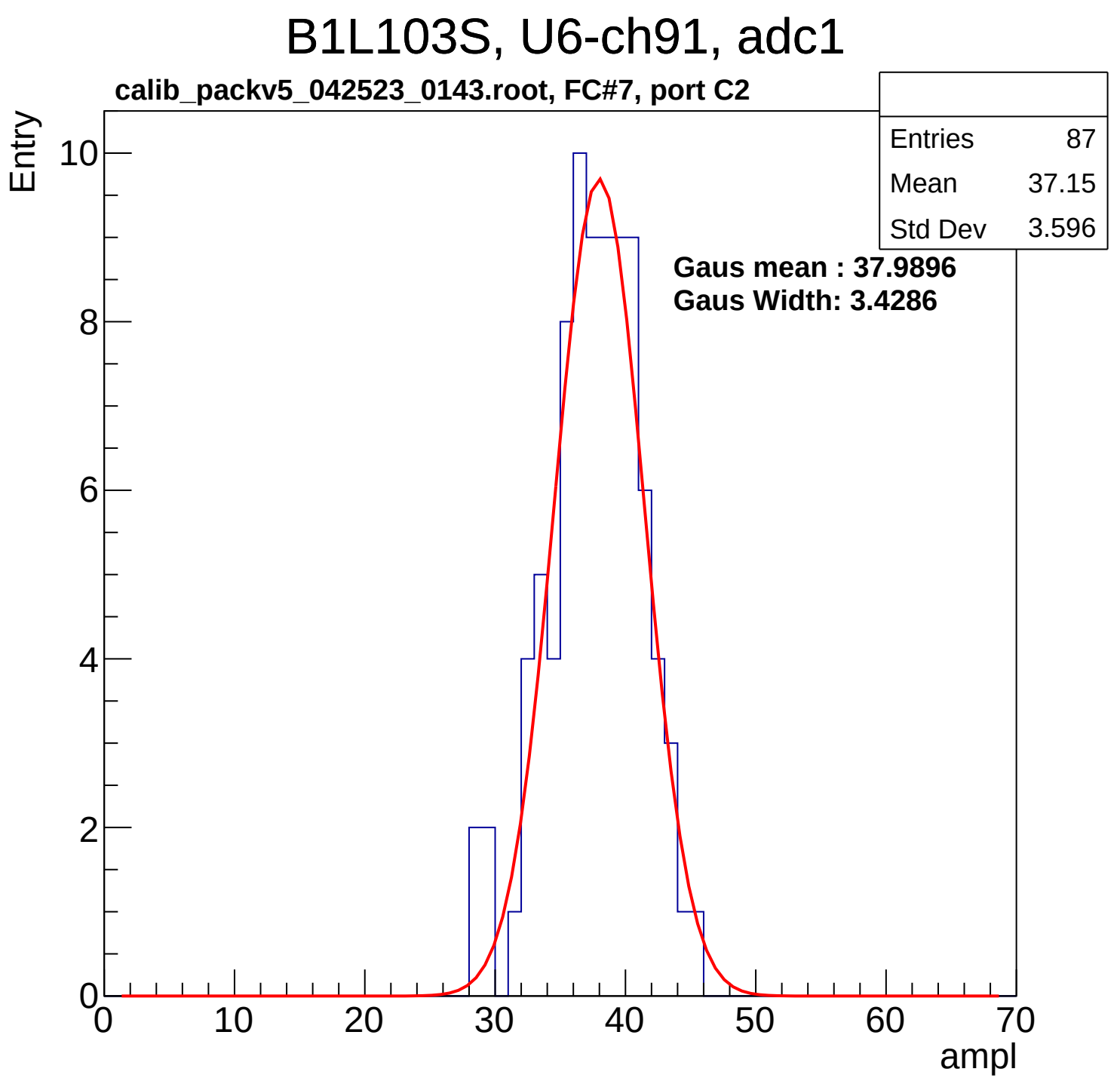
B1L103S, U6-ch91, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 37.15
Std Dev 3.596
Gaus mean : 37.9896
Gaus Width: 3.4286
Entry
ampl
0
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8
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0
10
20
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40
50
60
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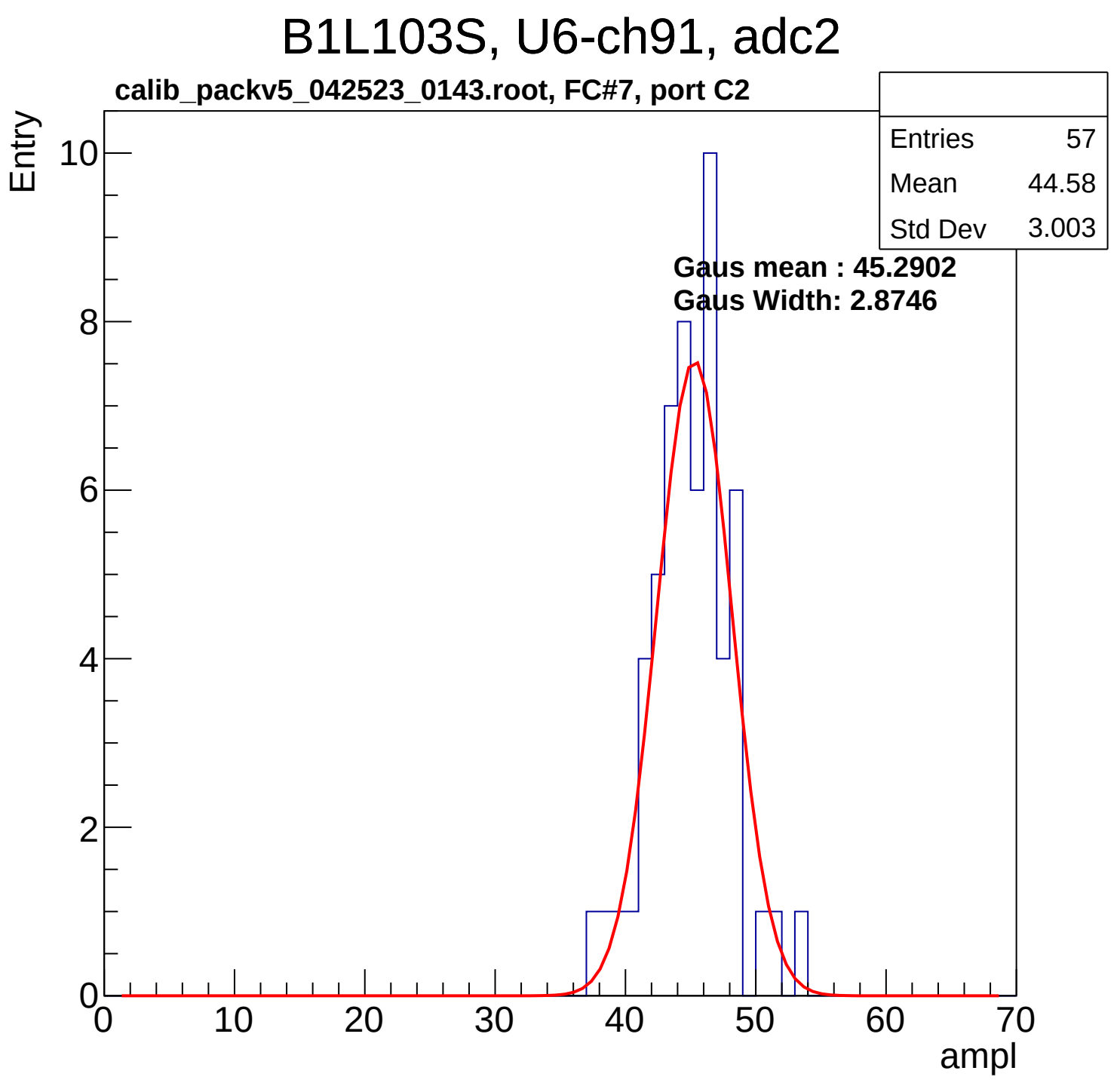
B1L103S, U6-ch91, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 44.58
Std Dev 3.003
Gaus mean : 45.2902
Gaus Width: 2.8746
Entry
ampl
0
2
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6
8
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0
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50
60
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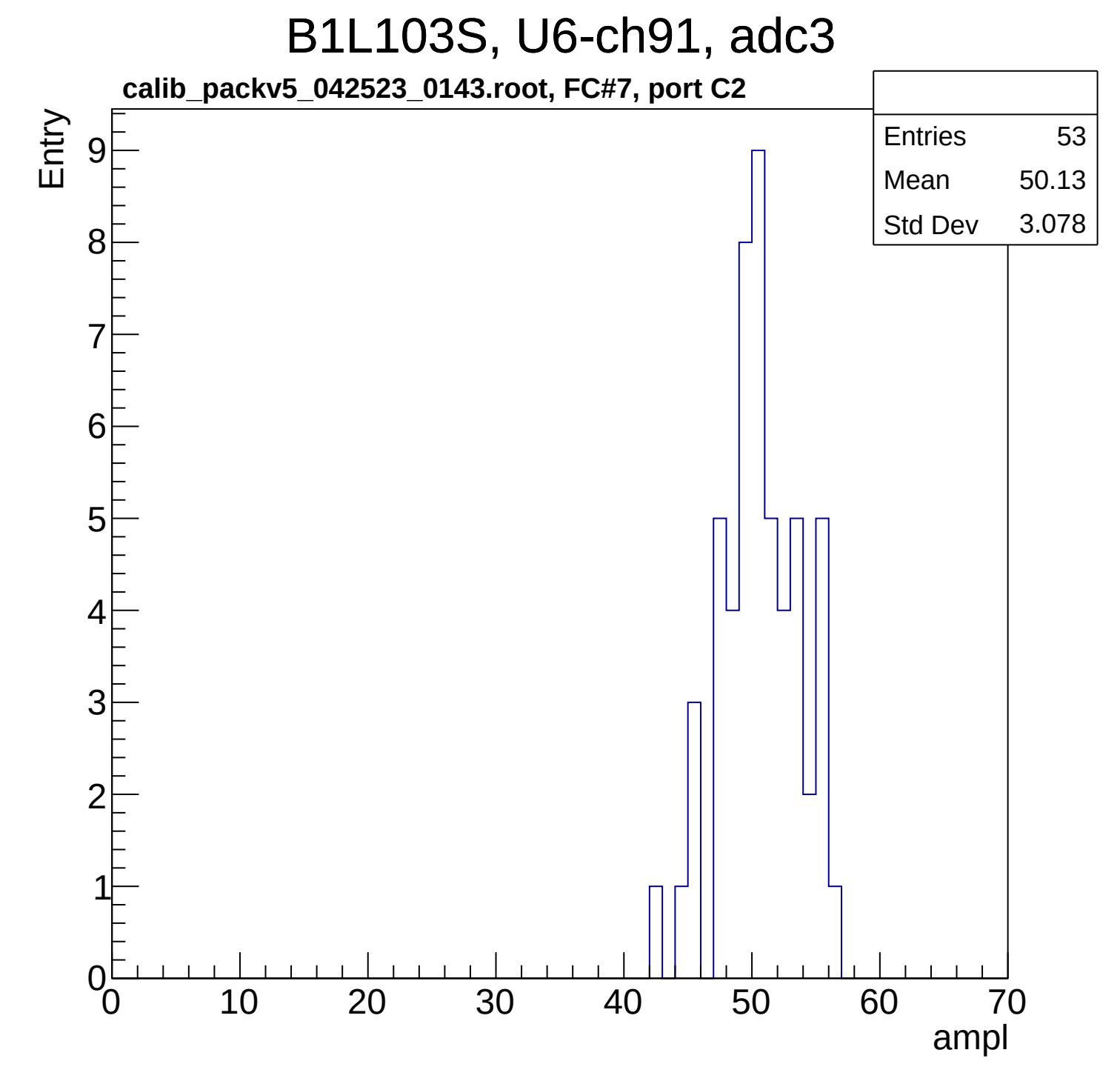
B1L103S, U6-ch91, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 50.13
Std Dev 3.078
Entry
ampl
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3
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7
8
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0
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40
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60
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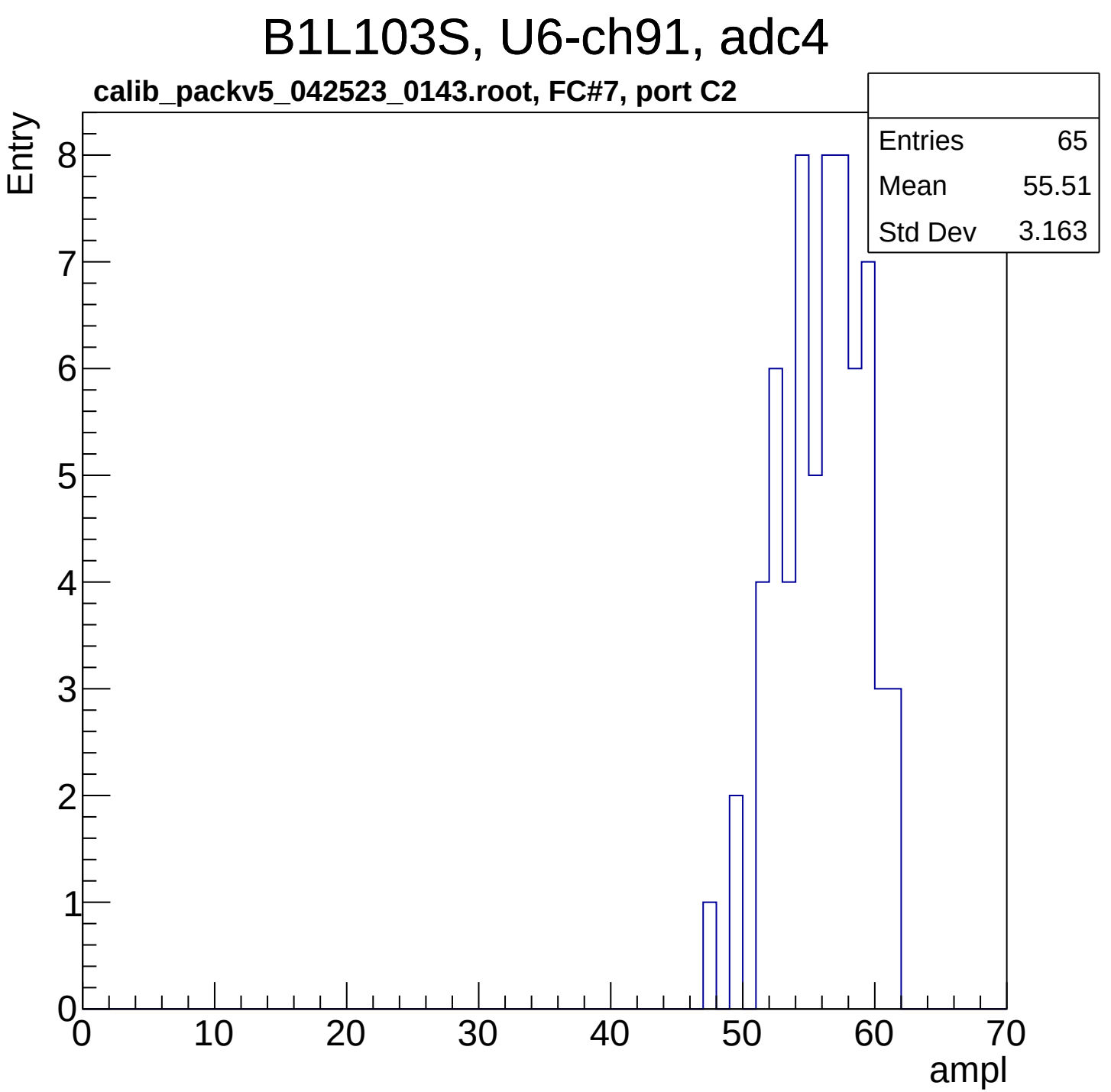
B1L103S, U6-ch91, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 55.51
Std Dev 3.163
Entry
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7
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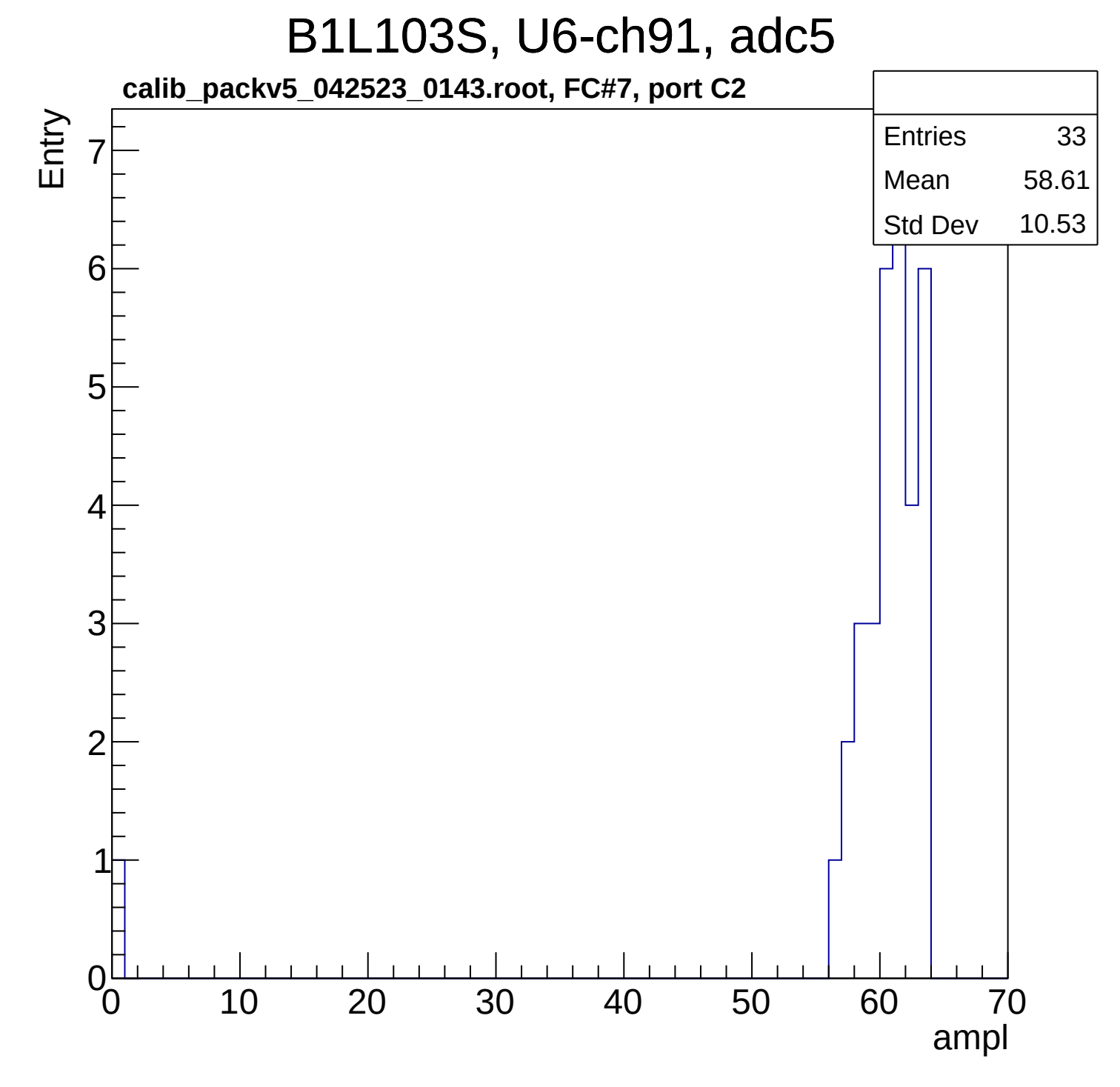
50
60
70
ampl


B1L103S, U6-ch91, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.61
Std Dev 10.53
Entry
ampl
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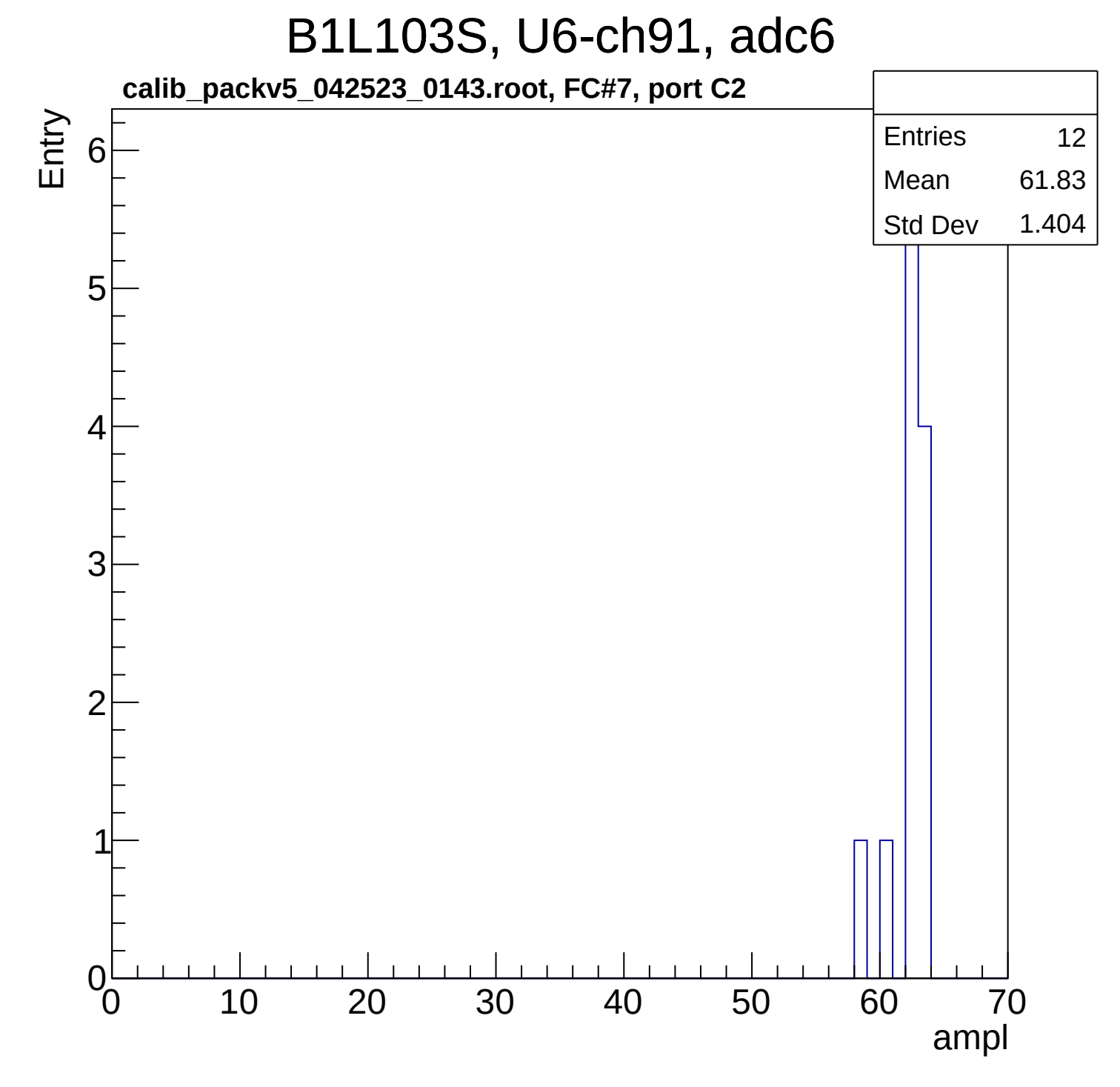
B1L103S, U6-ch91, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.83
Std Dev 1.404
Entry
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2
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4
5
6
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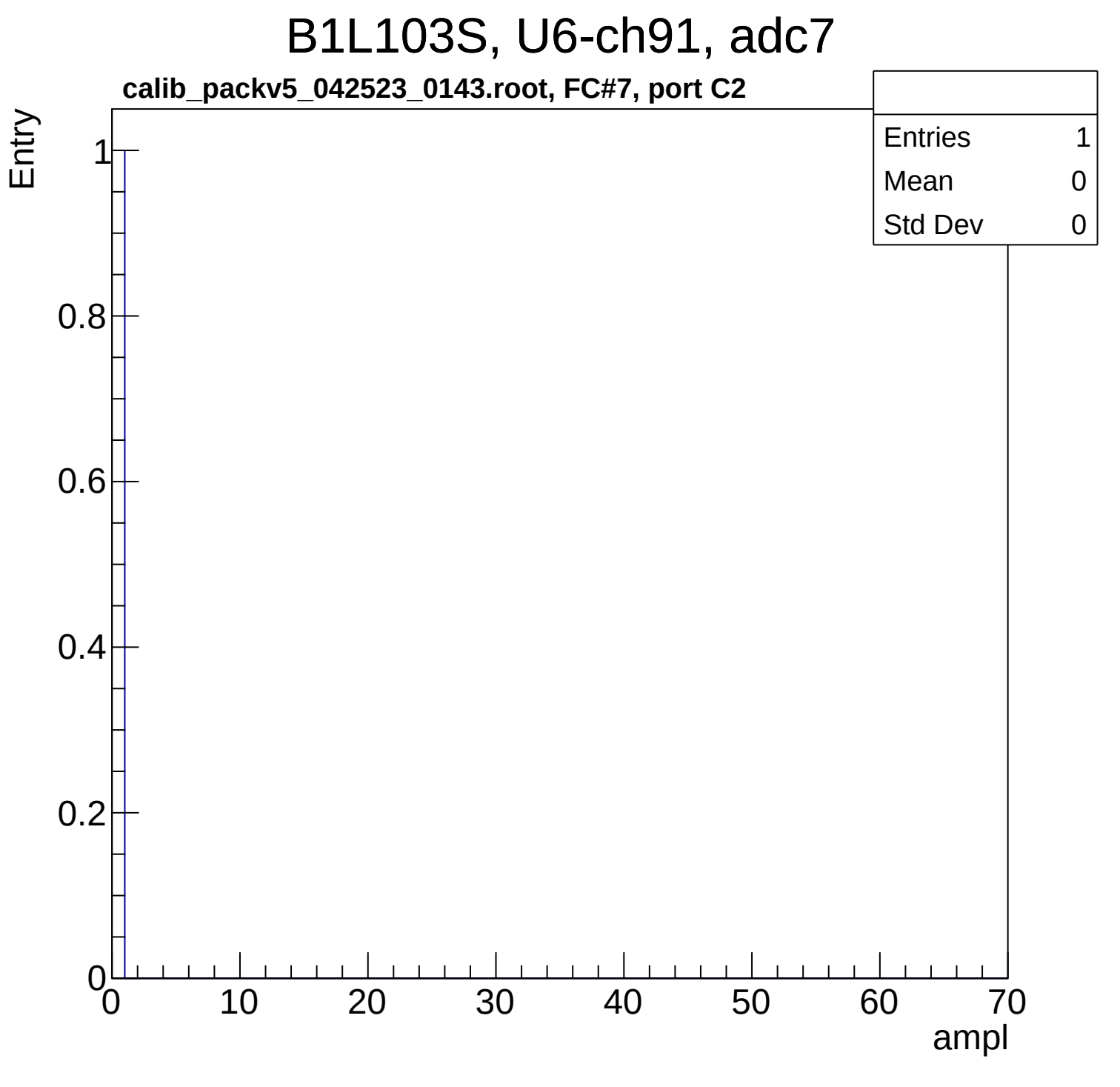
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch91, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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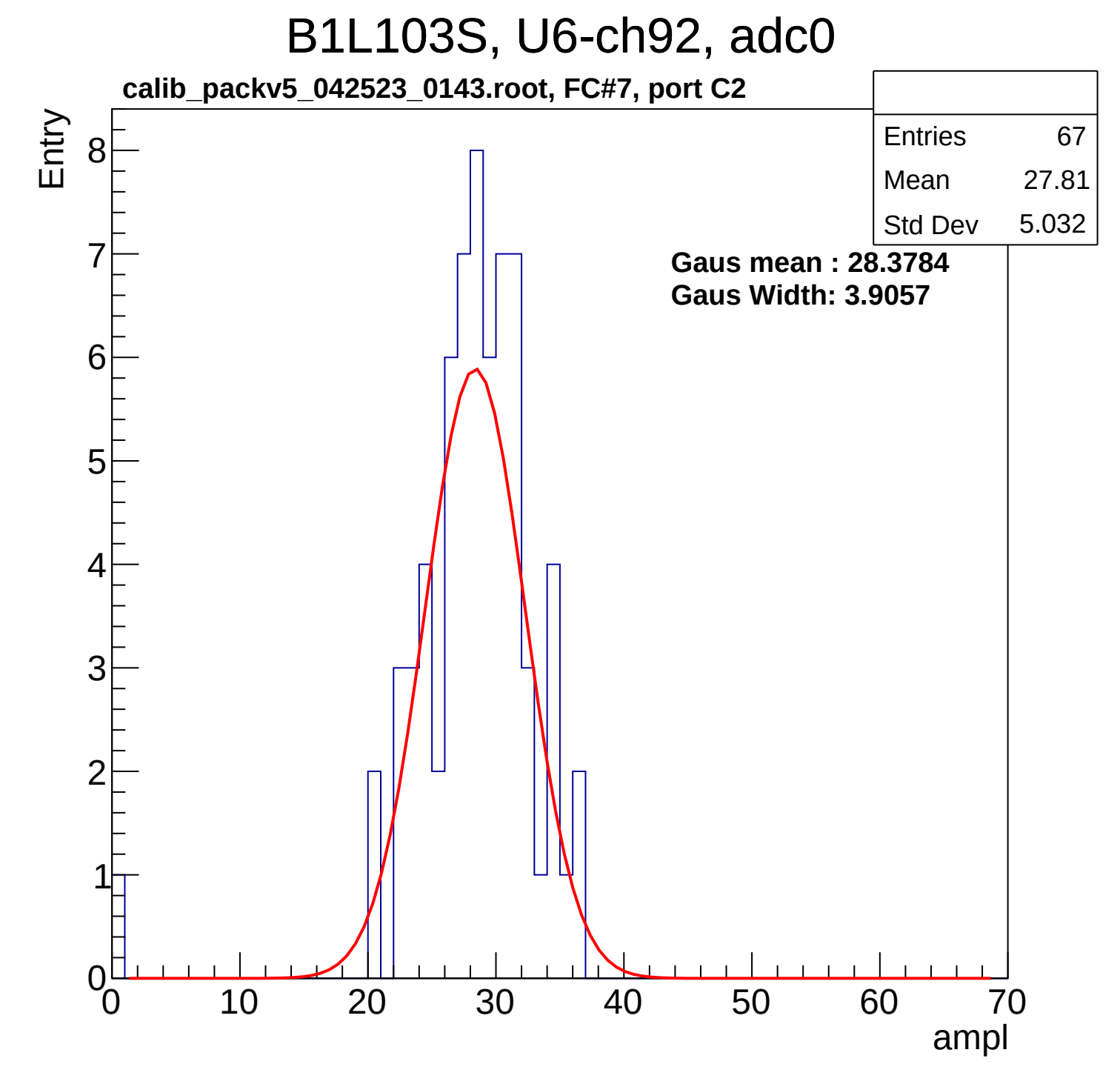
40
50
60
70
ampl


B1L103S, U6-ch92, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 27.81
Std Dev 5.032
Gaus mean : 28.3784
Gaus Width: 3.9057
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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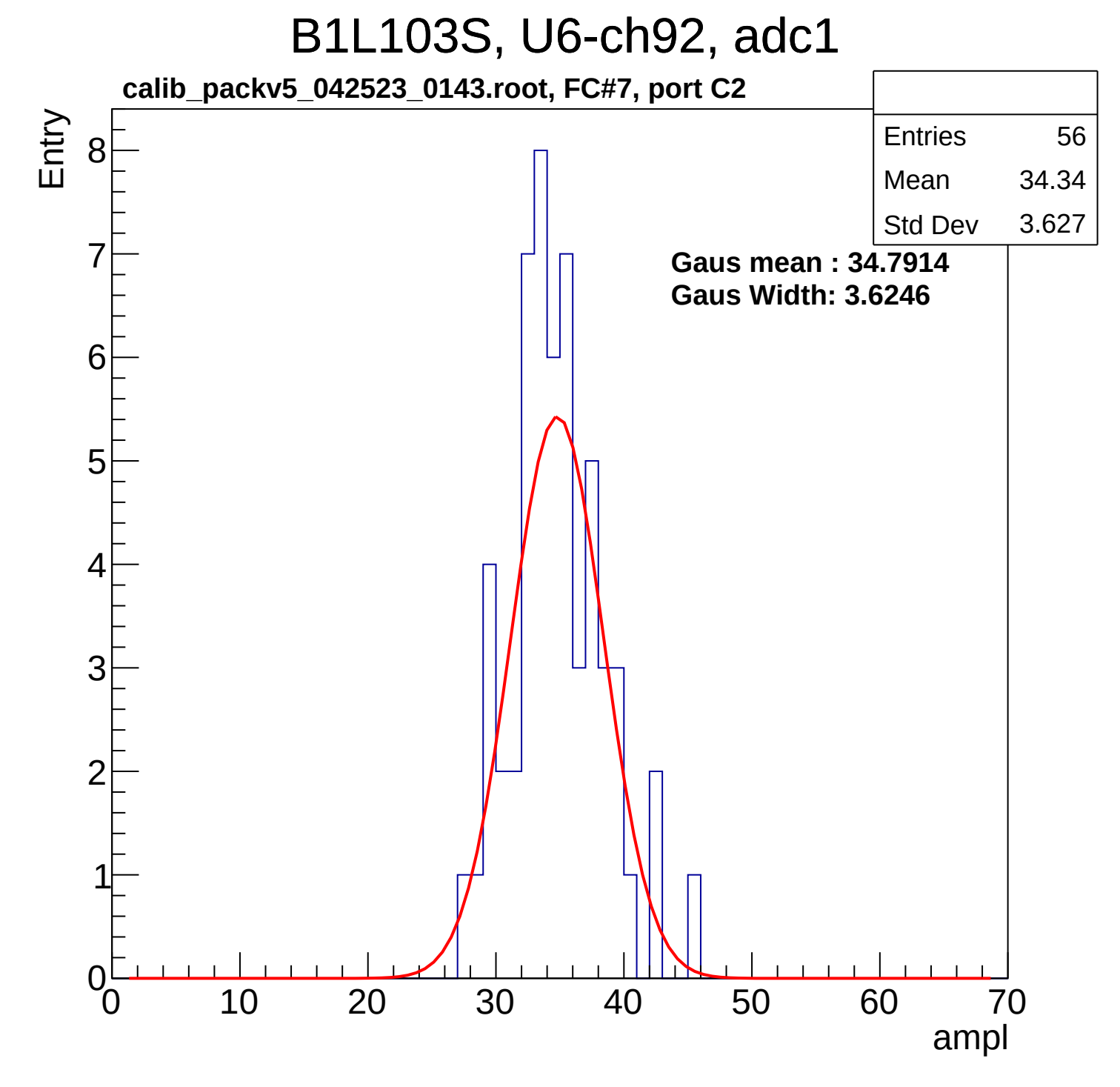
B1L103S, U6-ch92, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 34.34
Std Dev 3.627
Gaus mean : 34.7914
Gaus Width: 3.6246
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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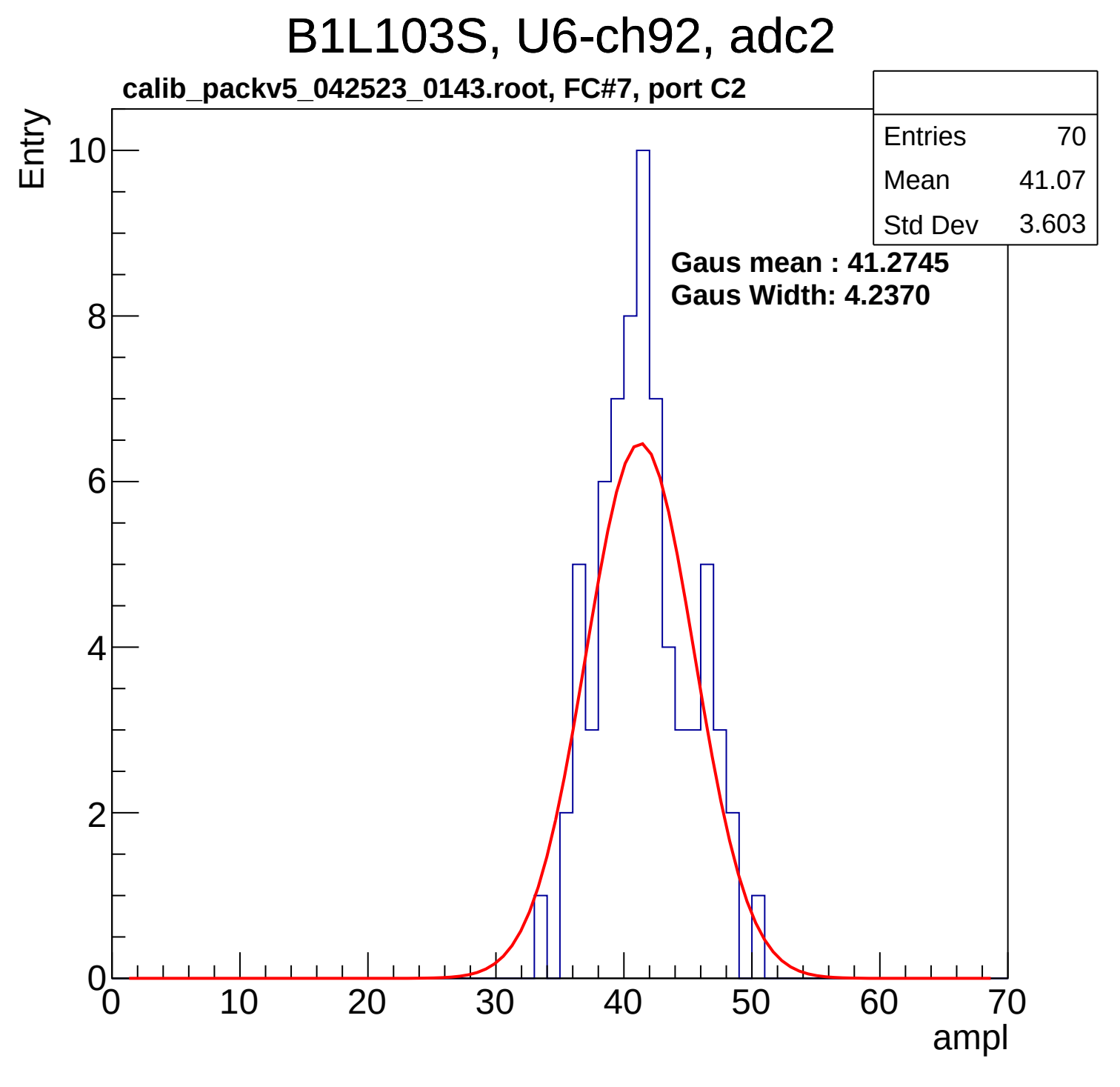
B1L103S, U6-ch92, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 41.07
Std Dev 3.603
Gaus mean : 41.2745
Gaus Width: 4.2370
Entry
ampl
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4
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8
10
0
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20
30
40
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60
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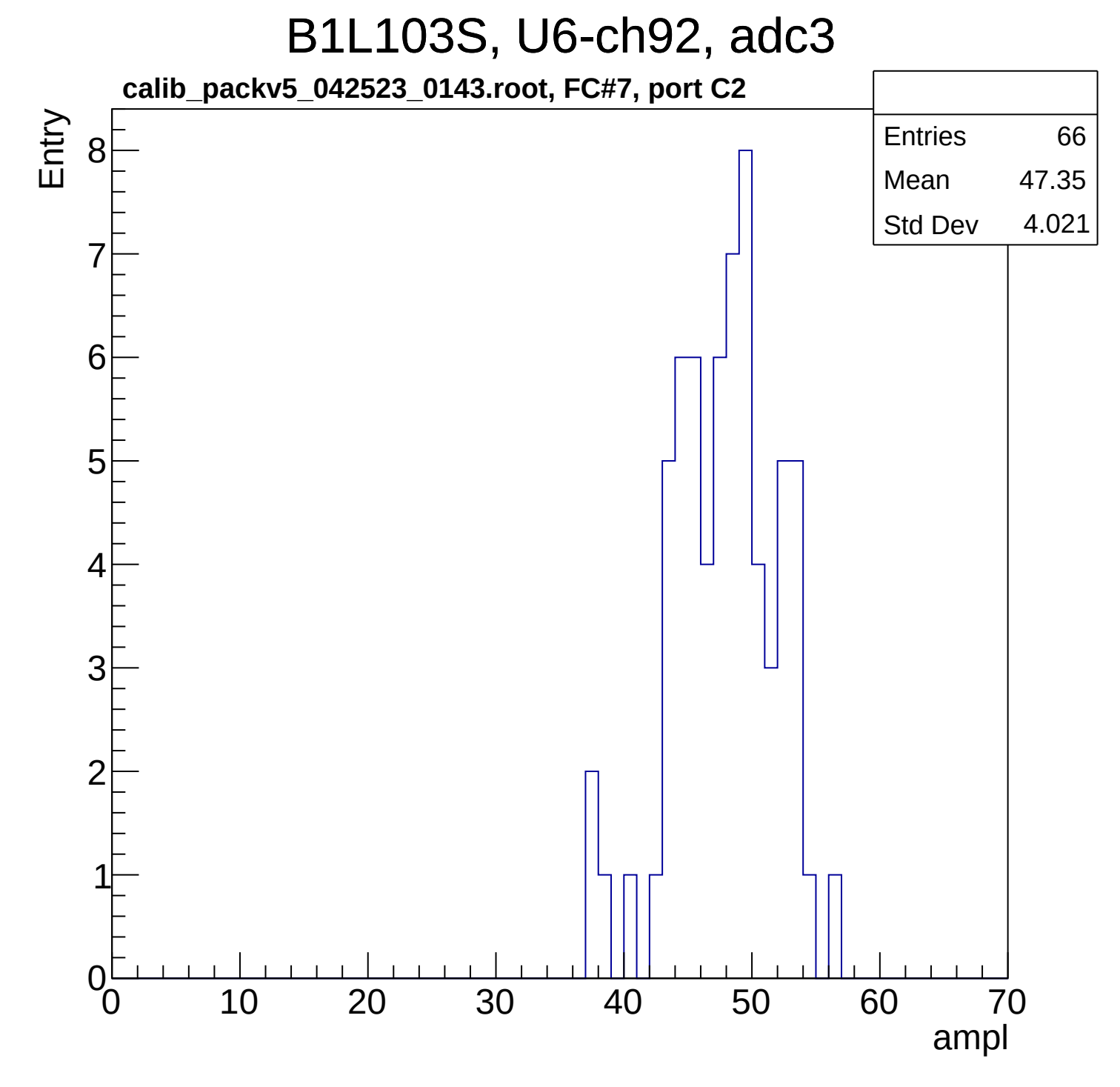
B1L103S, U6-ch92, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 47.35
Std Dev 4.021
Entry
ampl
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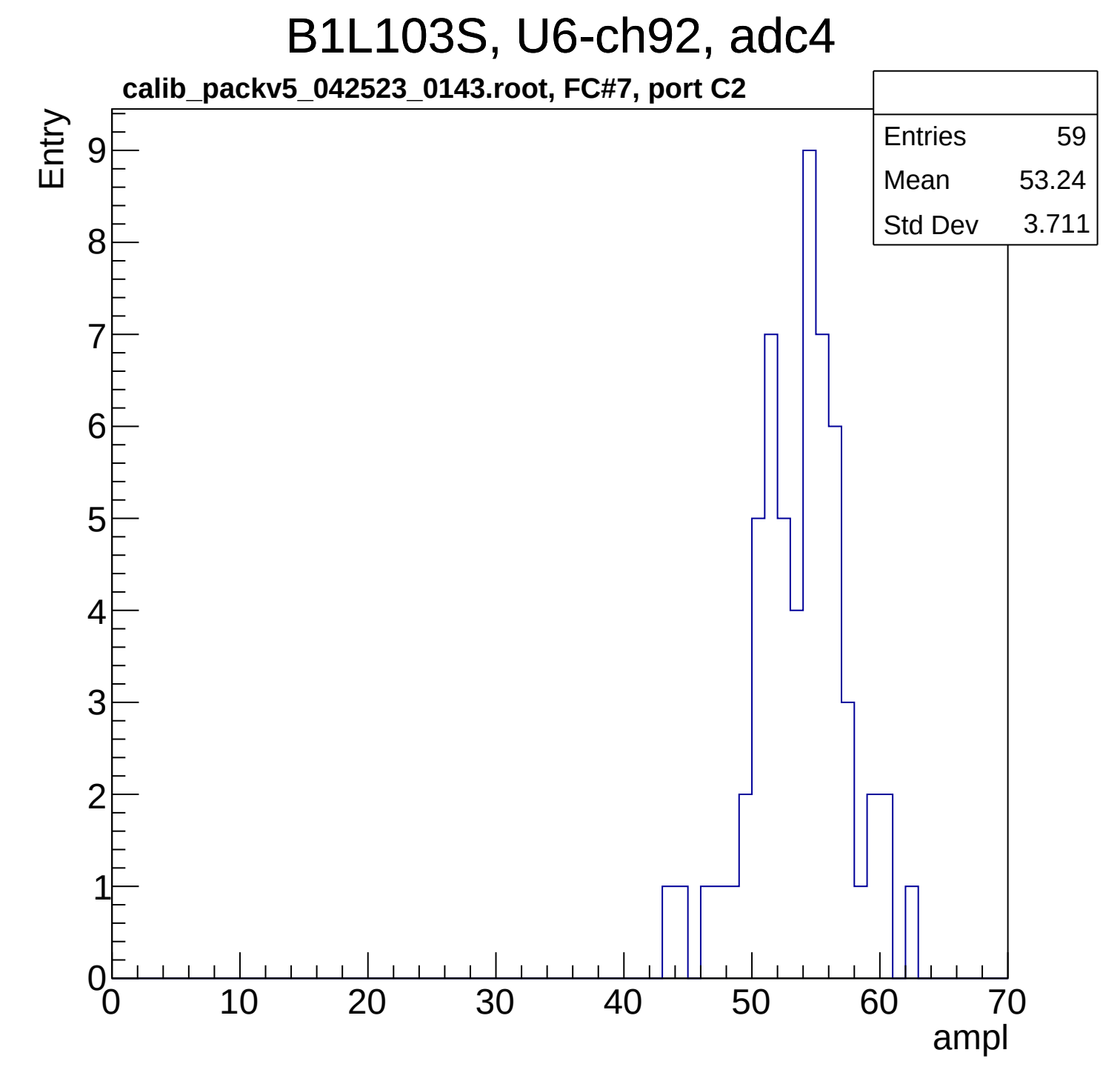
B1L103S, U6-ch92, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 53.24
Std Dev 3.711
Entry
ampl
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3
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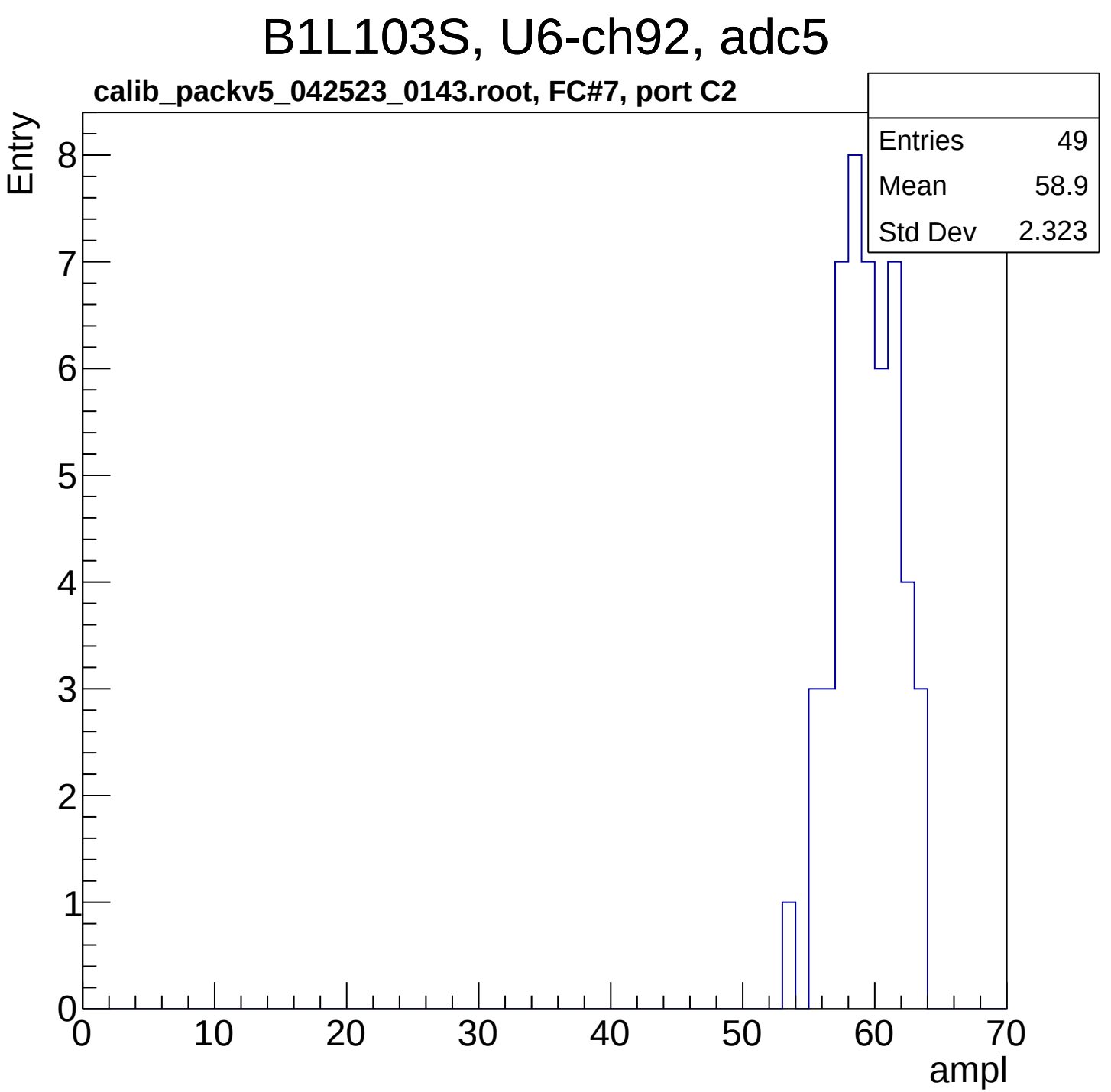
B1L103S, U6-ch92, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.9
Std Dev 2.323
Entry
ampl
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3
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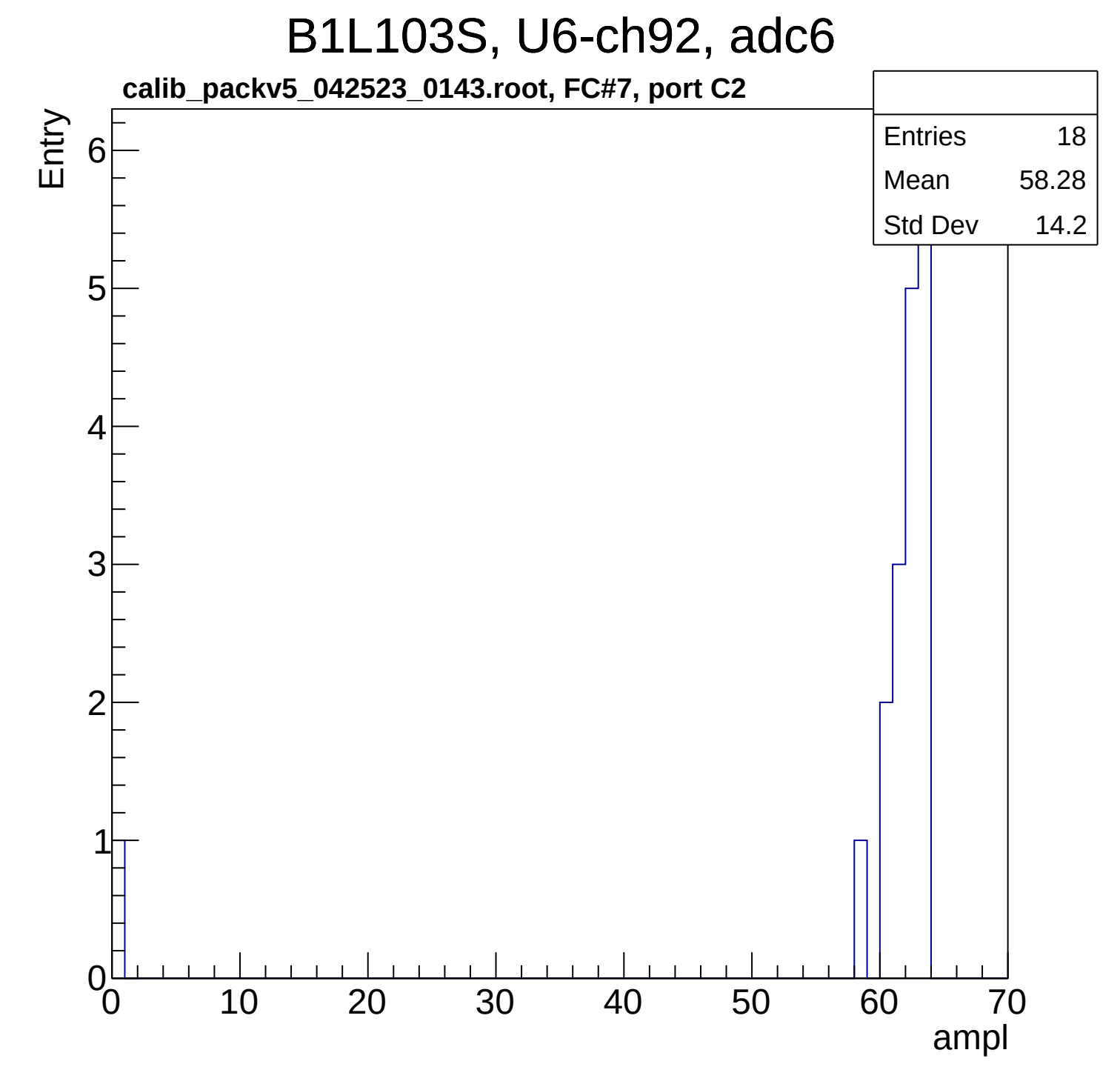
B1L103S, U6-ch92, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 58.28
Std Dev 14.2
Entry
0
1
2
3
4
5
6
0
10
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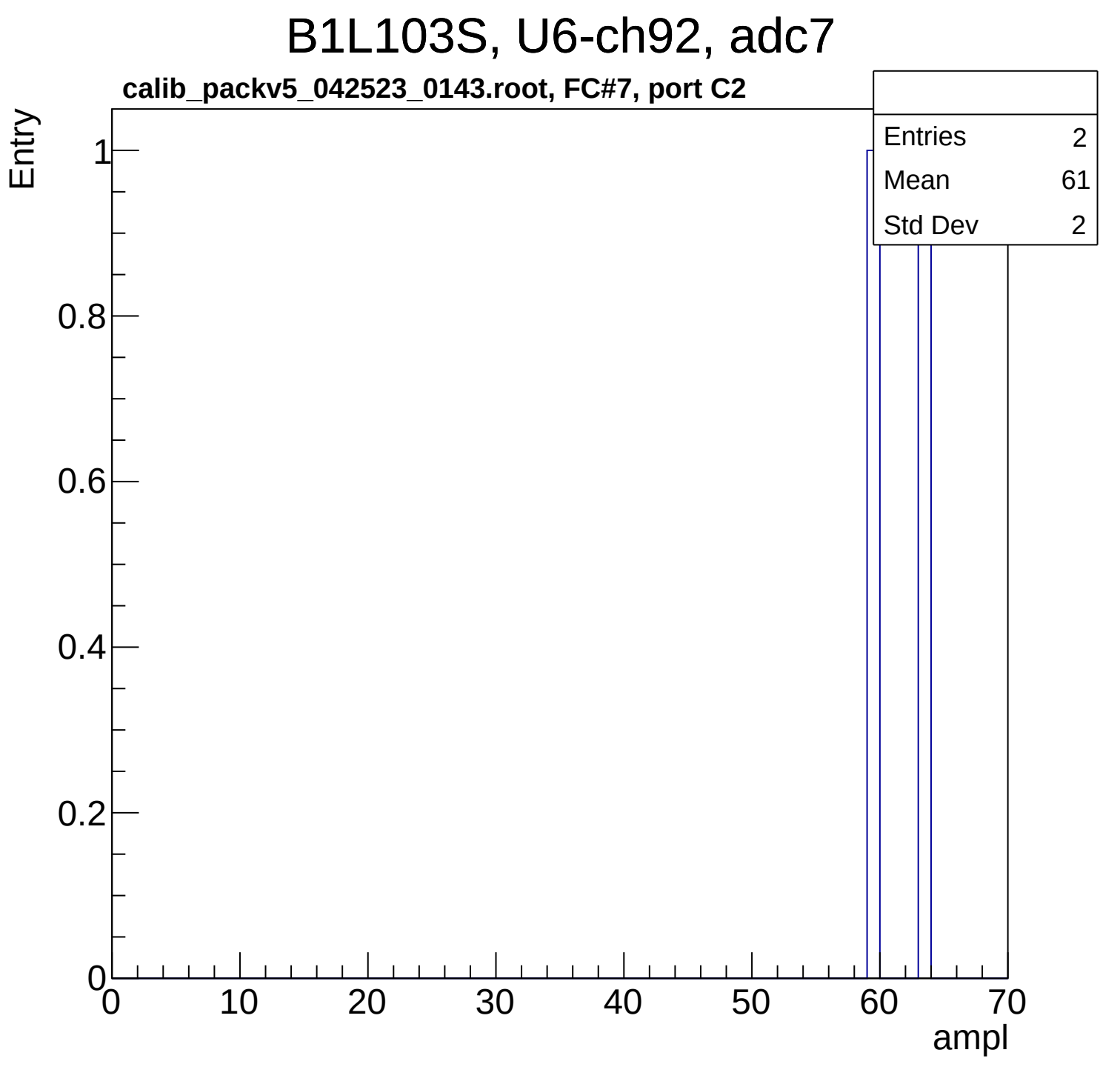
70
ampl


B1L103S, U6-ch92, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
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10
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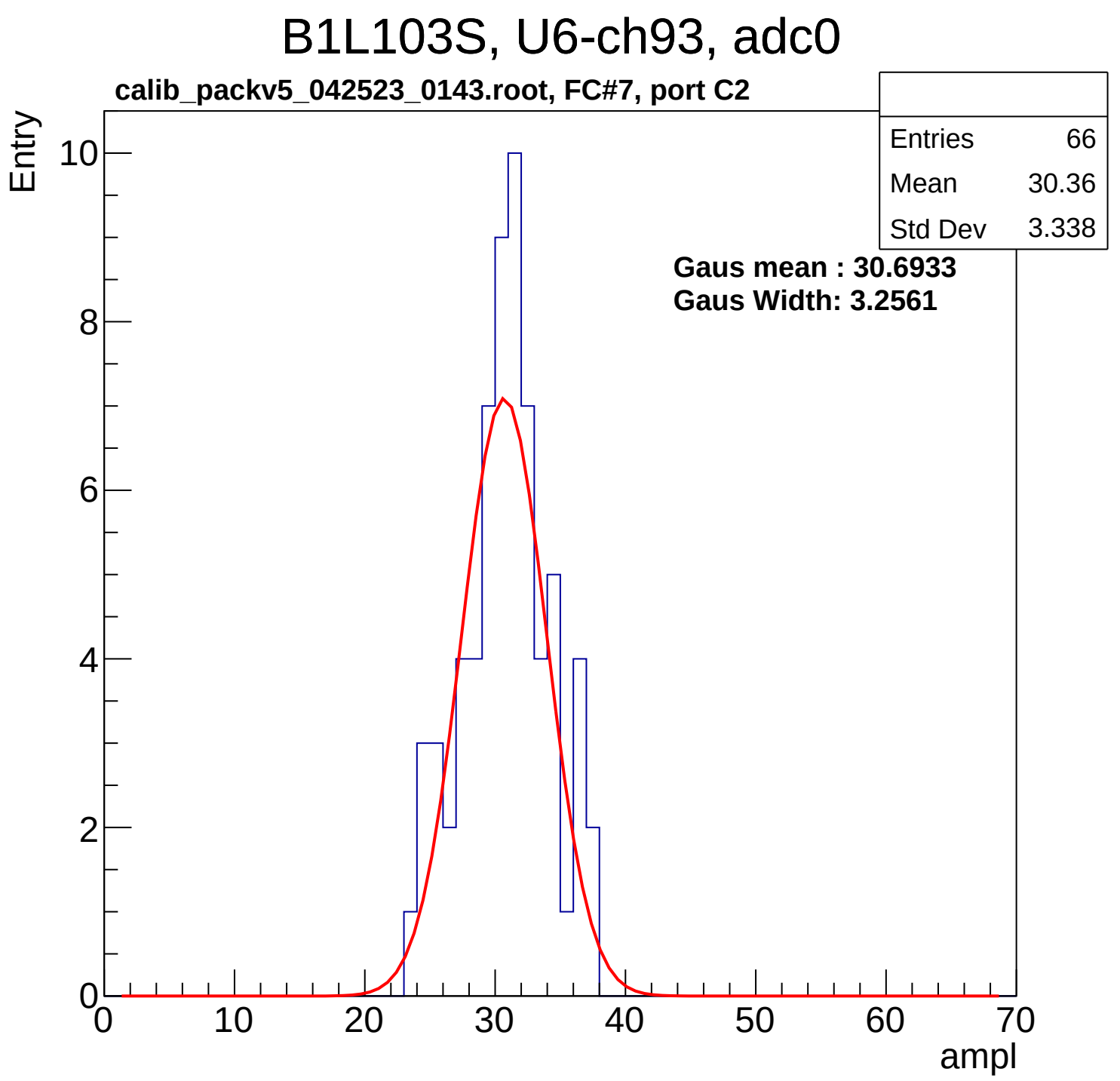
70
ampl


B1L103S, U6-ch93, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 30.36
Std Dev 3.338
Gaus mean : 30.6933
Gaus Width: 3.2561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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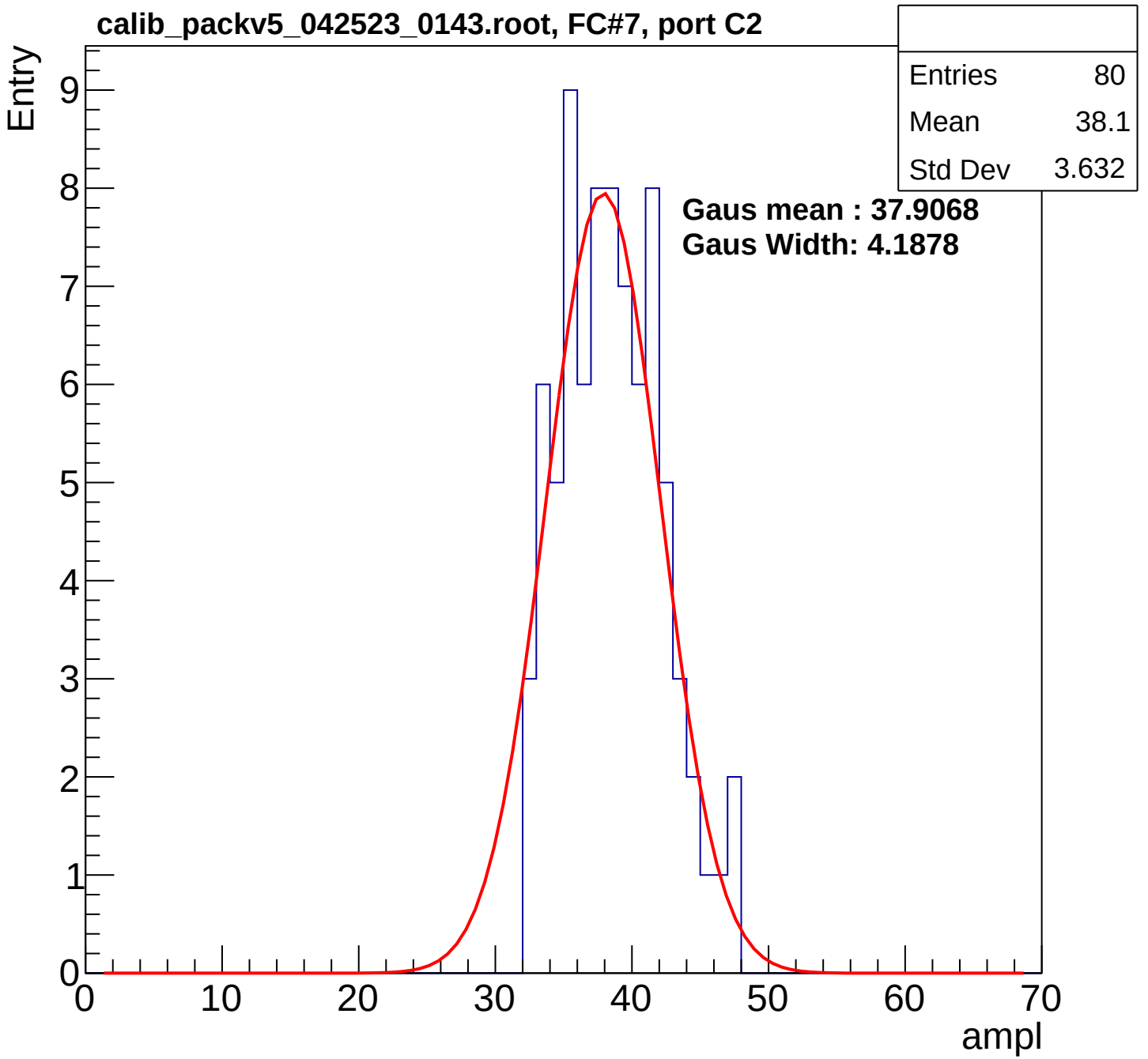
B1L103S, U6-ch93, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 38.1
Std Dev 3.632
Gaus mean : 37.9068
Gaus Width: 4.1878
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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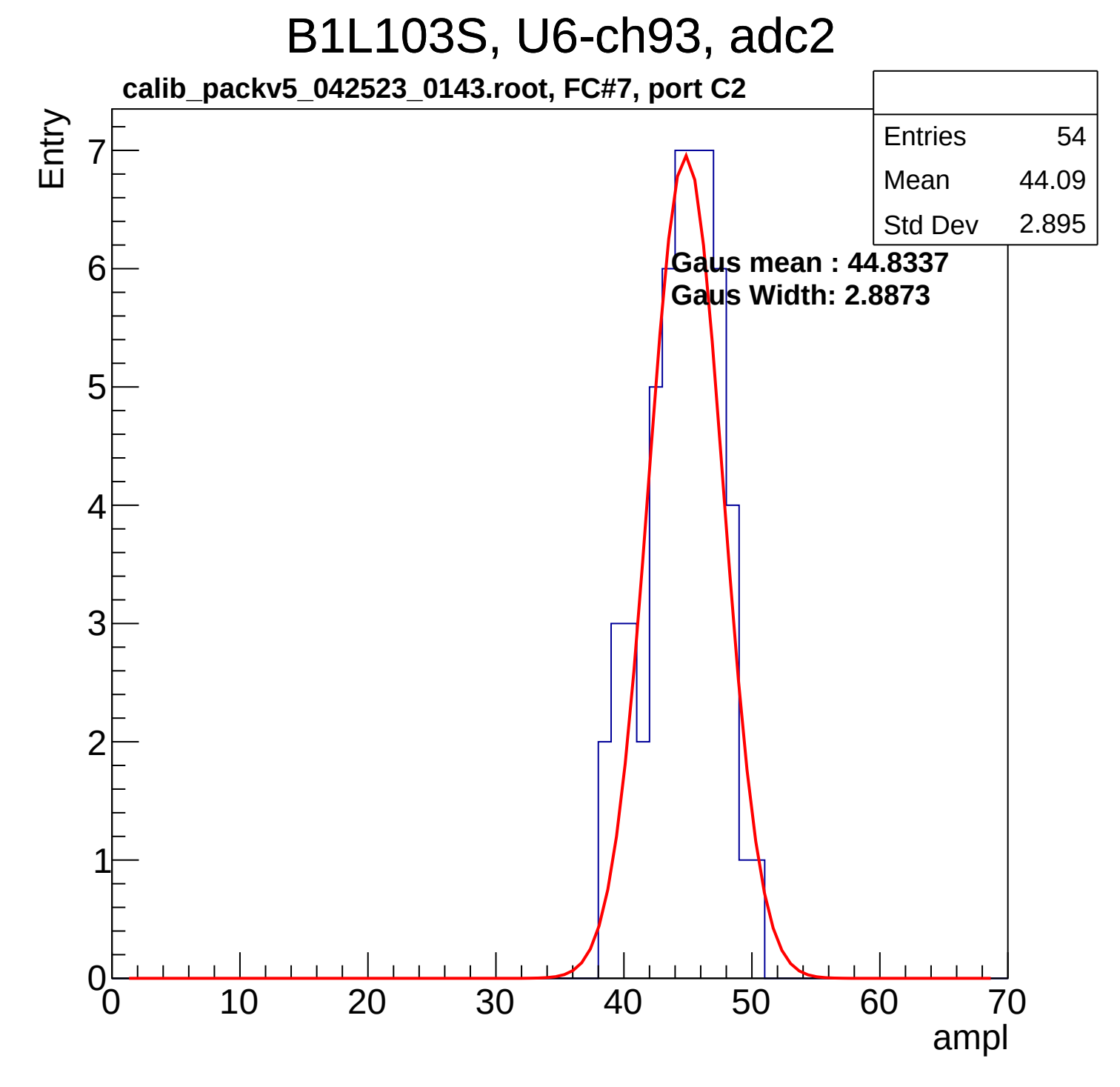
B1L103S, U6-ch93, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 44.09
Std Dev 2.895
Gaus mean : 44.8337
Gaus Width: 2.8873
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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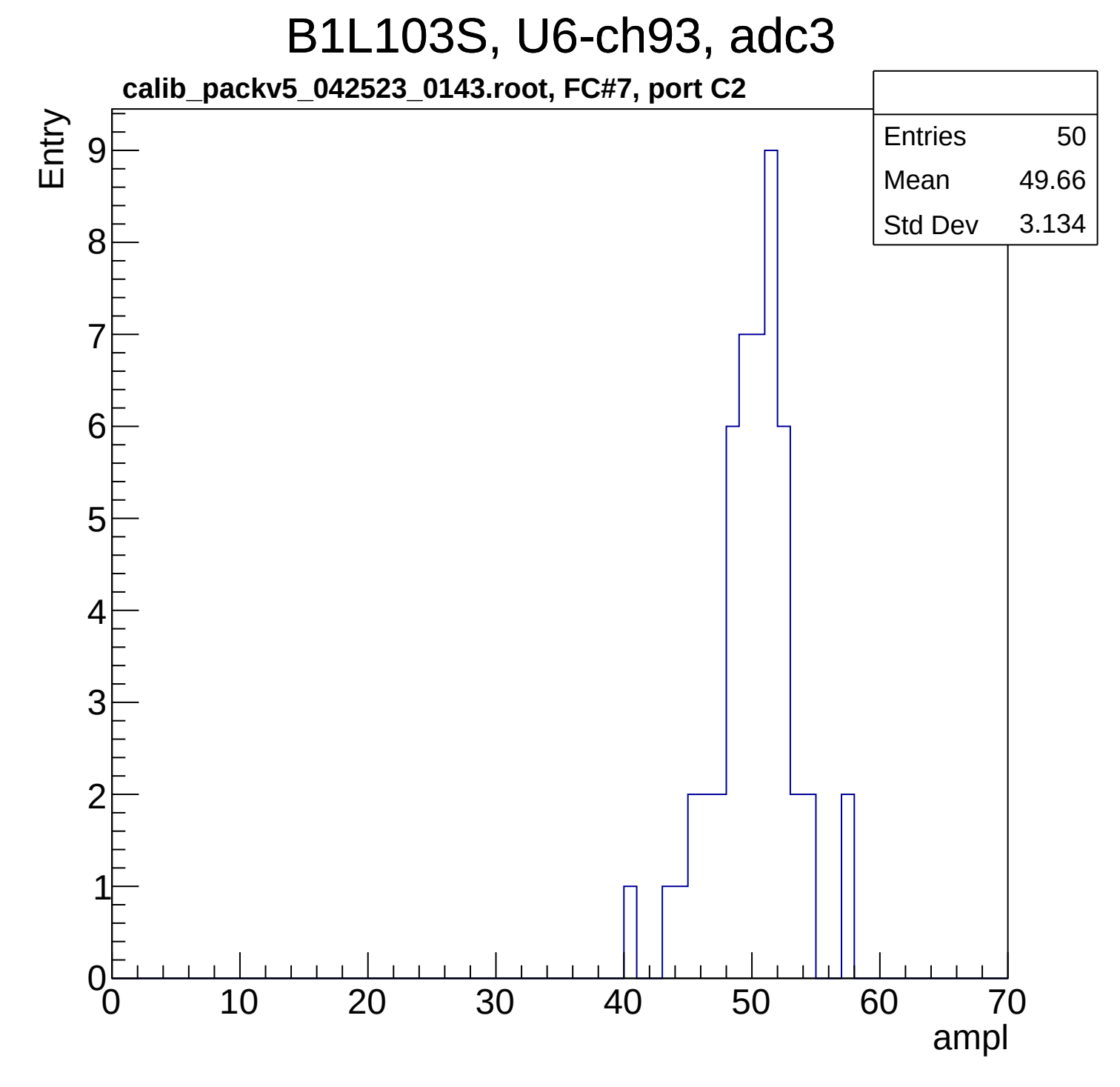
B1L103S, U6-ch93, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 49.66
Std Dev 3.134
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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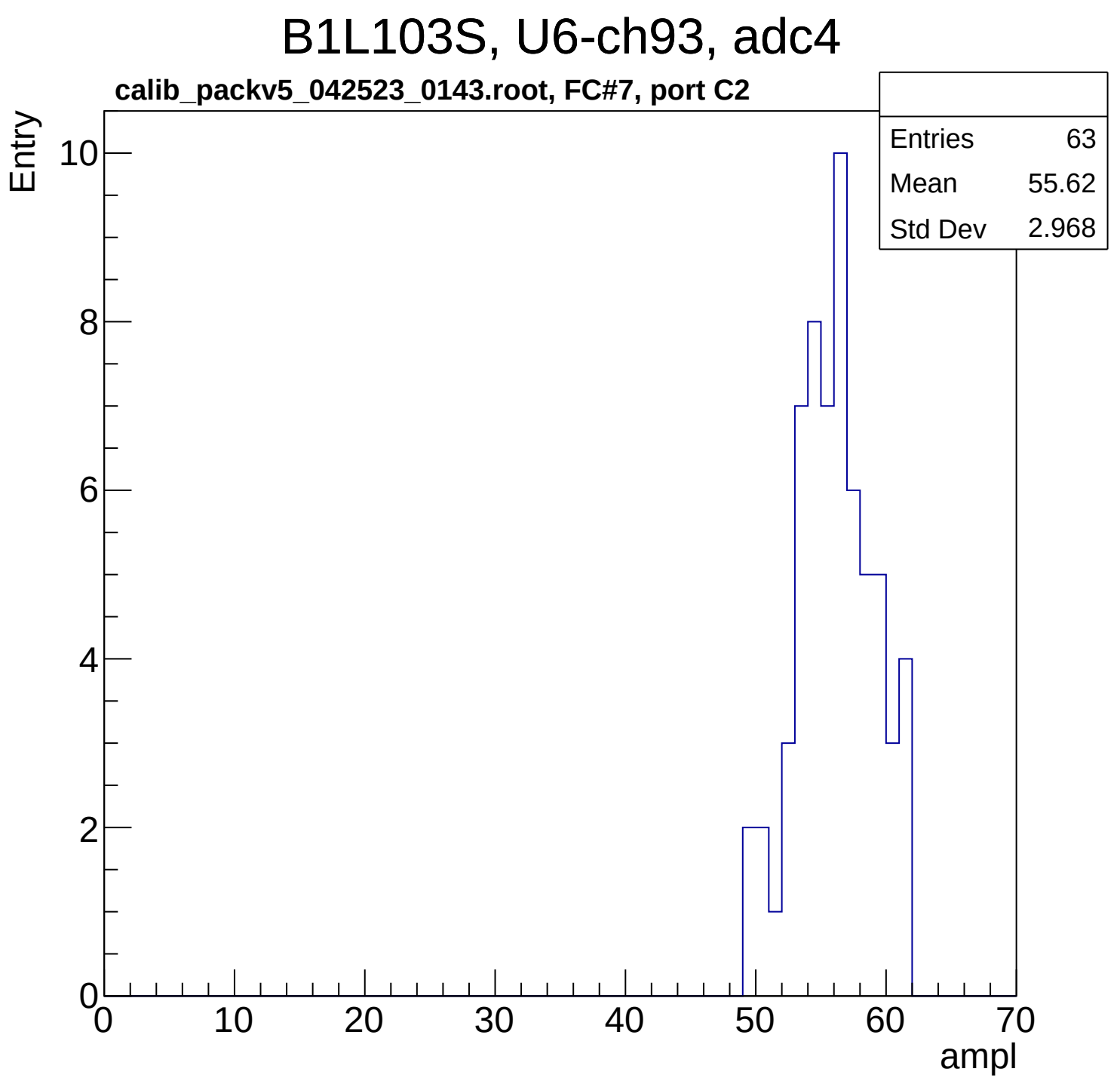
B1L103S, U6-ch93, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 55.62
Std Dev 2.968
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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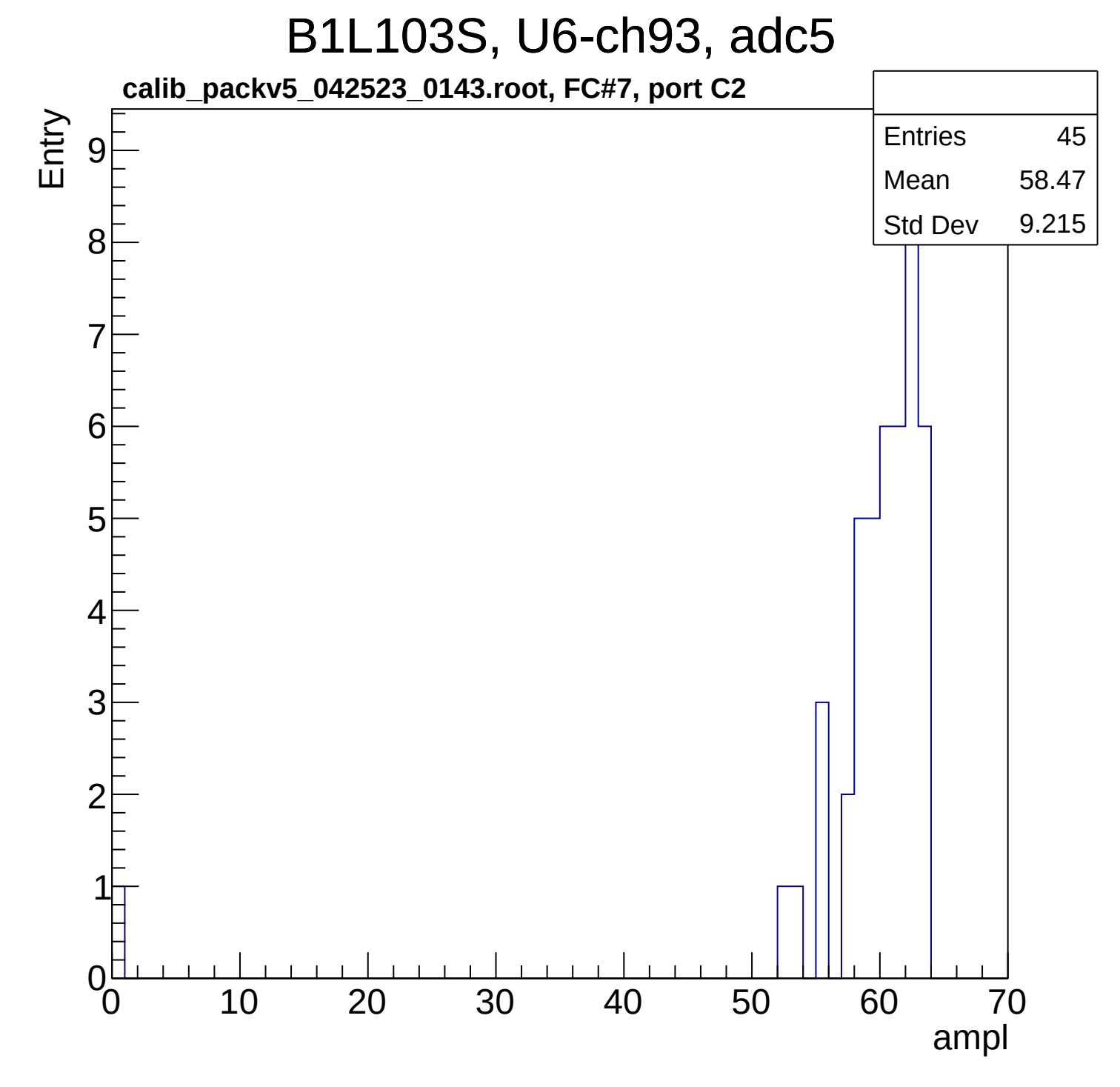
B1L103S, U6-ch93, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.47
Std Dev 9.215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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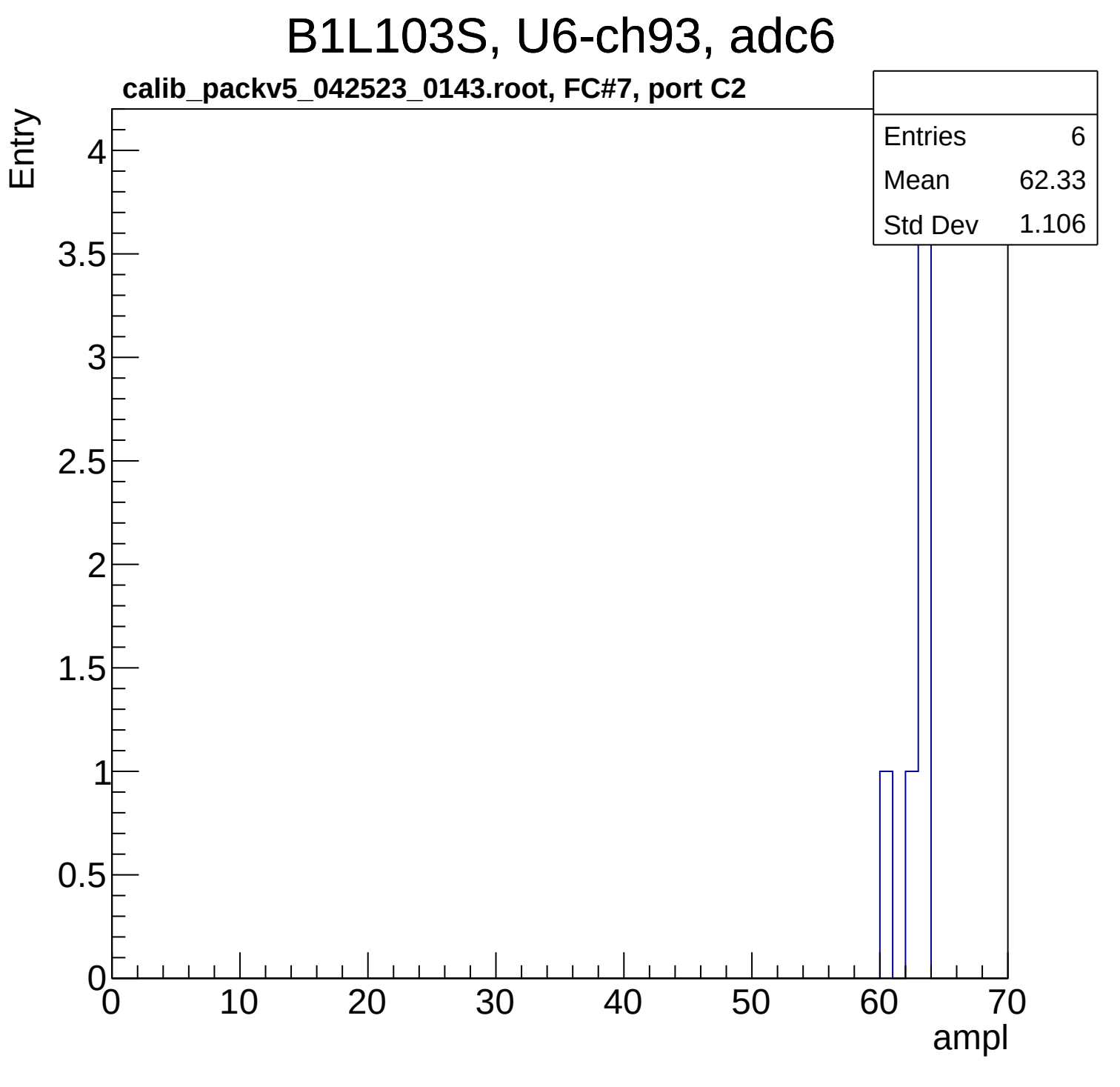
B1L103S, U6-ch93, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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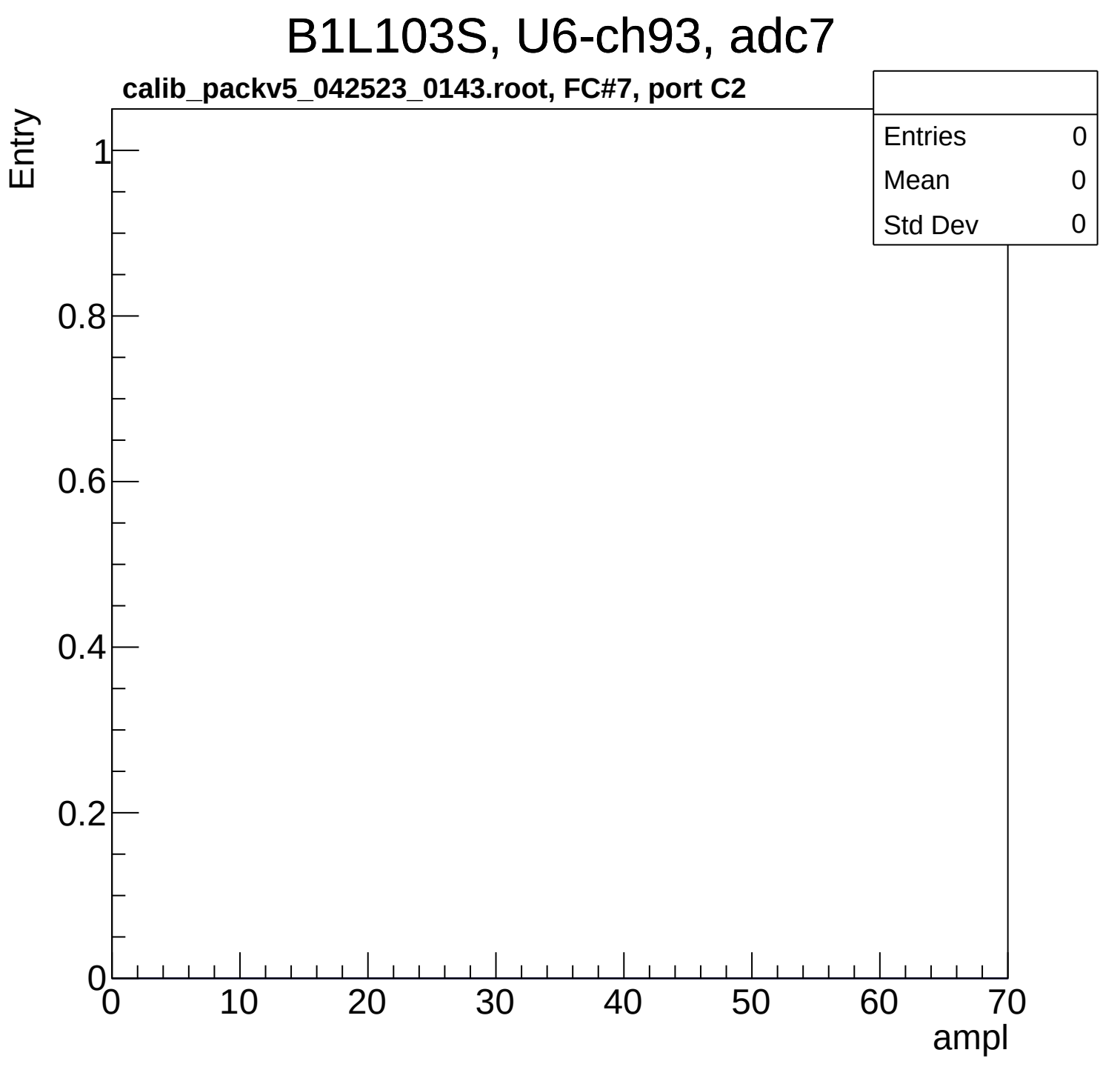
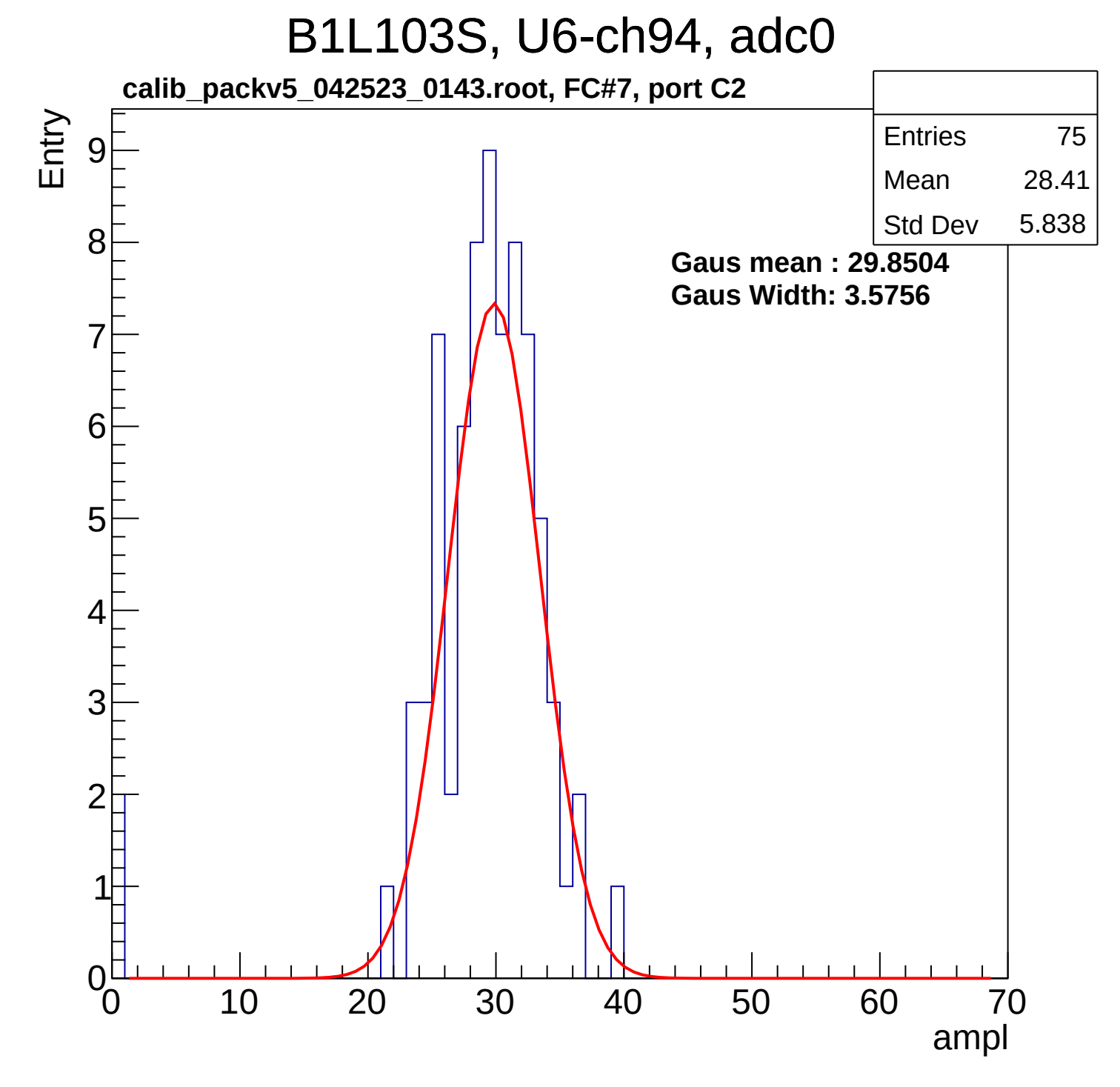
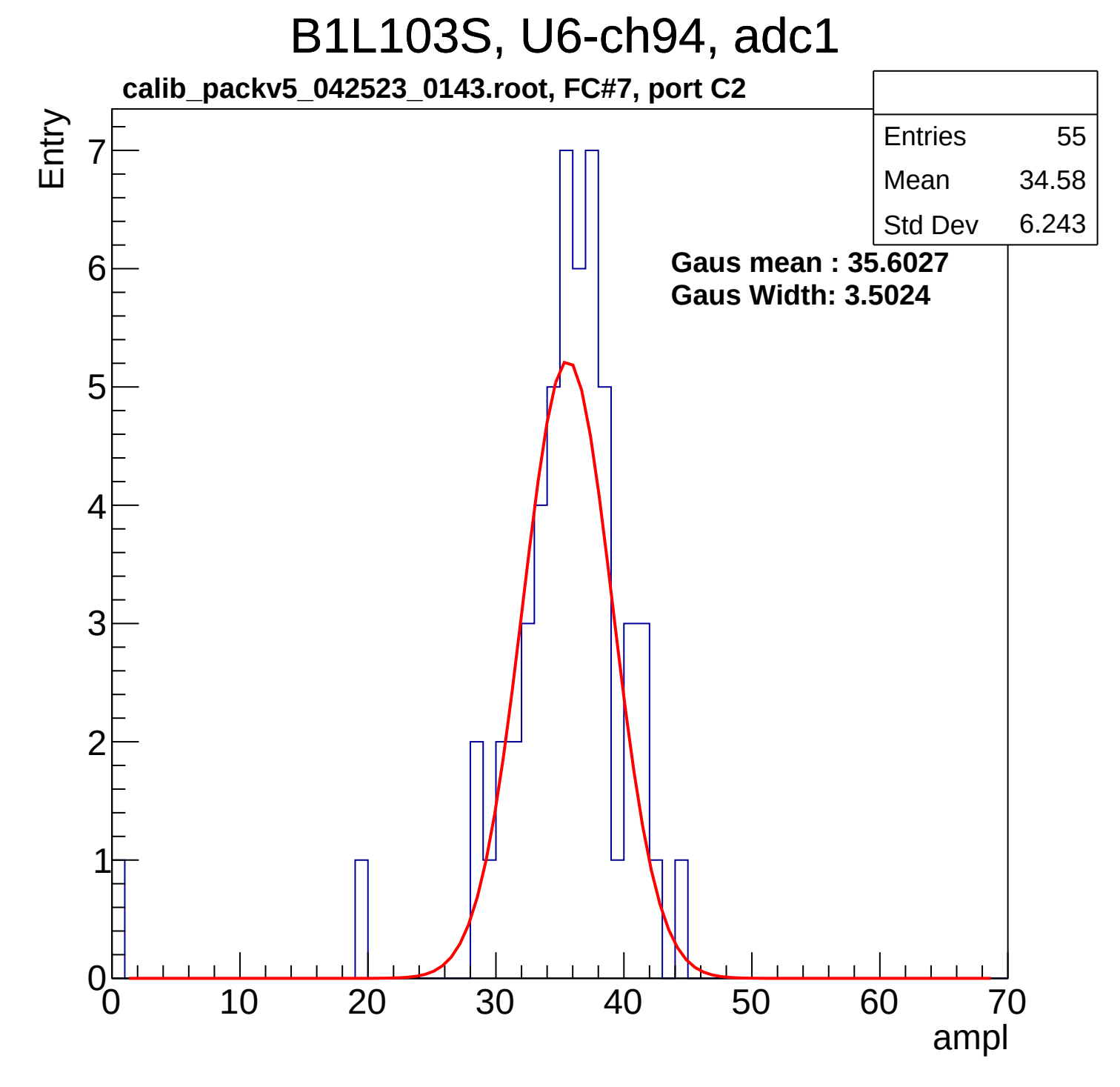

B1L103S, U6-ch93, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch94, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 28.41
Std Dev 5.838
Gaus mean : 29.8504
Gaus Width: 3.5756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch94, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 34.58
Std Dev 6.243
Gaus mean : 35.6027
Gaus Width: 3.5024
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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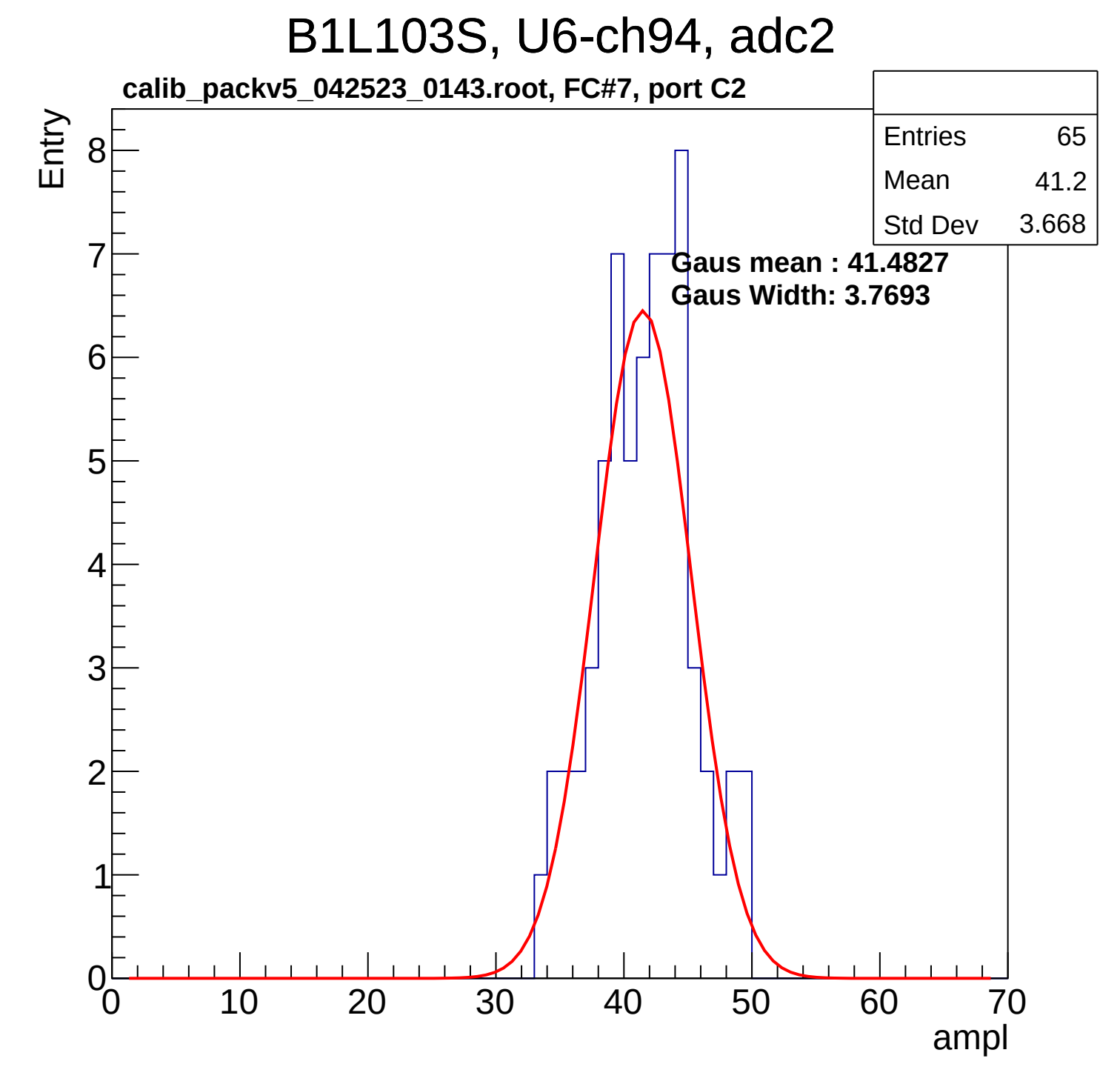
B1L103S, U6-ch94, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.2
Std Dev 3.668
Gaus mean : 41.4827
Gaus Width: 3.7693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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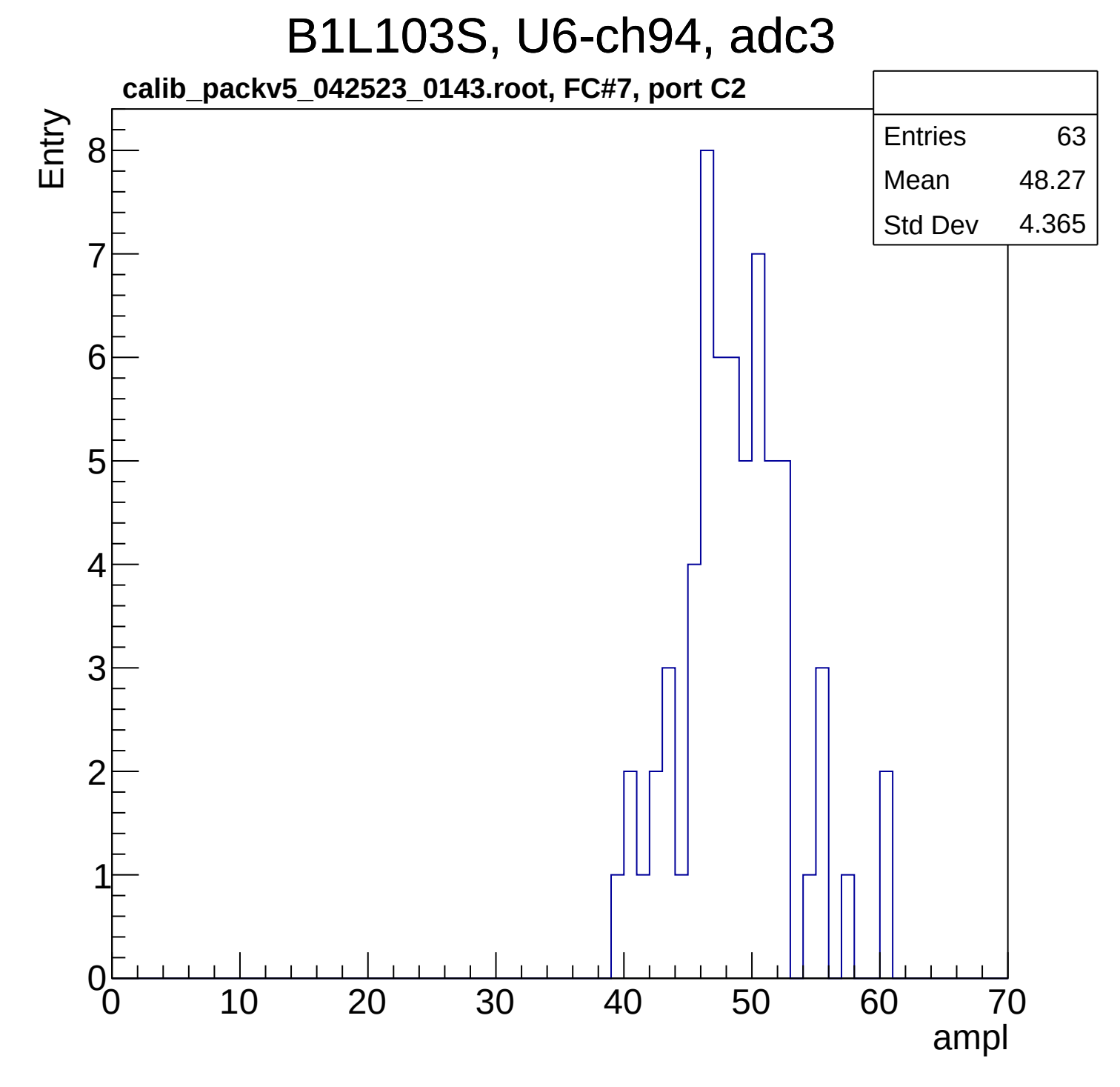
B1L103S, U6-ch94, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.27
Std Dev 4.365
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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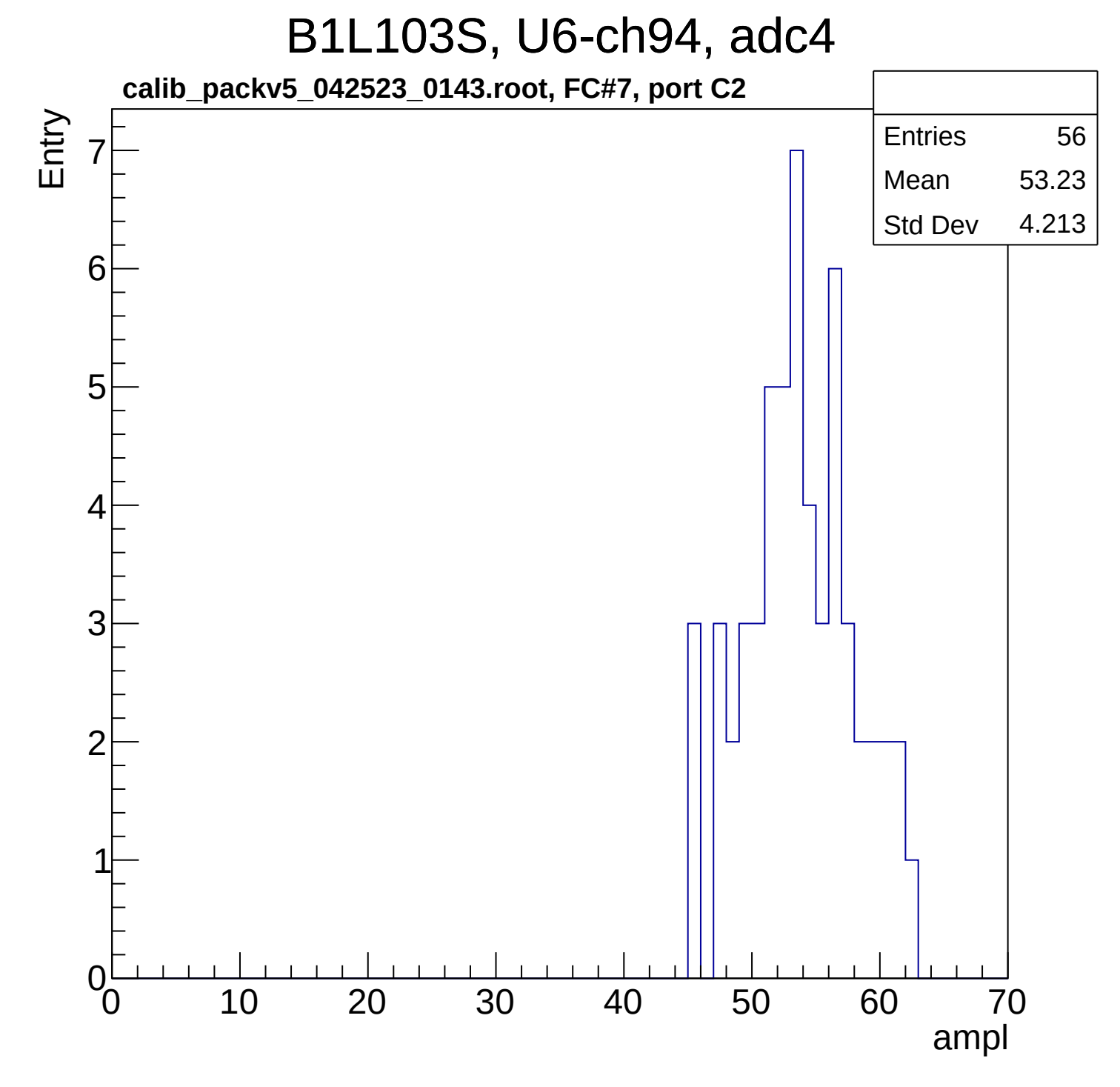
B1L103S, U6-ch94, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.23
Std Dev 4.213
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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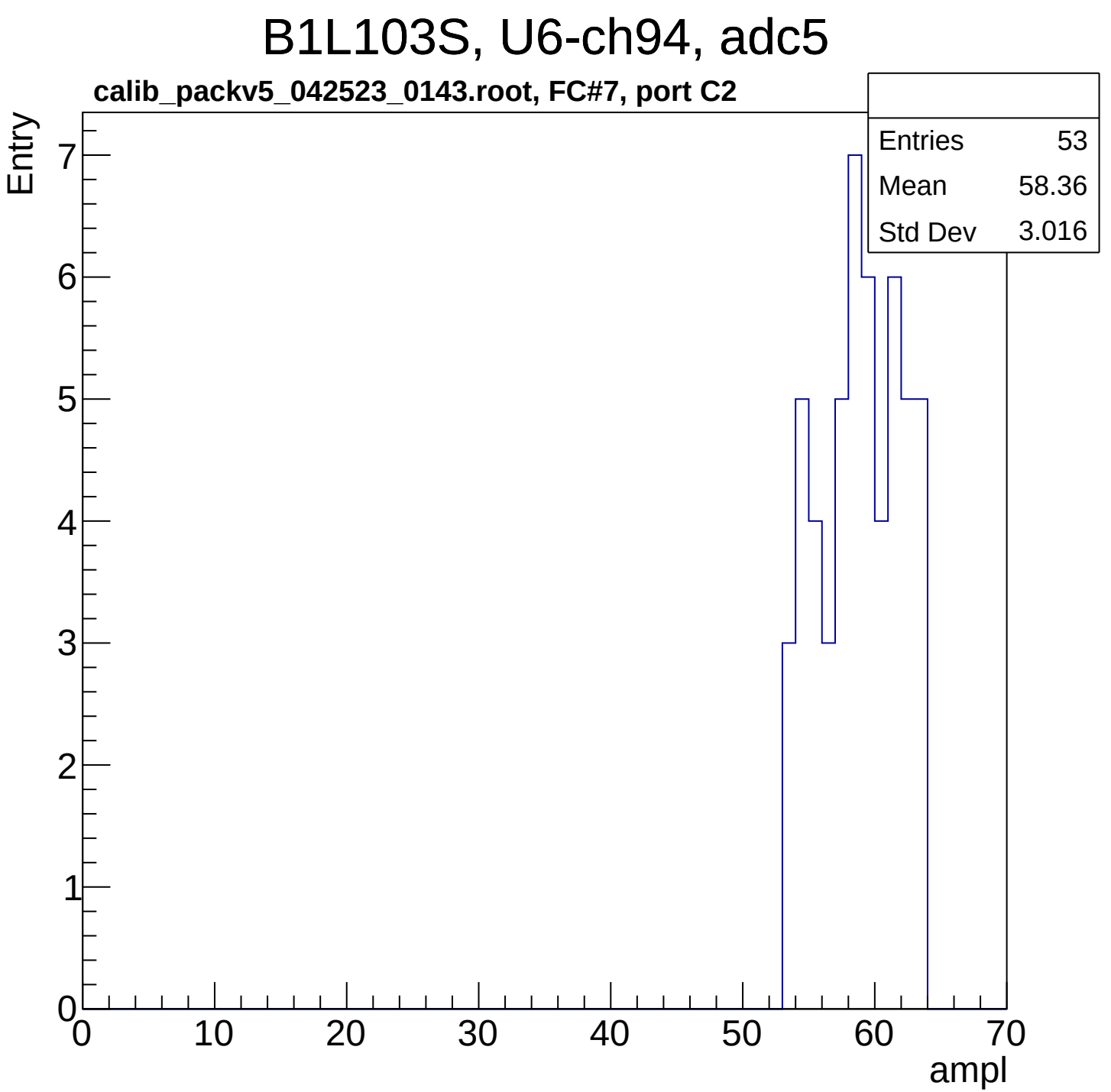
B1L103S, U6-ch94, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.36
Std Dev 3.016
Entry
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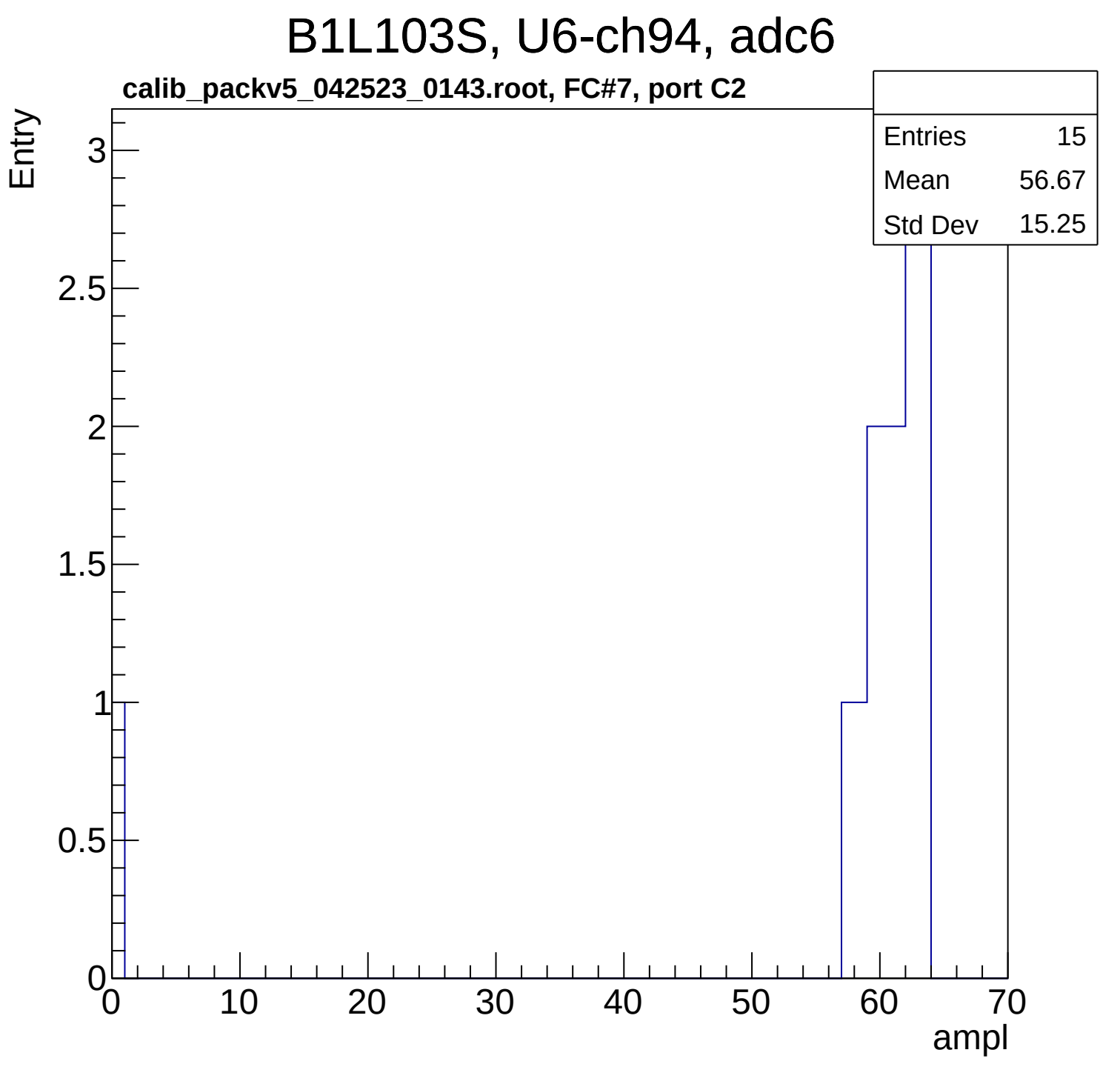
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch94, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 56.67
Std Dev 15.25
Entry
ampl
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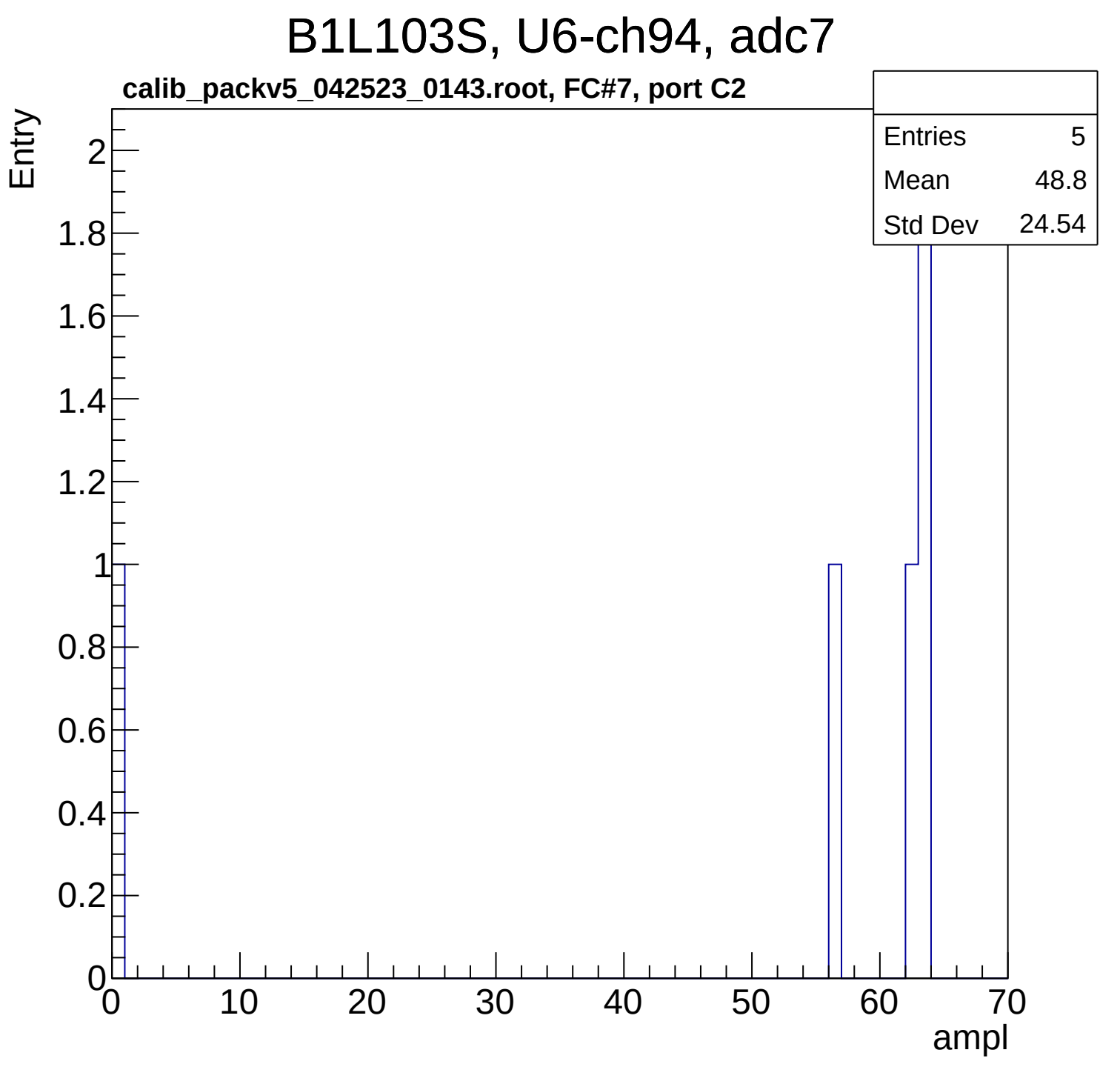
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch94, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 48.8
Std Dev 24.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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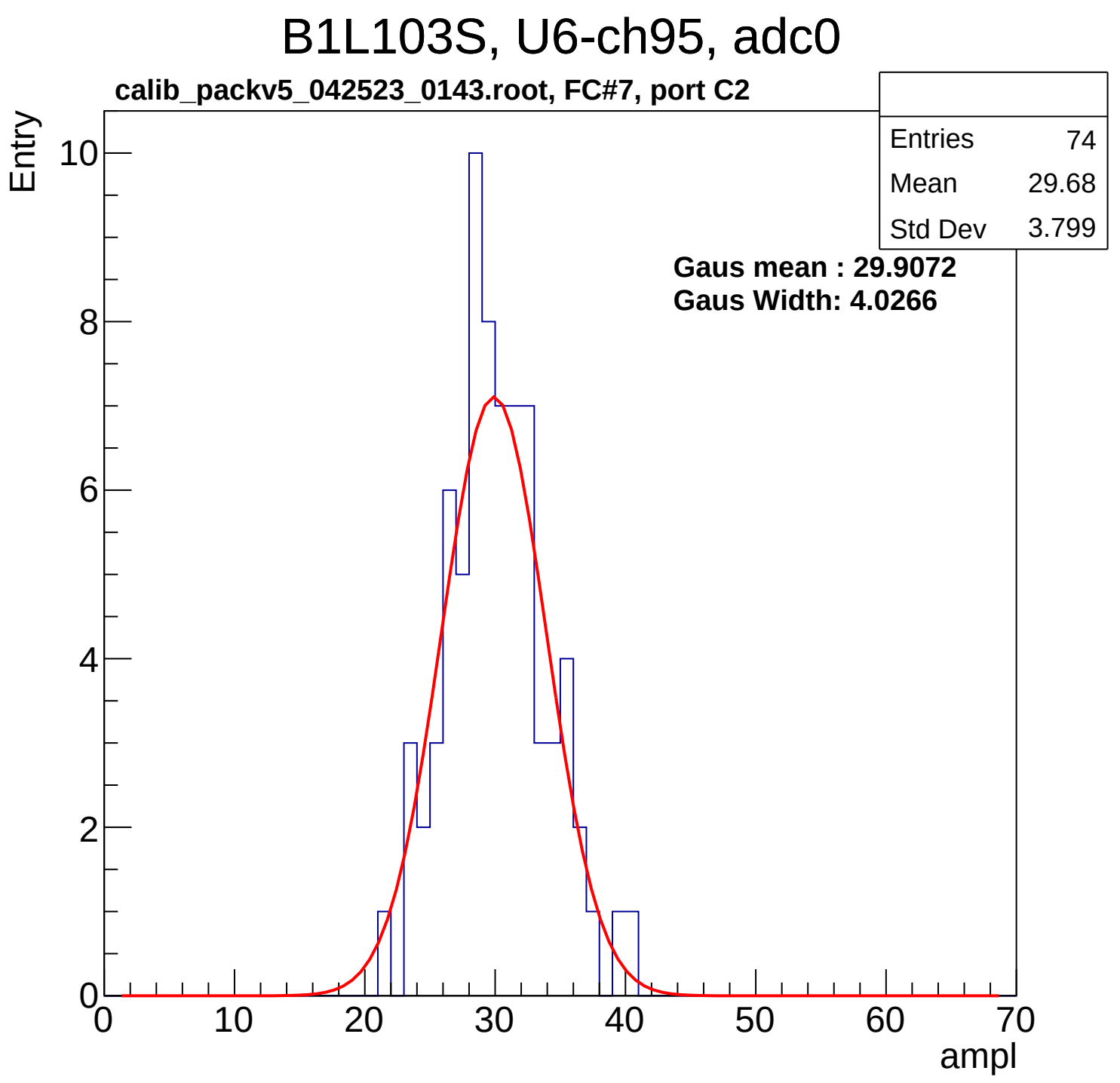
B1L103S, U6-ch95, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.68
Std Dev 3.799
Gaus mean : 29.9072
Gaus Width: 4.0266
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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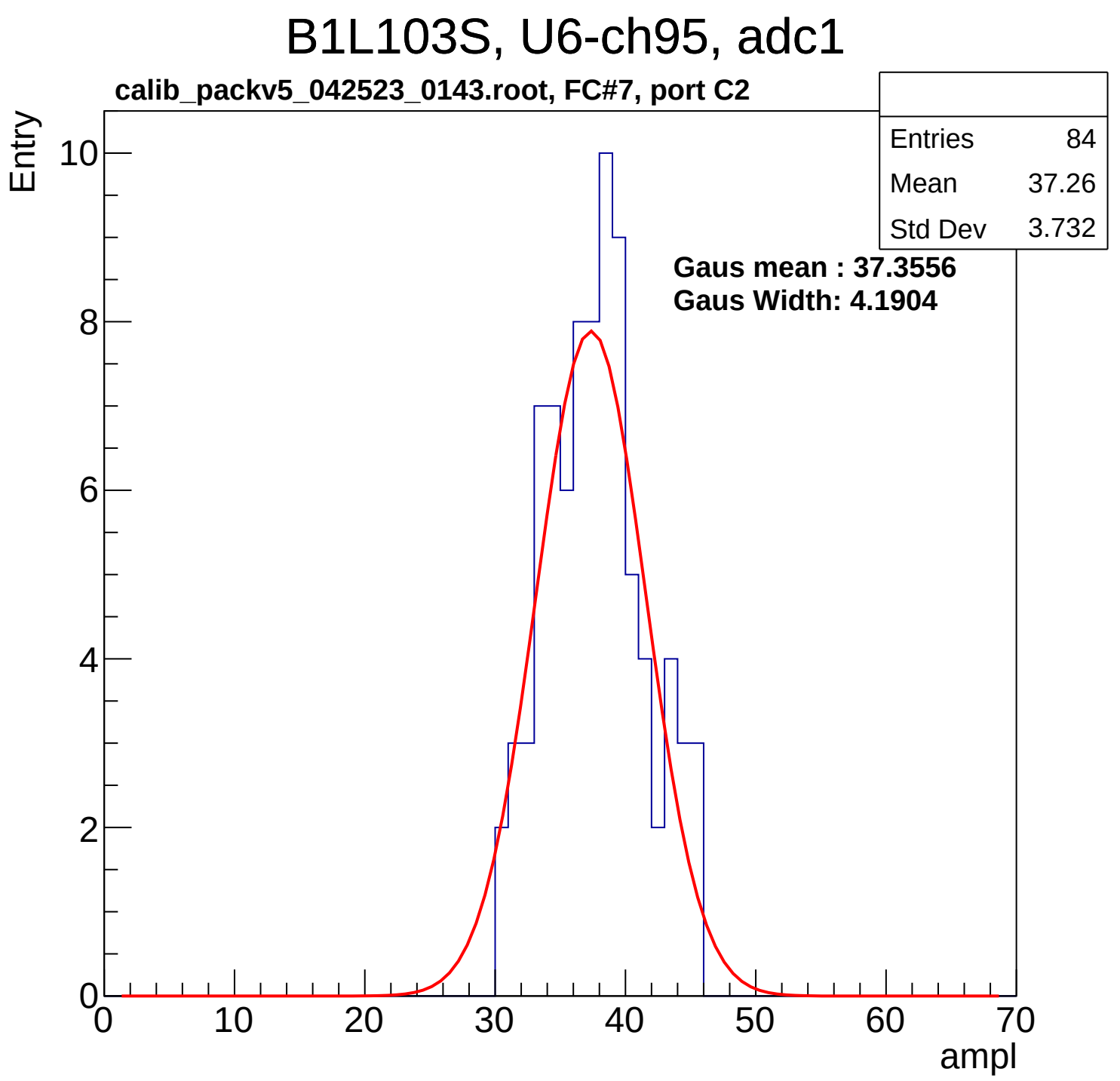
B1L103S, U6-ch95, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 37.26
Std Dev 3.732
Gaus mean : 37.3556
Gaus Width: 4.1904
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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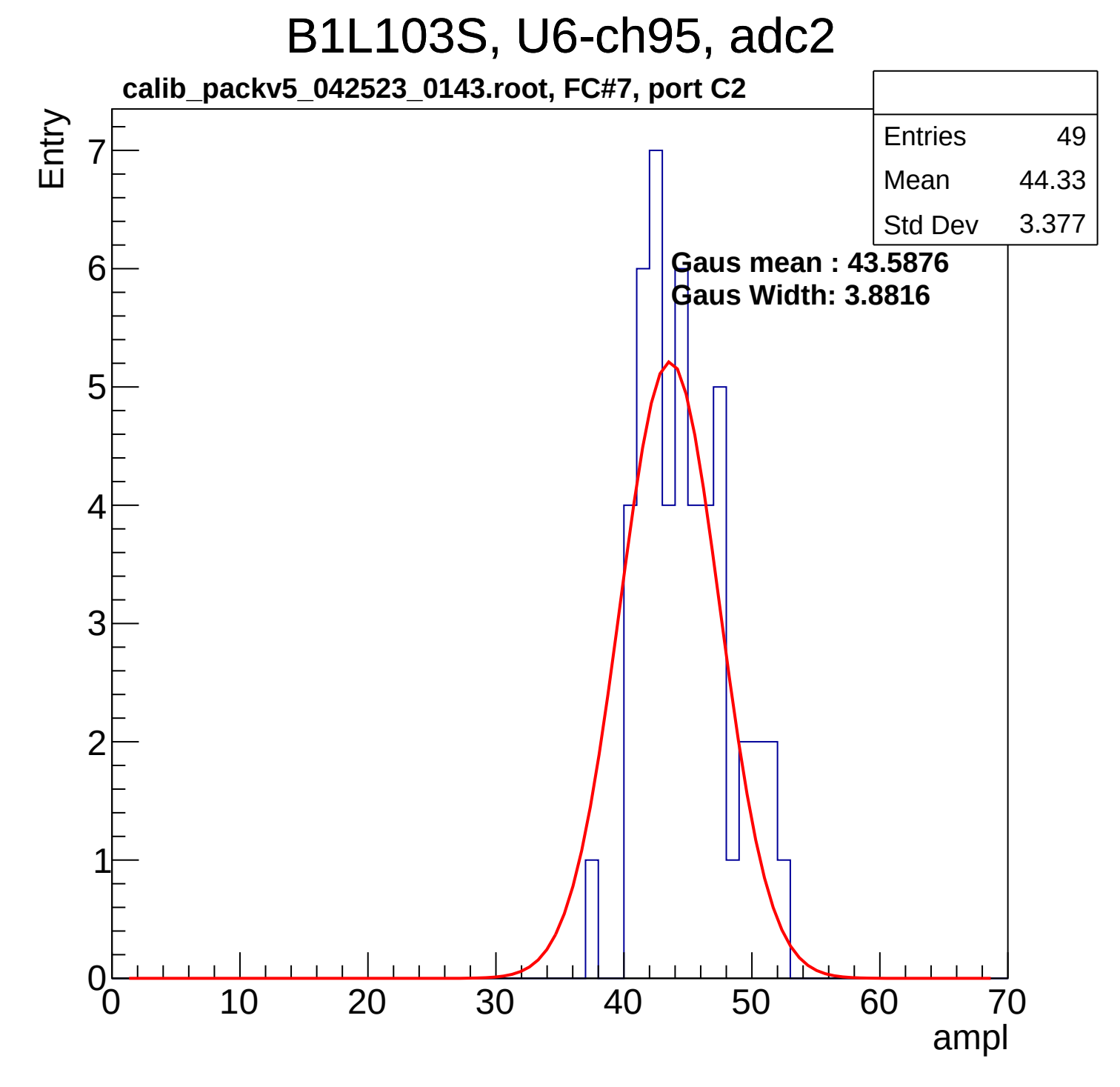
B1L103S, U6-ch95, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 44.33
Std Dev 3.377
Gaus mean : 43.5876
Gaus Width: 3.8816
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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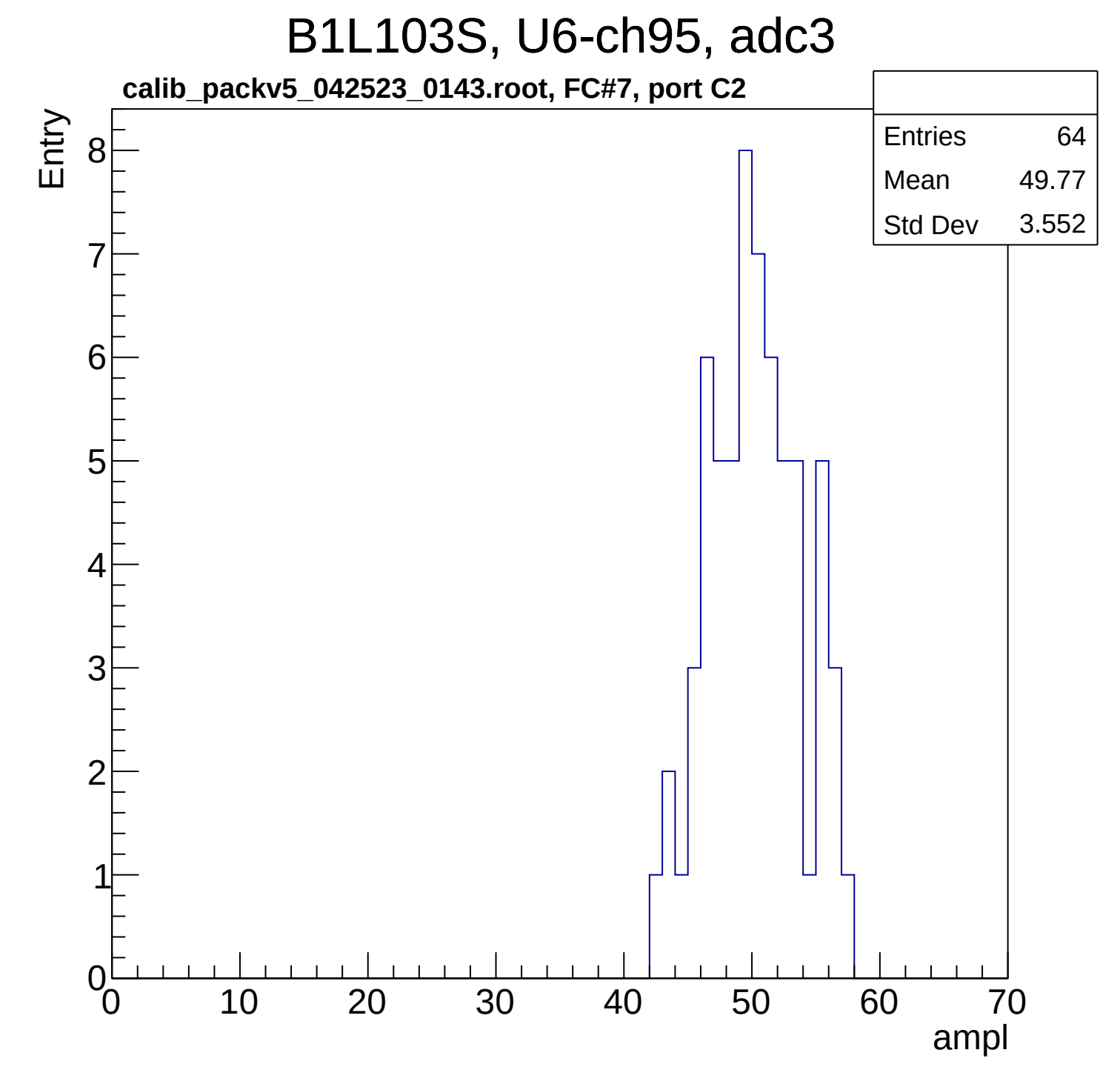
B1L103S, U6-ch95, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.77
Std Dev 3.552
Entry
ampl
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5
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0
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60
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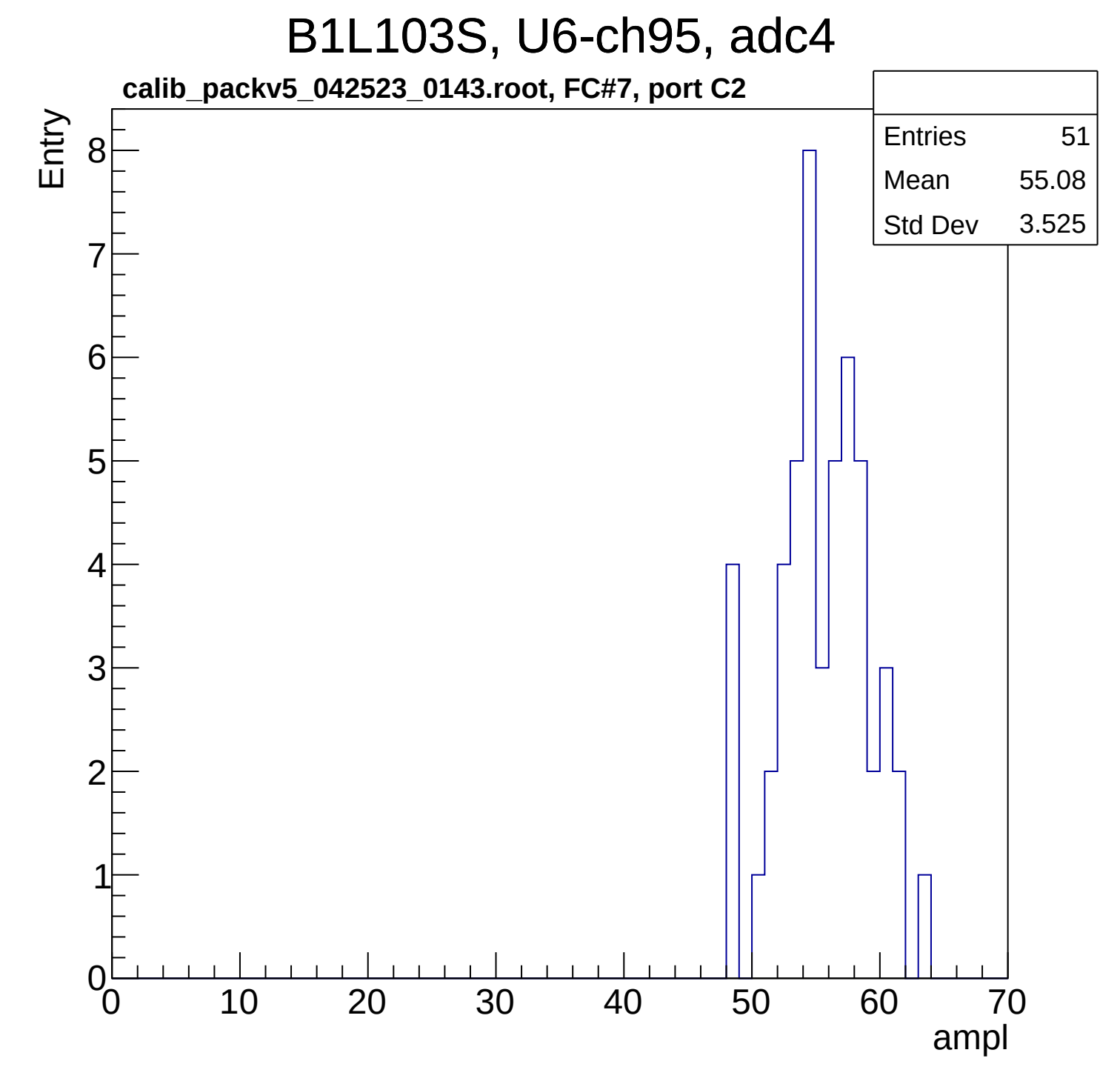
B1L103S, U6-ch95, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.08
Std Dev 3.525
Entry
ampl
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1
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3
4
5
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0
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60
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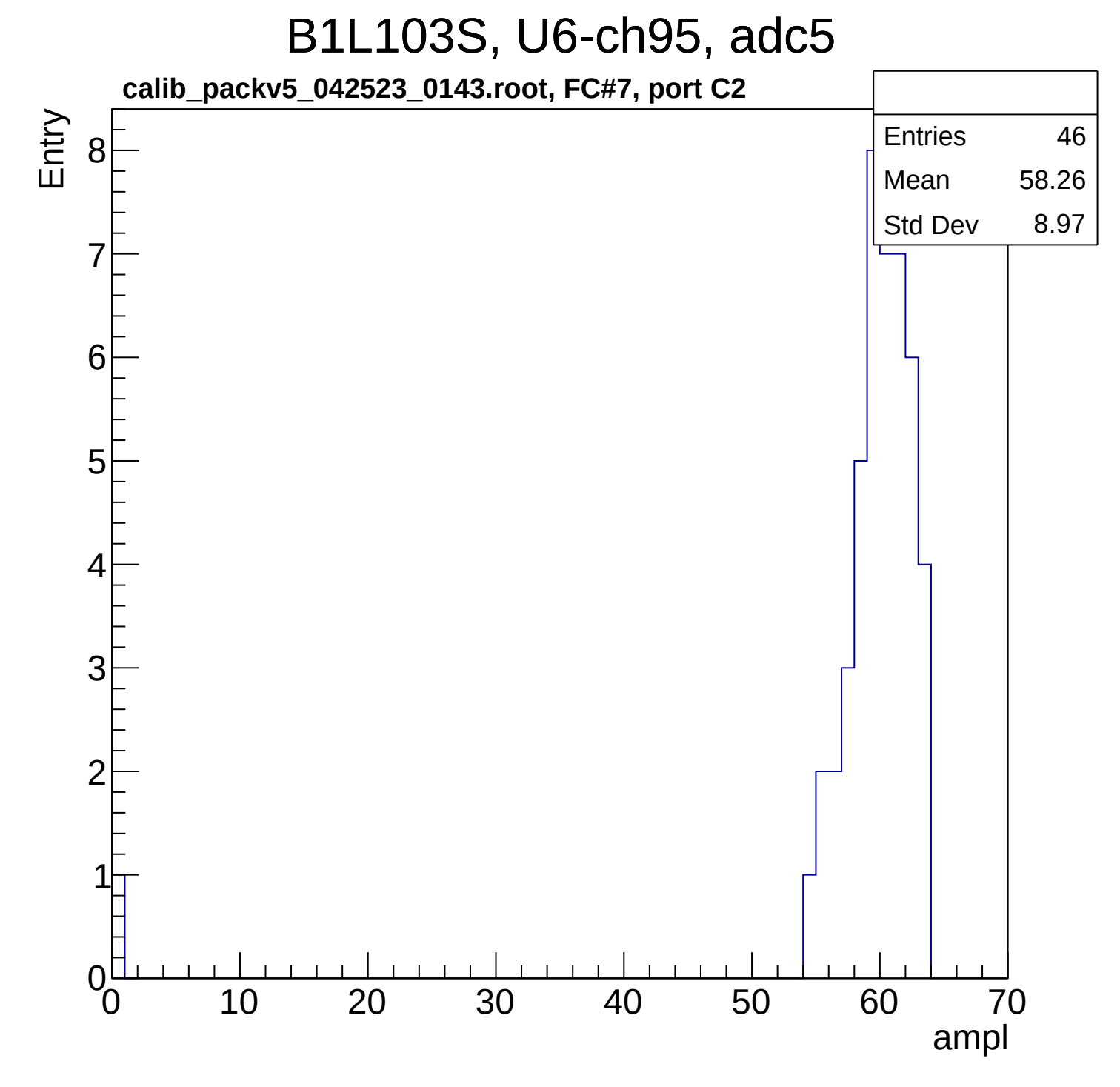
B1L103S, U6-ch95, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.26
Std Dev 8.97
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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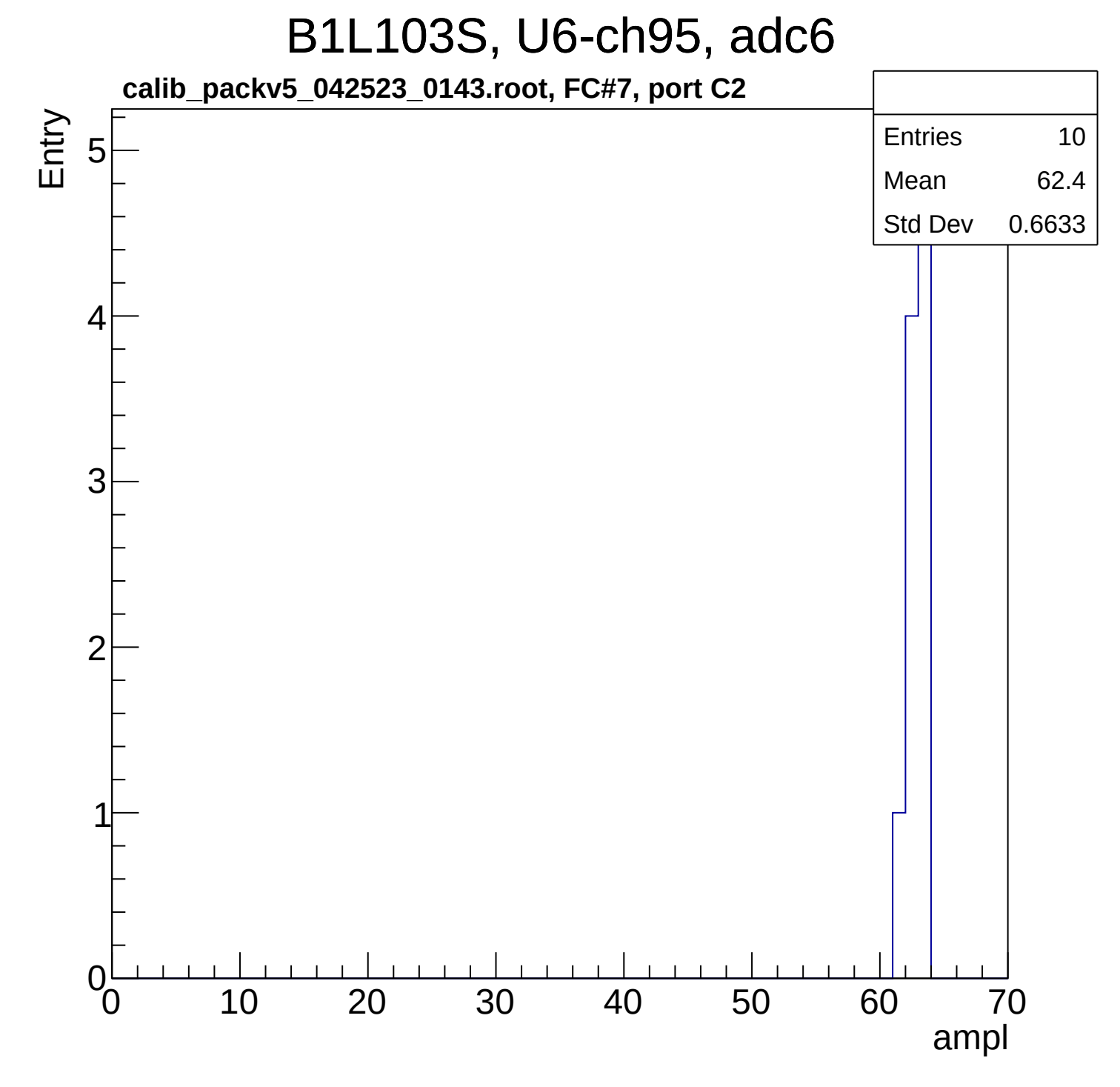
B1L103S, U6-ch95, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 62.4
Std Dev 0.6633
Entry
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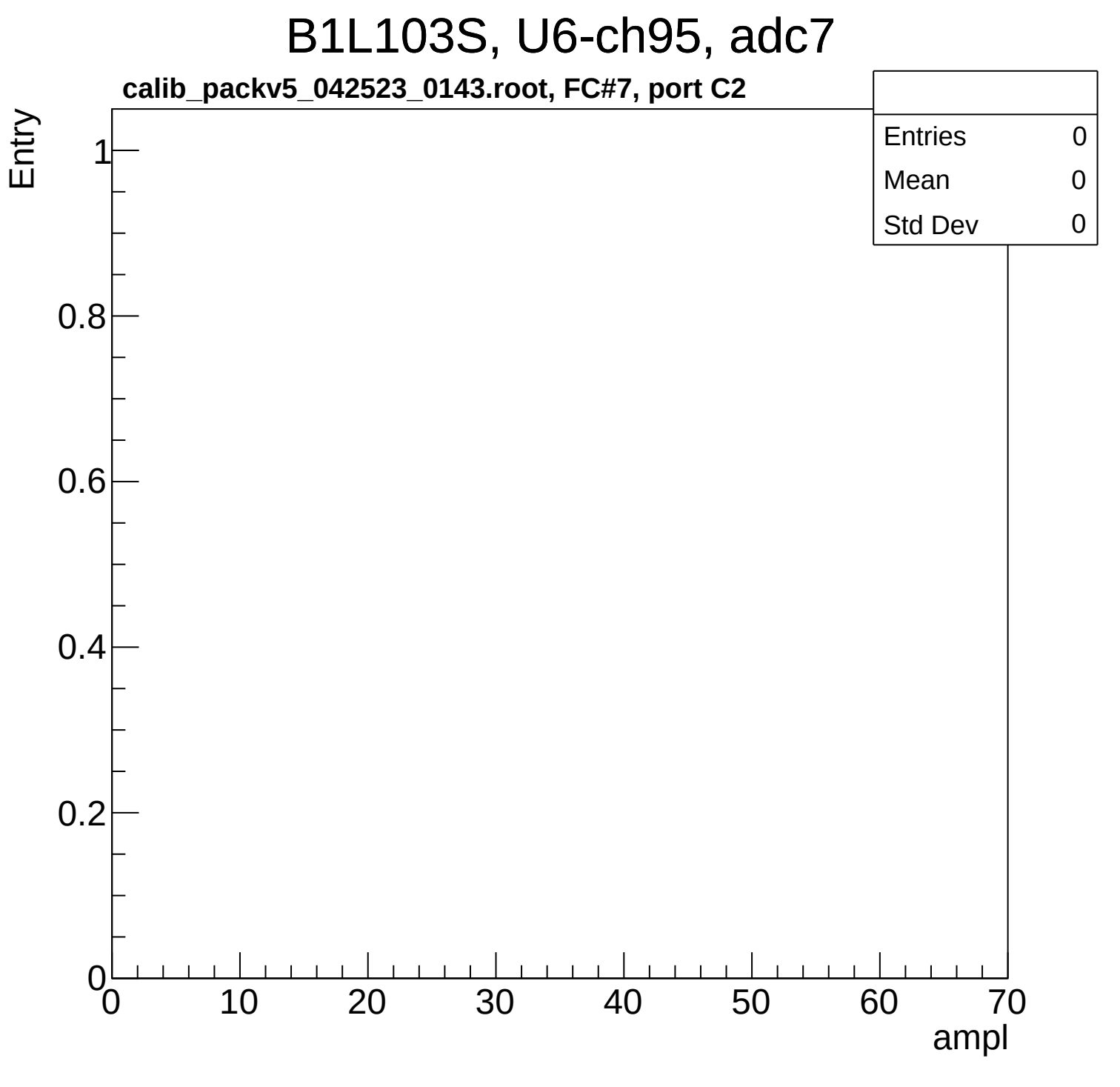
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch95, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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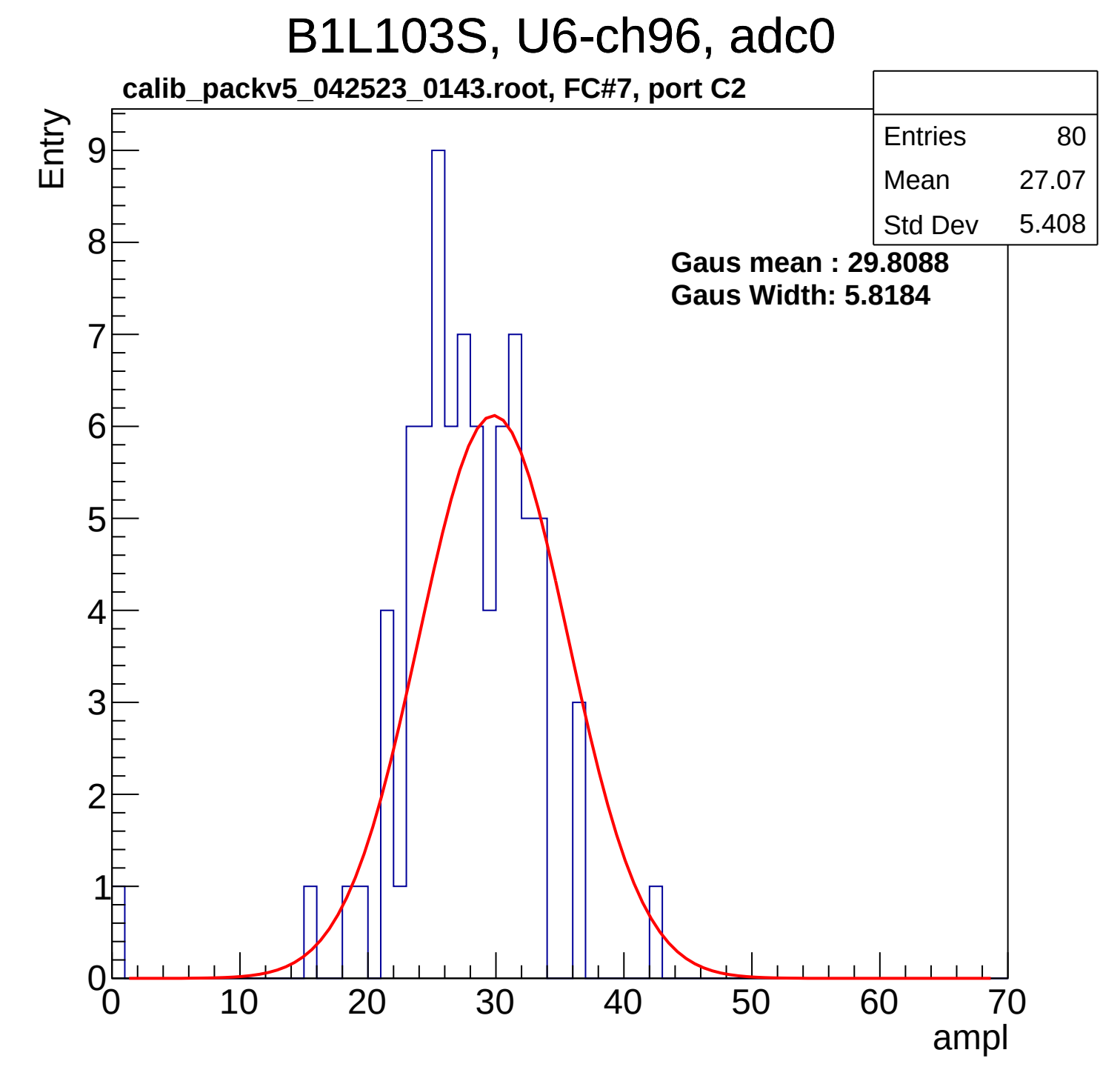
50
60
70
ampl


B1L103S, U6-ch96, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 27.07
Std Dev 5.408
Gaus mean : 29.8088
Gaus Width: 5.8184
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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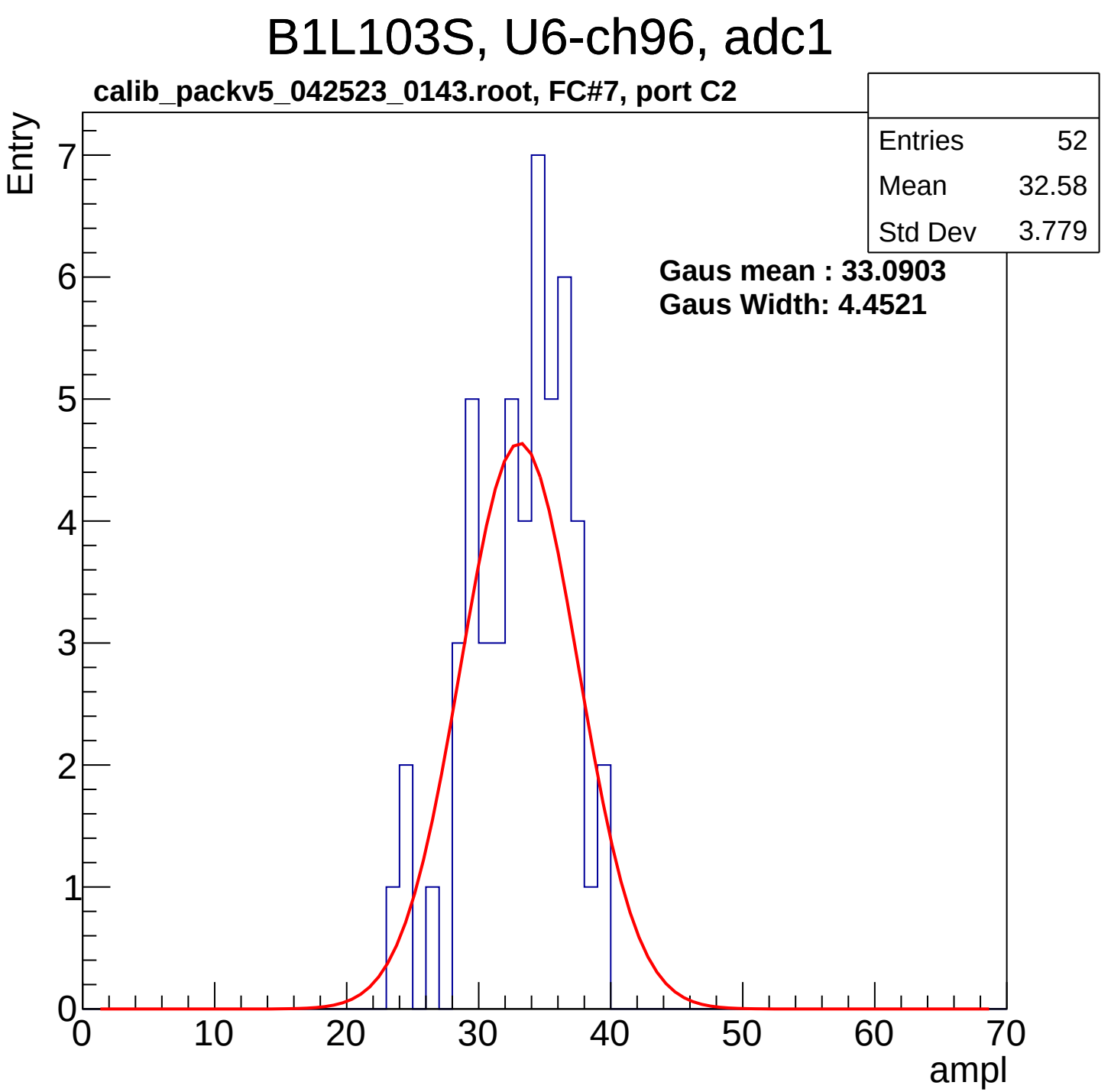
B1L103S, U6-ch96, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 32.58
Std Dev 3.779
Gaus mean : 33.0903
Gaus Width: 4.4521
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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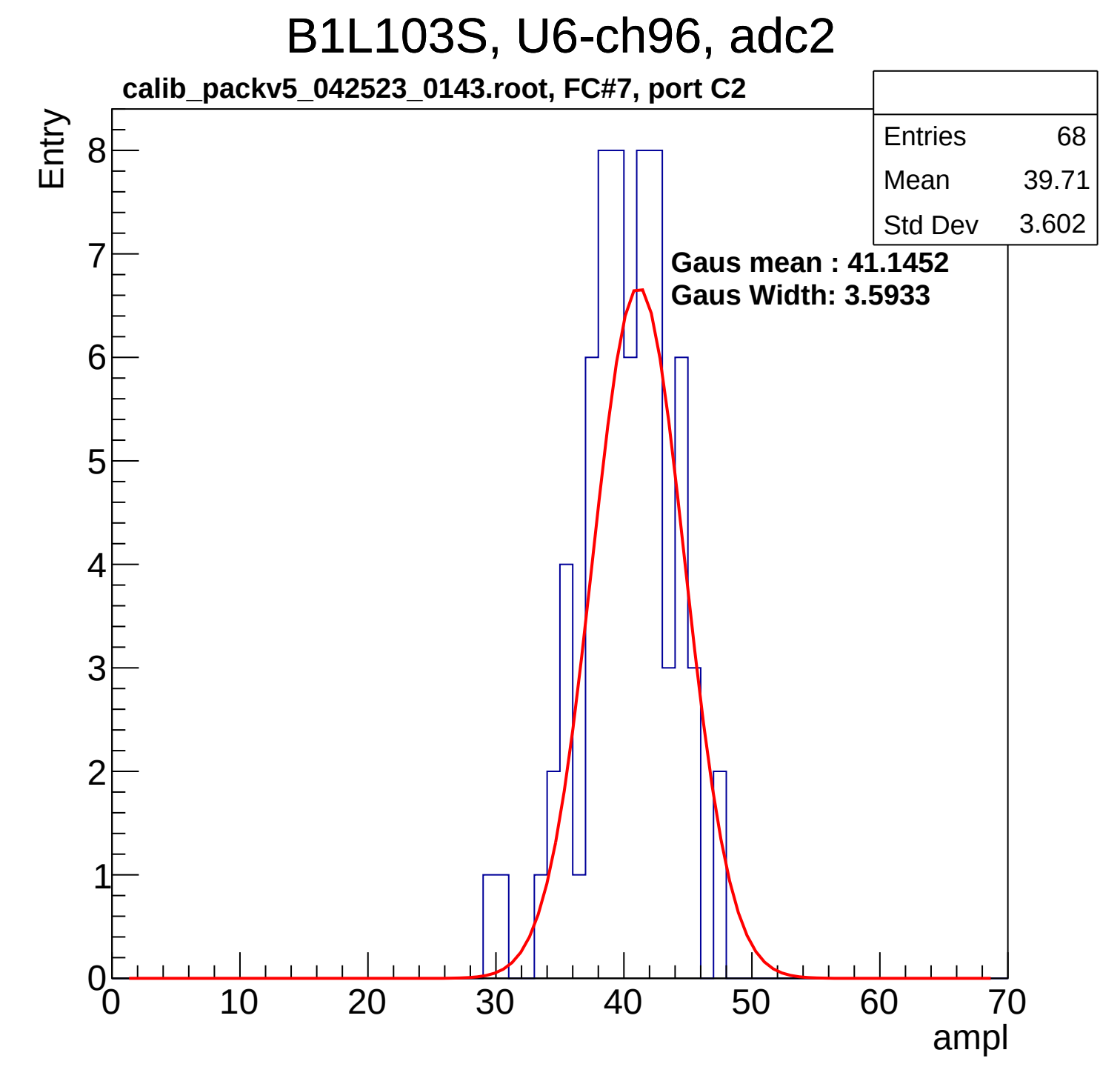
B1L103S, U6-ch96, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 39.71
Std Dev 3.602
Gaus mean : 41.1452
Gaus Width: 3.5933
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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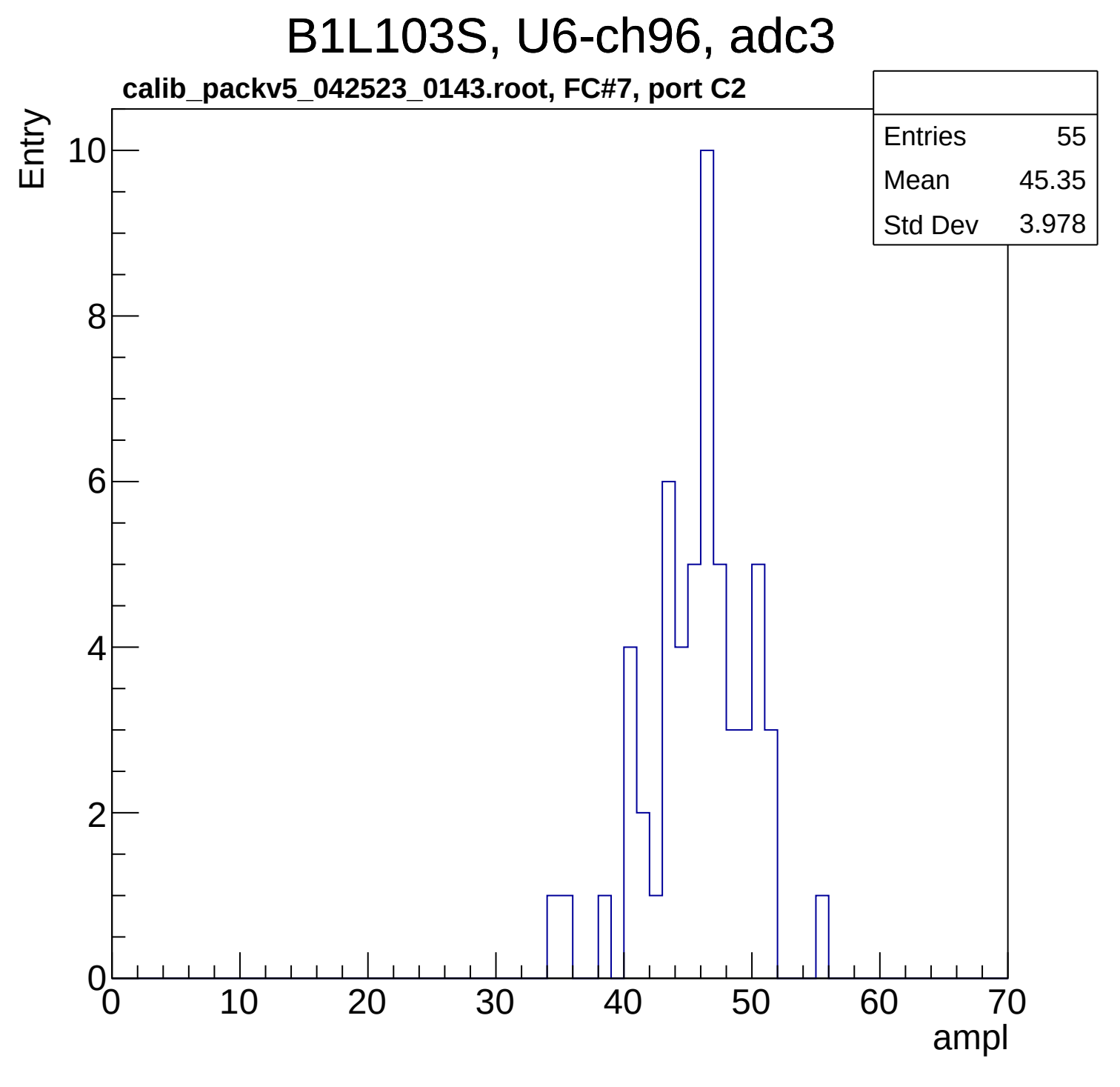
B1L103S, U6-ch96, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 45.35
Std Dev 3.978
Entry
0
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10
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50
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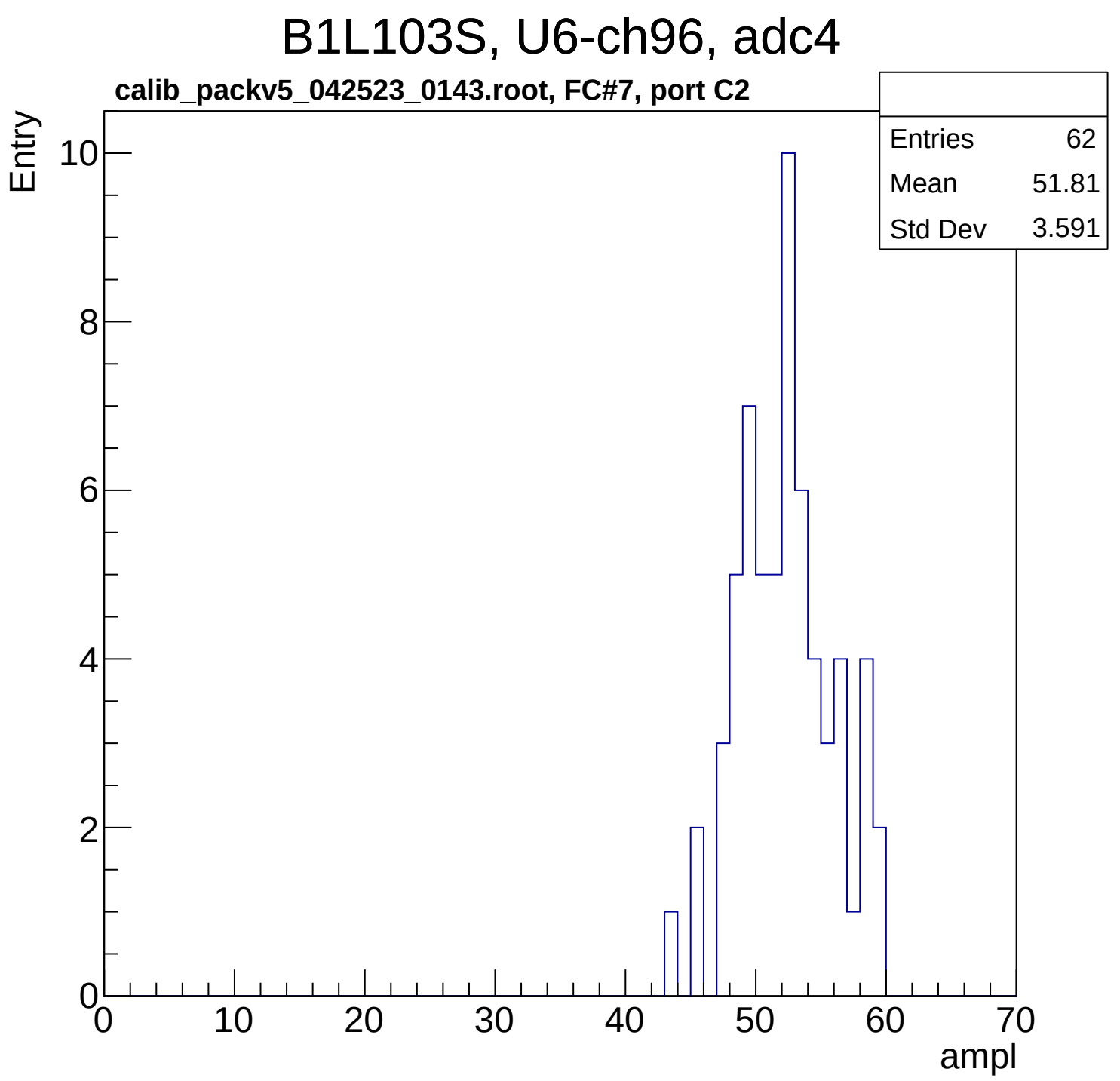
70
ampl


B1L103S, U6-ch96, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 51.81
Std Dev 3.591
Entry
ampl
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4
6
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60
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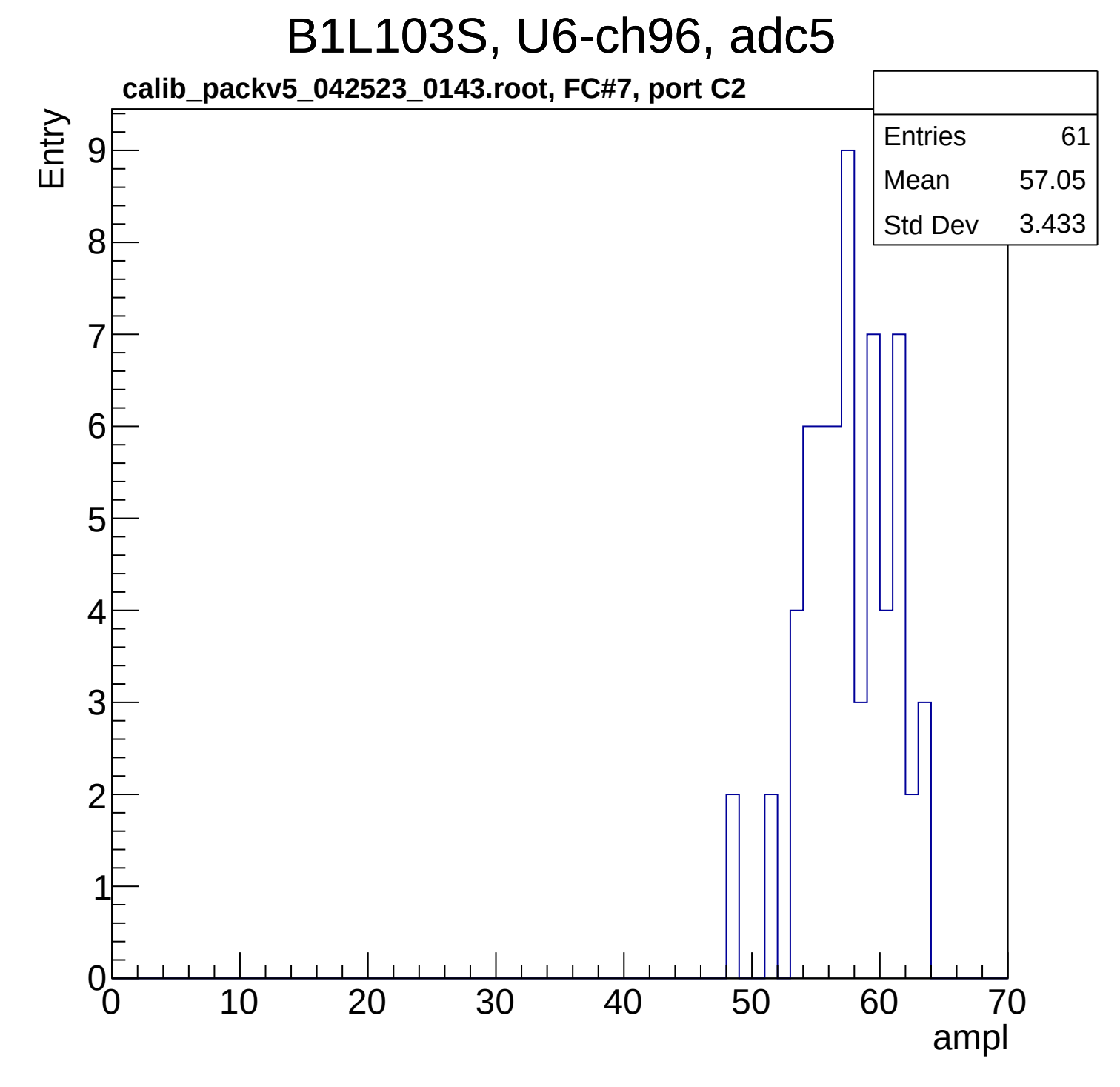
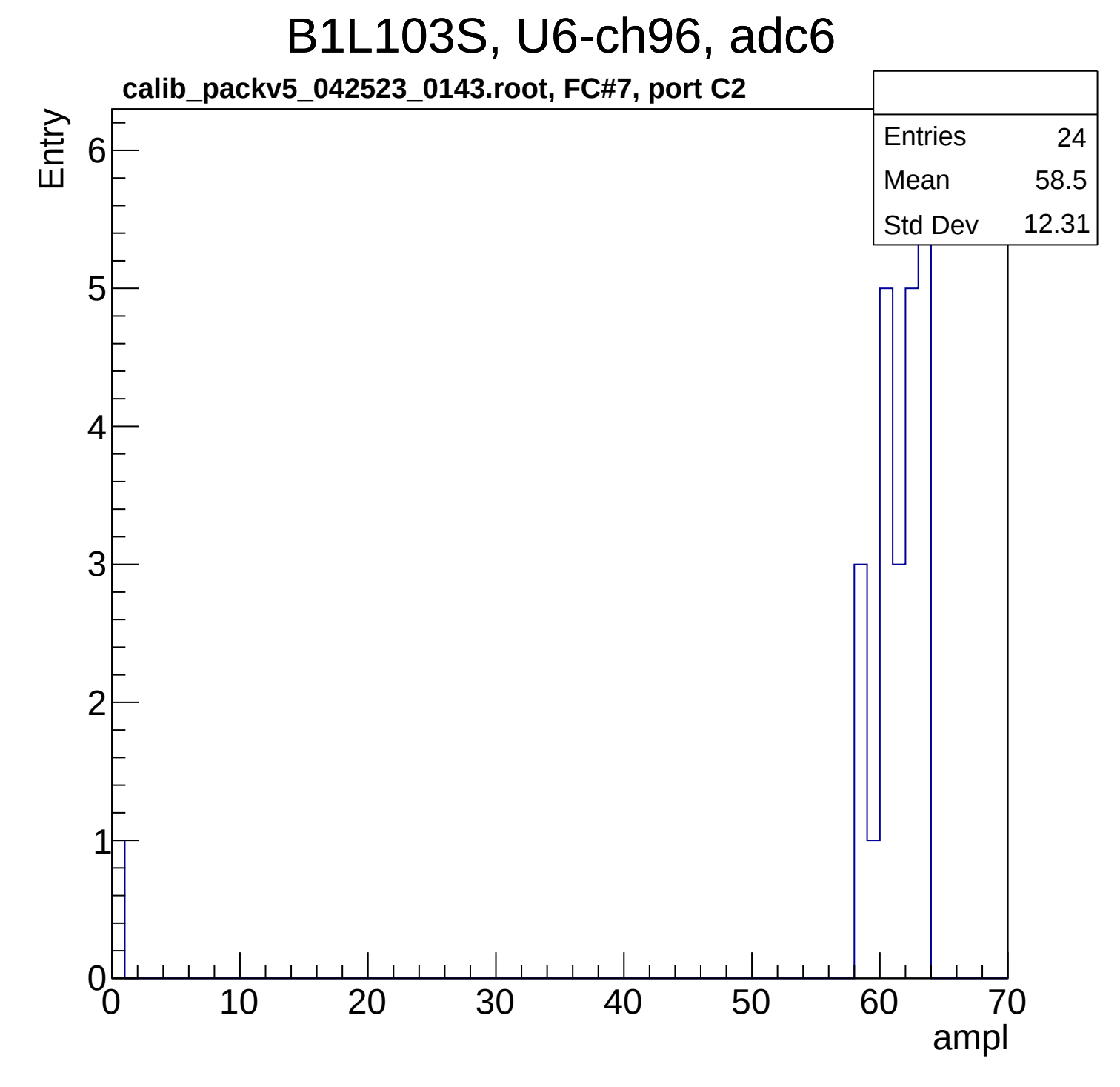

B1L103S, U6-ch96, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 57.05
Std Dev 3.433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch96, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 58.5
Std Dev 12.31
Entry
0
1
2
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4
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6
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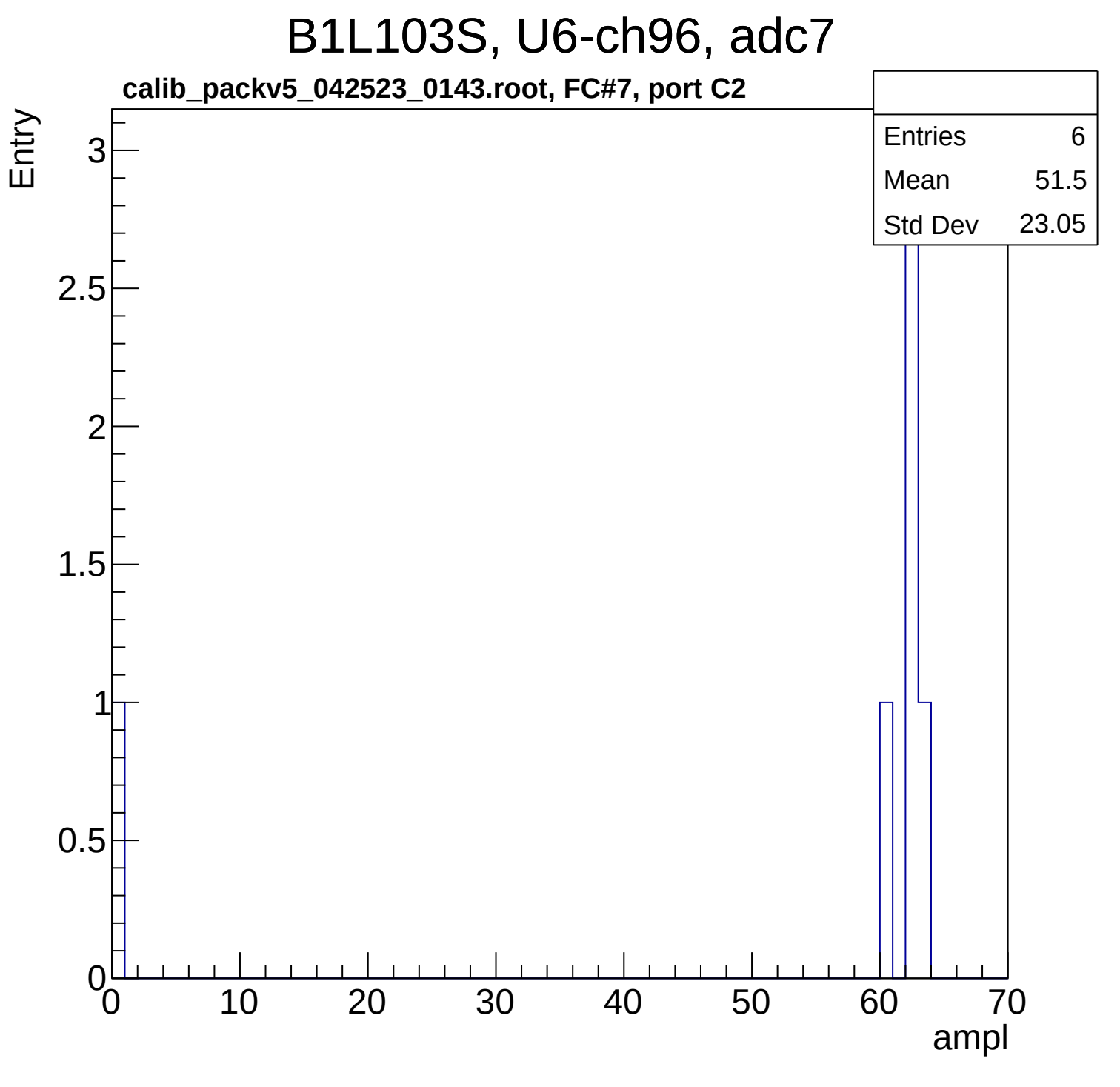
ampl


B1L103S, U6-ch96, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.5
Std Dev 23.05
Entry
ampl
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0.5
1
1.5
2
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60
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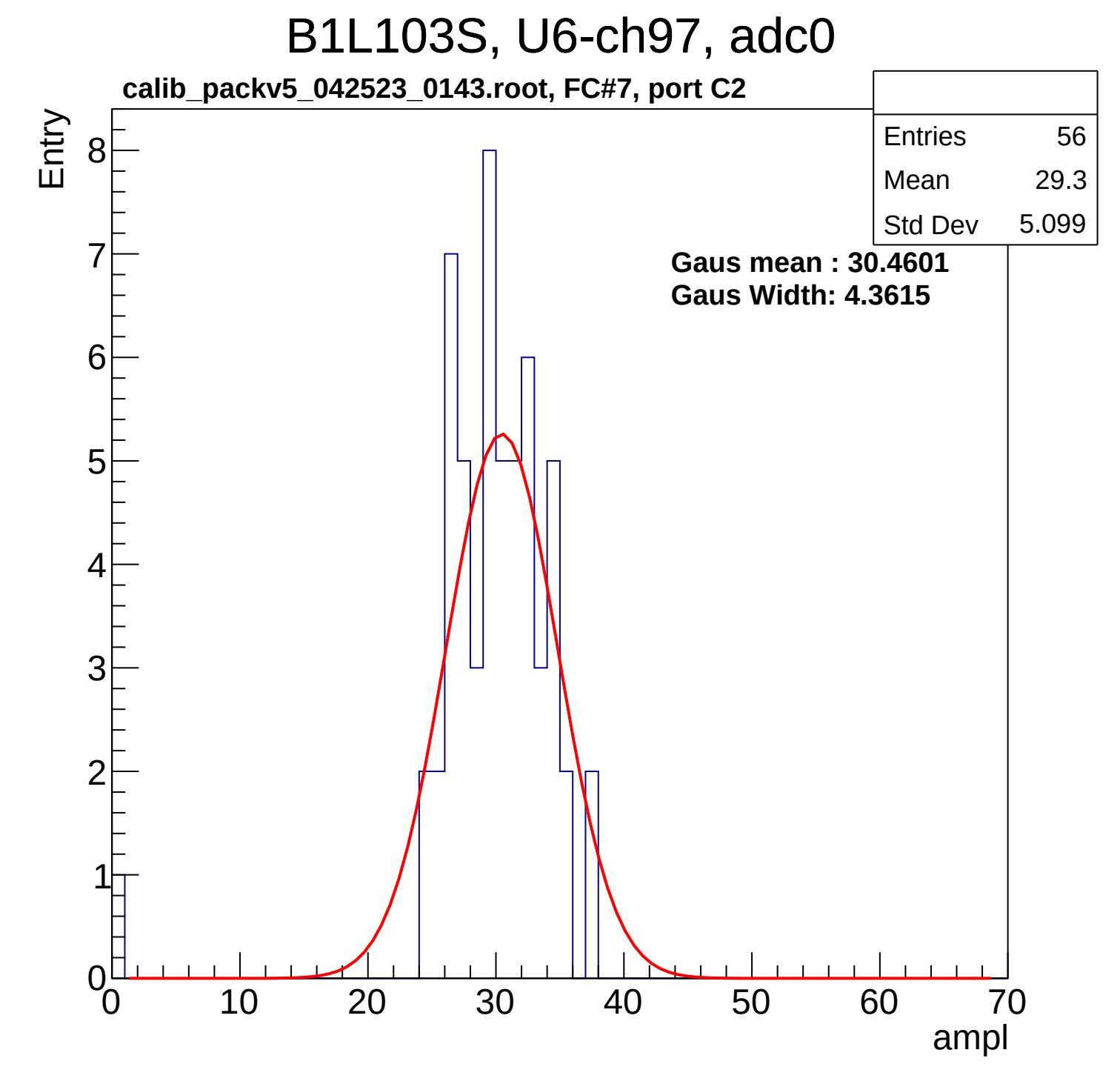
B1L103S, U6-ch97, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 29.3
Std Dev 5.099
Gaus mean : 30.4601
Gaus Width: 4.3615
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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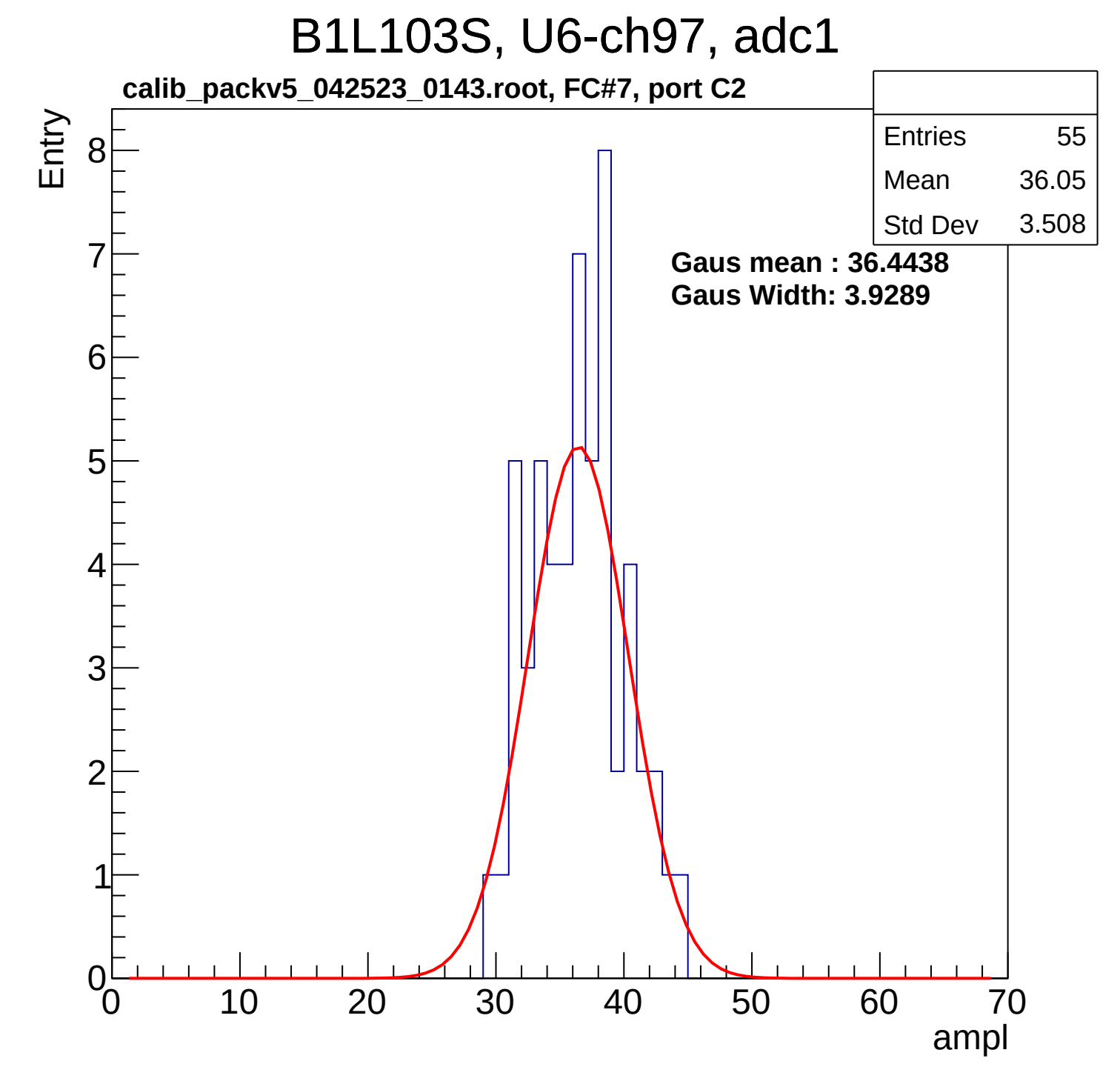
B1L103S, U6-ch97, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 36.05
Std Dev 3.508
Gaus mean : 36.4438
Gaus Width: 3.9289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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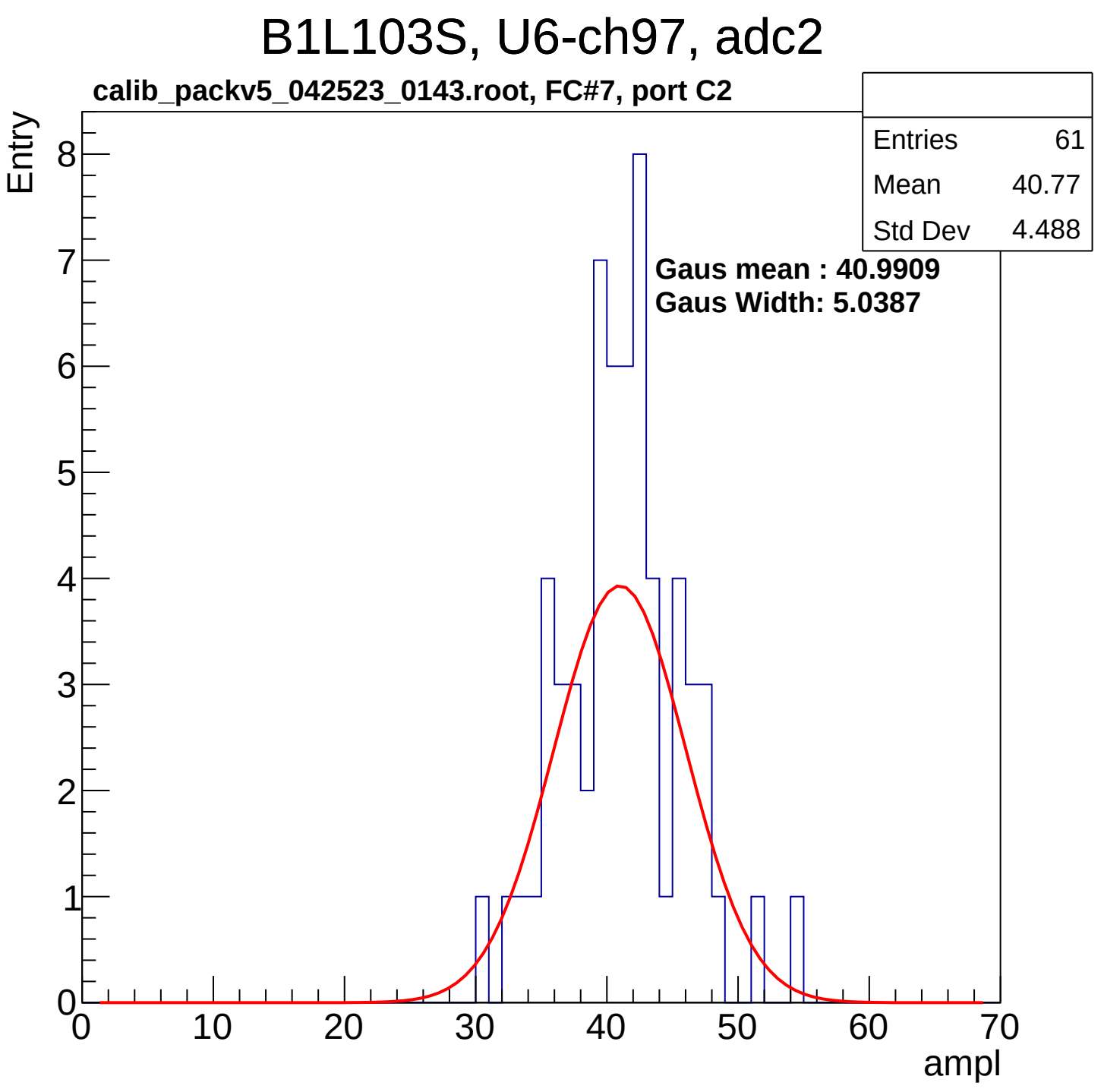
B1L103S, U6-ch97, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 40.77
Std Dev 4.488
Gaus mean : 40.9909
Gaus Width: 5.0387
Entry
ampl
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1
2
3
4
5
6
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0
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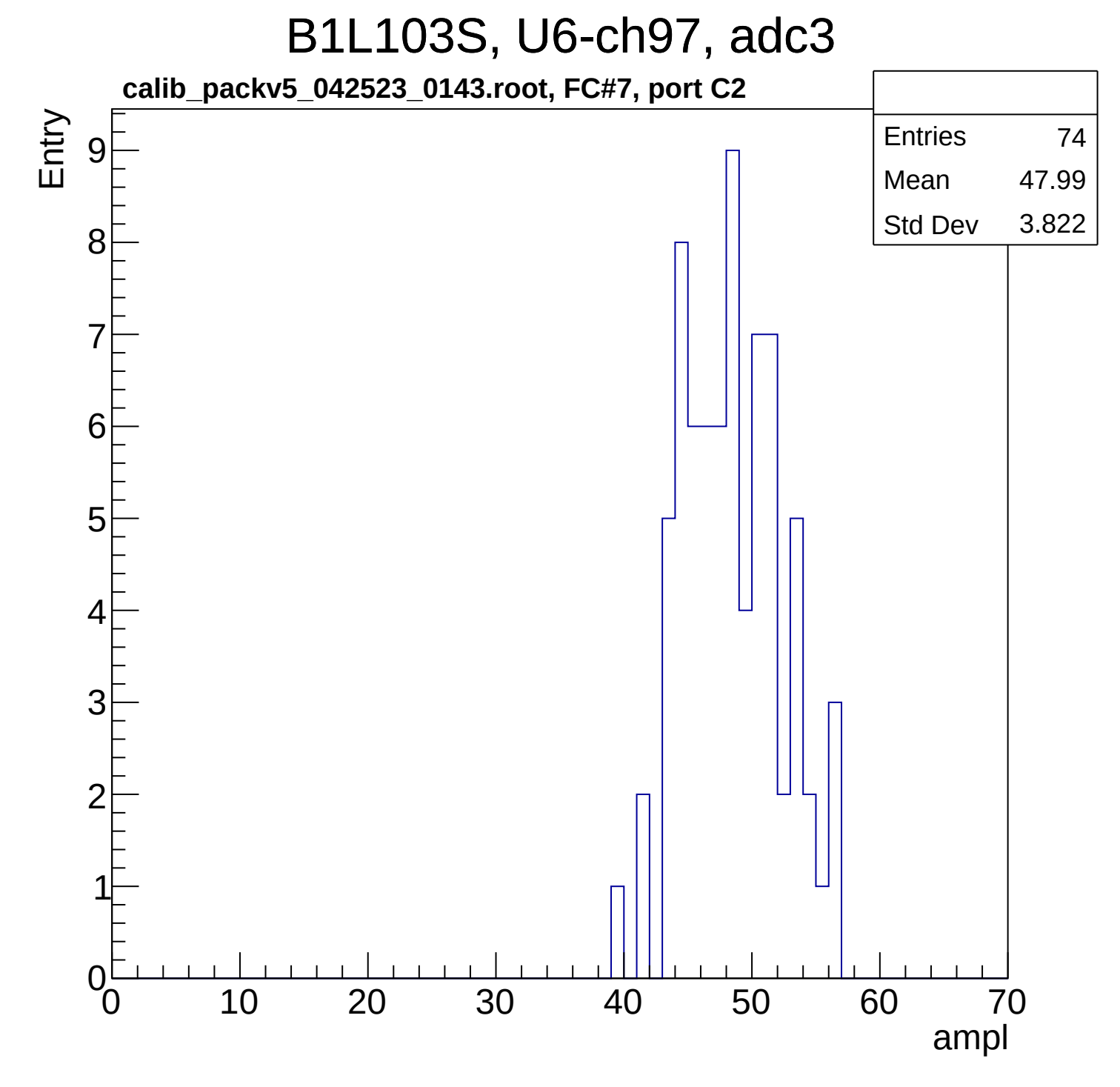
B1L103S, U6-ch97, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 47.99
Std Dev 3.822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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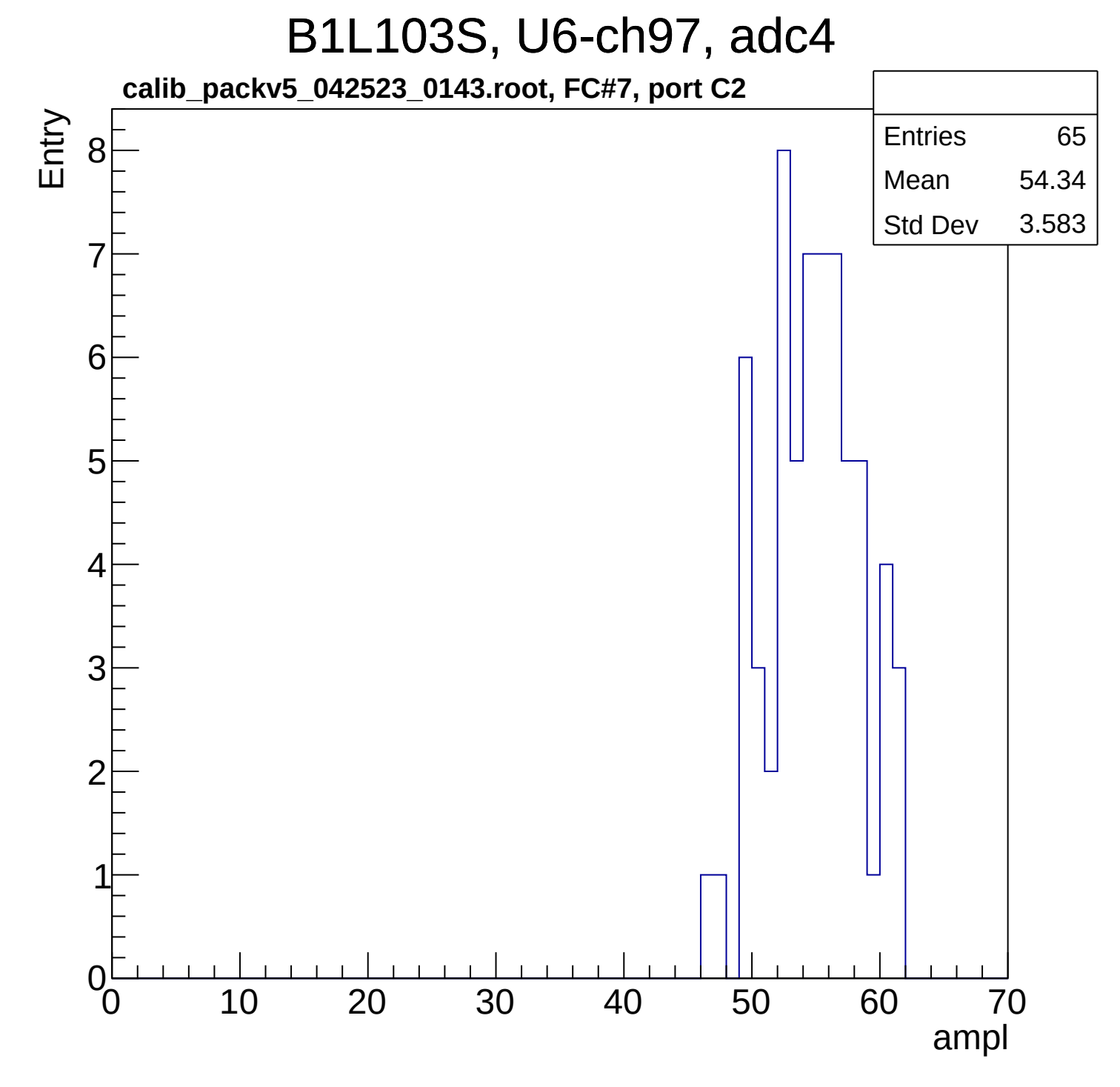
B1L103S, U6-ch97, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 54.34
Std Dev 3.583
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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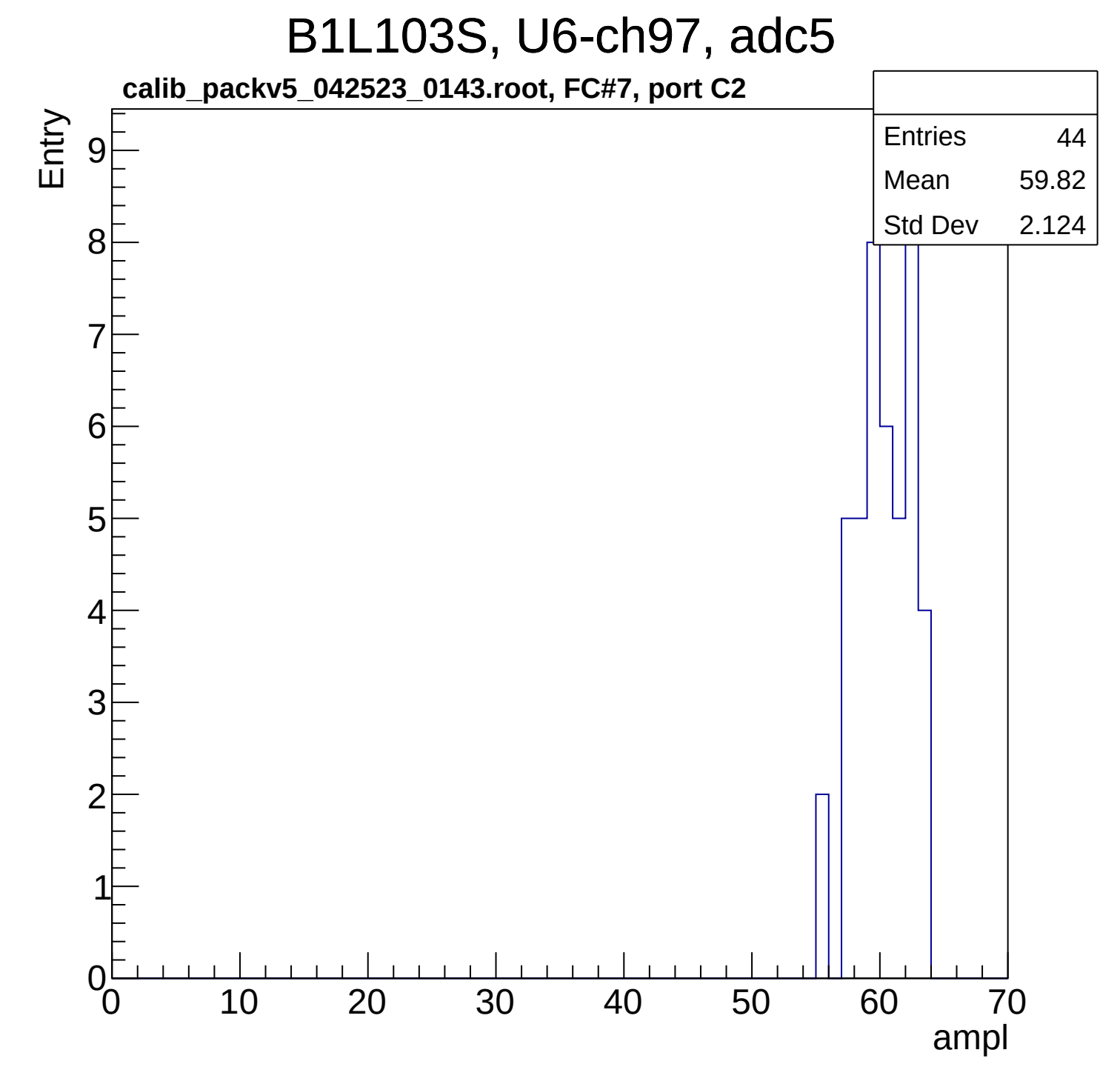
B1L103S, U6-ch97, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.82
Std Dev 2.124
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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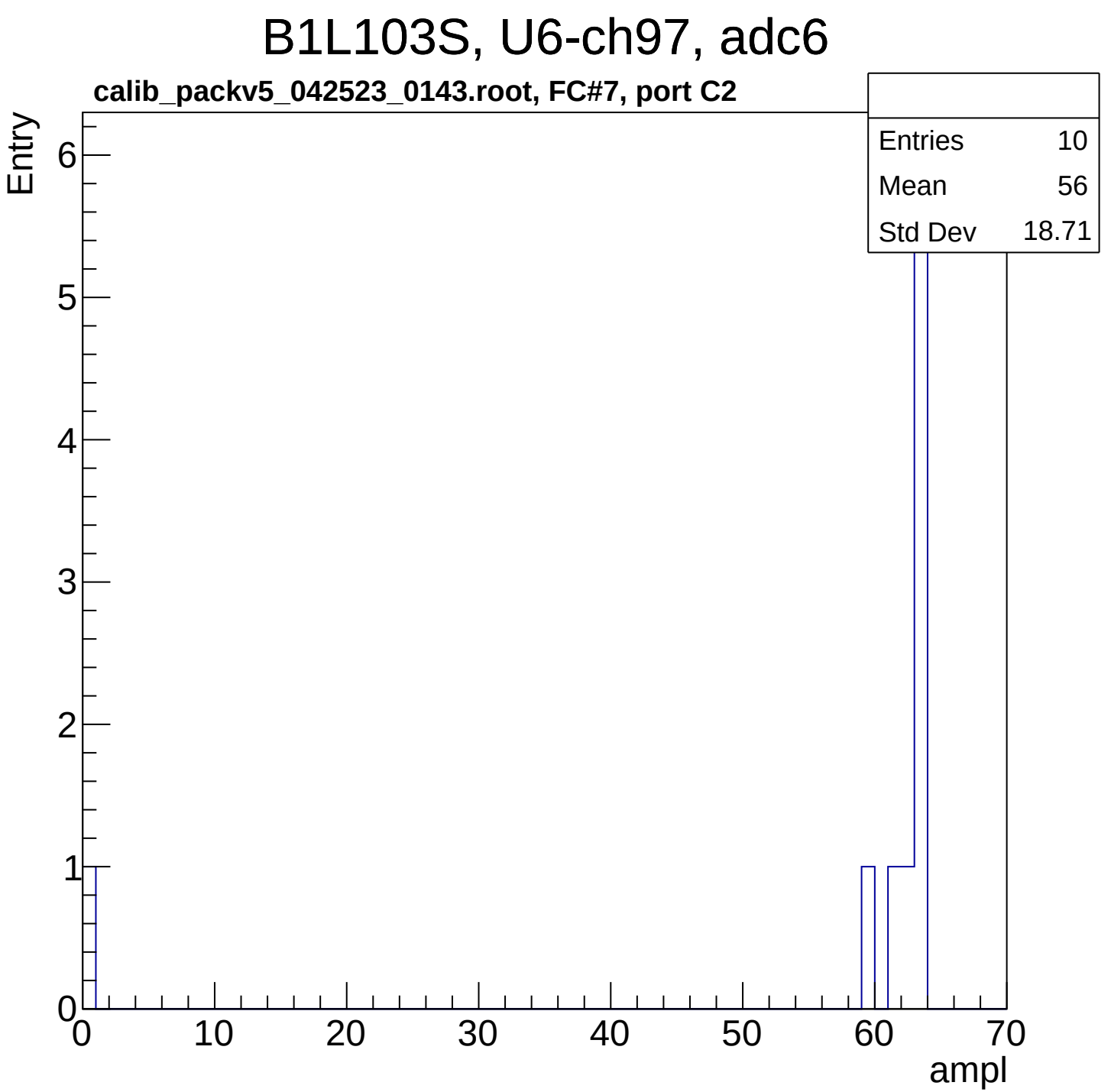
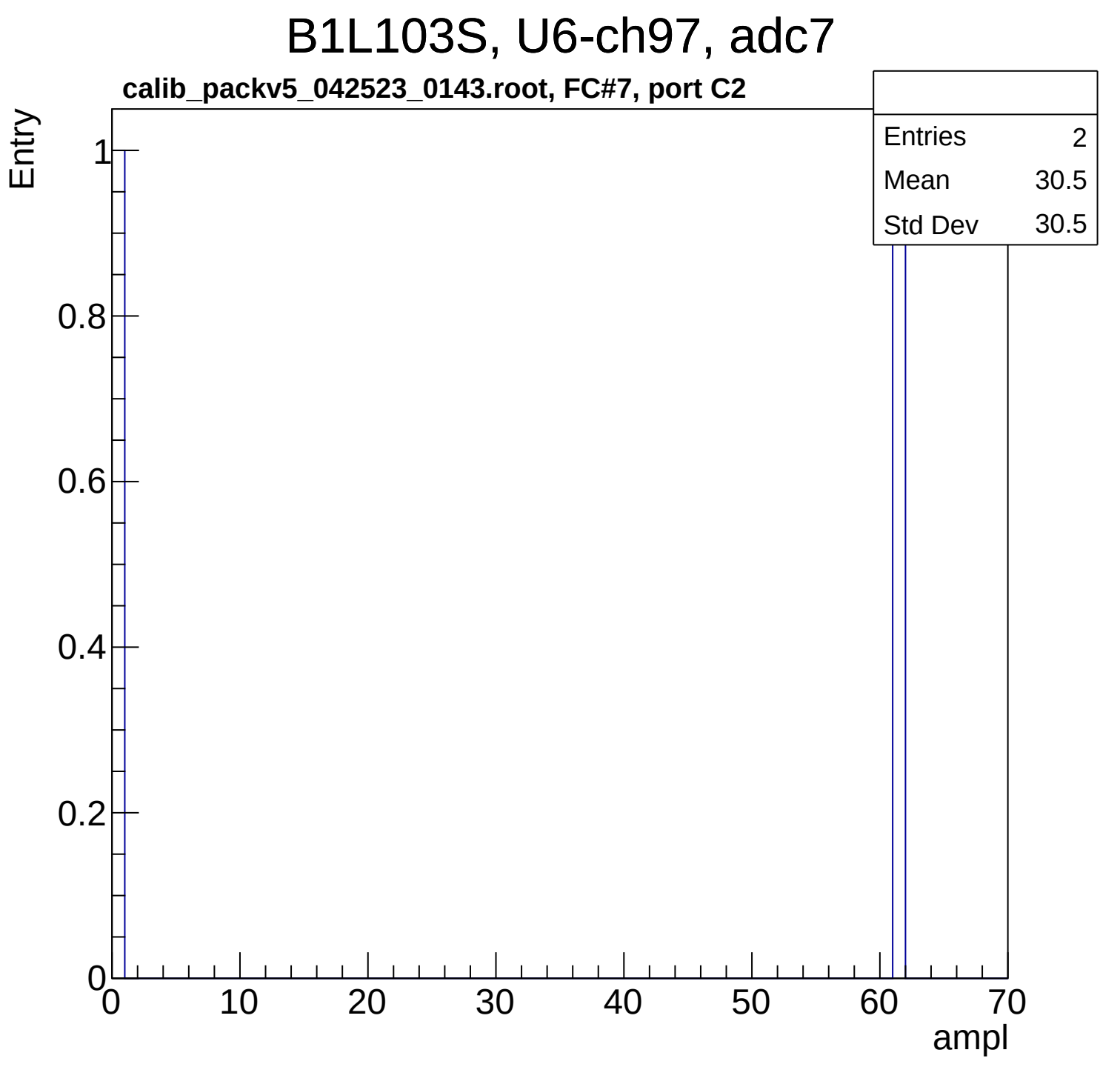
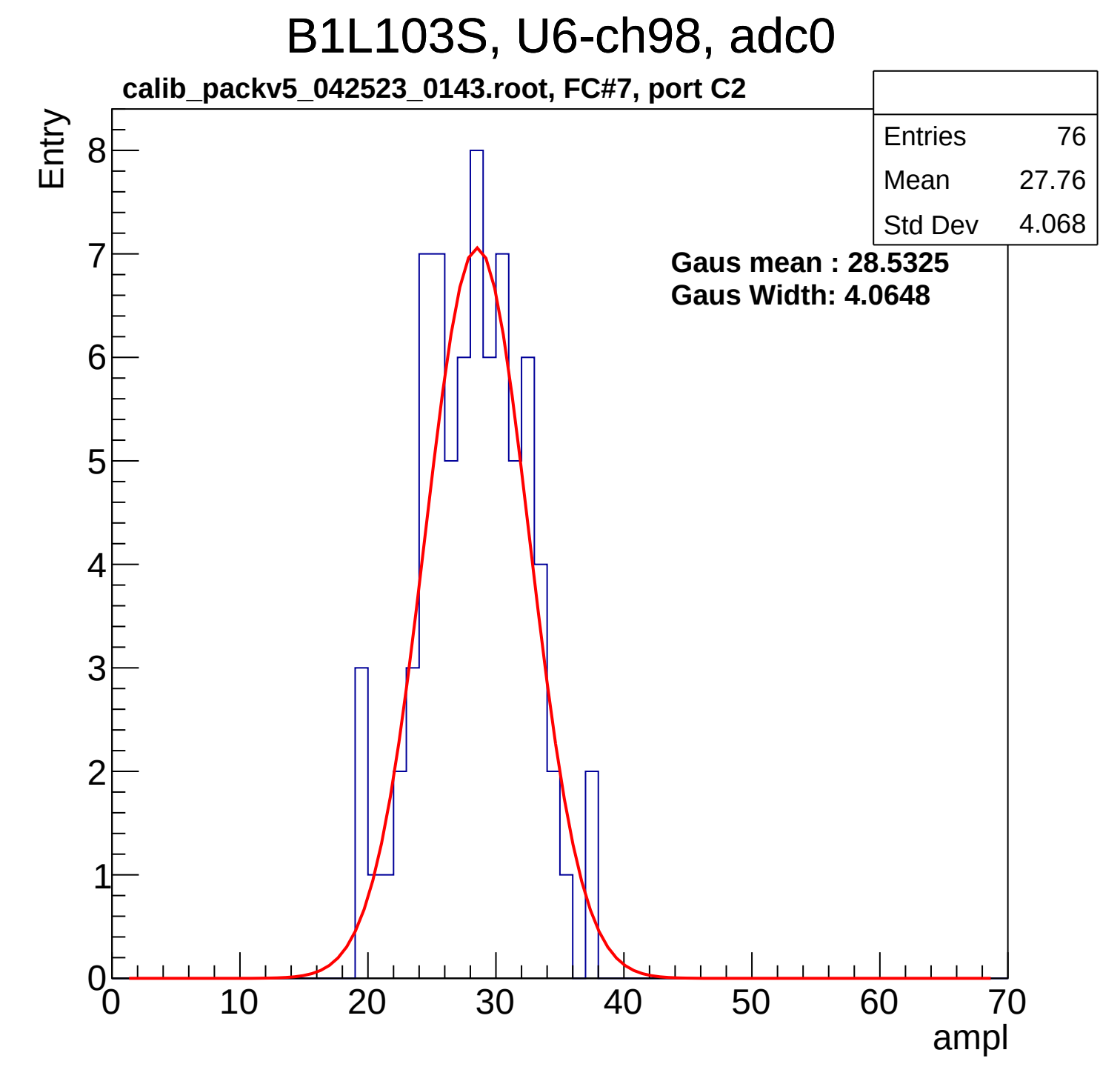
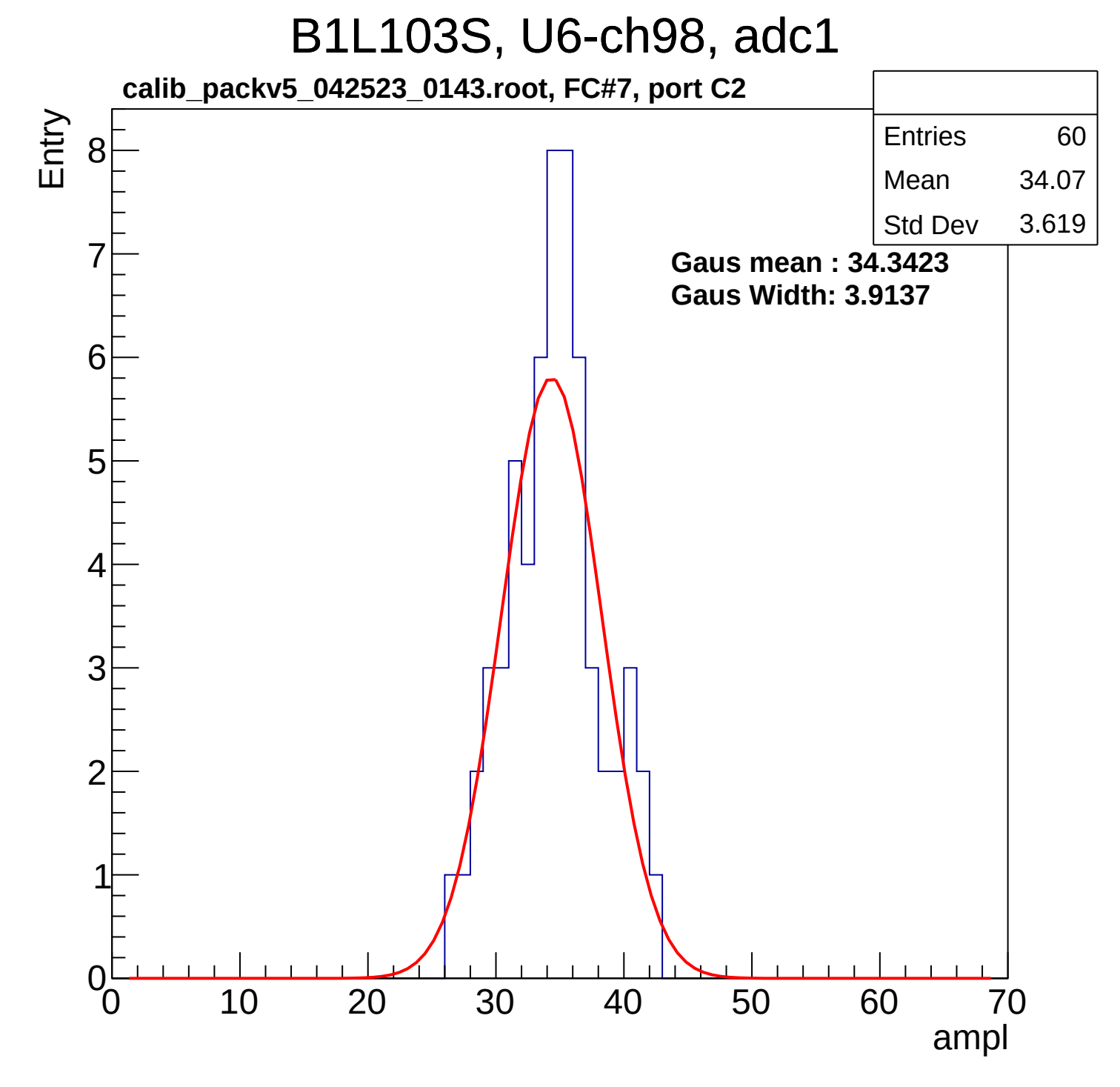

B1L103S, U6-ch97, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56
Std Dev 18.71
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch97, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch98, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 27.76
Std Dev 4.068
Gaus mean : 28.5325
Gaus Width: 4.0648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch98, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 34.07
Std Dev 3.619
Gaus mean : 34.3423
Gaus Width: 3.9137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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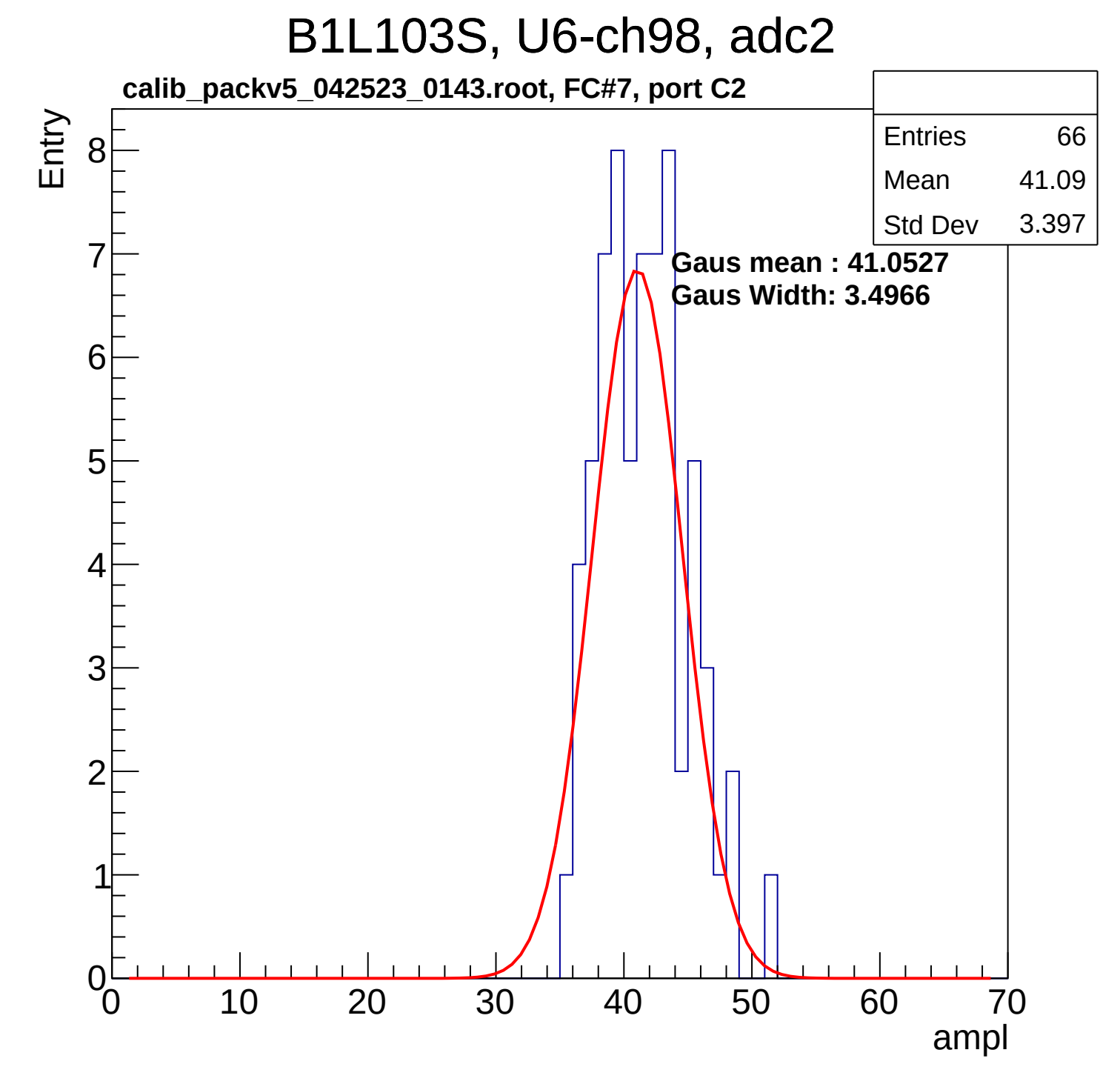
B1L103S, U6-ch98, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 41.09
Std Dev 3.397
Gaus mean : 41.0527
Gaus Width: 3.4966
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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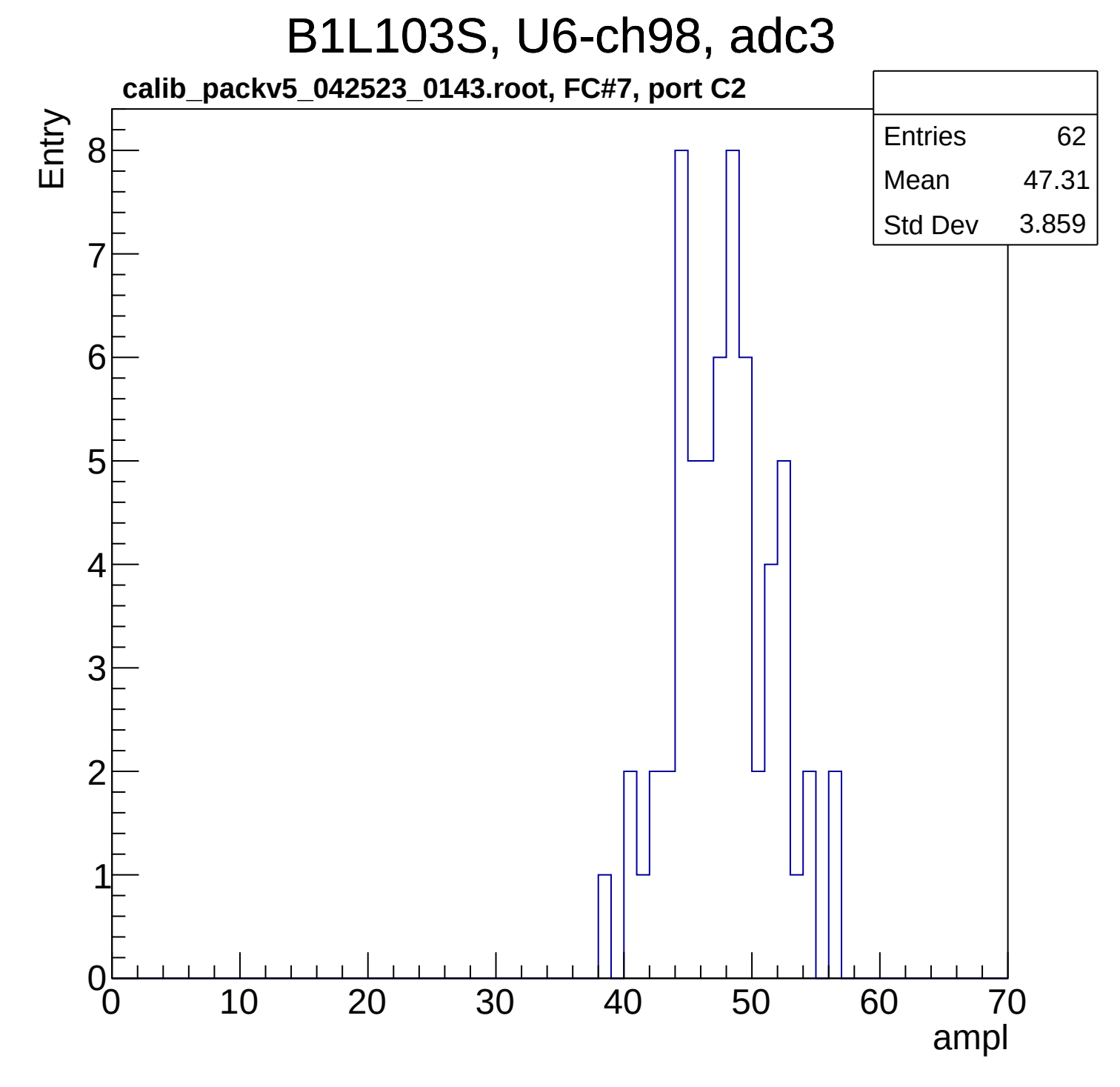
B1L103S, U6-ch98, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 47.31
Std Dev 3.859
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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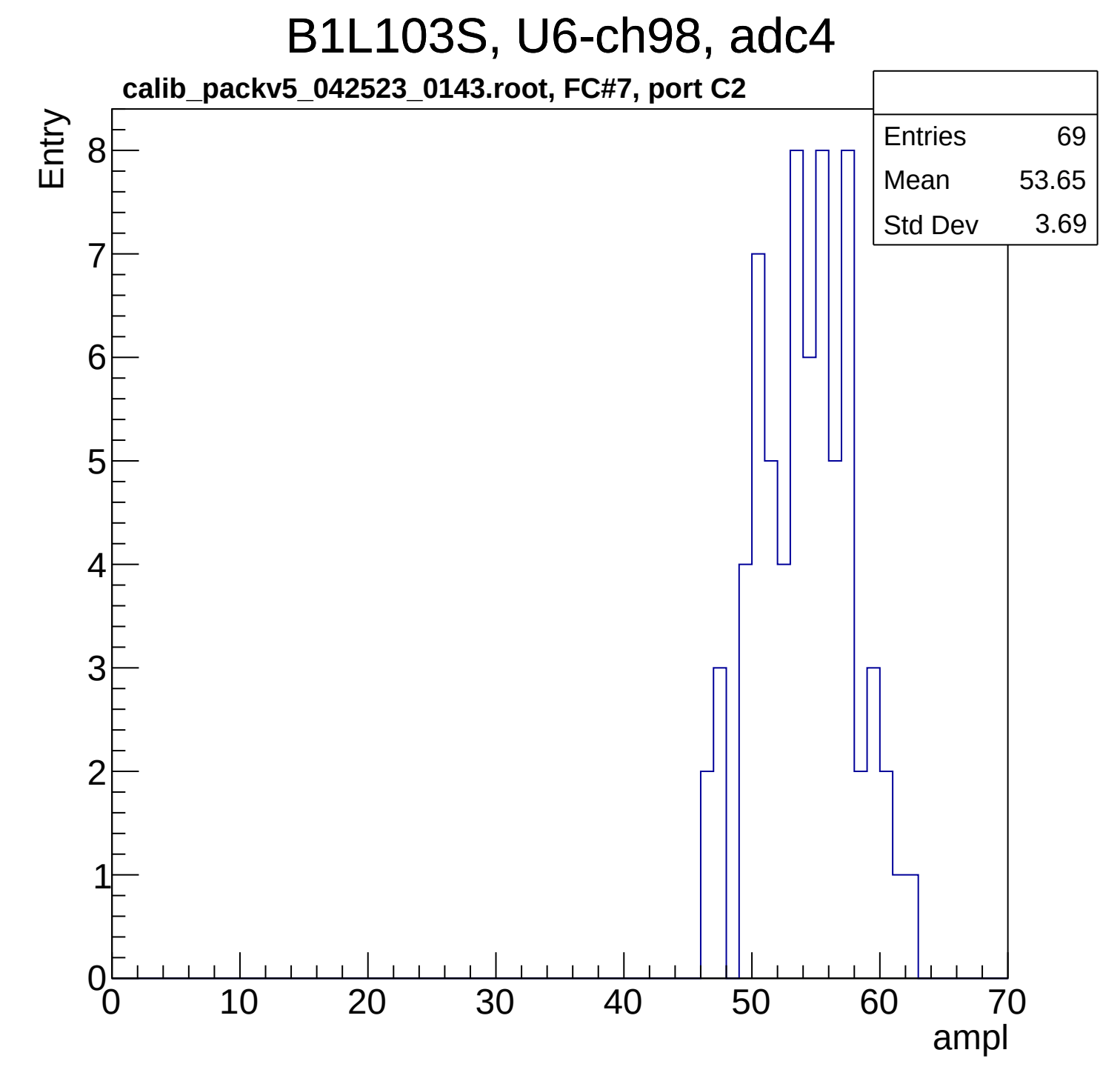
B1L103S, U6-ch98, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 53.65
Std Dev 3.69
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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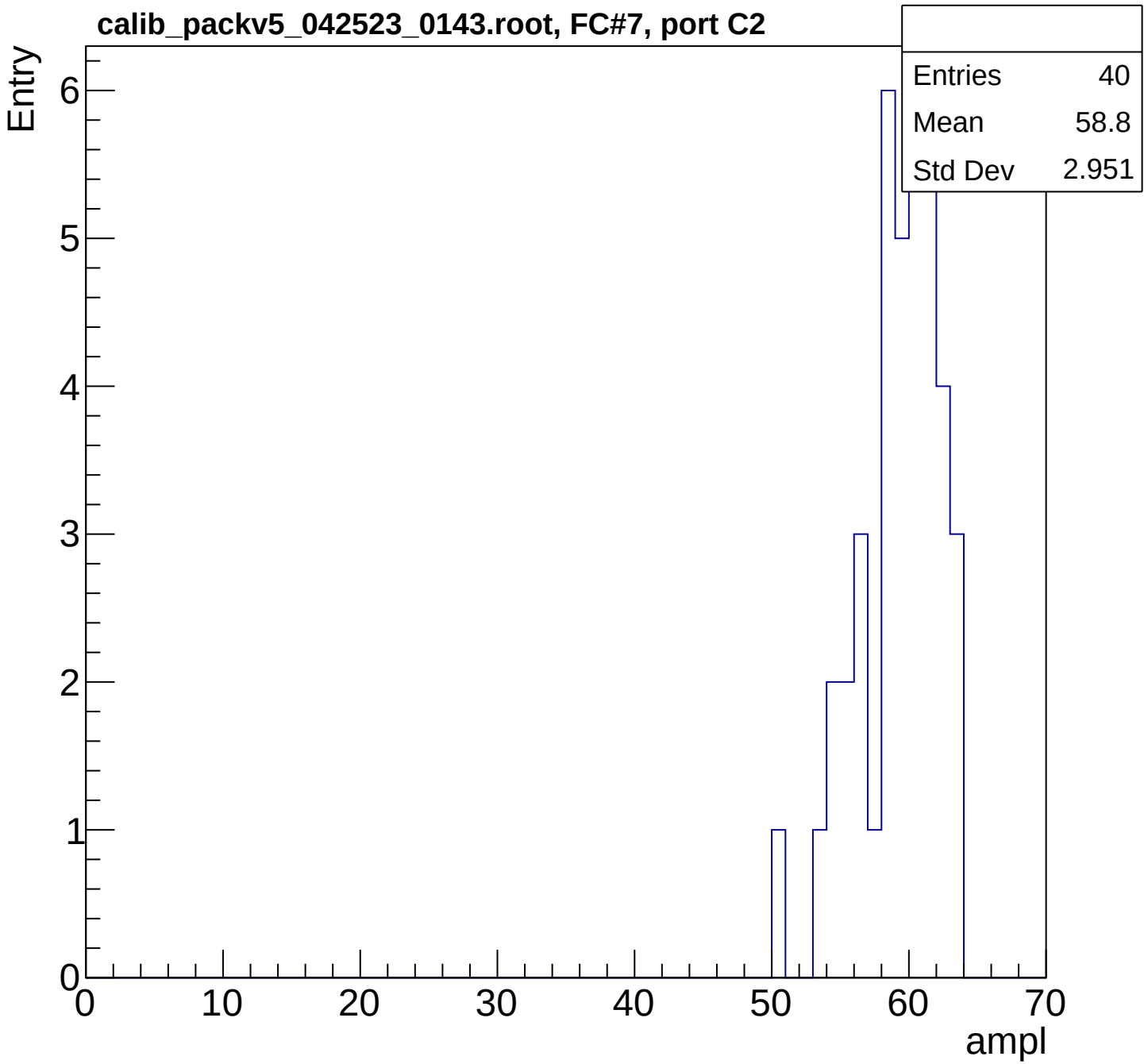
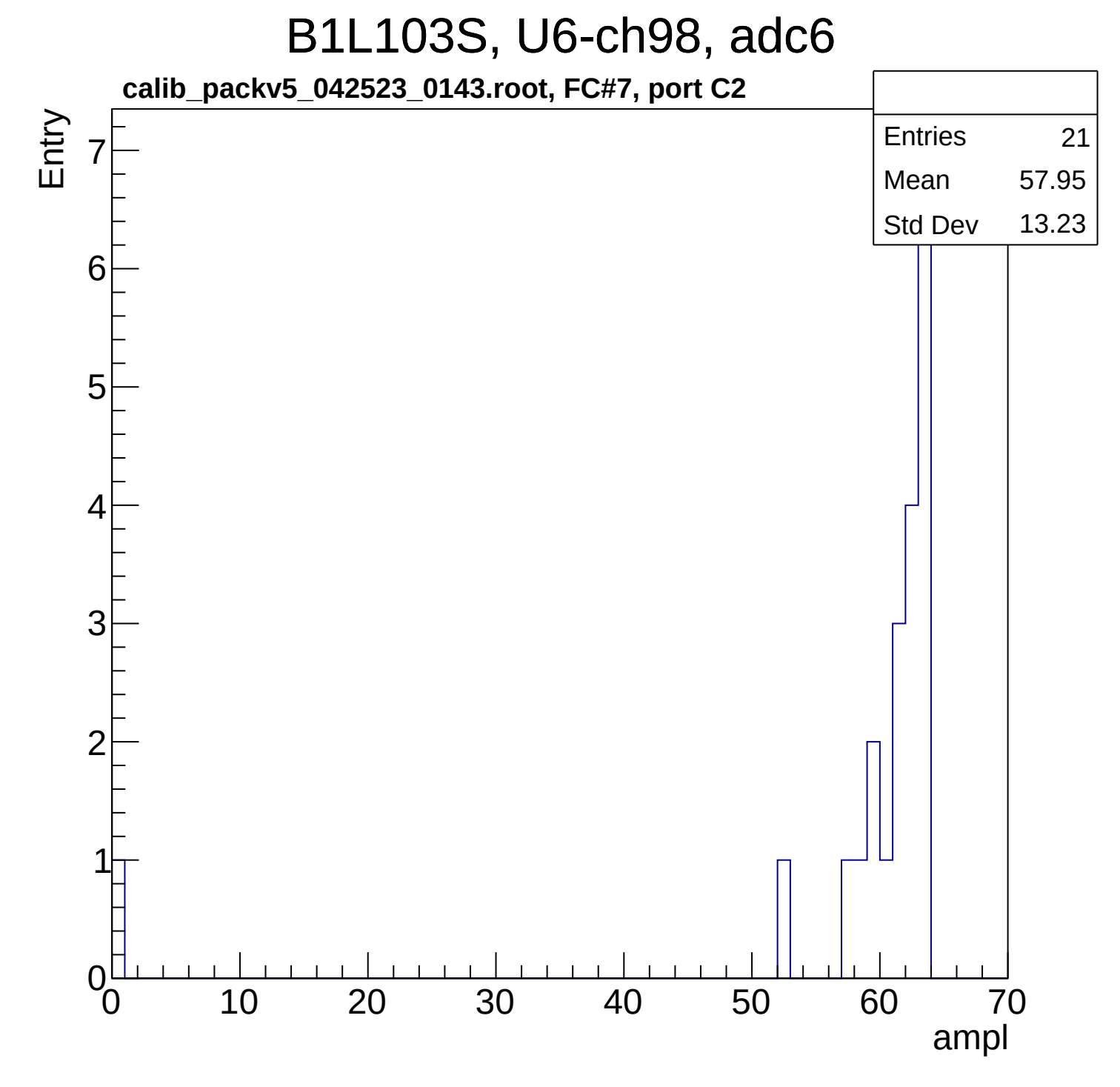
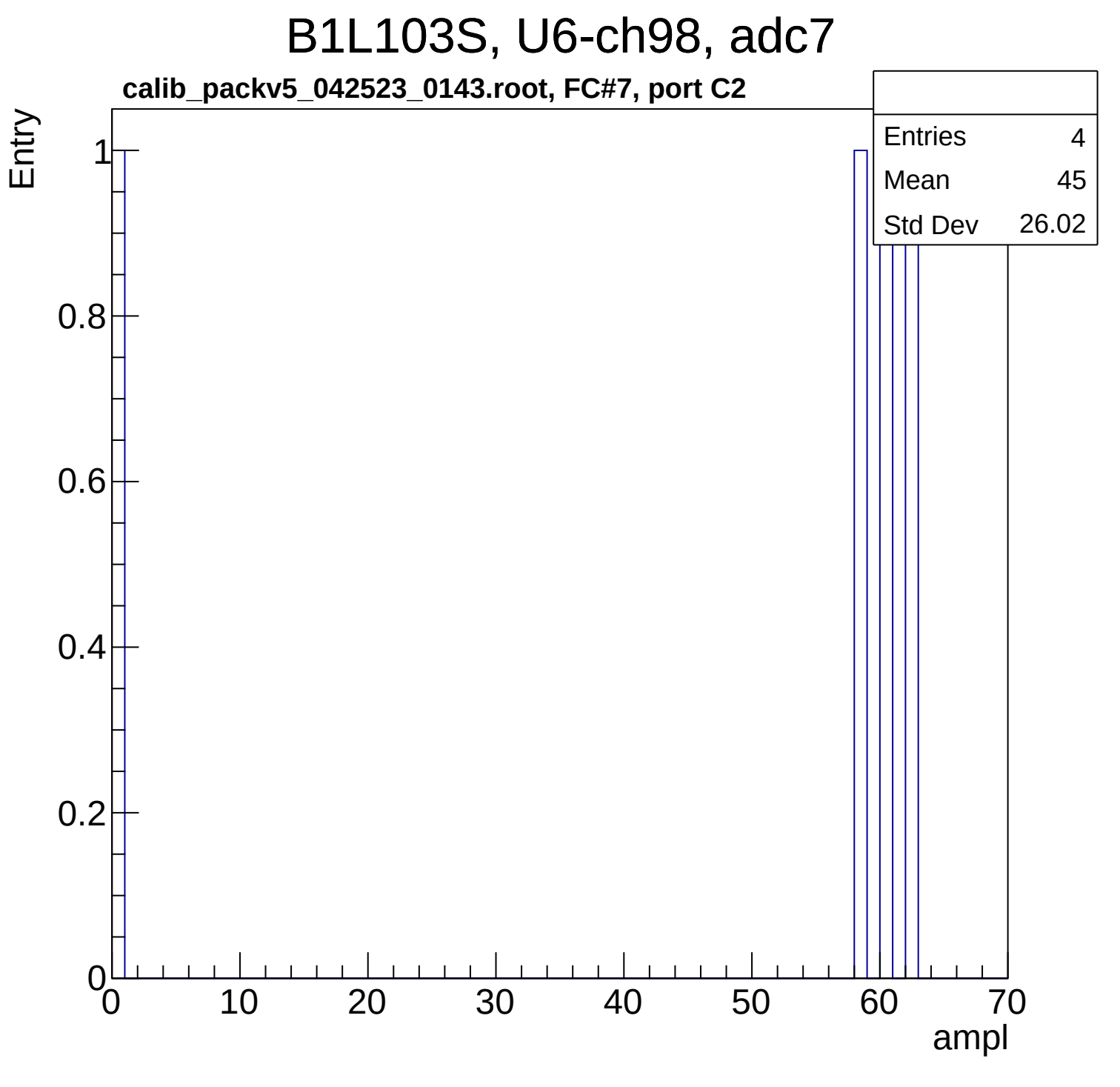

B1L103S, U6-ch98, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.8
Std Dev 2.951
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch98, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 57.95
Std Dev 13.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch98, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 45
Std Dev 26.02
Entry
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0.6
0.4
0.2
0
0
10
20
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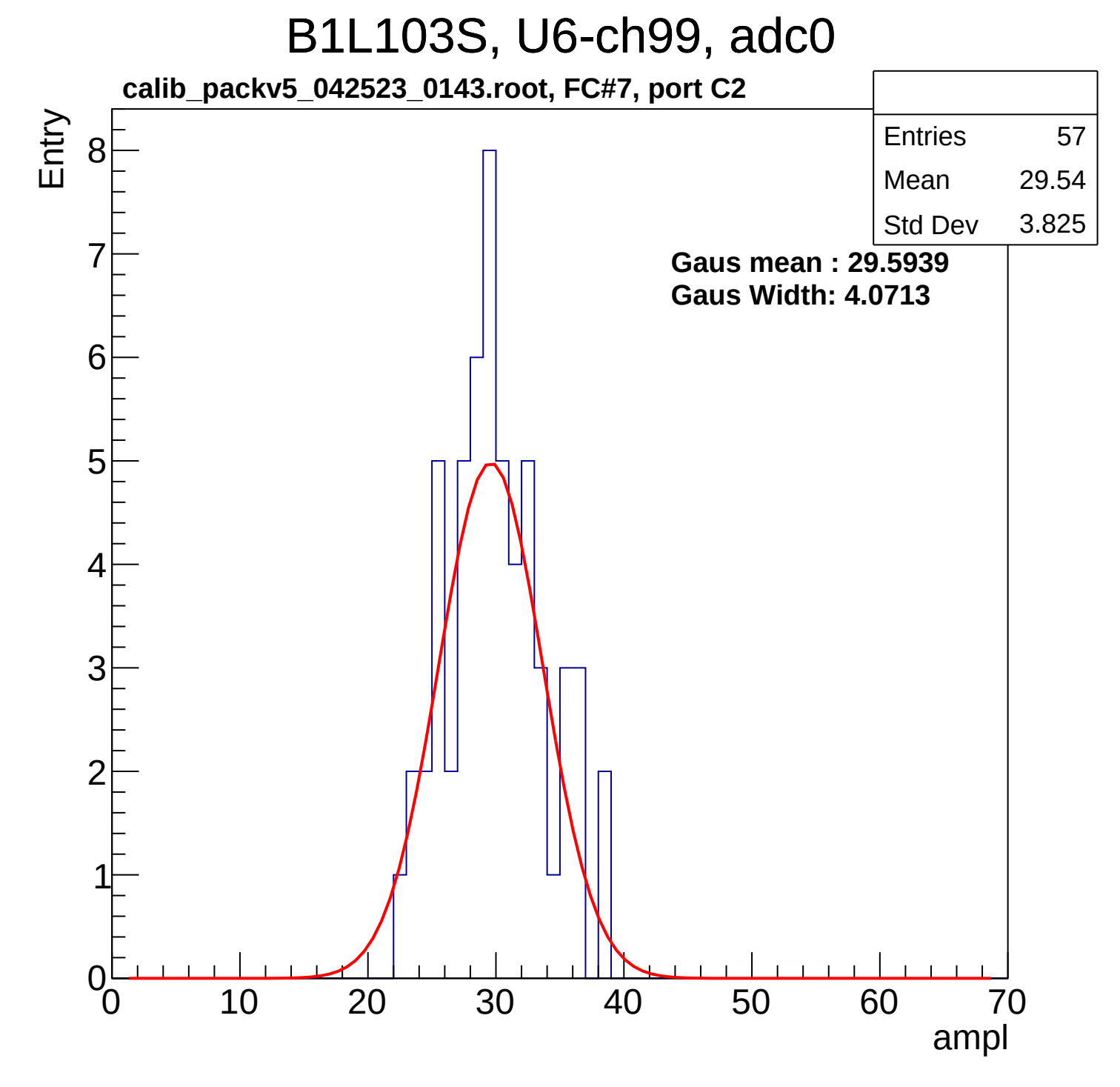
40
50
60
70
ampl


B1L103S, U6-ch99, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 29.54
Std Dev 3.825
Gaus mean : 29.5939
Gaus Width: 4.0713
Entry
ampl
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1
2
3
4
5
6
7
8
0
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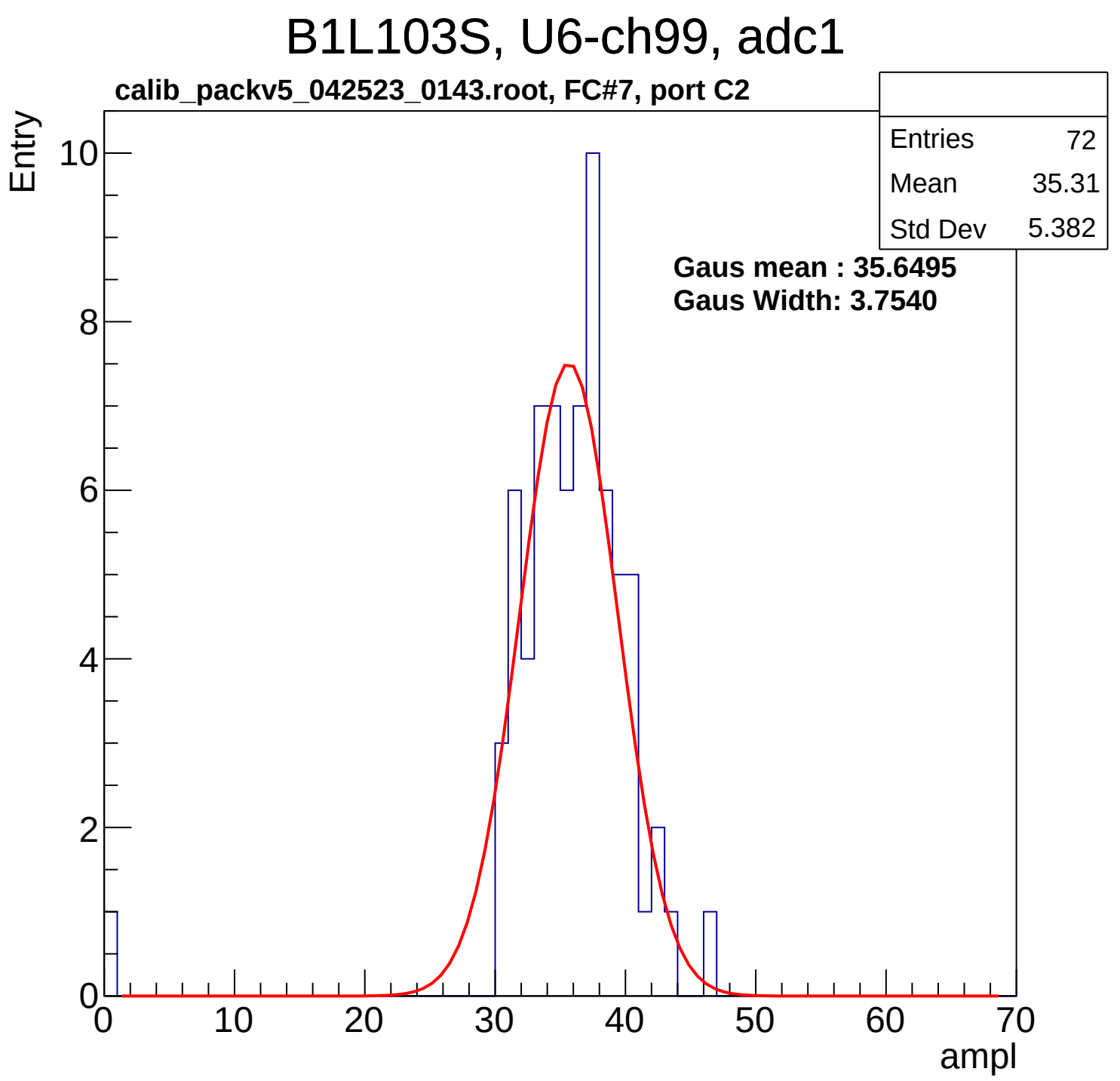
20
30
40
50
60
70


B1L103S, U6-ch99, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.31
Std Dev 5.382
Gaus mean : 35.6495
Gaus Width: 3.7540
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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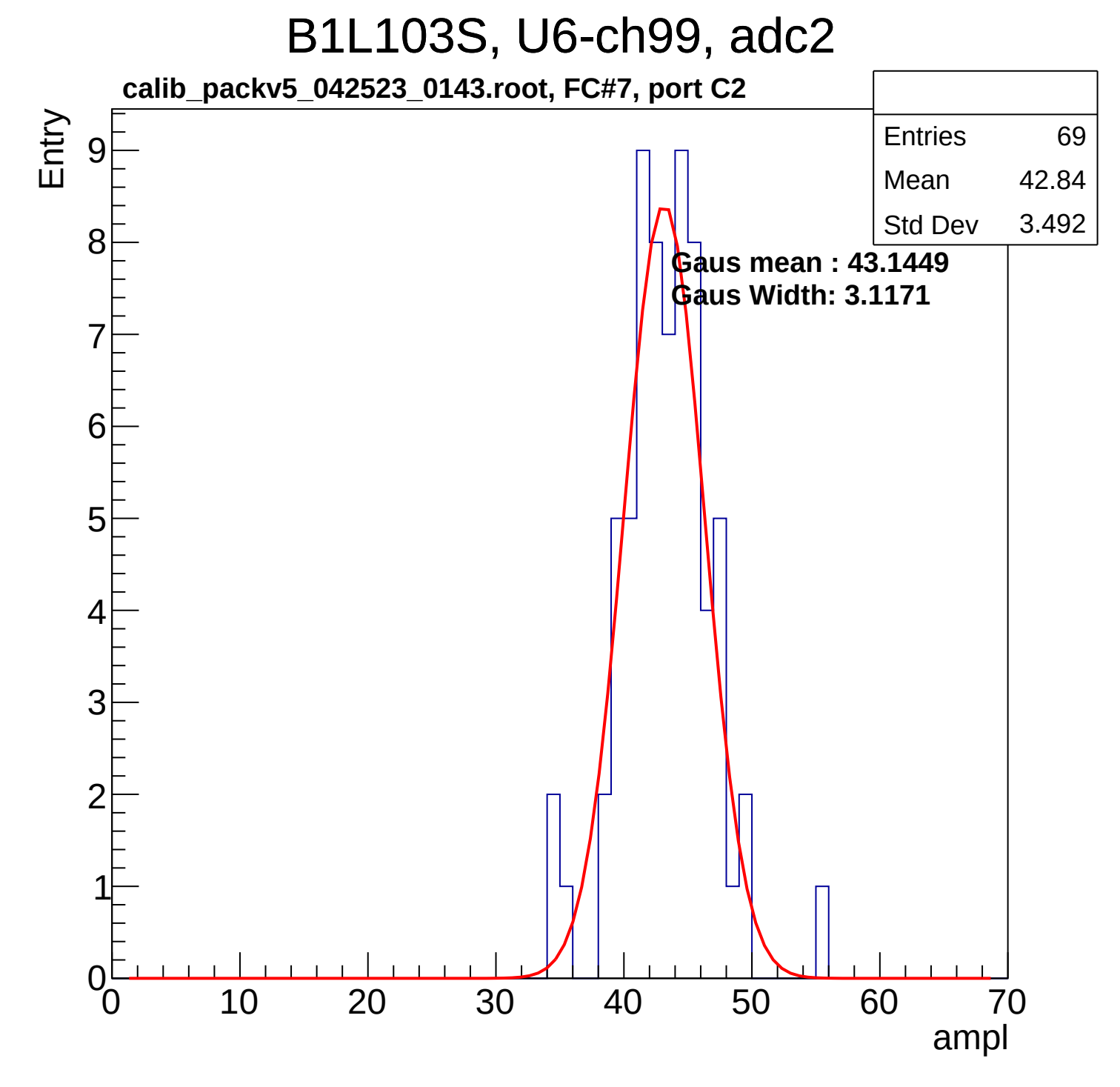
B1L103S, U6-ch99, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 42.84
Std Dev 3.492
Gaus mean : 43.1449
Gaus Width: 3.1171
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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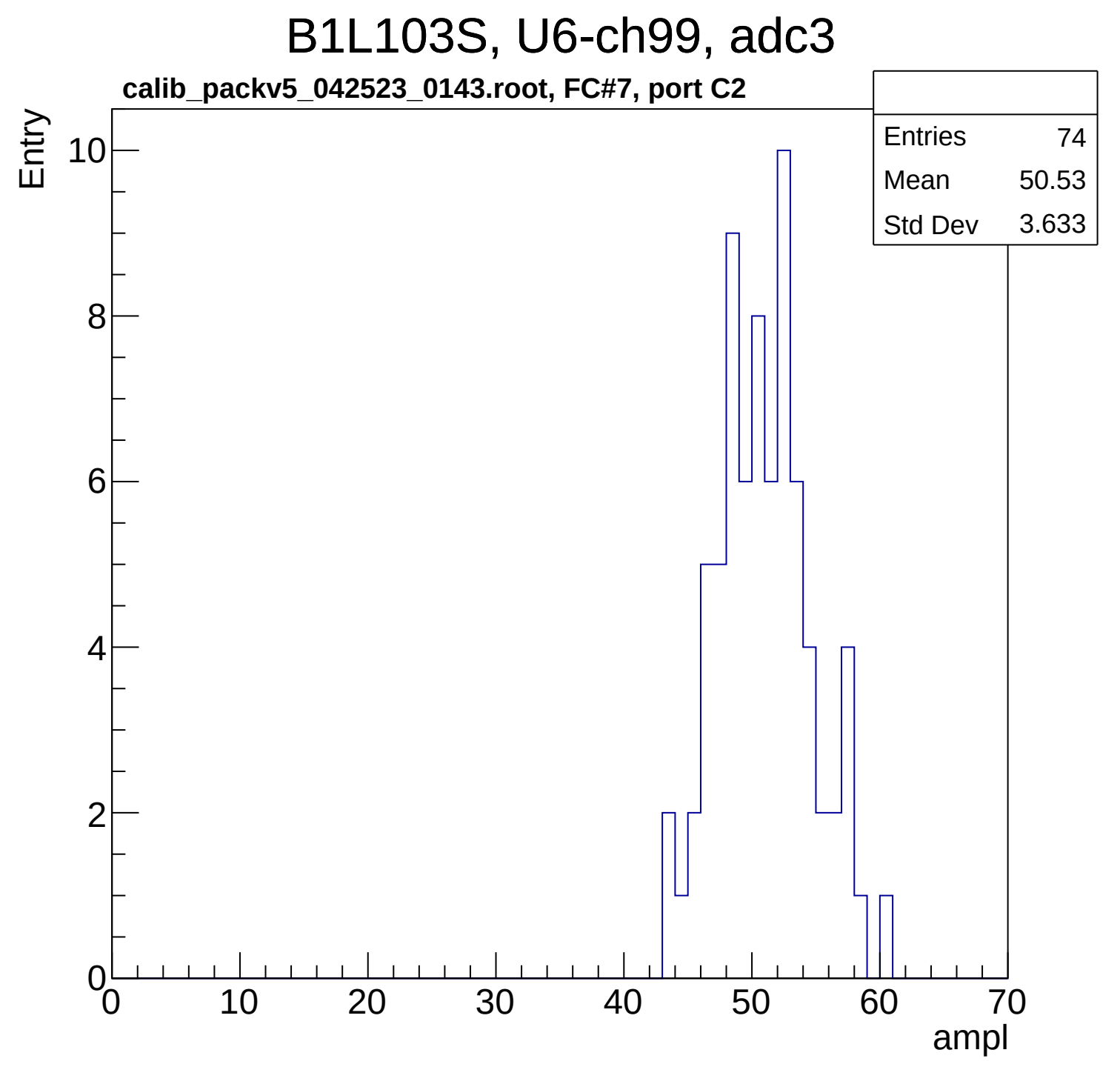
B1L103S, U6-ch99, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 50.53
Std Dev 3.633
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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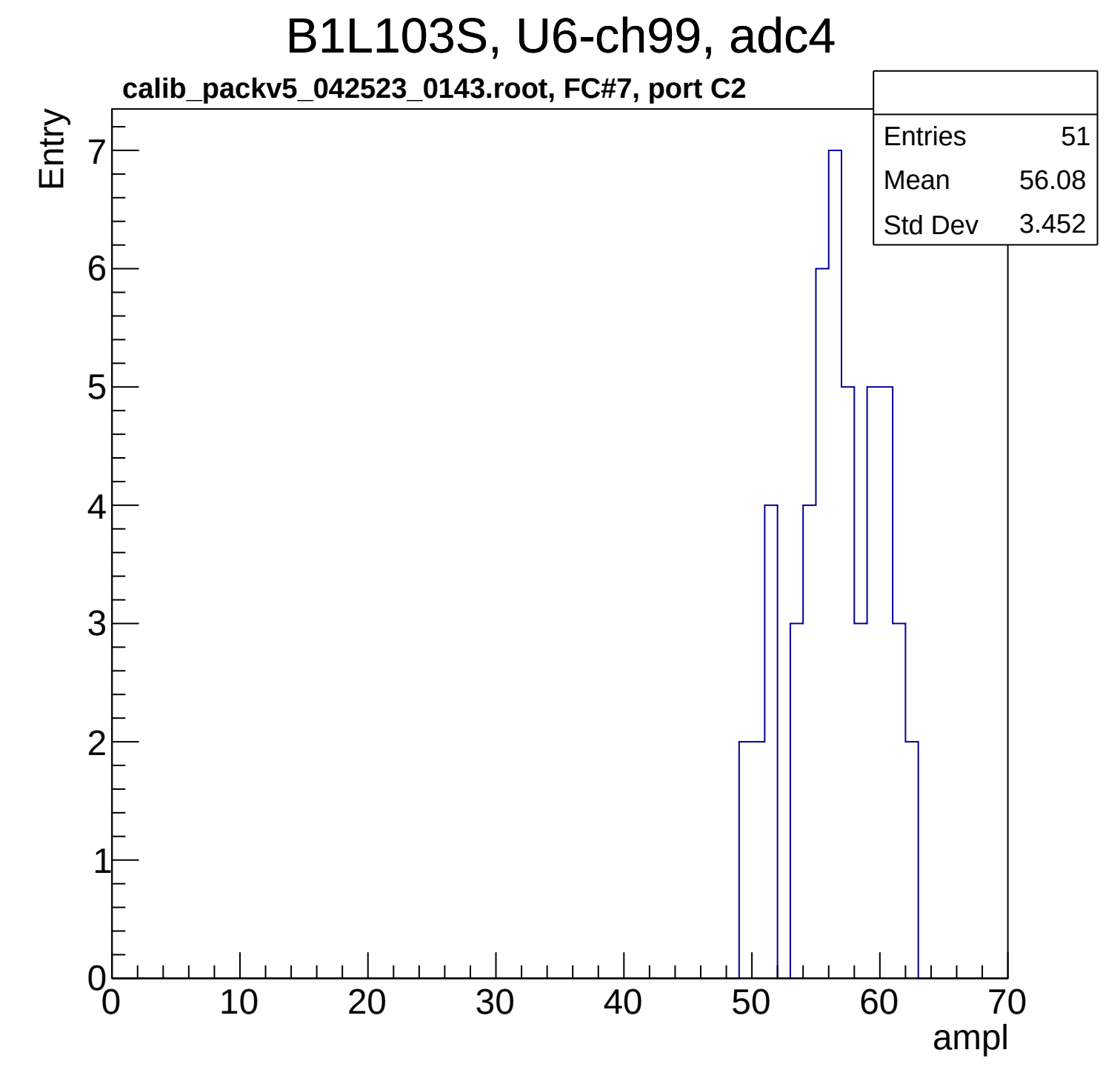
60
70


B1L103S, U6-ch99, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 56.08
Std Dev 3.452
Entry
ampl
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1
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3
4
5
6
7
0
10
20
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40
50
60
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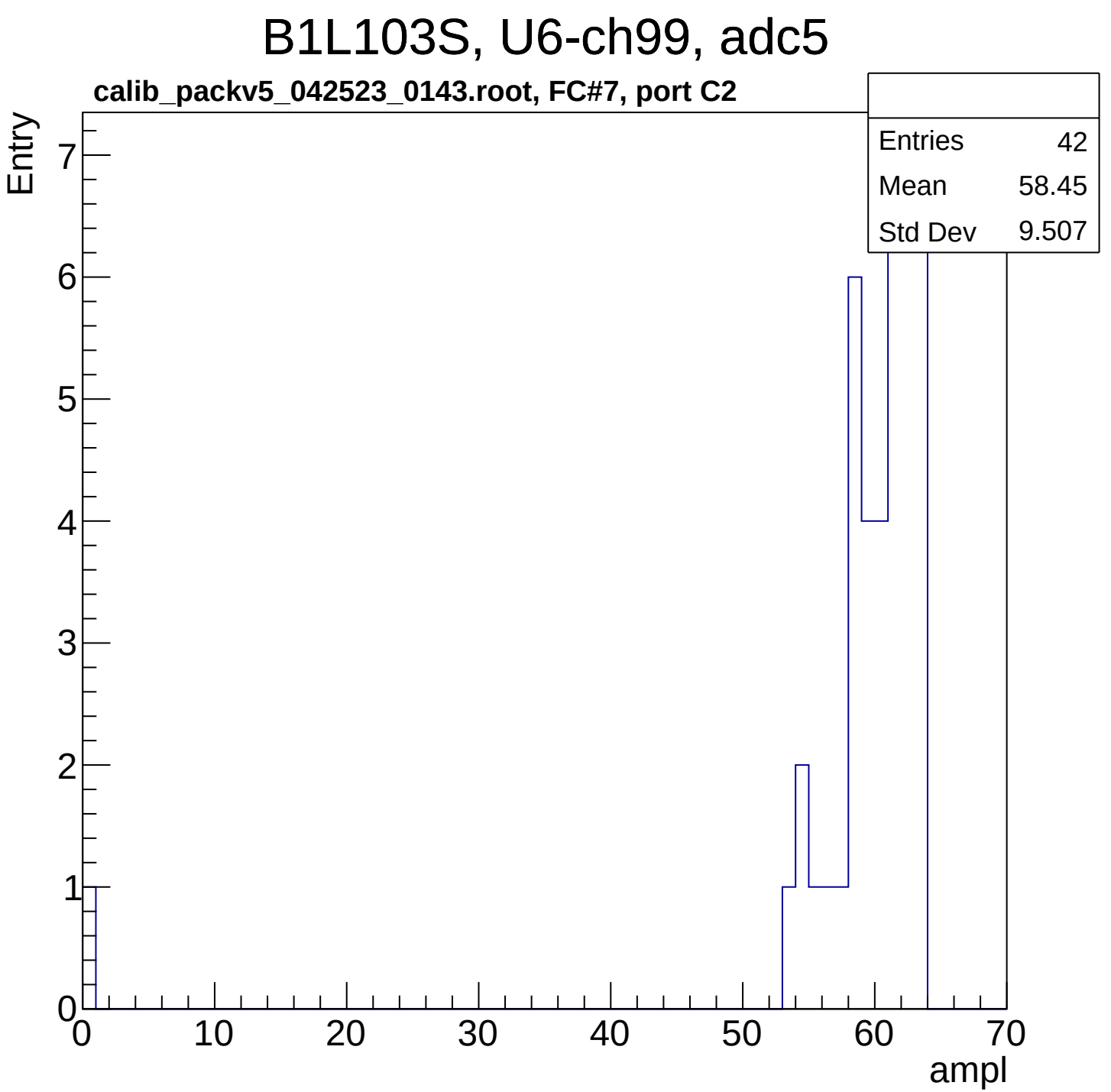
B1L103S, U6-ch99, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.45
Std Dev 9.507
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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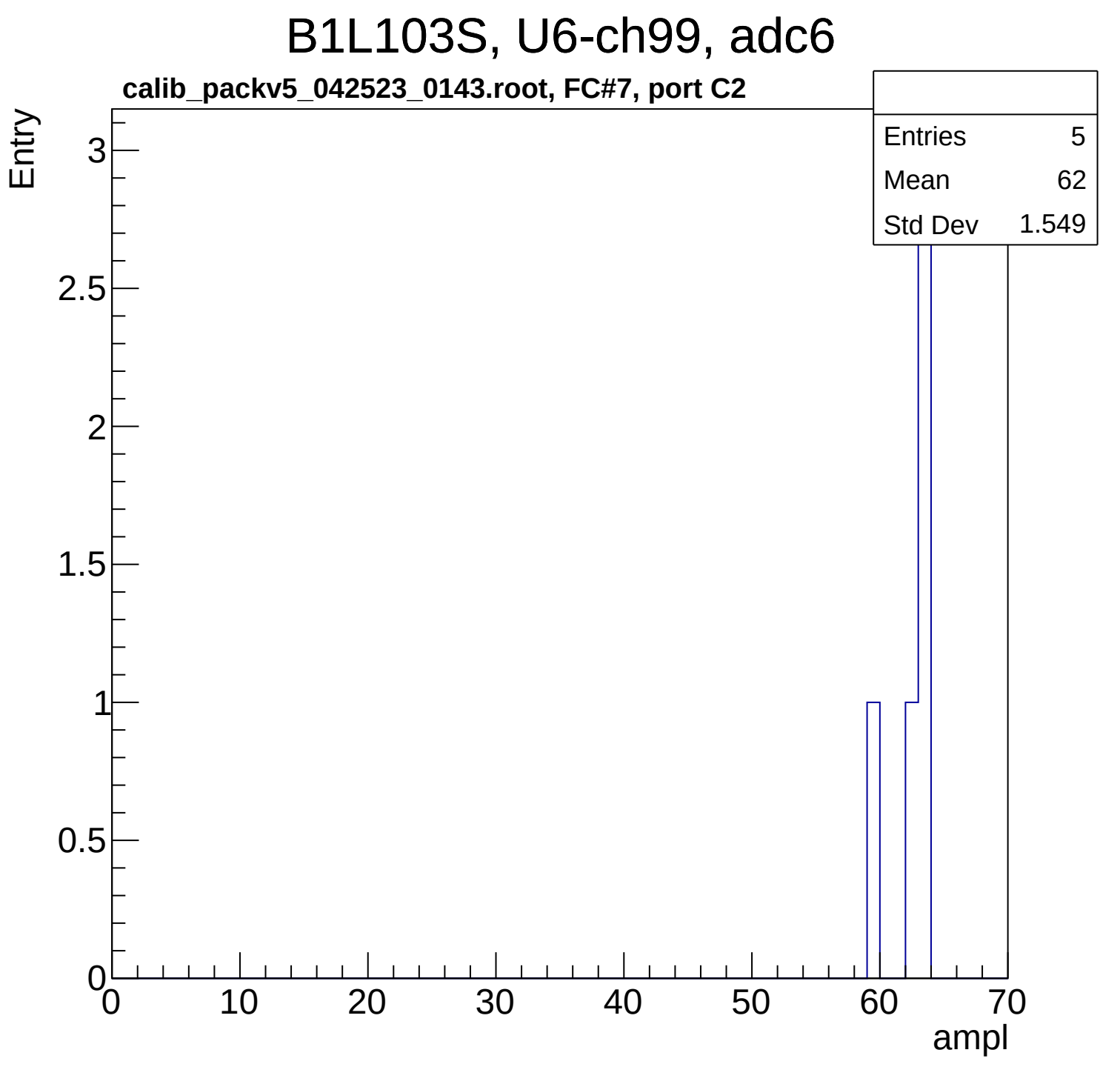
B1L103S, U6-ch99, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62
Std Dev 1.549
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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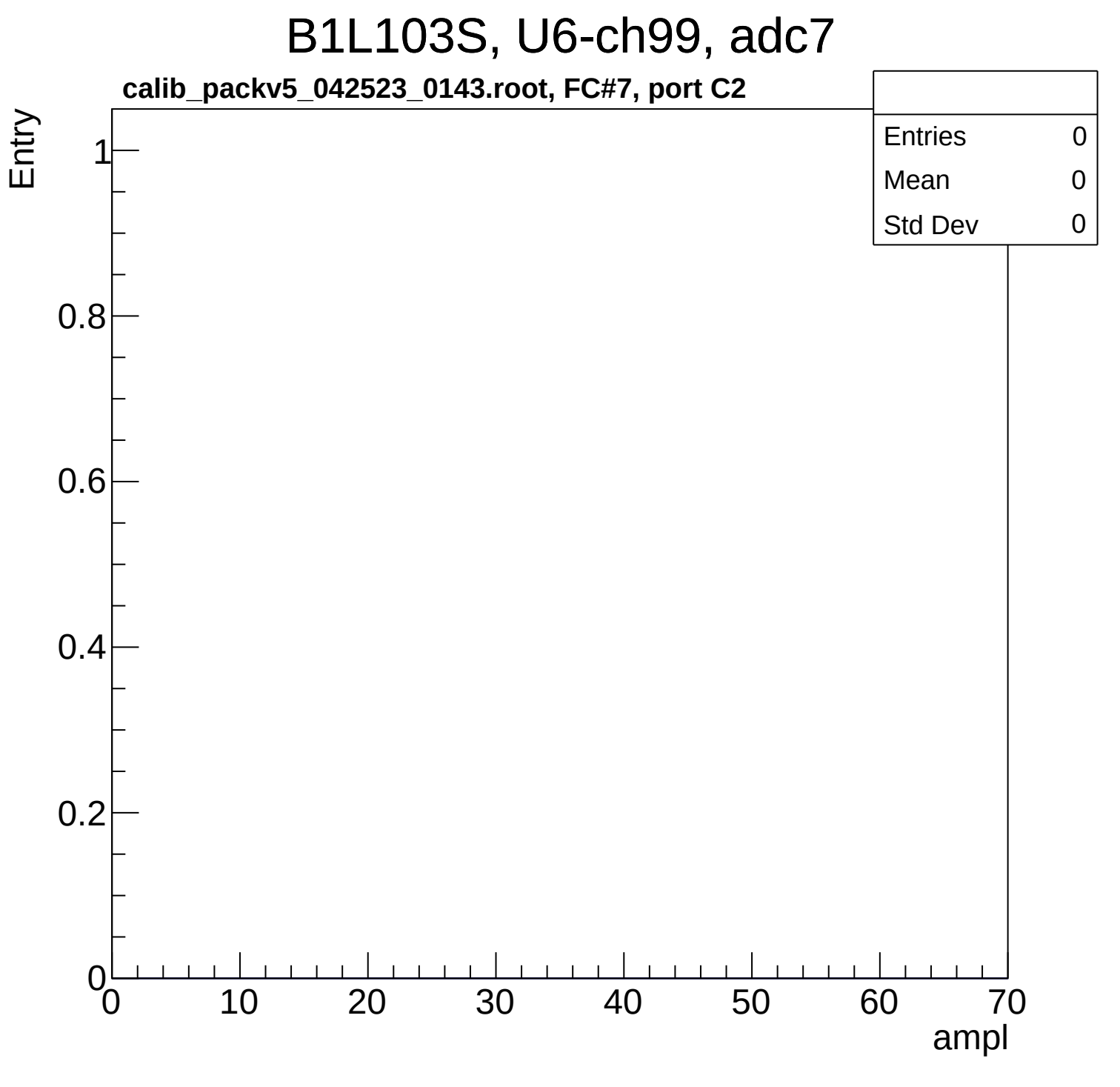
B1L103S, U6-ch99, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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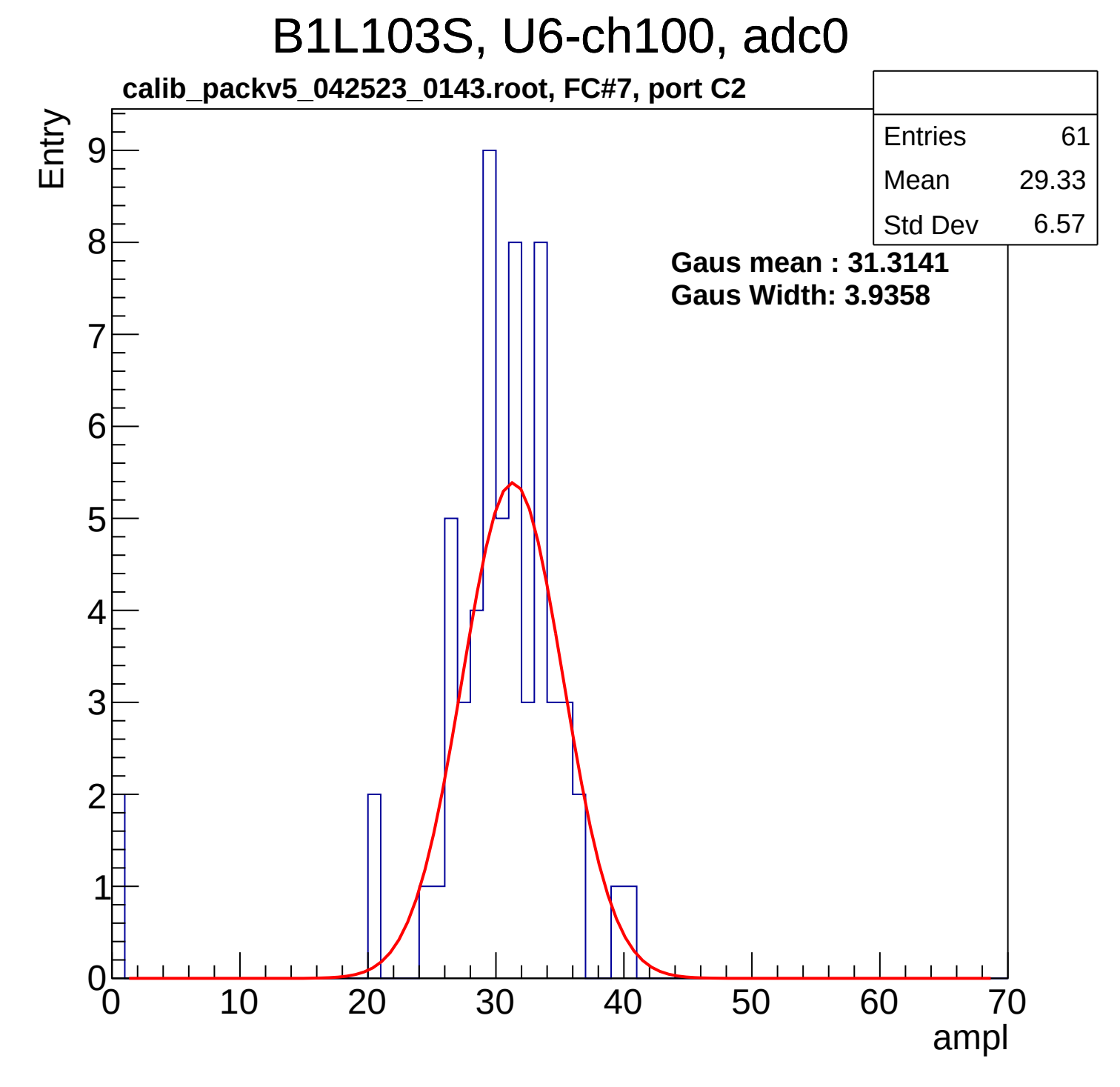
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch100, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 29.33
Std Dev 6.57
Gaus mean : 31.3141
Gaus Width: 3.9358
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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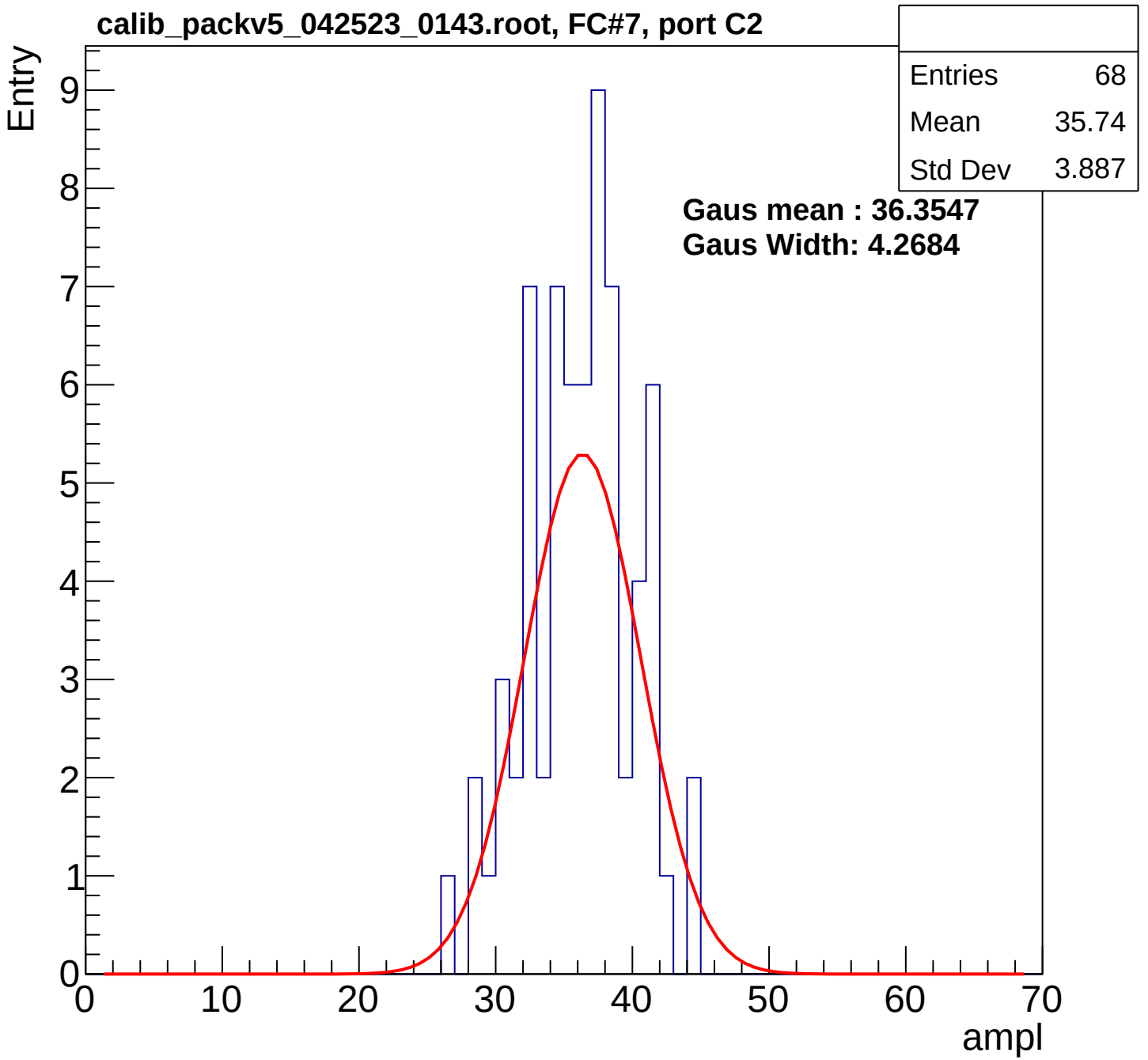
B1L103S, U6-ch100, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 35.74
Std Dev 3.887
Gaus mean : 36.3547
Gaus Width: 4.2684
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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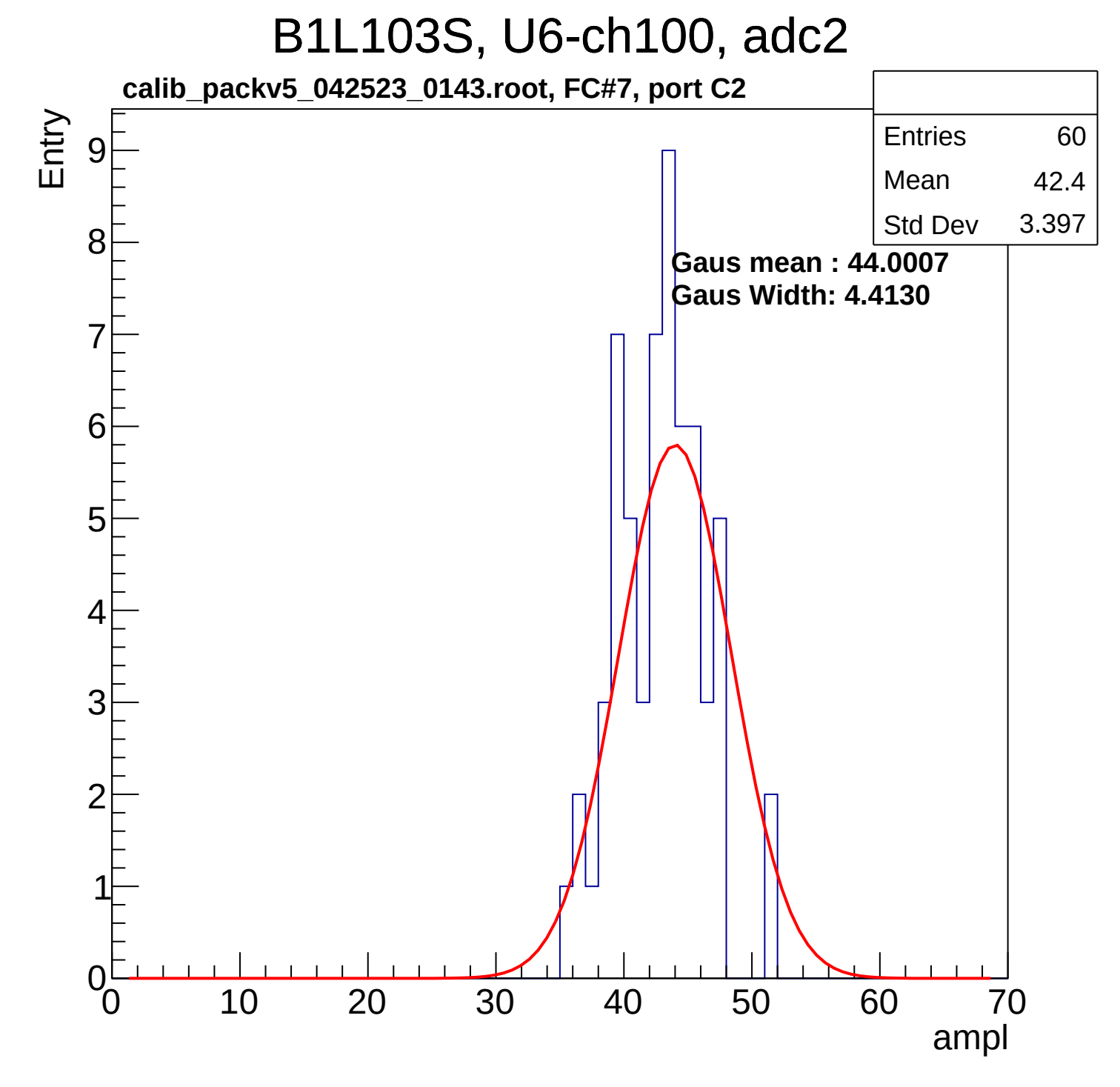
B1L103S, U6-ch100, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 42.4
Std Dev 3.397
Gaus mean : 44.0007
Gaus Width: 4.4130
Entry
ampl
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2
3
4
5
6
7
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0
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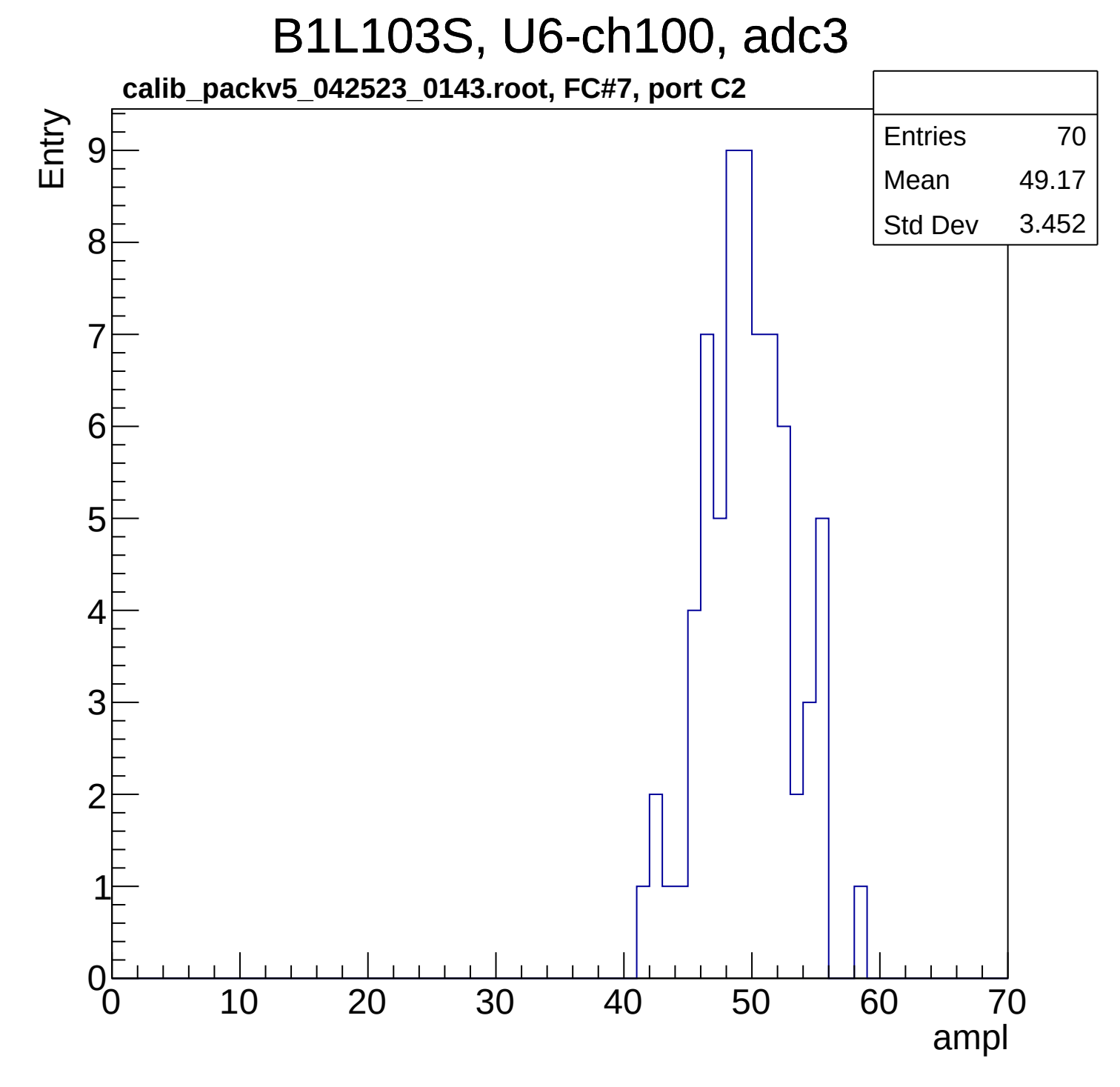
B1L103S, U6-ch100, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.17
Std Dev 3.452
Entry
ampl
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3
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5
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7
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9
0
10
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60
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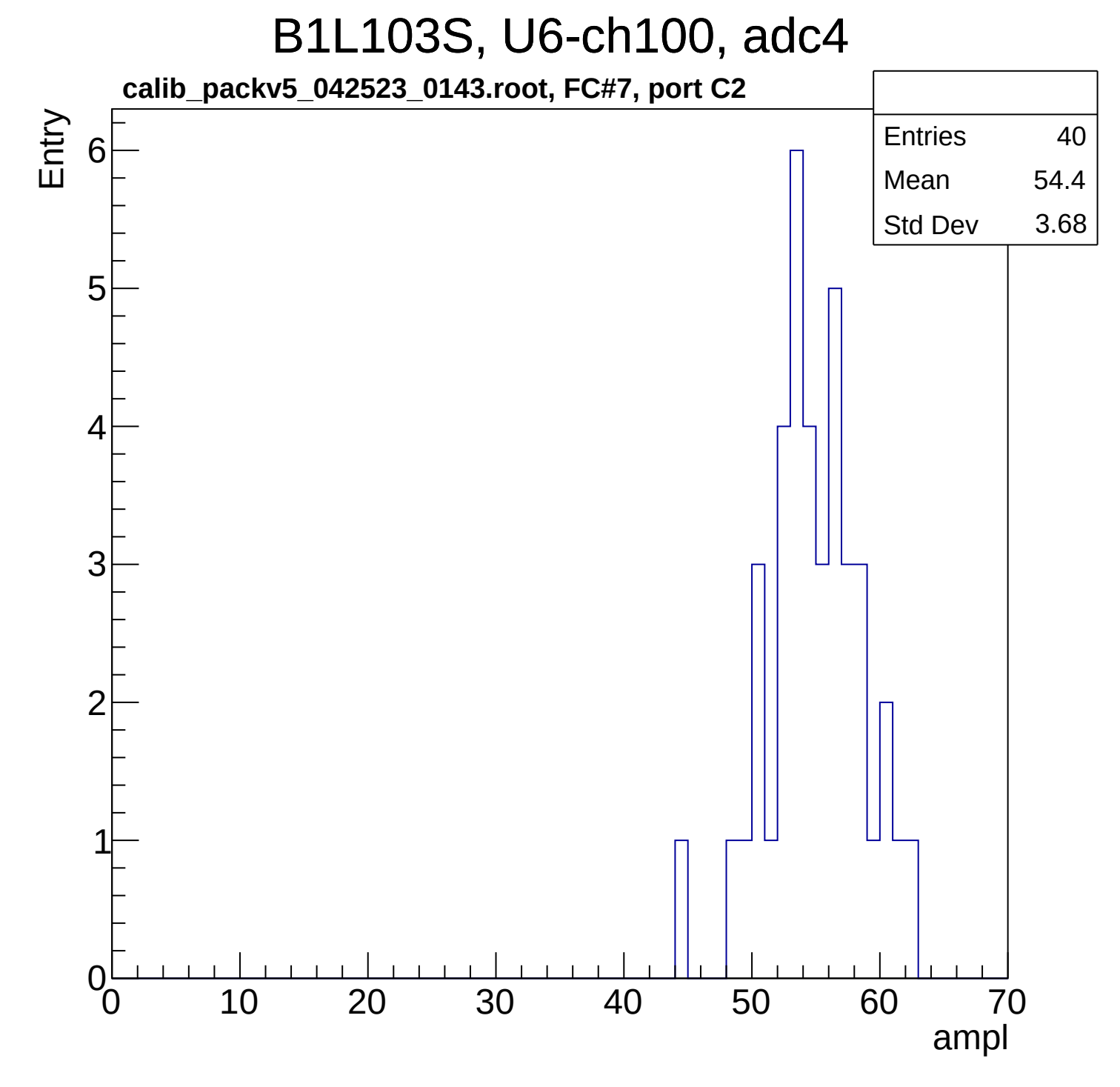
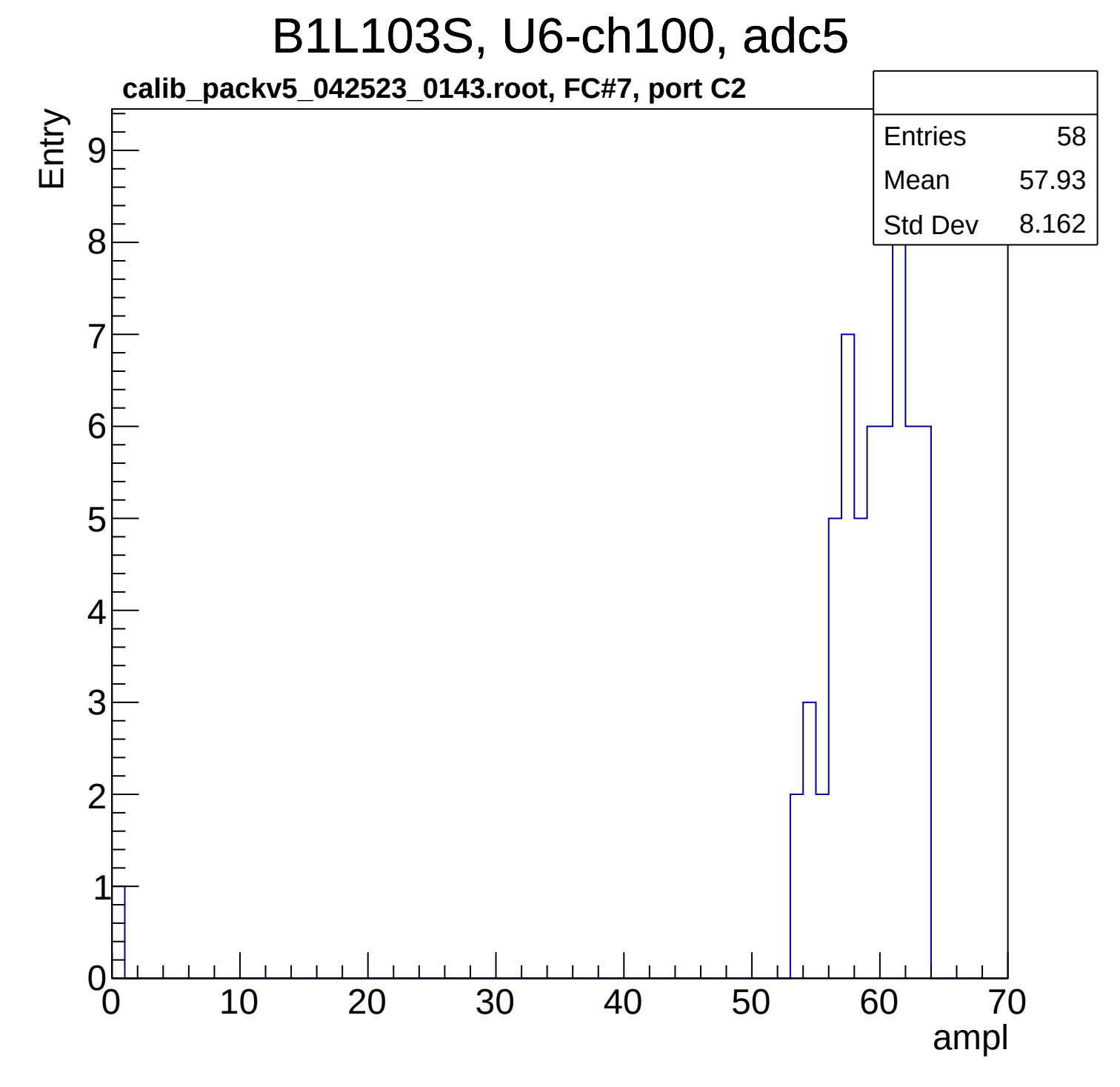

B1L103S, U6-ch100, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 54.4
Std Dev 3.68
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch100, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.93
Std Dev 8.162
Entry
ampl
0
1
2
3
4
5
6
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8
9
0
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20
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40
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60
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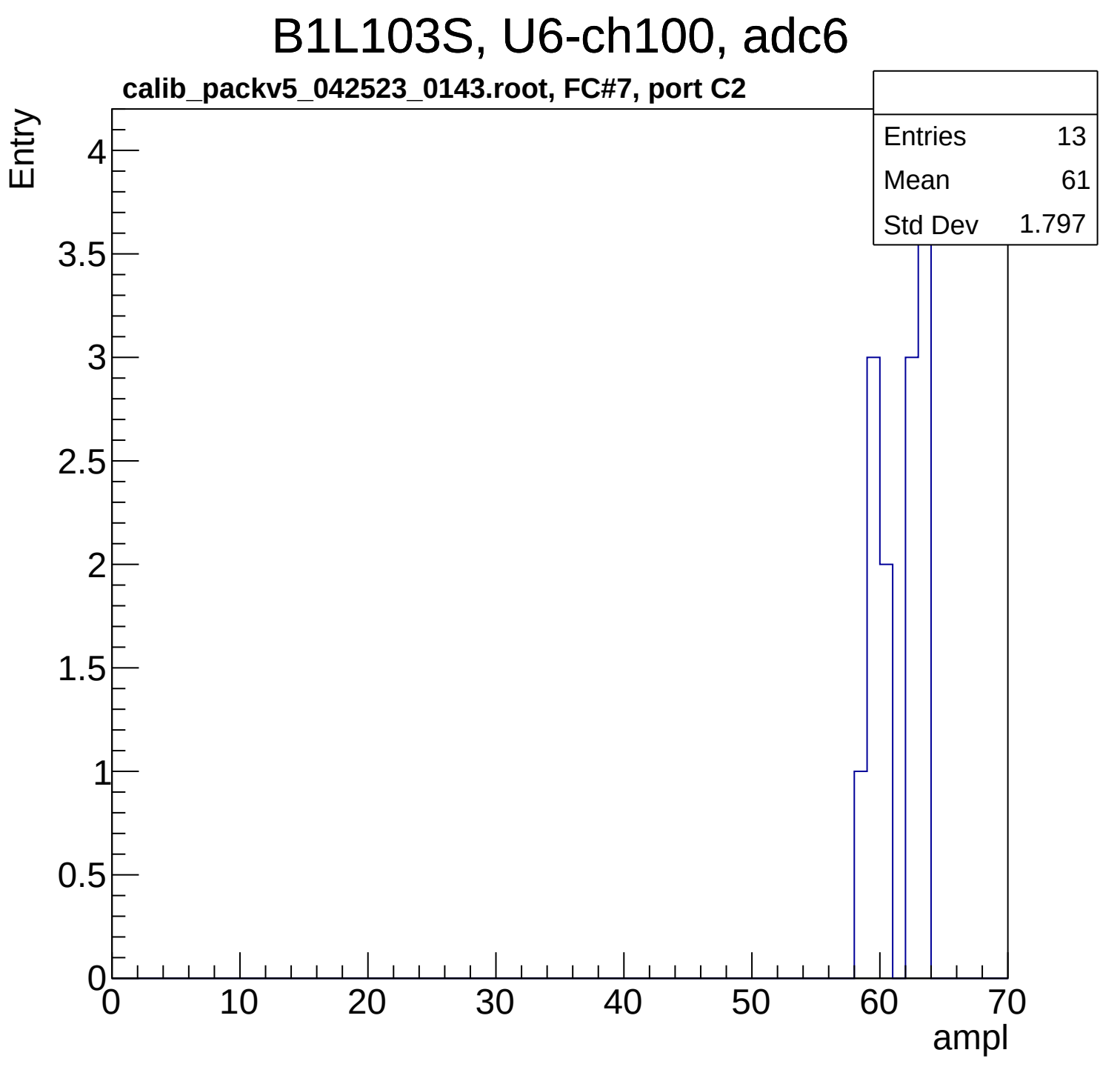
B1L103S, U6-ch100, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61
Std Dev 1.797
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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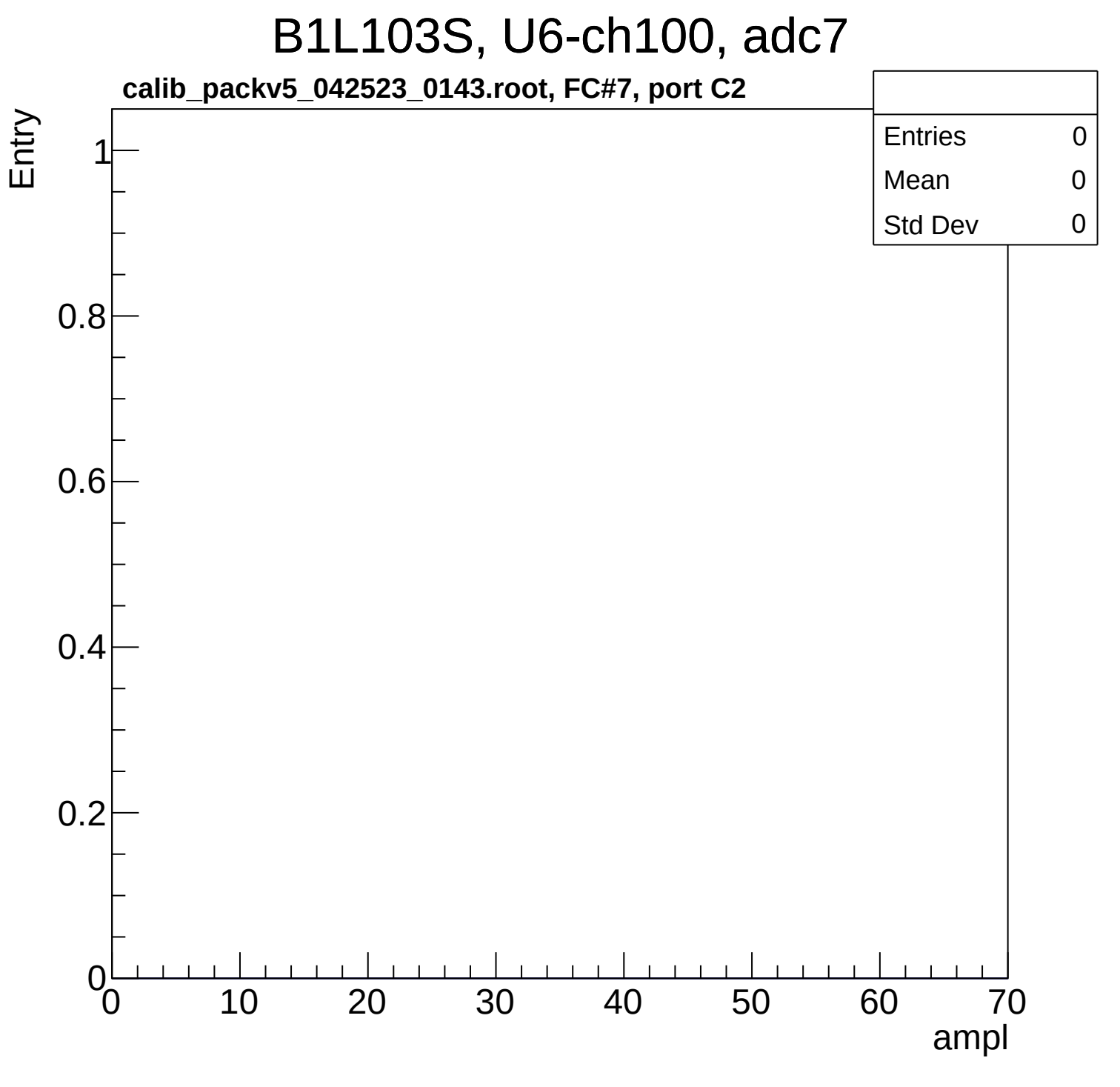
B1L103S, U6-ch100, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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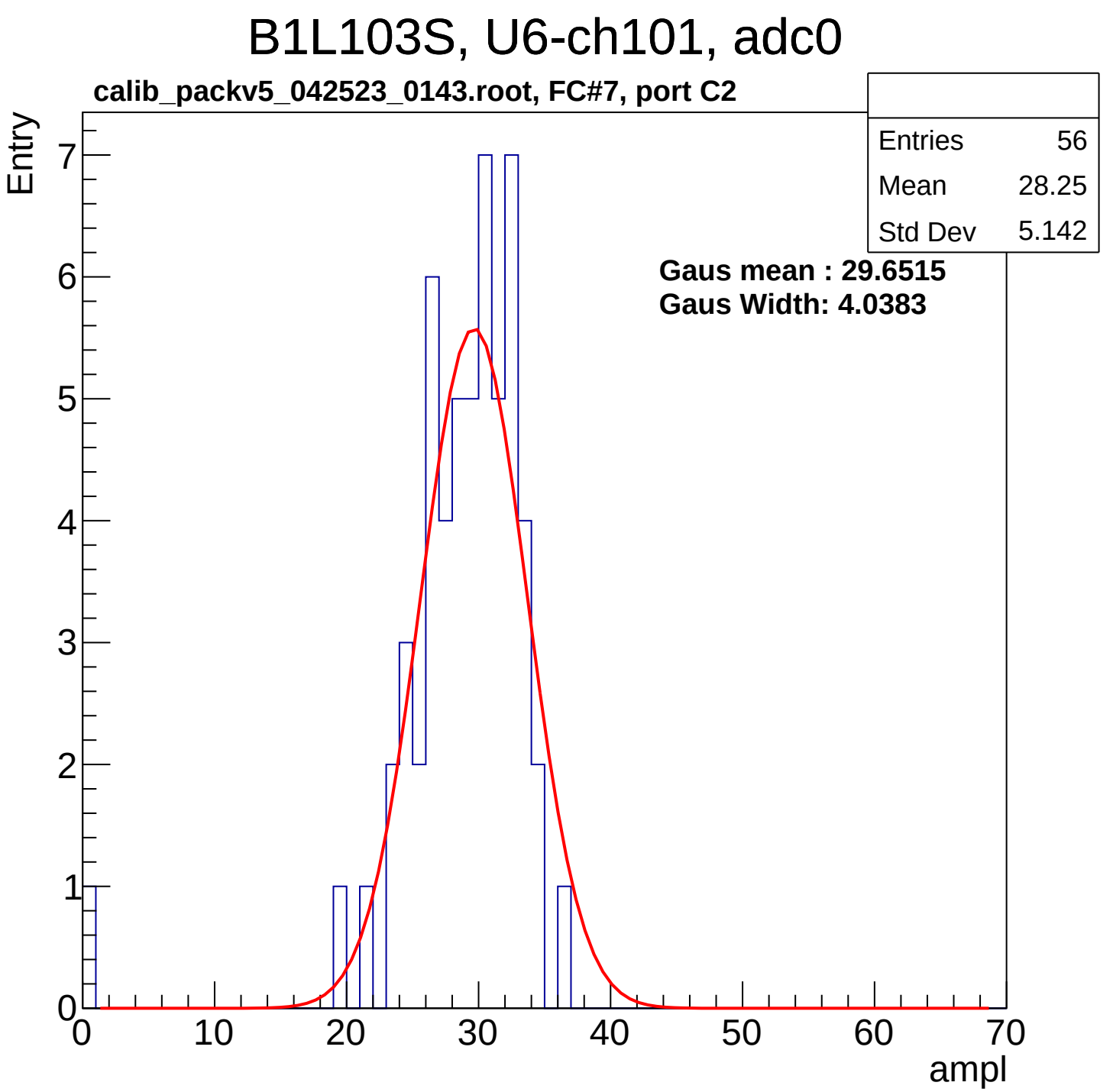
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch101, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 28.25
Std Dev 5.142
Gaus mean : 29.6515
Gaus Width: 4.0383
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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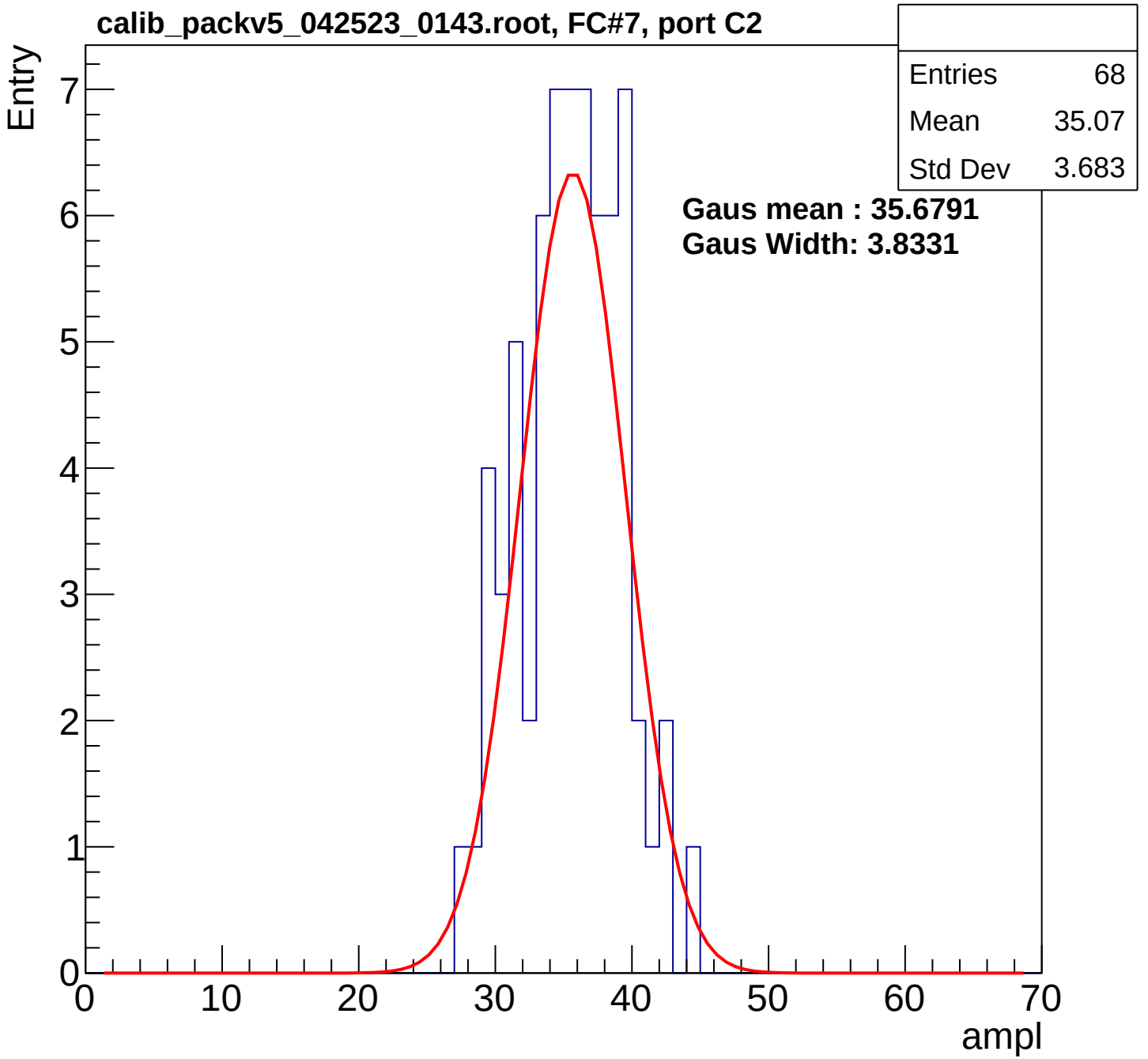
B1L103S, U6-ch101, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 35.07
Std Dev 3.683
Gaus mean : 35.6791
Gaus Width: 3.8331
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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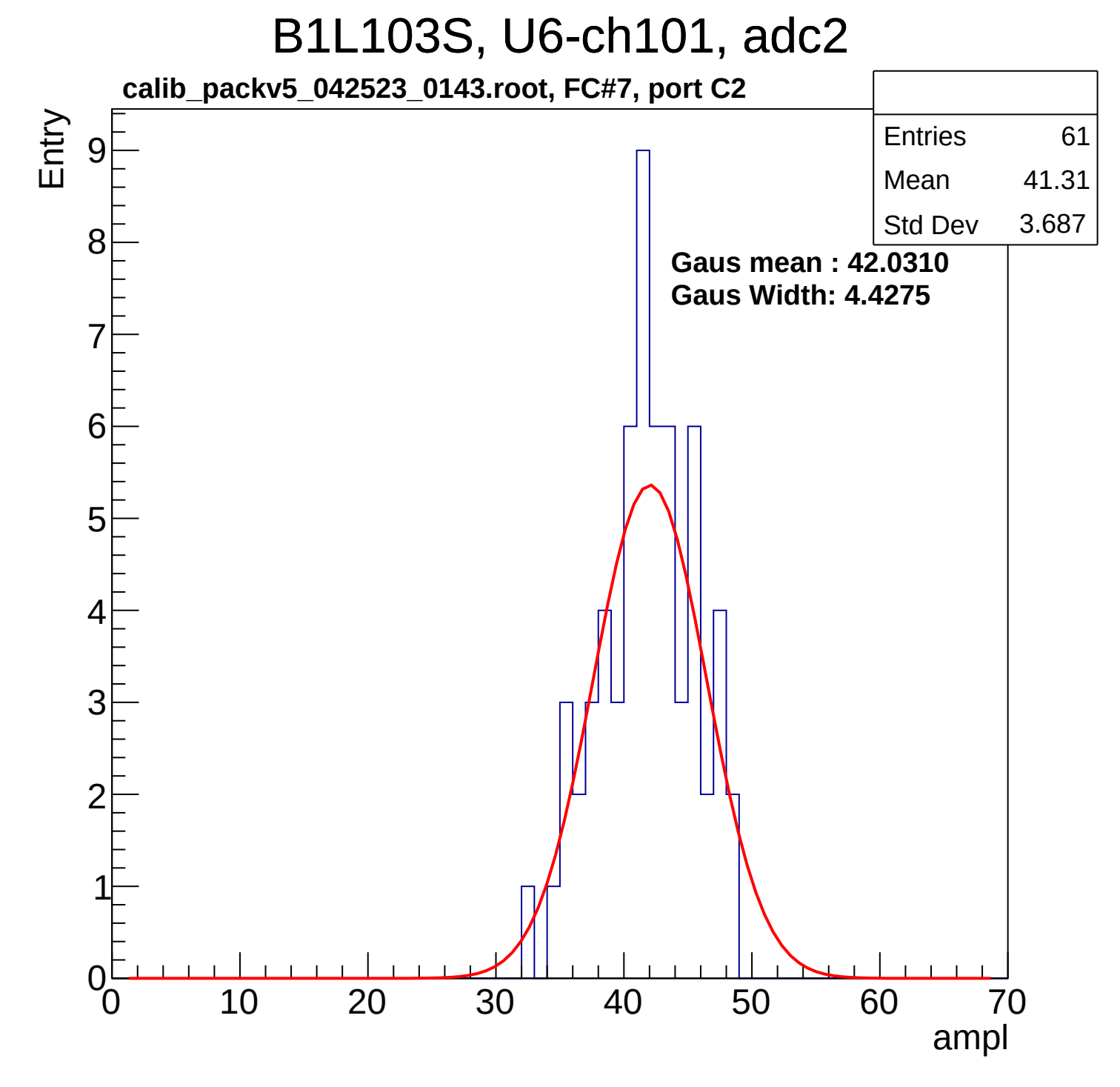
B1L103S, U6-ch101, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.31
Std Dev 3.687
Gaus mean : 42.0310
Gaus Width: 4.4275
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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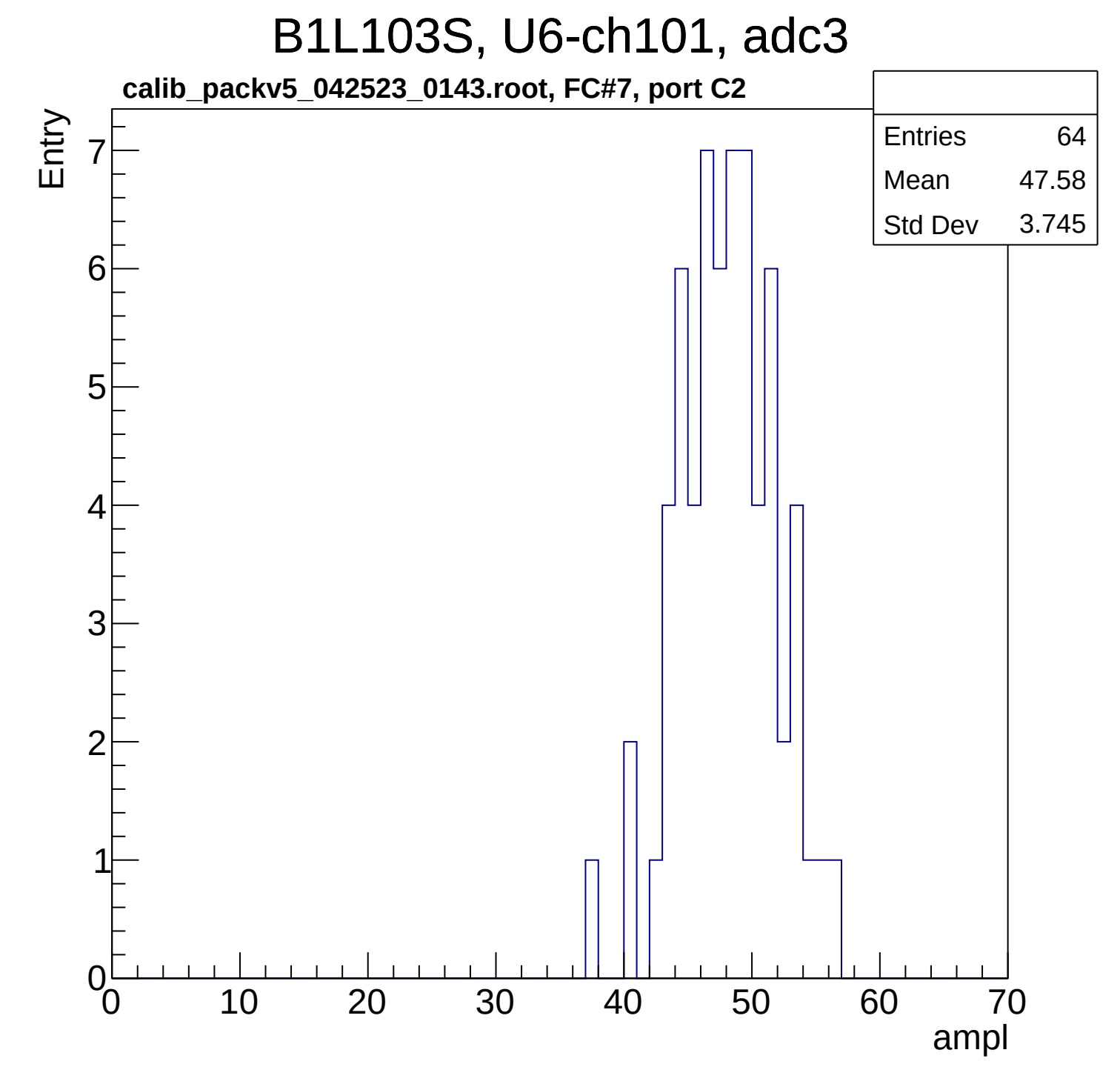
B1L103S, U6-ch101, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.58
Std Dev 3.745
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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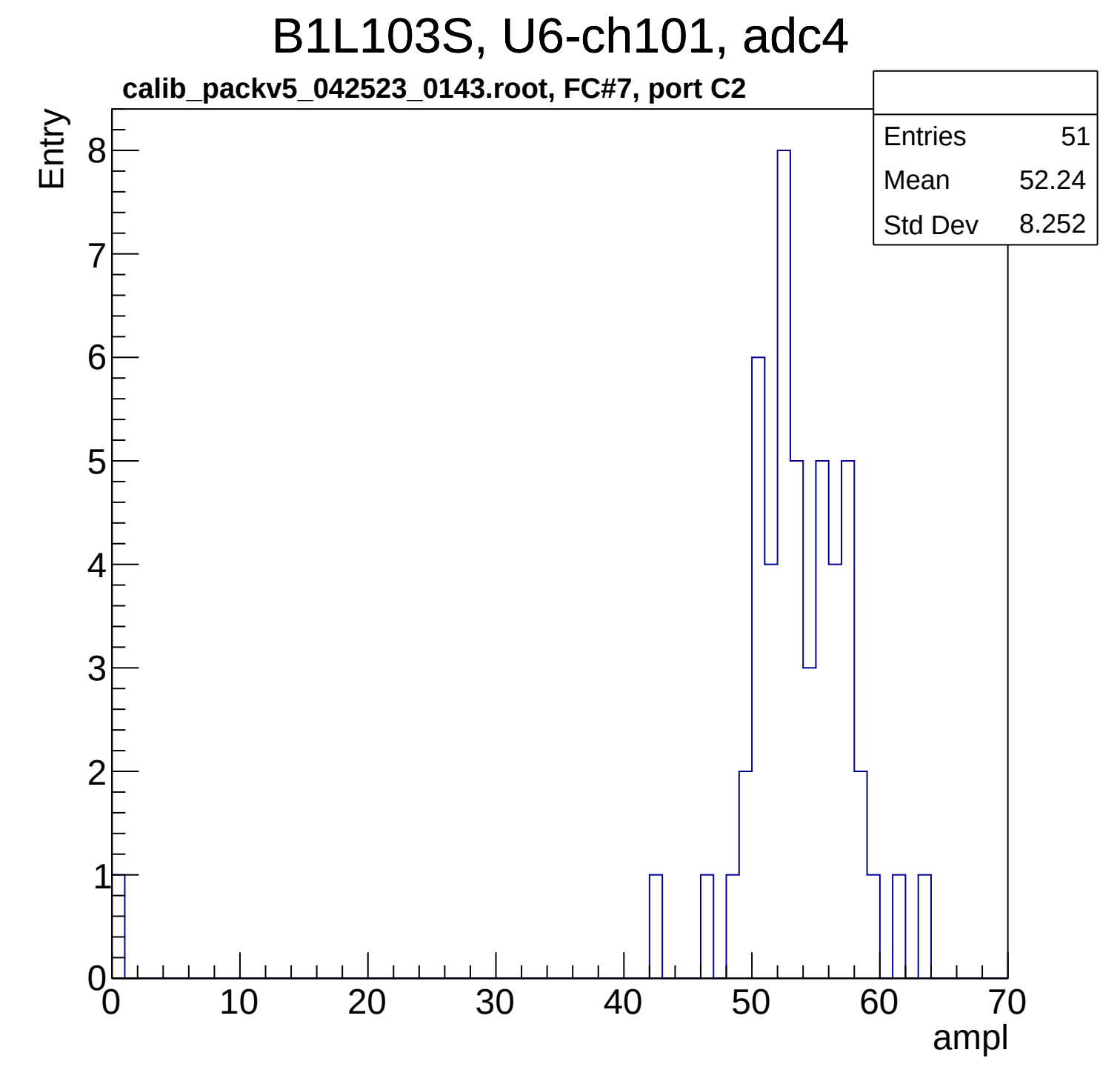
B1L103S, U6-ch101, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 52.24
Std Dev 8.252
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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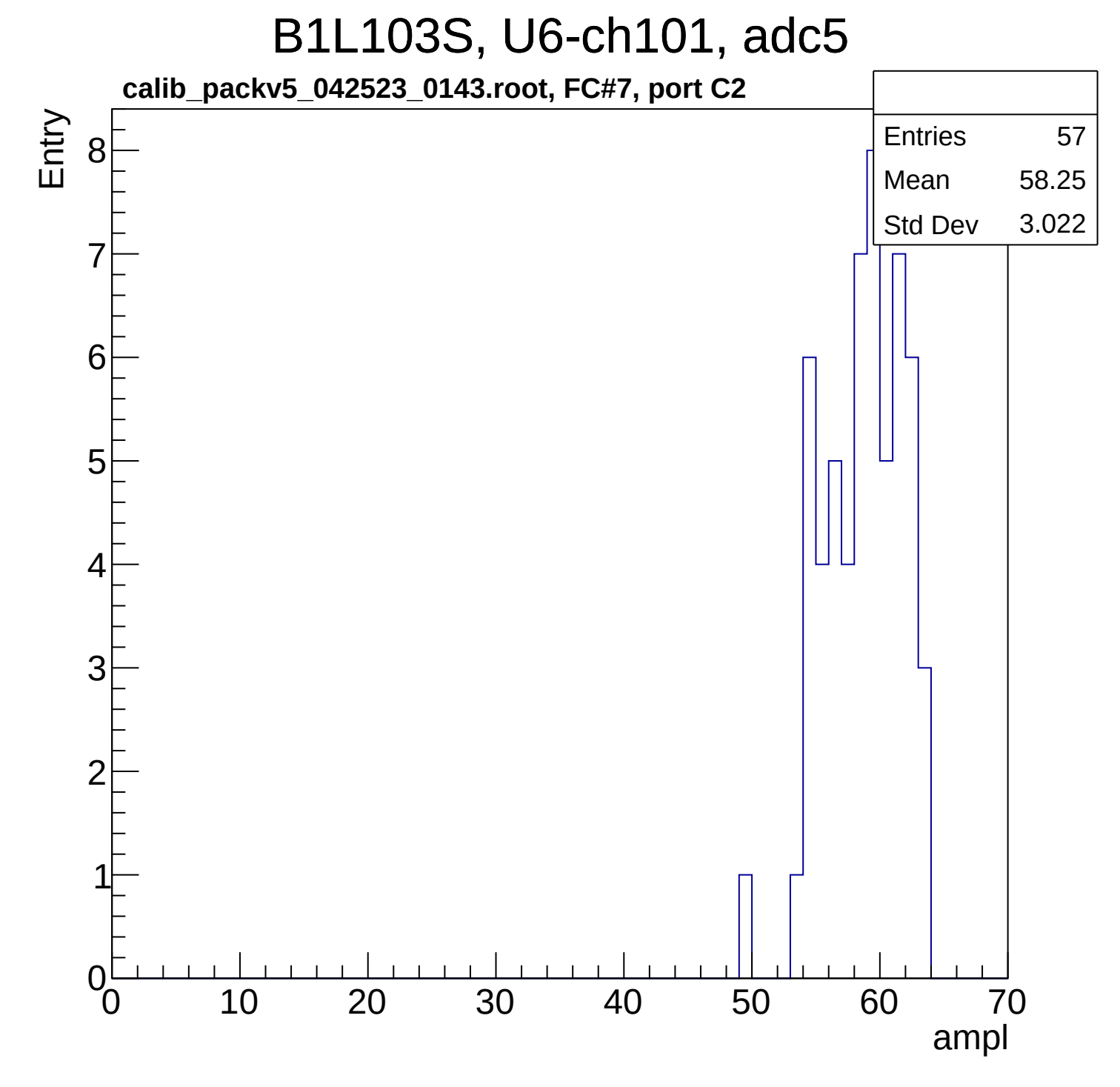
B1L103S, U6-ch101, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 58.25
Std Dev 3.022
Entry
ampl
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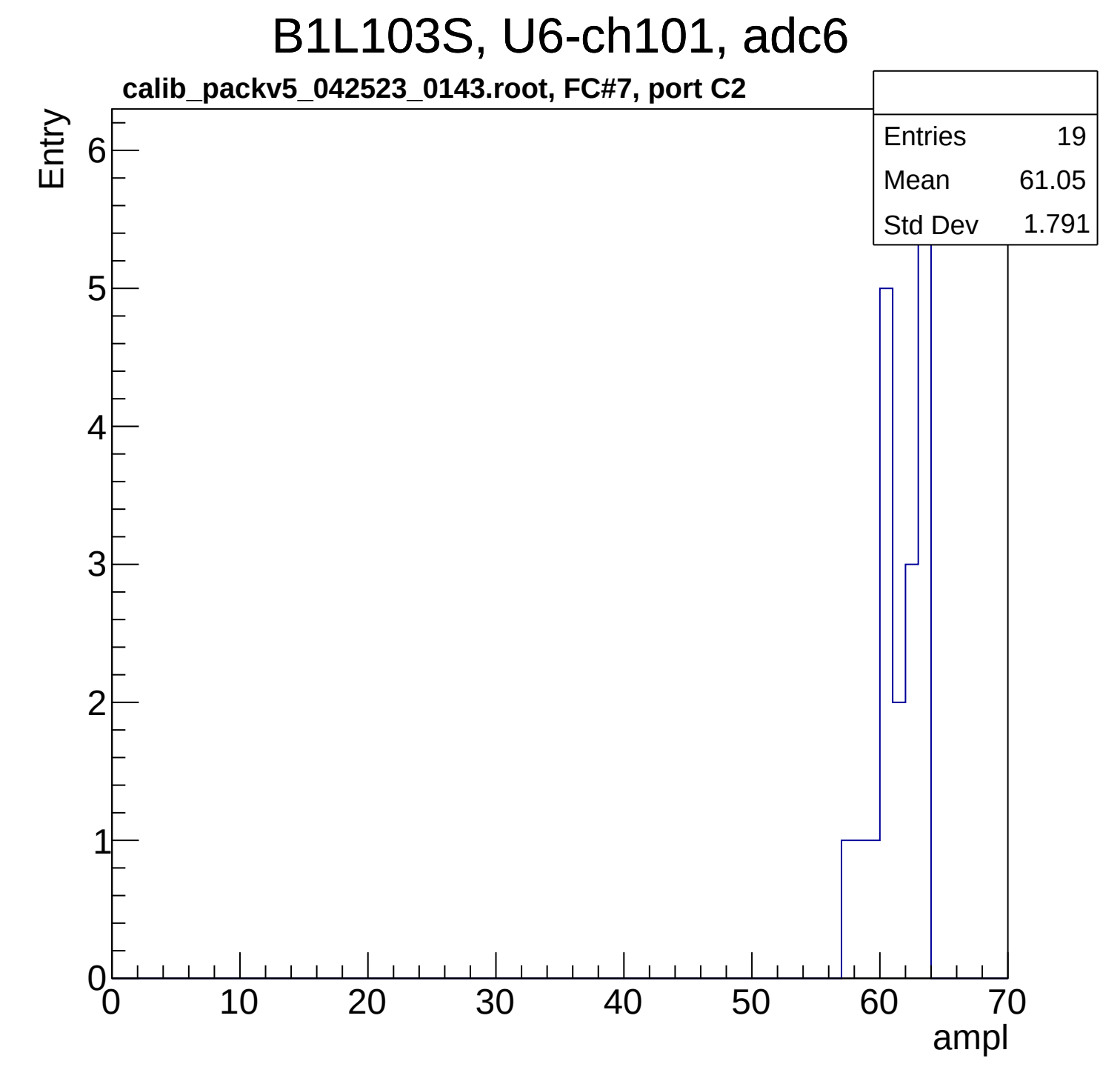
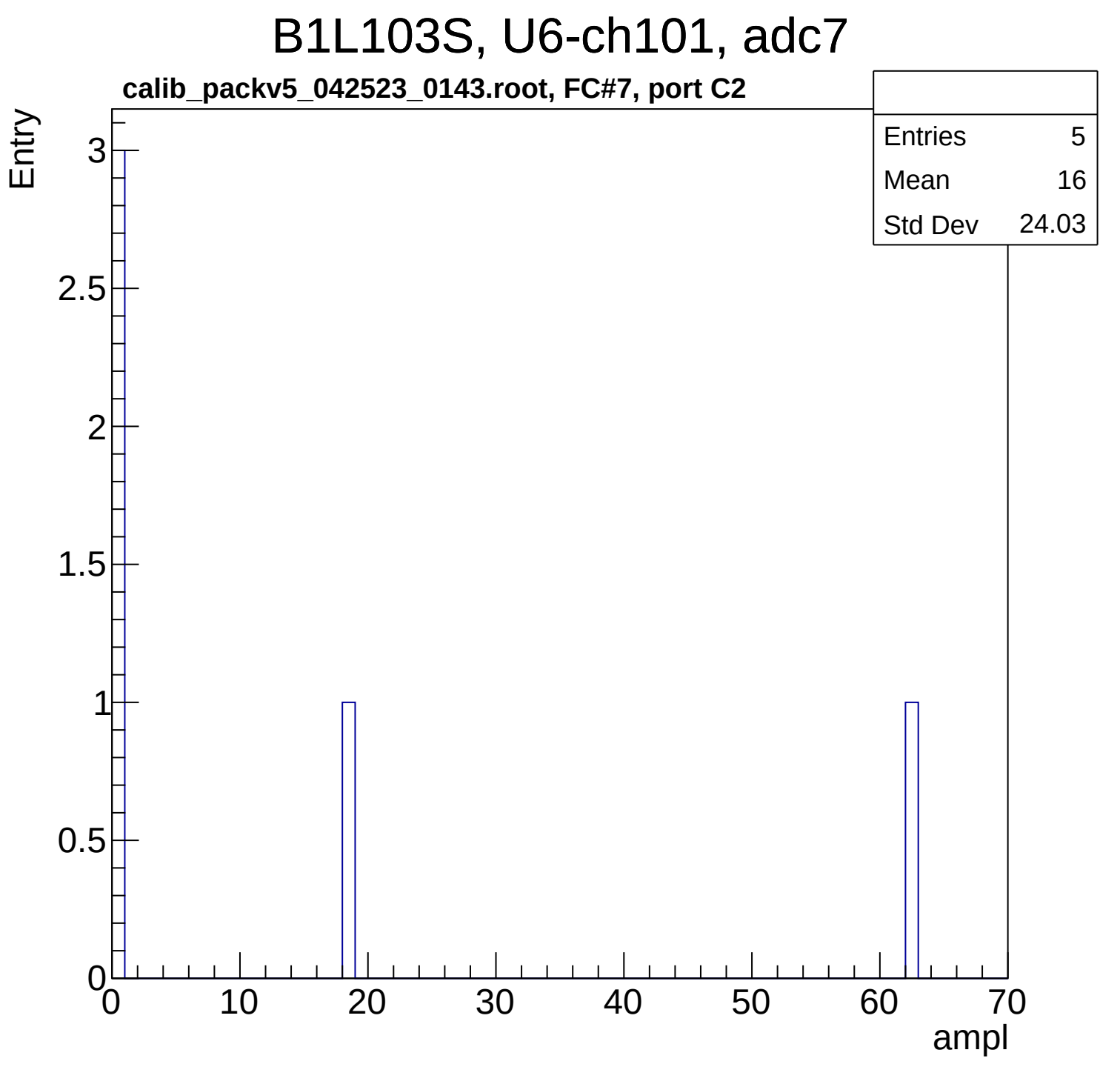
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch101, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 61.05
Std Dev 1.791
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch101, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 16
Std Dev 24.03
Entry
ampl
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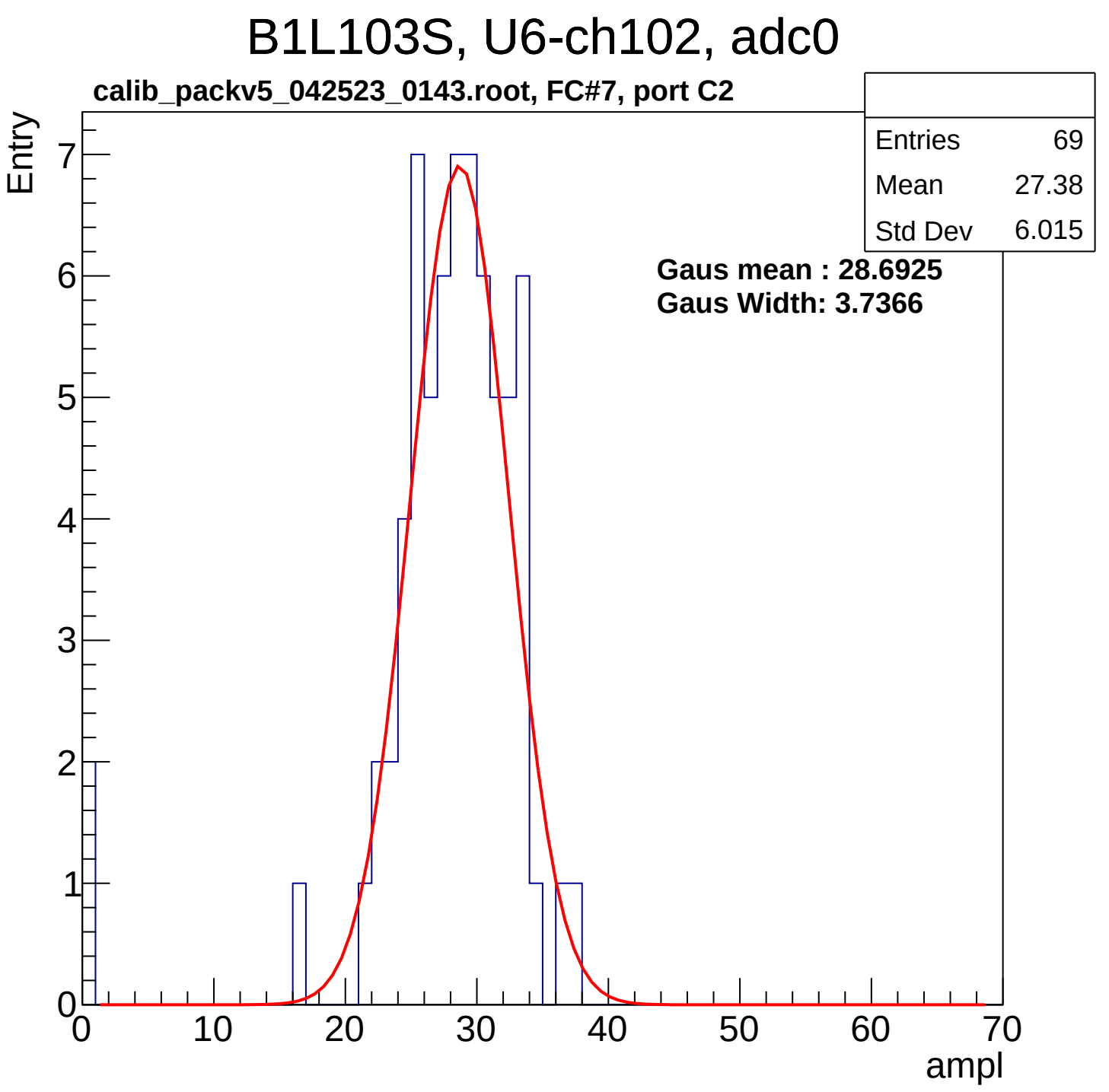
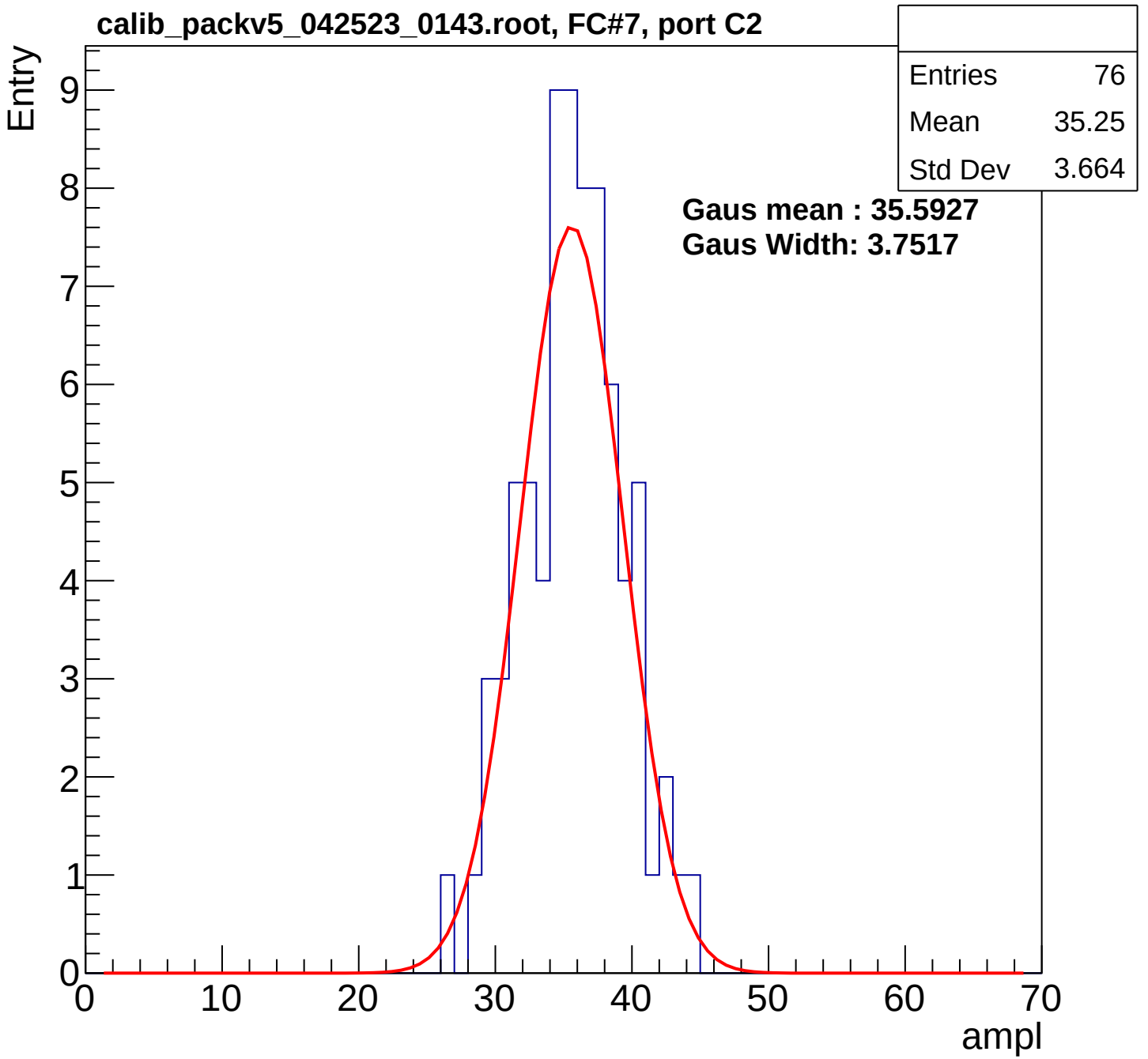
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch102, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 27.38
Std Dev 6.015
Gaus mean : 28.6925
Gaus Width: 3.7366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch102, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 35.25
Std Dev 3.664
Gaus mean : 35.5927
Gaus Width: 3.7517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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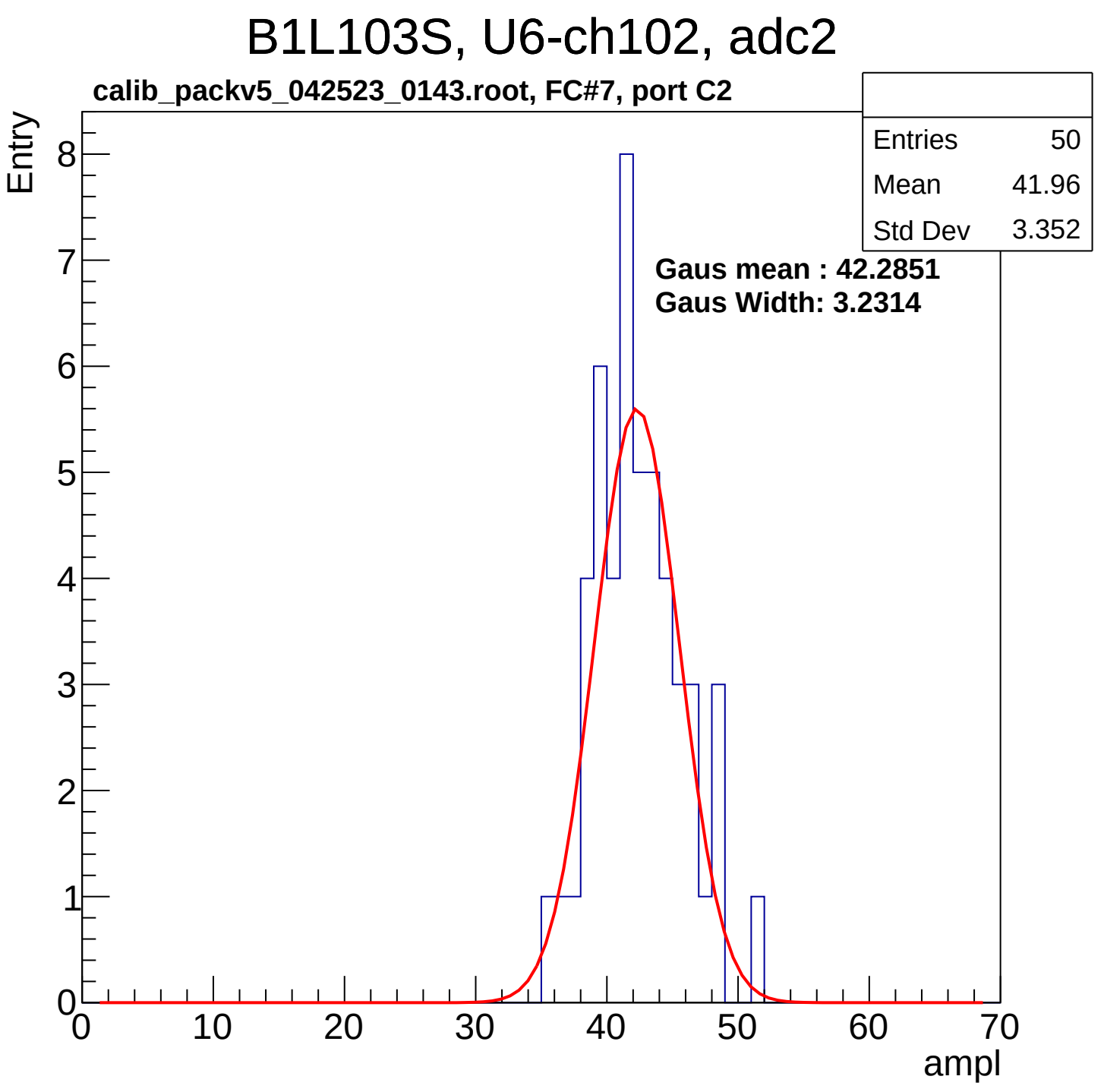
B1L103S, U6-ch102, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 41.96
Std Dev 3.352
Gaus mean : 42.2851
Gaus Width: 3.2314
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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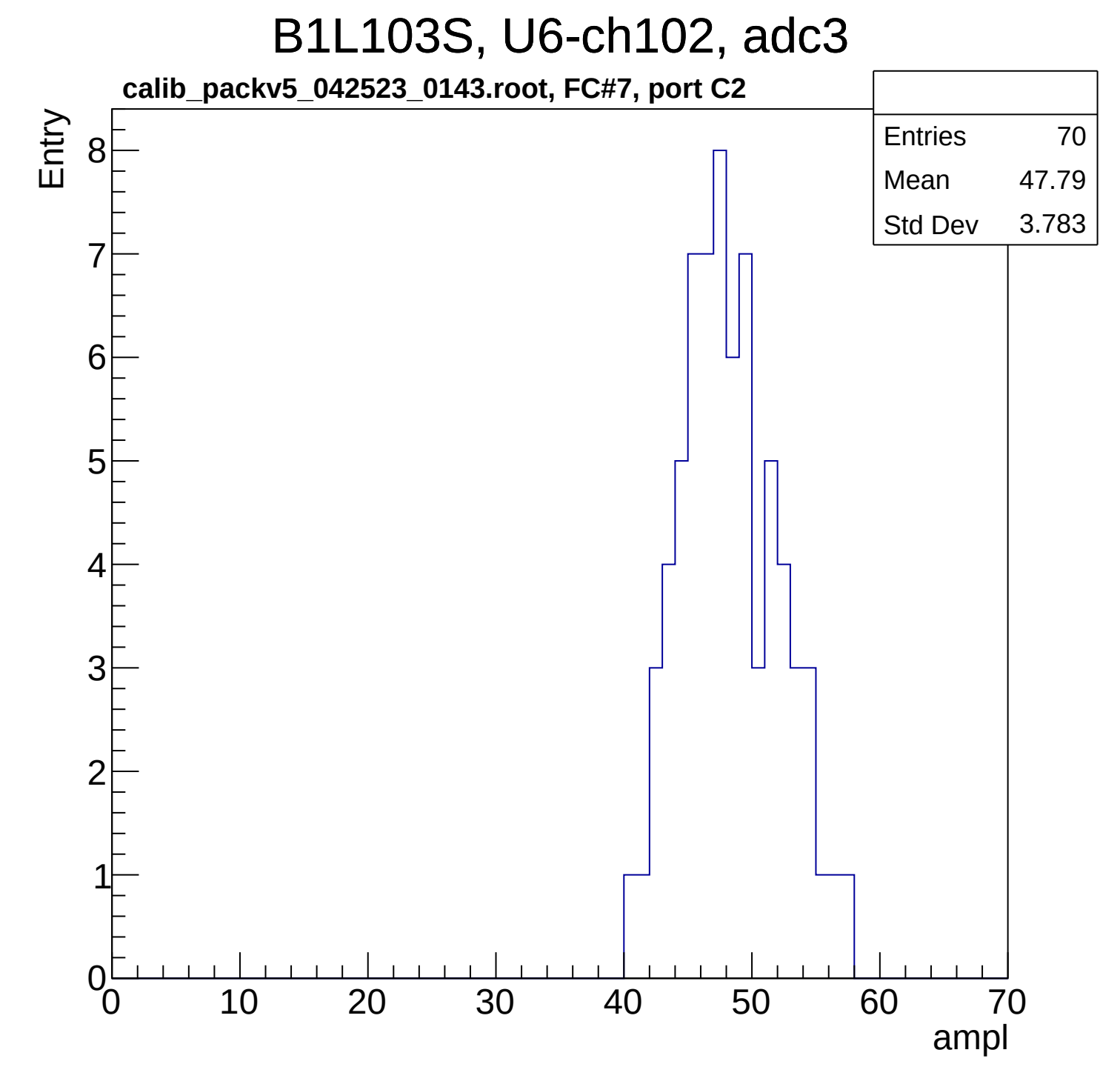
B1L103S, U6-ch102, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 47.79
Std Dev 3.783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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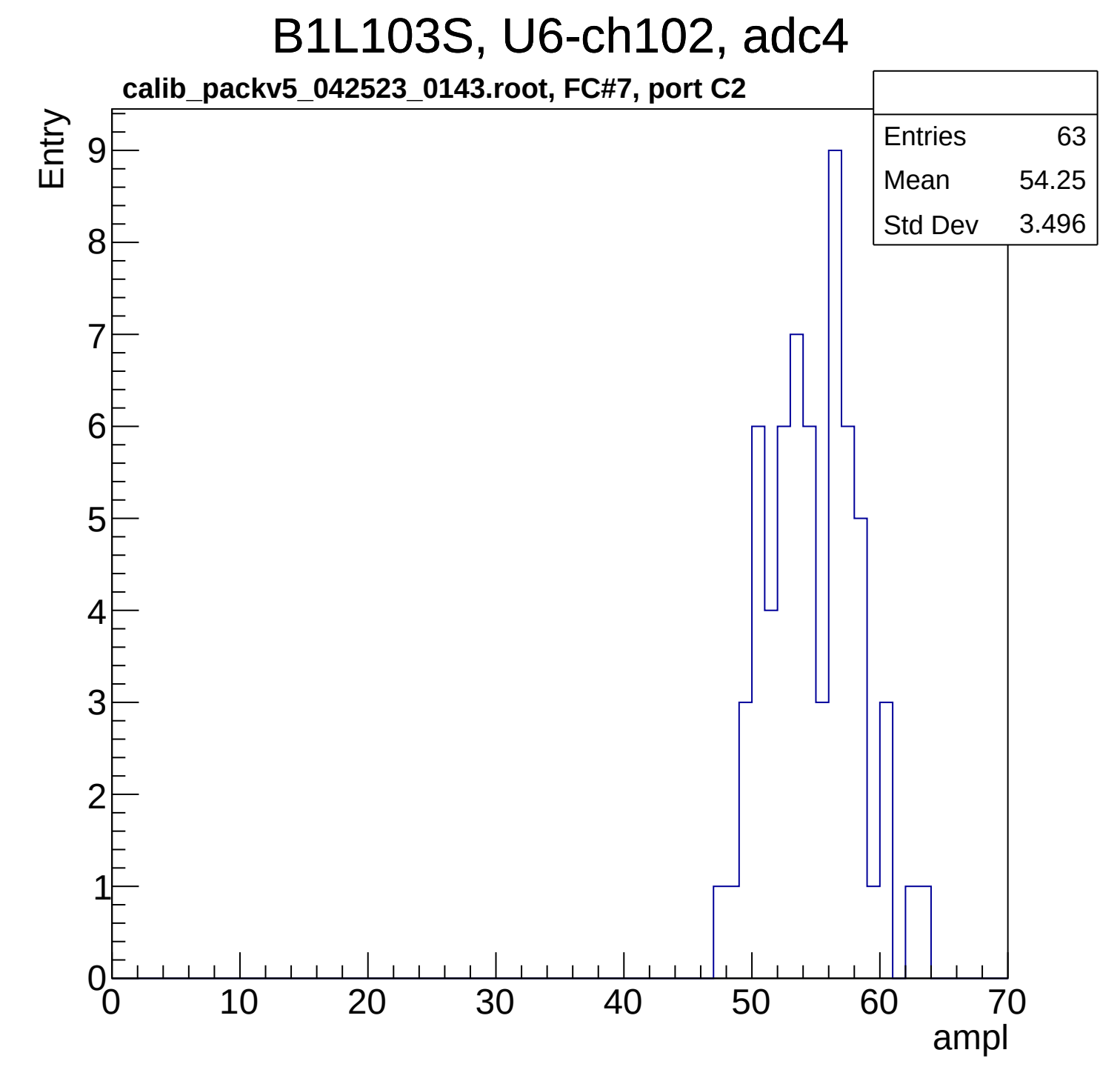
B1L103S, U6-ch102, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.25
Std Dev 3.496
Entry
ampl
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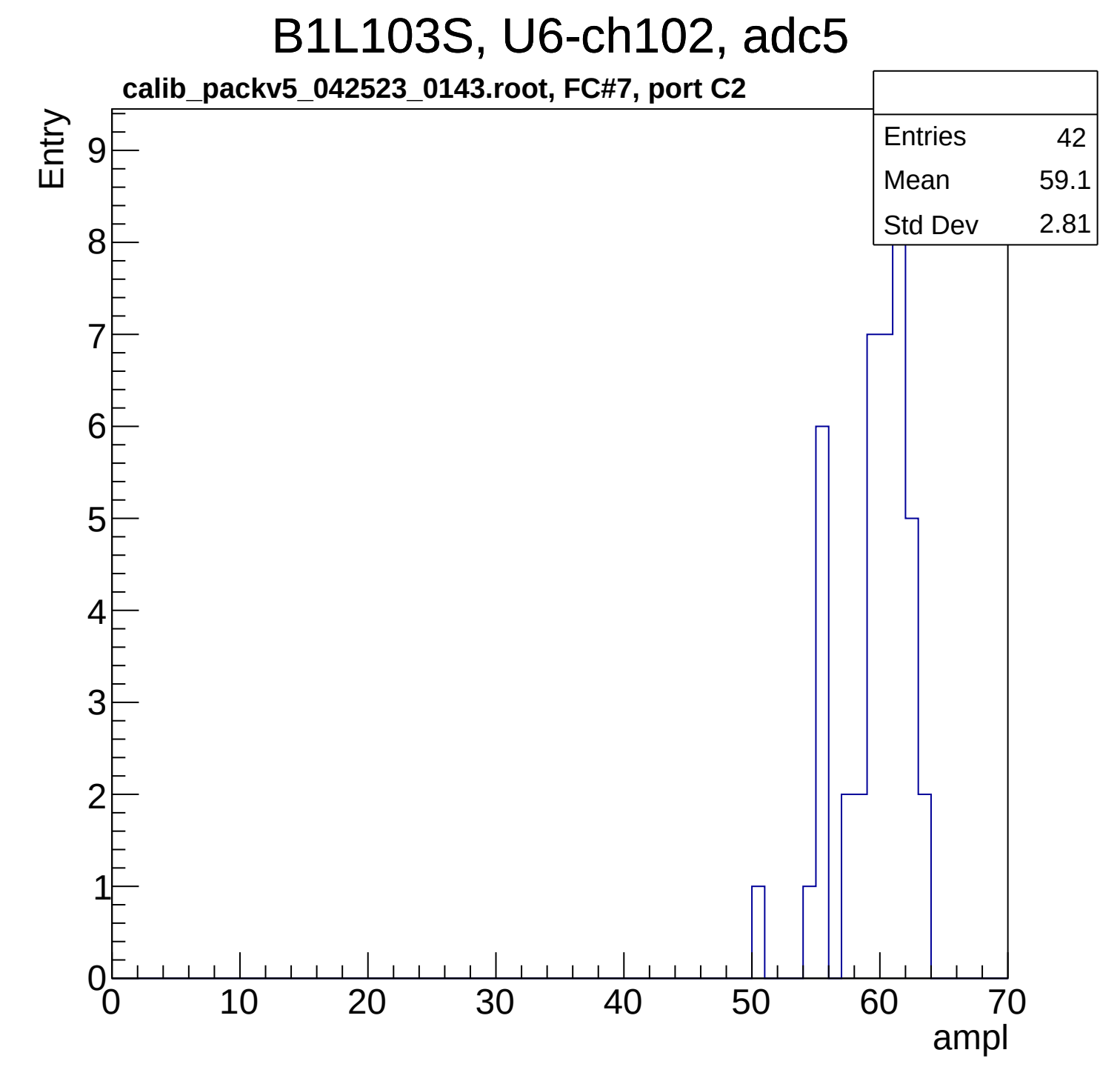
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch102, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 59.1
Std Dev 2.81
Entry
ampl
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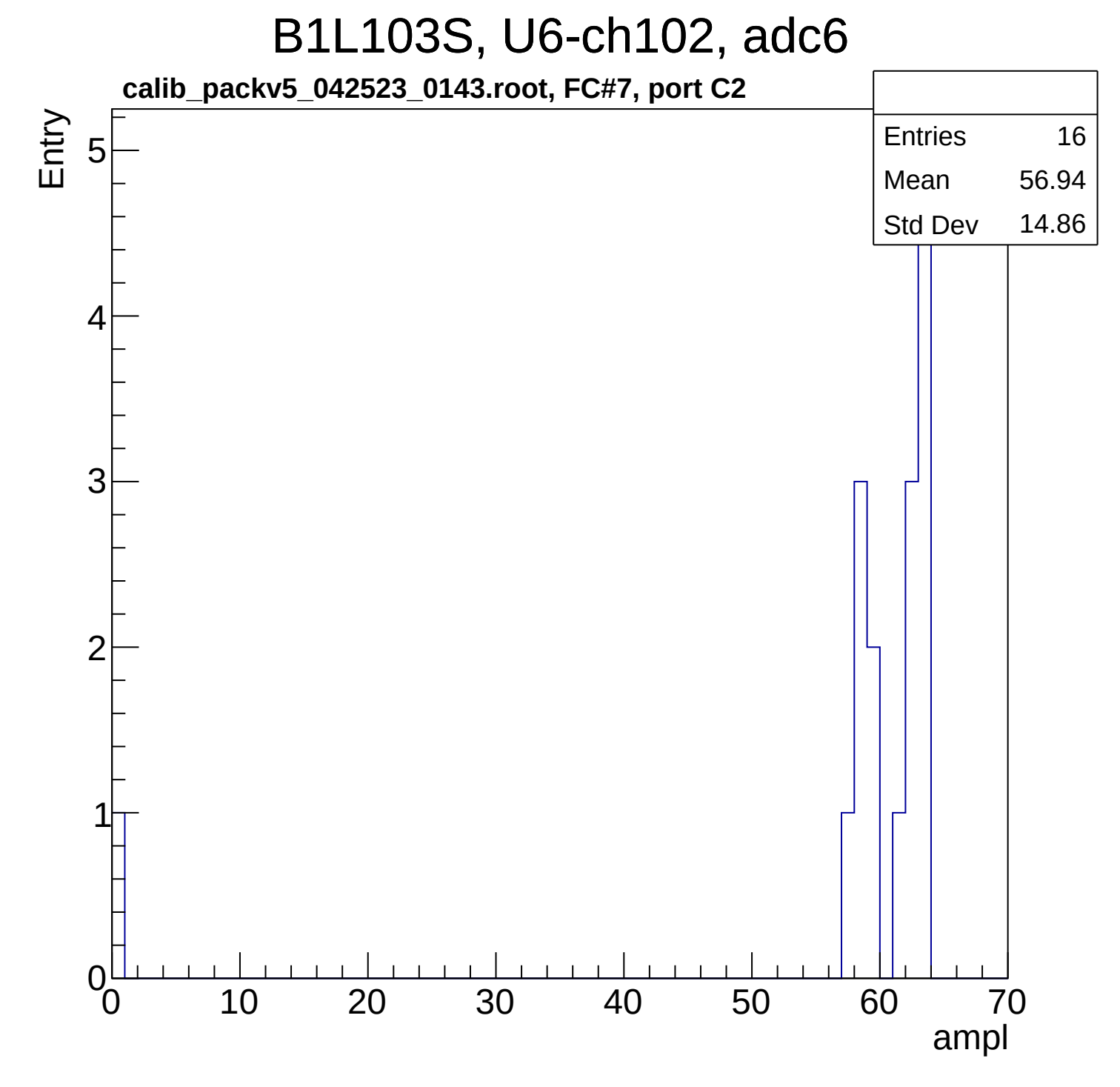
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch102, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 56.94
Std Dev 14.86
Entry
0
1
2
3
4
5
0
10
20
30
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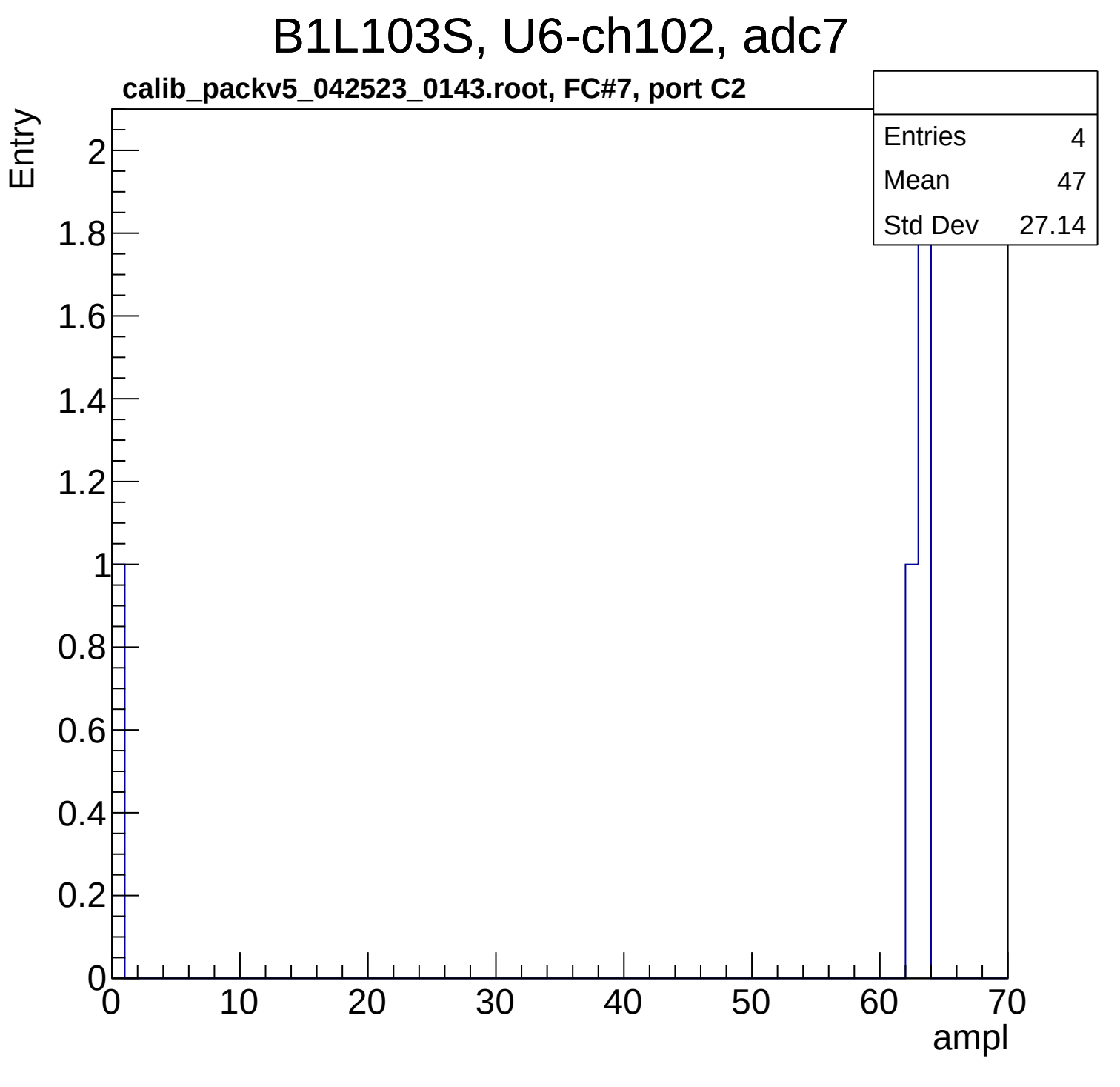
50
60
70
ampl


B1L103S, U6-ch102, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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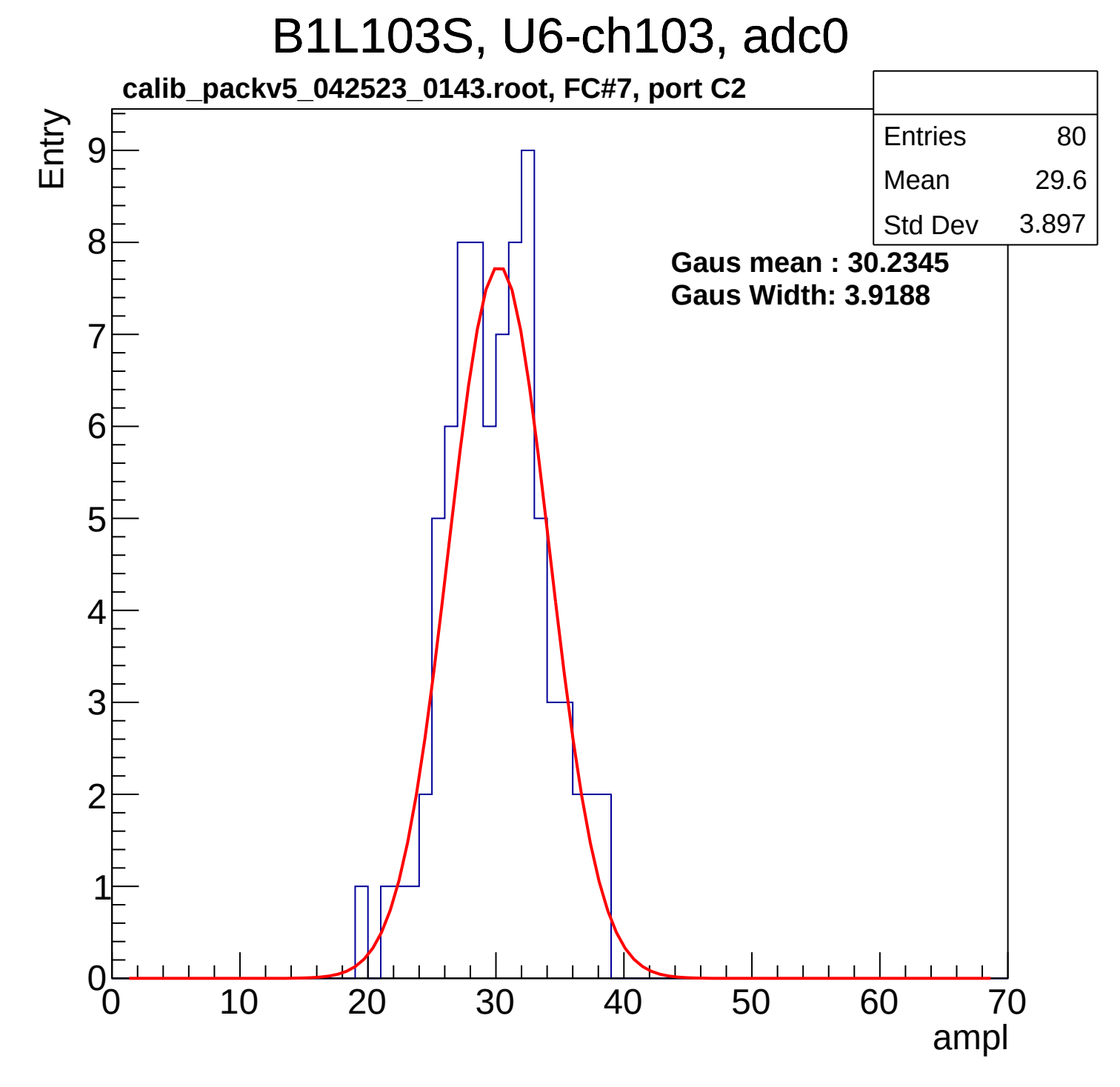
40
50
60
70


B1L103S, U6-ch103, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 29.6
Std Dev 3.897
Gaus mean : 30.2345
Gaus Width: 3.9188
Entry
ampl
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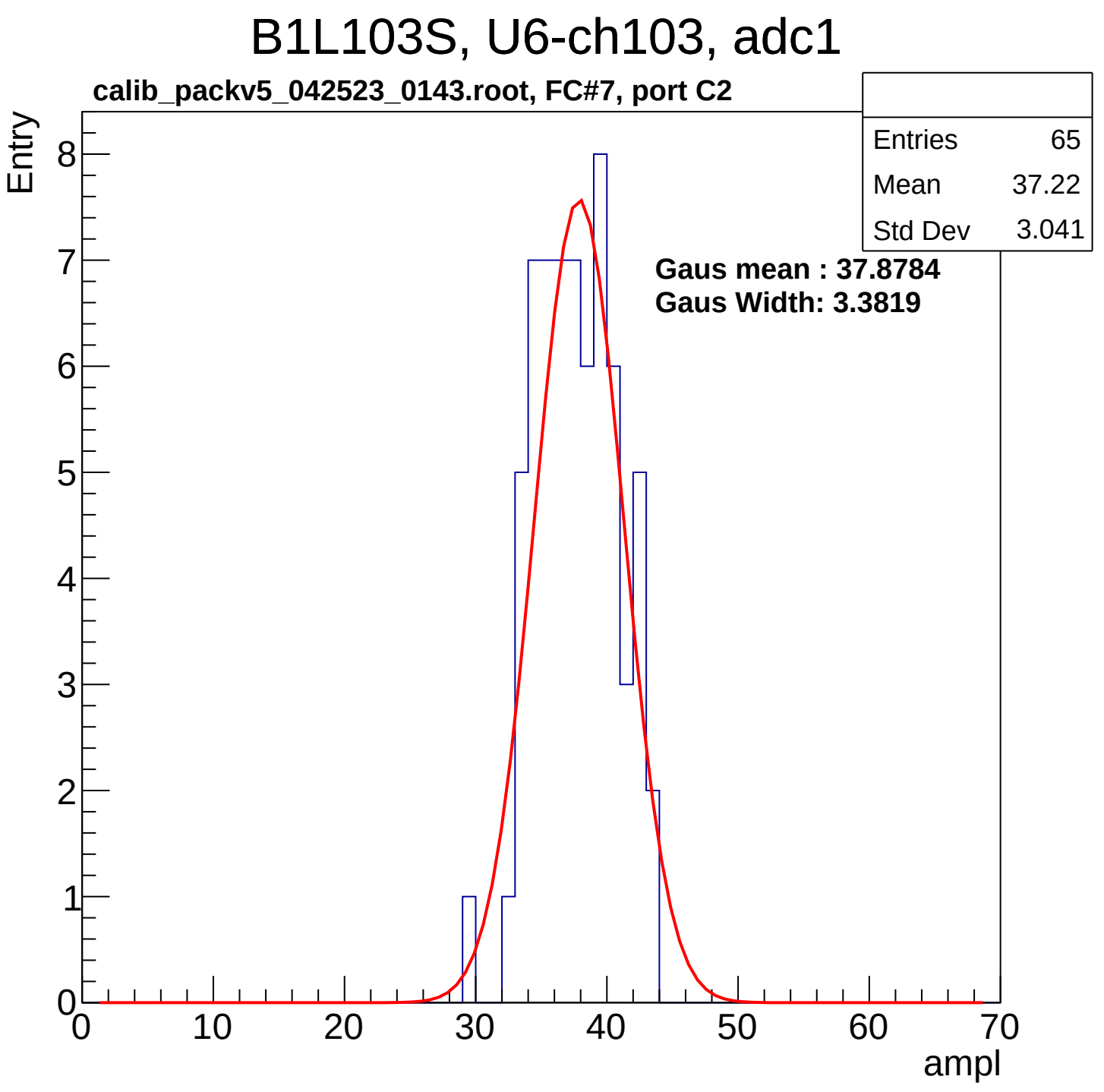
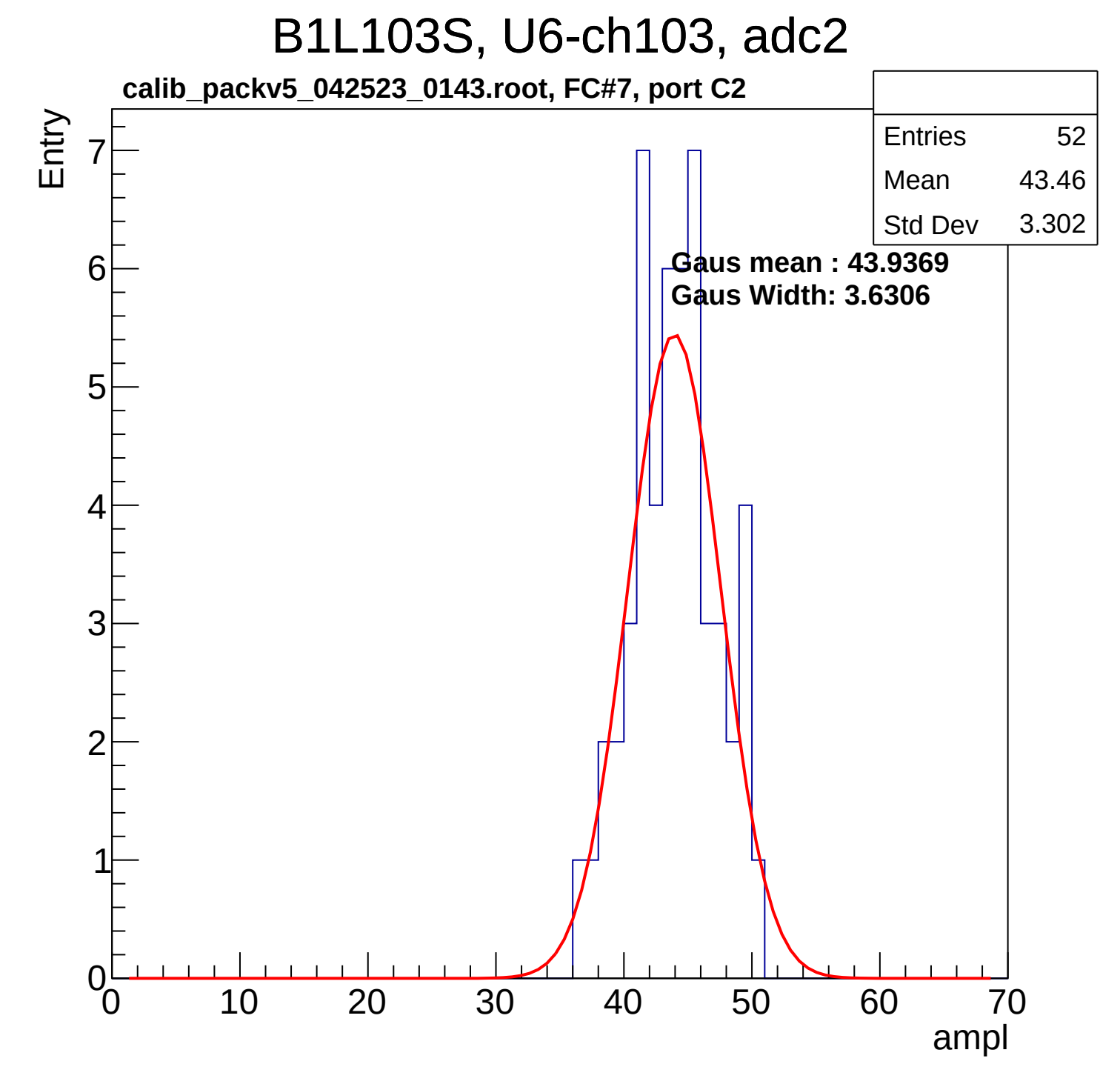
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch103, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 37.22
Std Dev 3.041
Gaus mean : 37.8784
Gaus Width: 3.3819
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch103, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 43.46
Std Dev 3.302
Gaus mean : 43.9369
Gaus Width: 3.6306
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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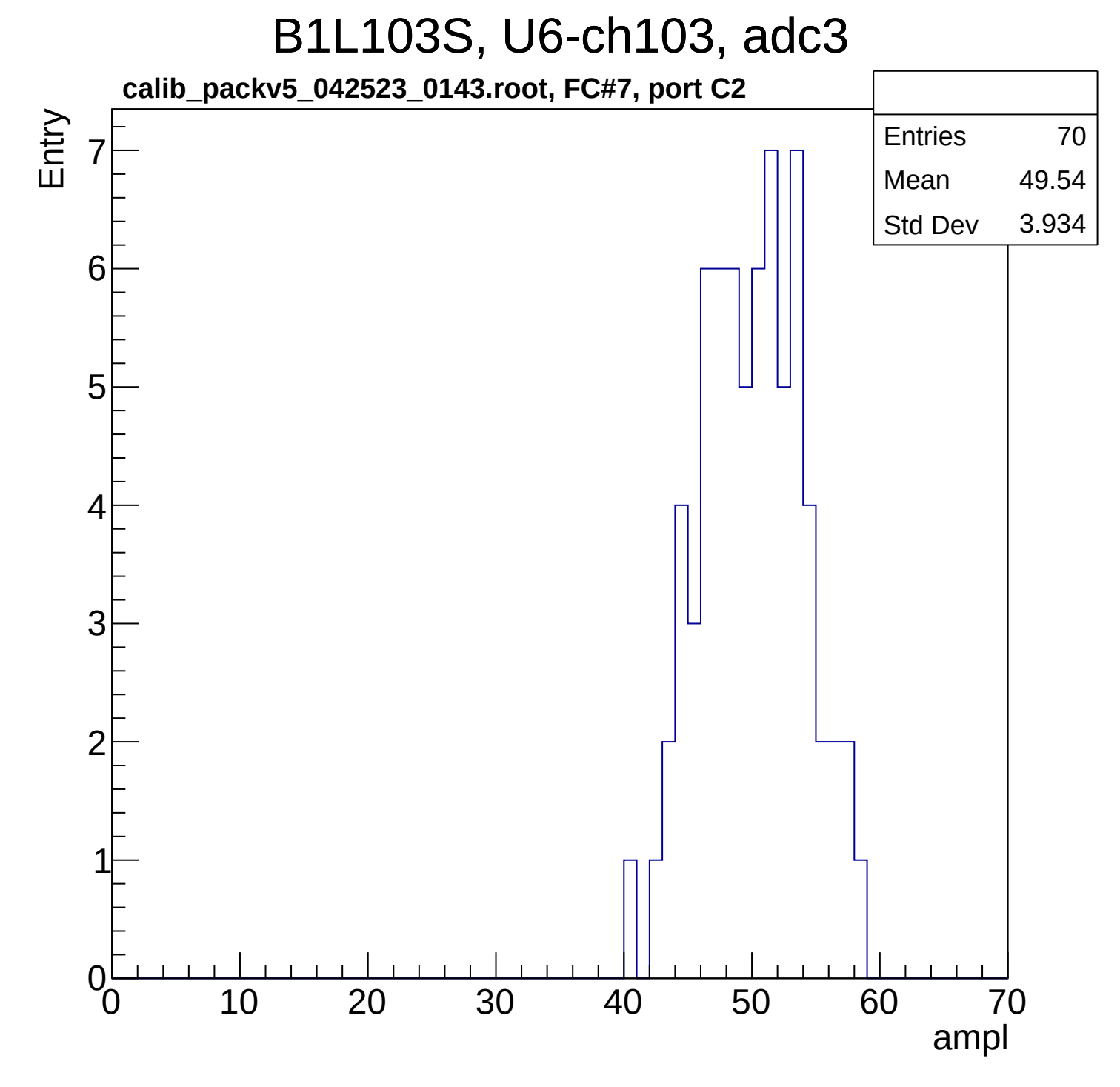
B1L103S, U6-ch103, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.54
Std Dev 3.934
Entry
ampl
0
1
2
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4
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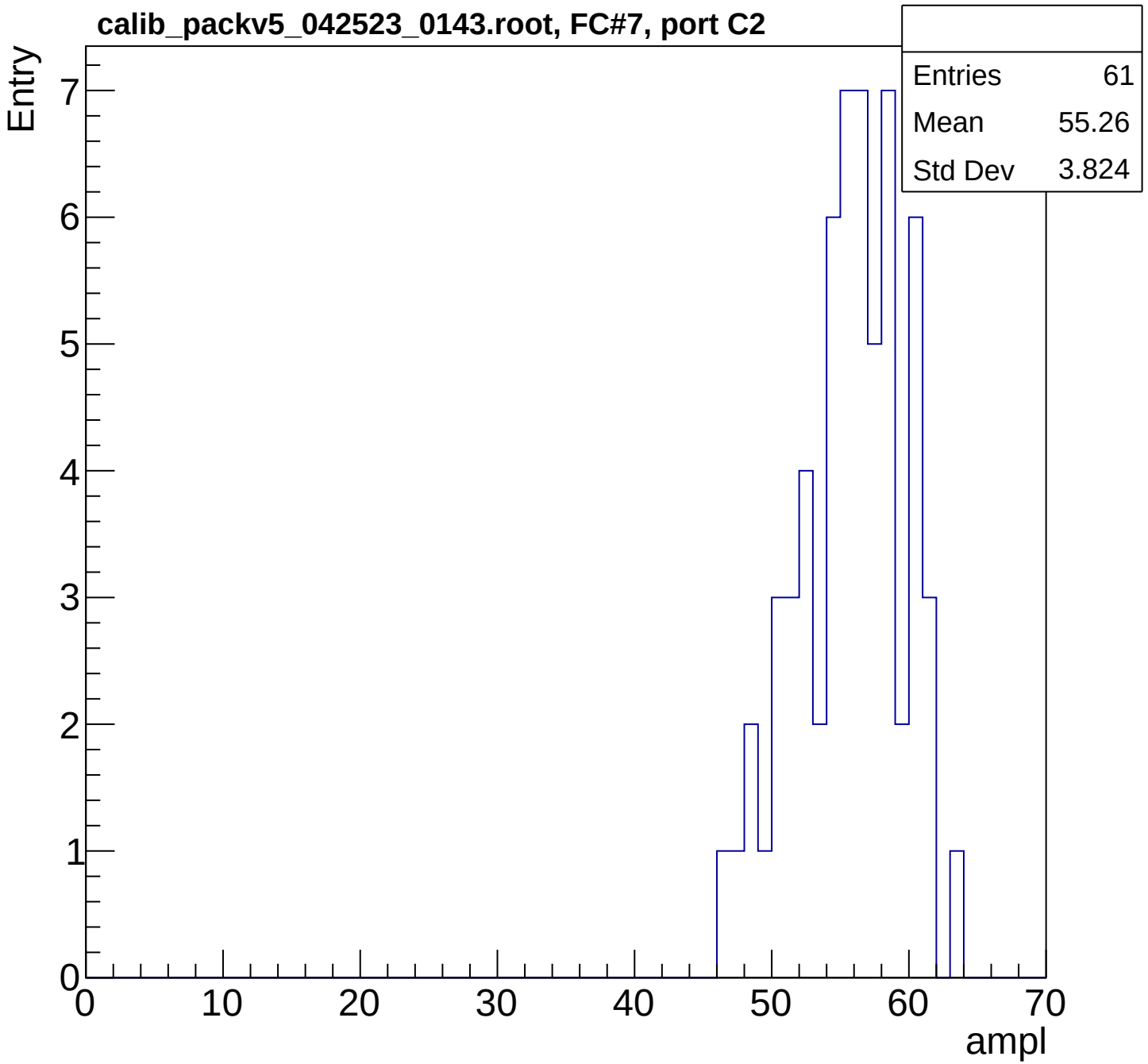
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch103, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 55.26
Std Dev 3.824
Entry
ampl
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6
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0
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40
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60
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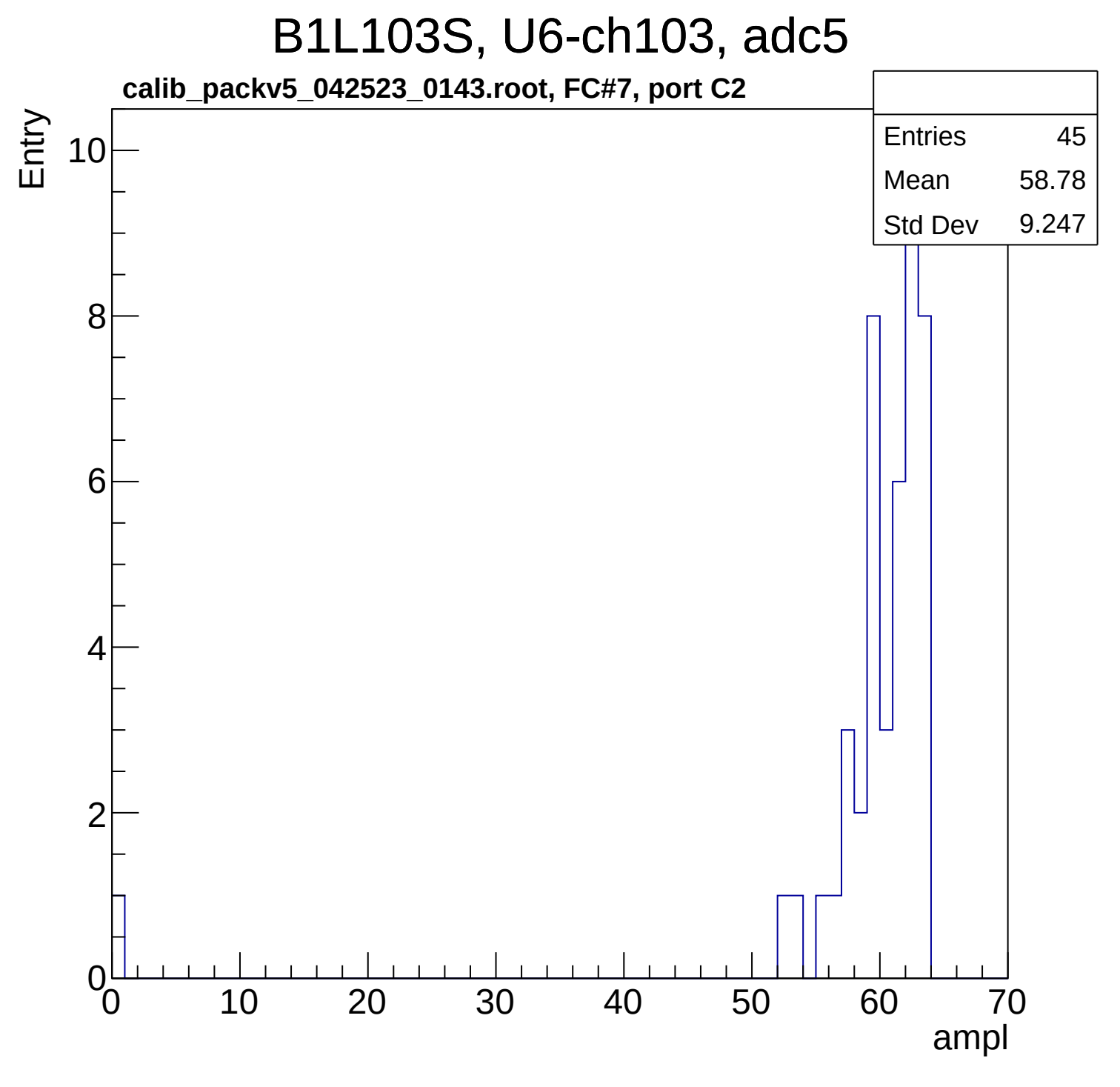
B1L103S, U6-ch103, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.78
Std Dev 9.247
Entry
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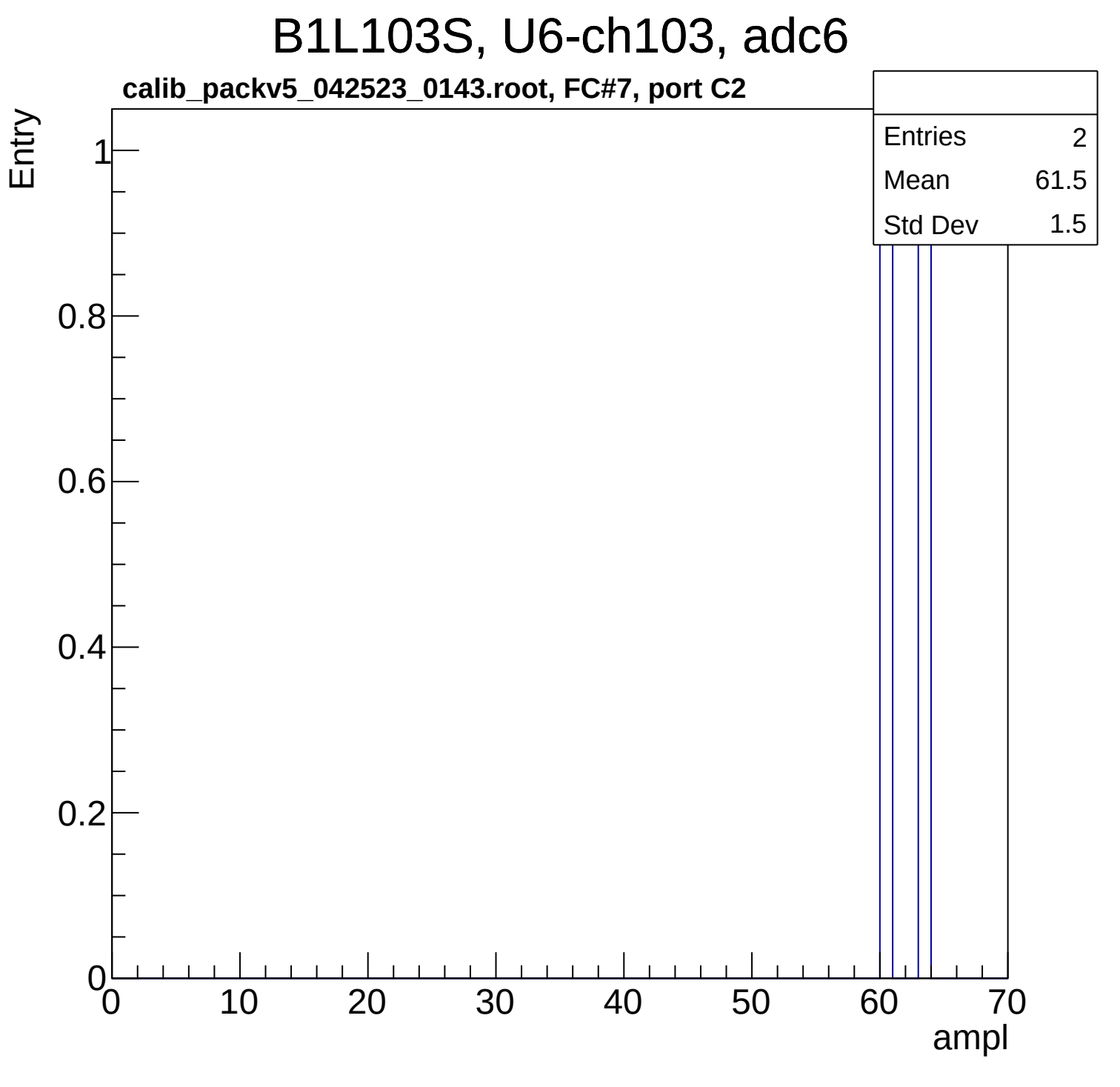
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch103, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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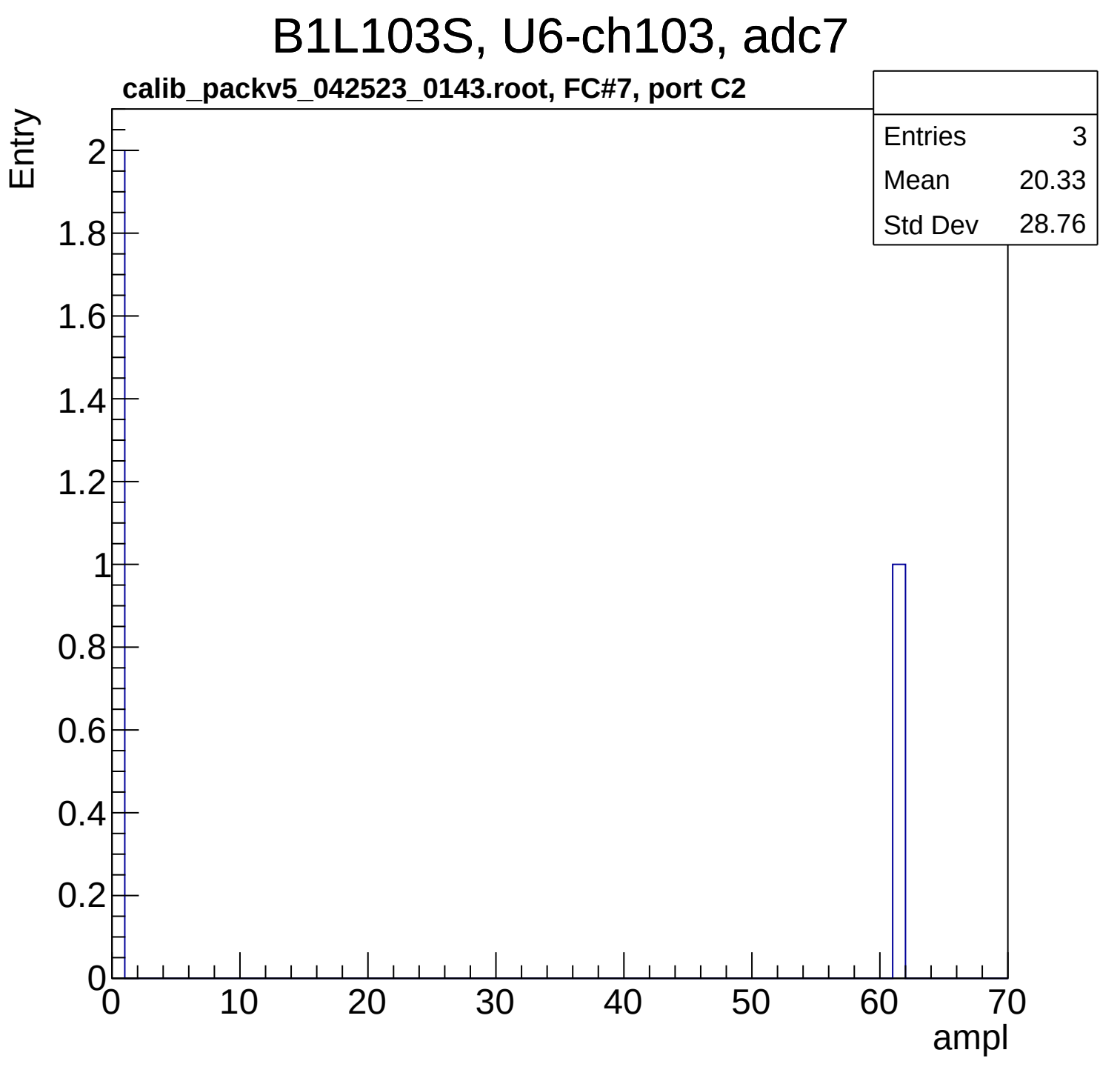
B1L103S, U6-ch103, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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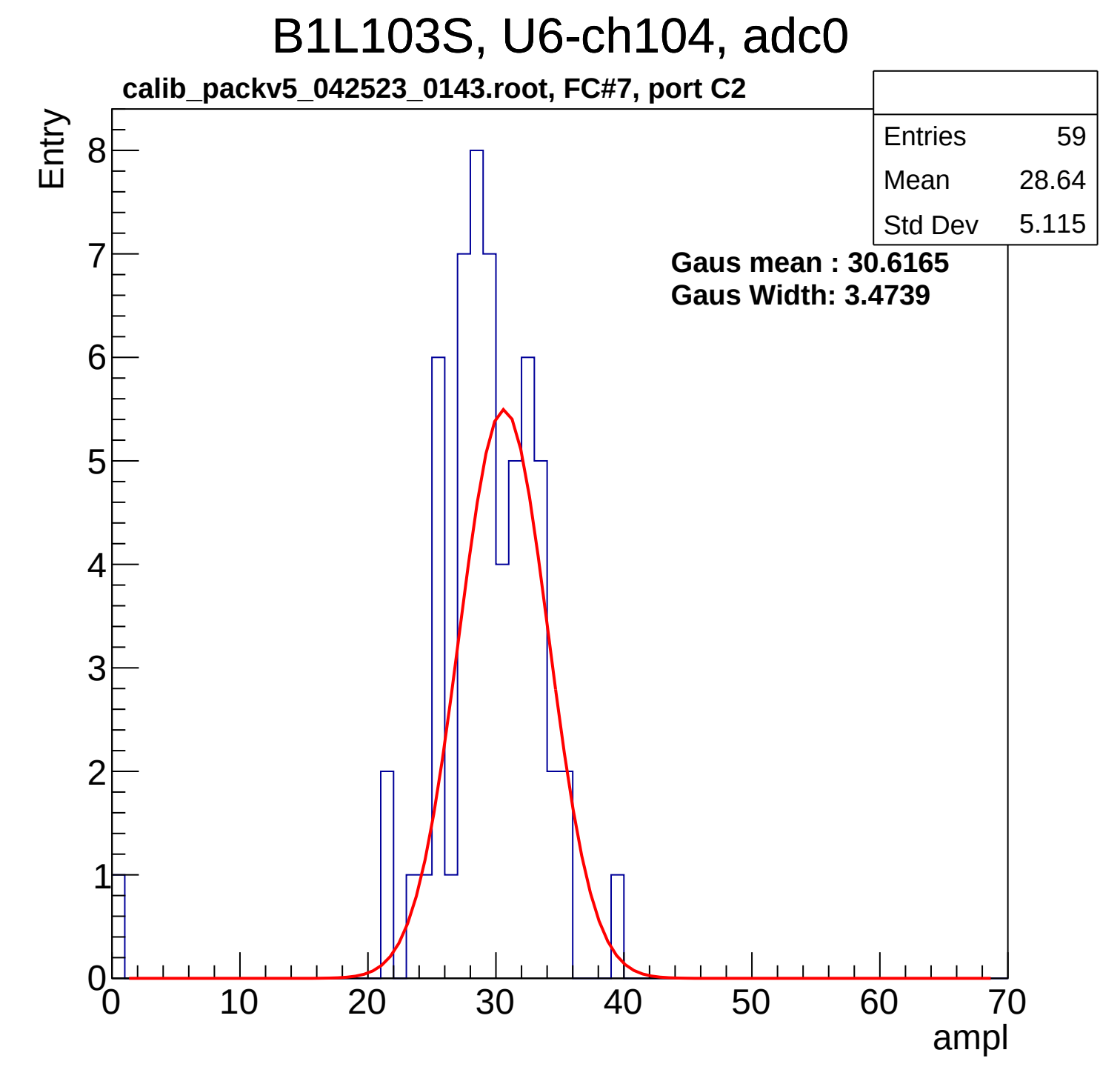
B1L103S, U6-ch104, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 28.64
Std Dev 5.115
Gaus mean : 30.6165
Gaus Width: 3.4739
Entry
ampl
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1
2
3
4
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6
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0
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40
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60
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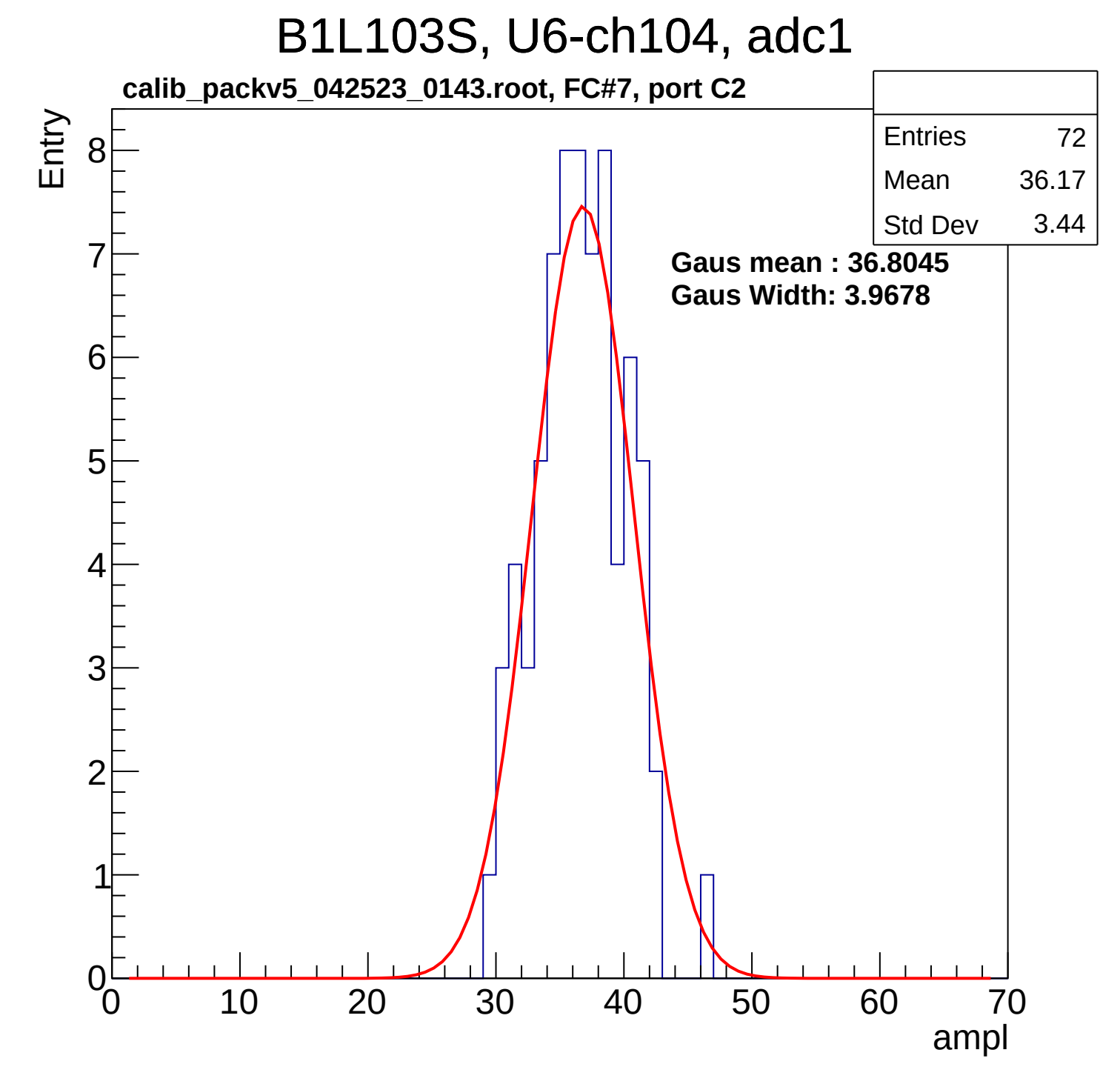
B1L103S, U6-ch104, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.17
Std Dev 3.44
Gaus mean : 36.8045
Gaus Width: 3.9678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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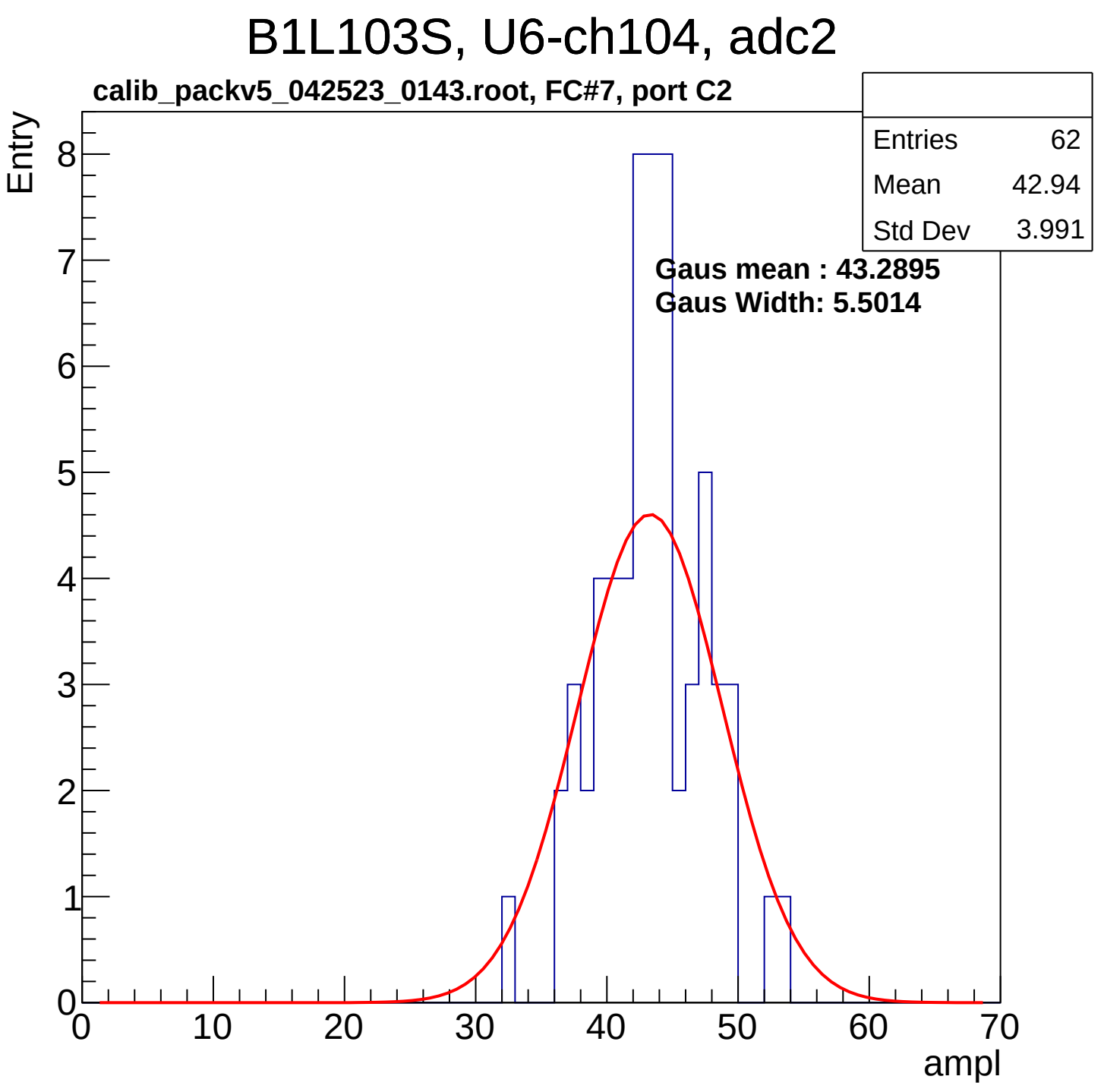
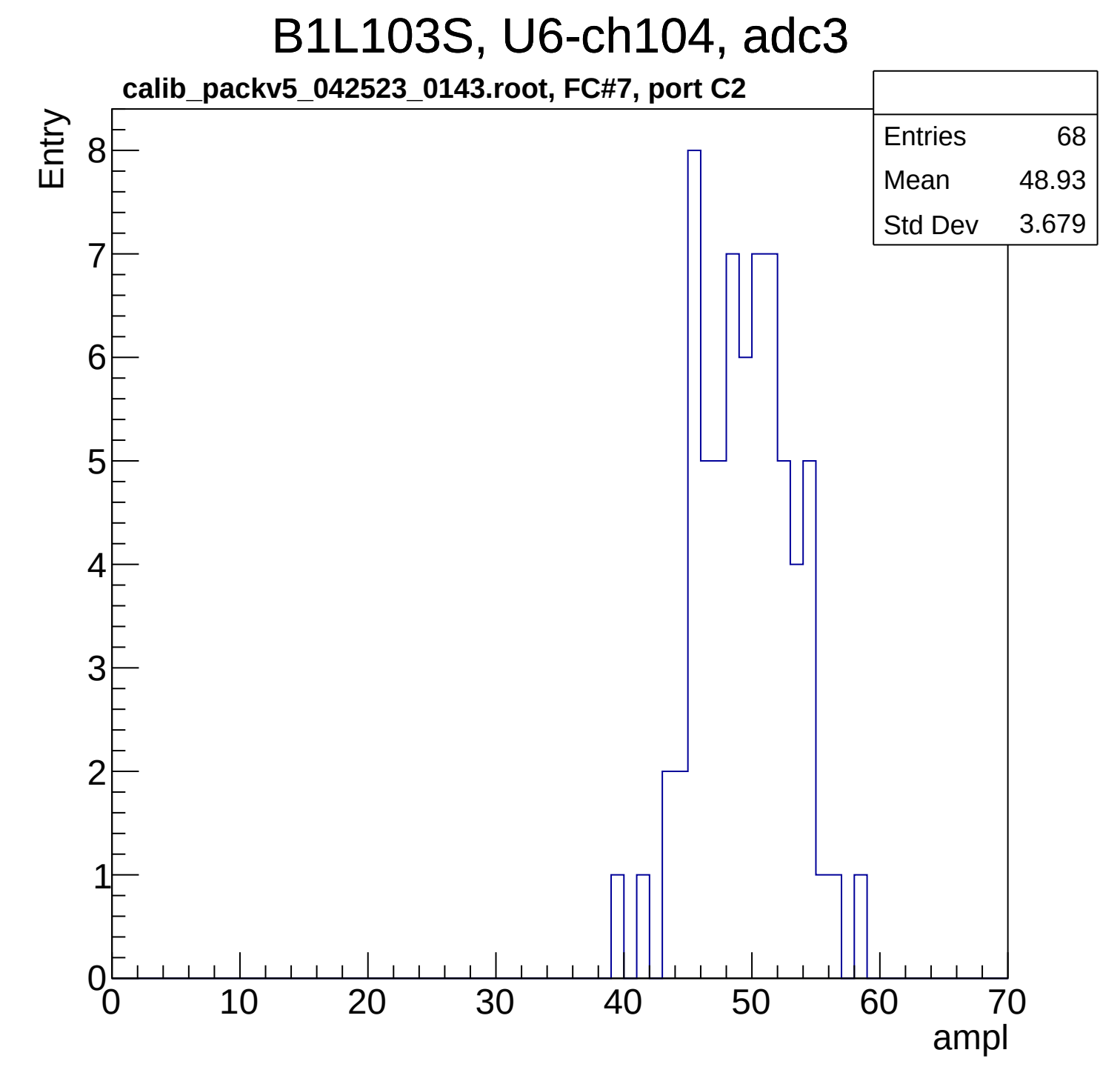

B1L103S, U6-ch104, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.94
Std Dev 3.991
Gaus mean : 43.2895
Gaus Width: 5.5014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch104, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 48.93
Std Dev 3.679
Entry
ampl
0
1
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4
5
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7
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0
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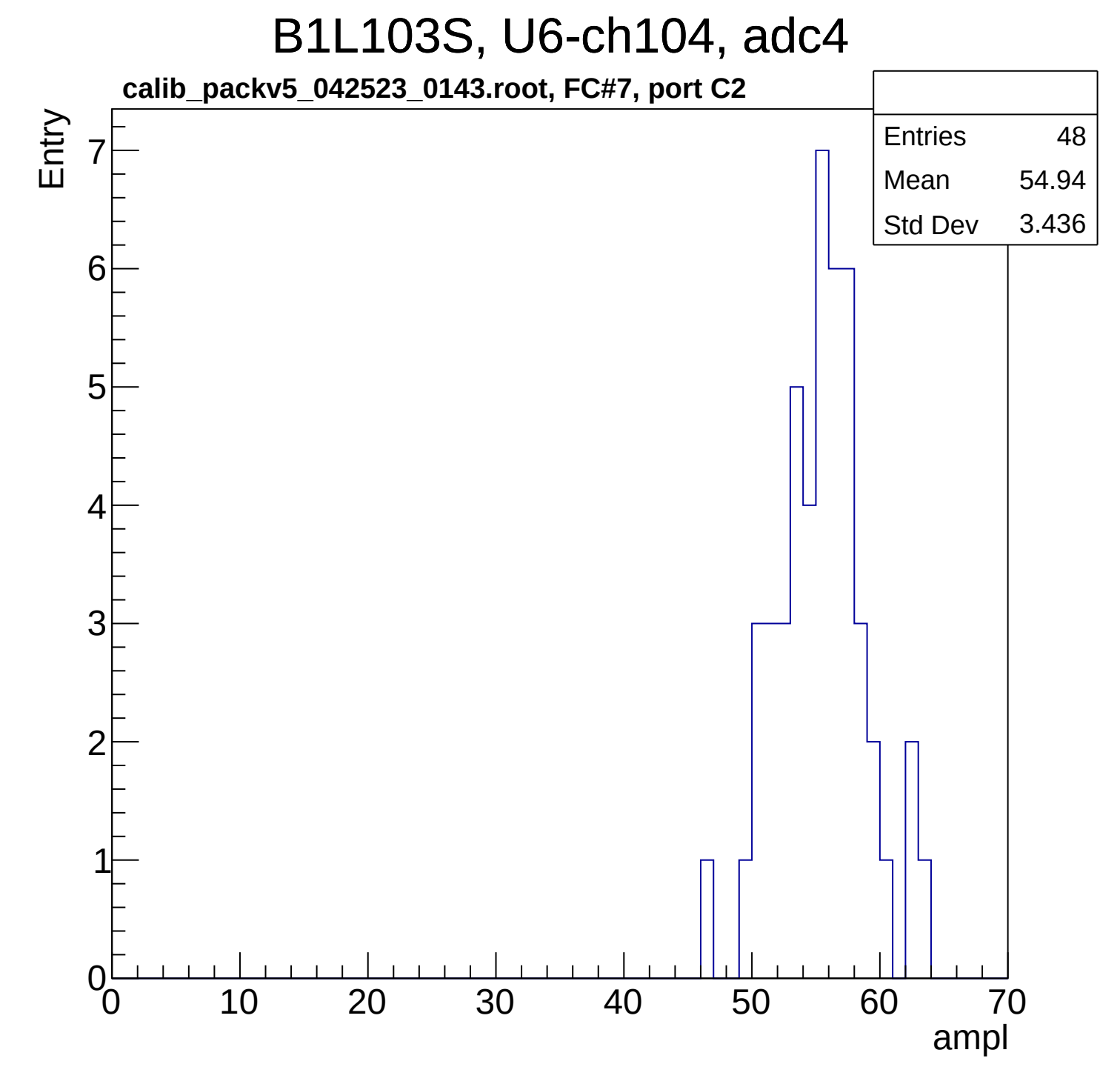
40
50
60
70


B1L103S, U6-ch104, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.94
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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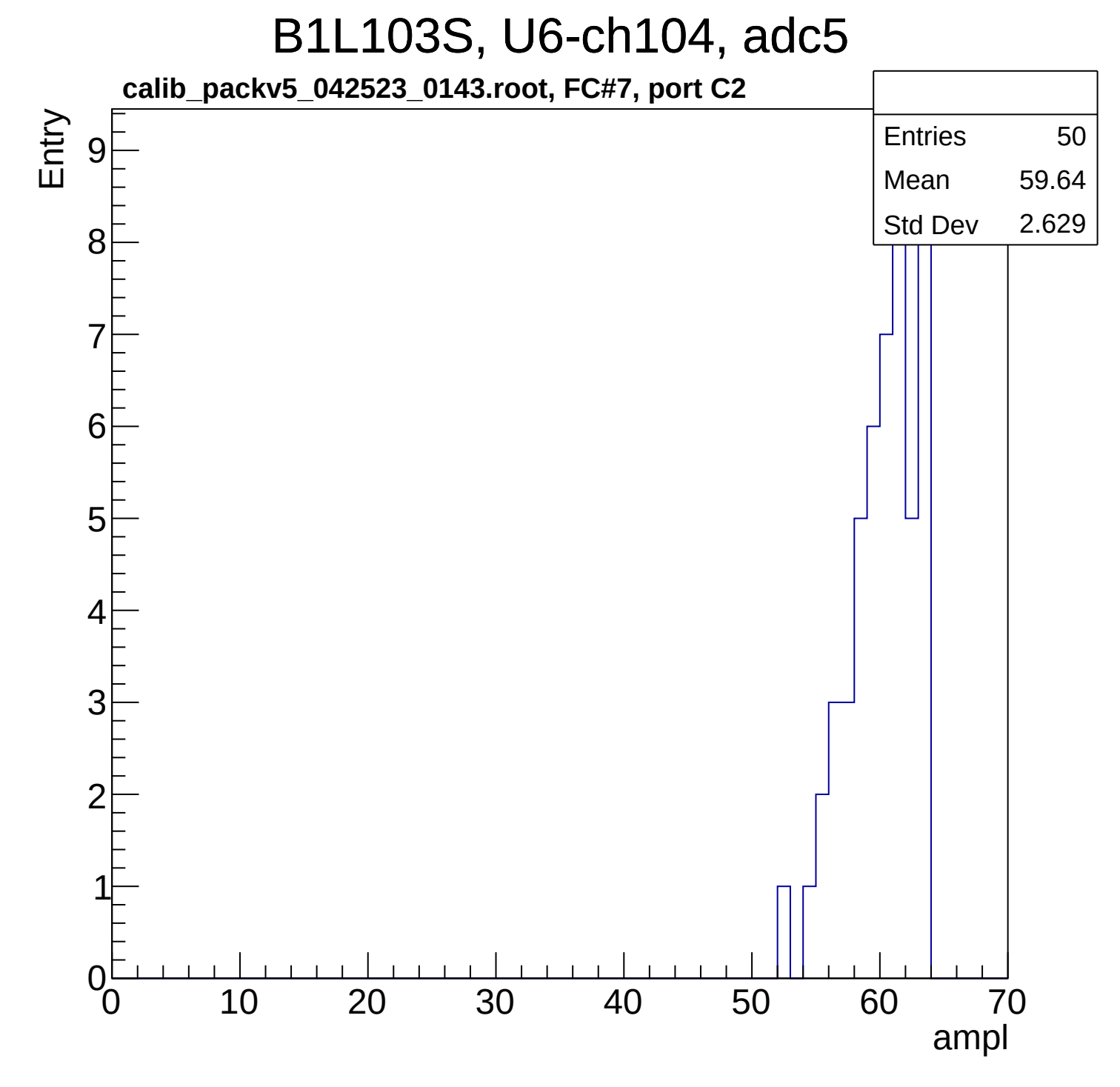
B1L103S, U6-ch104, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.64
Std Dev 2.629
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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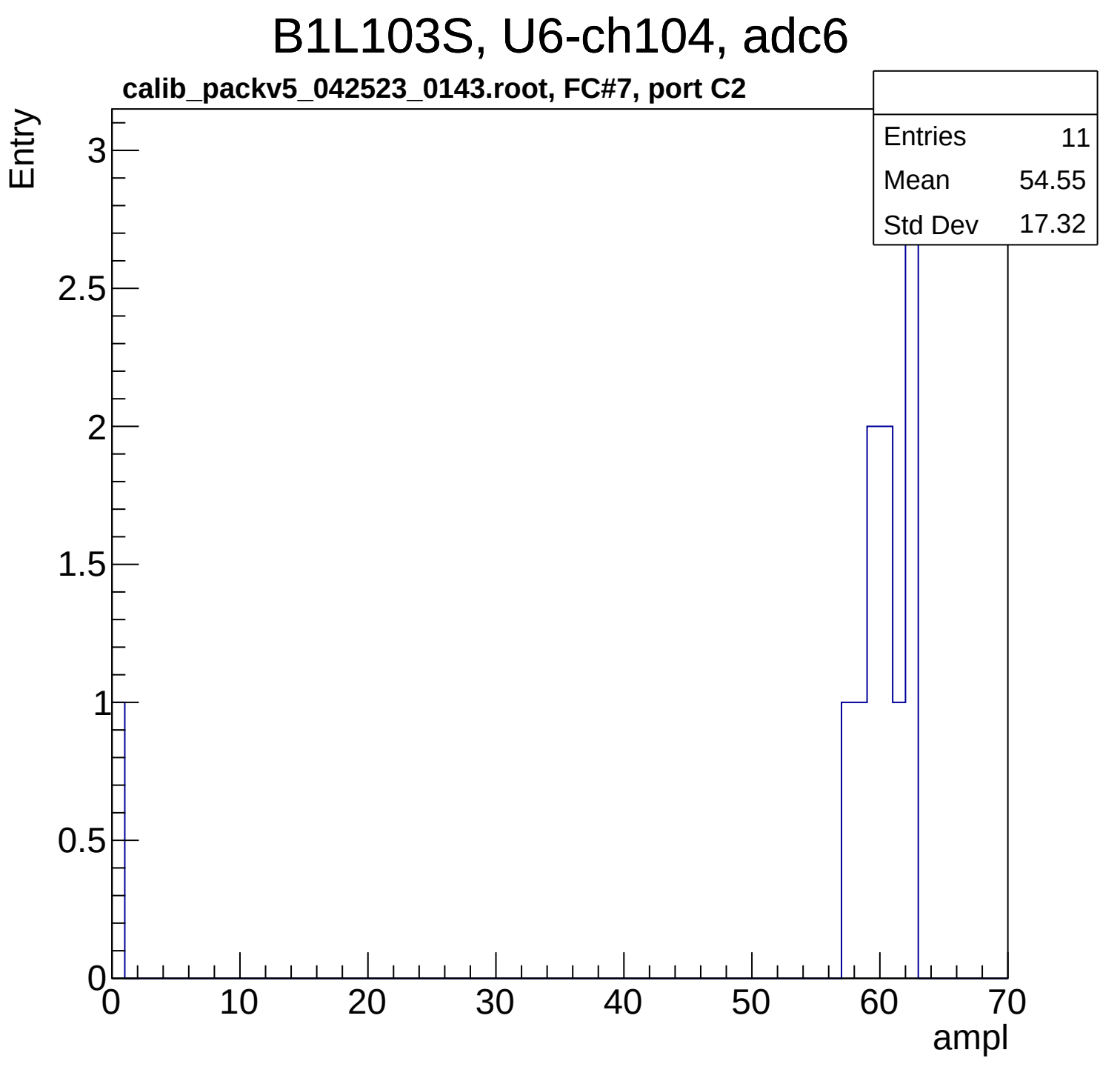
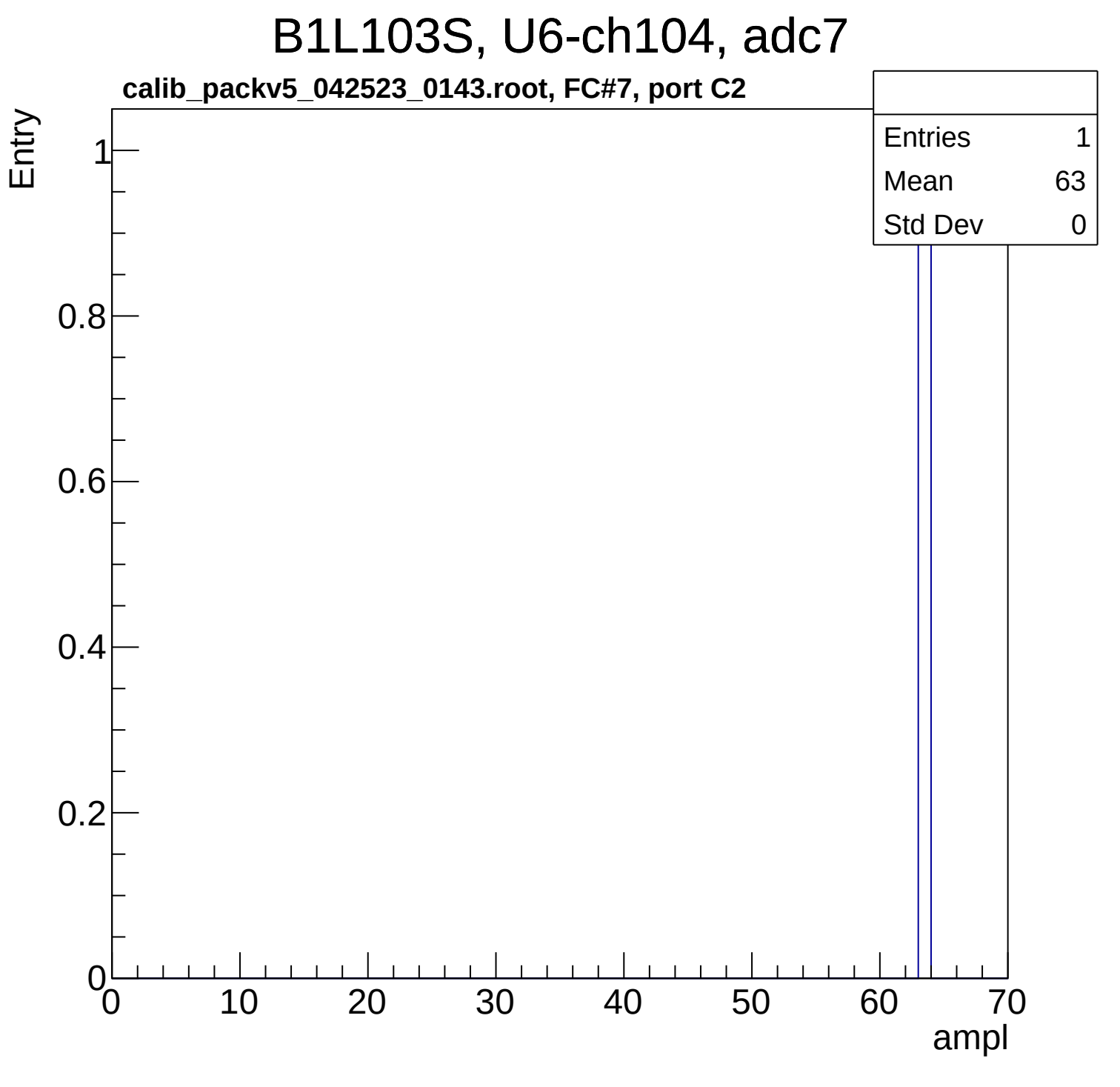
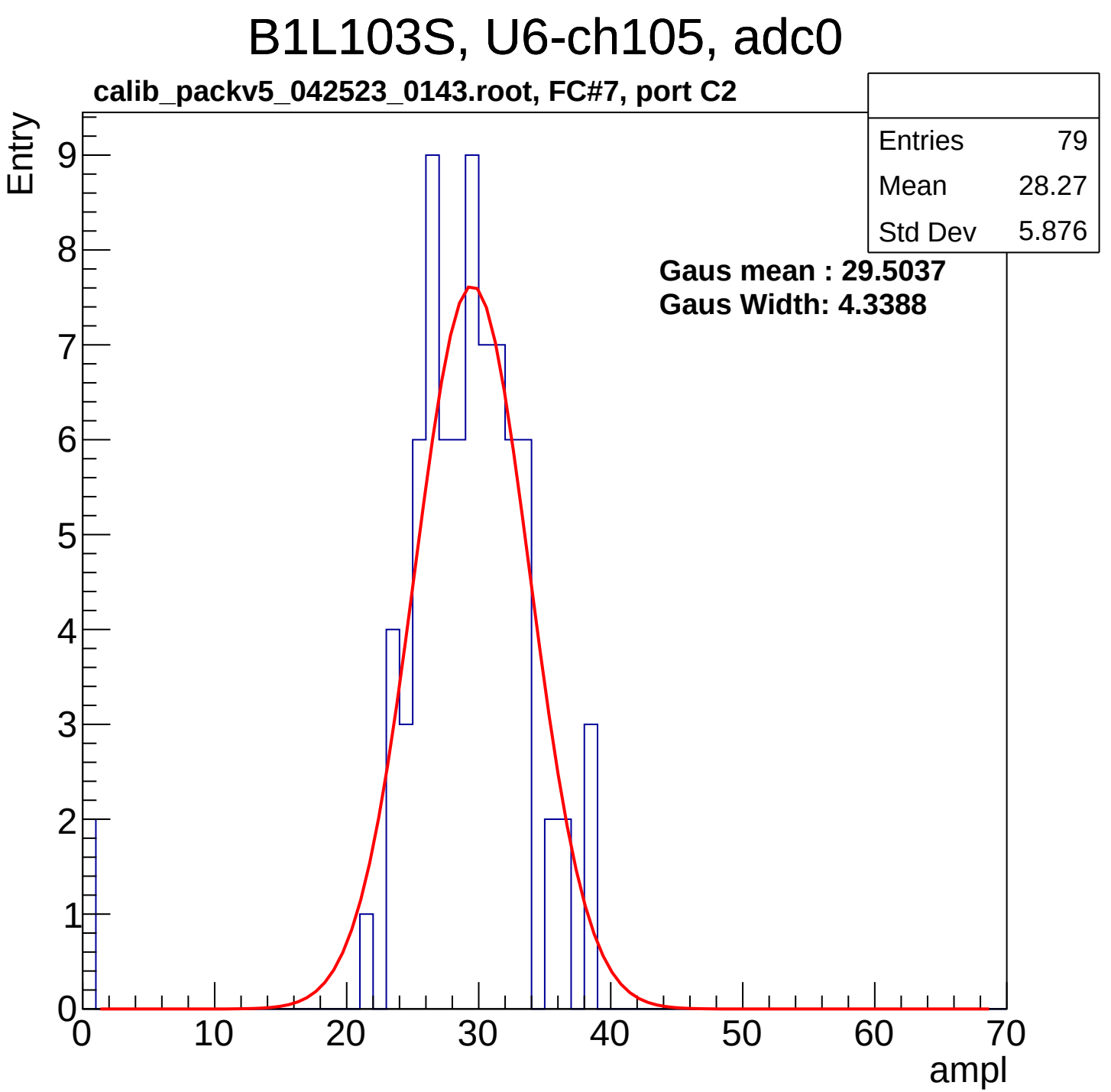

B1L103S, U6-ch104, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 54.55
Std Dev 17.32
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch104, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch105, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 28.27
Std Dev 5.876
Gaus mean : 29.5037
Gaus Width: 4.3388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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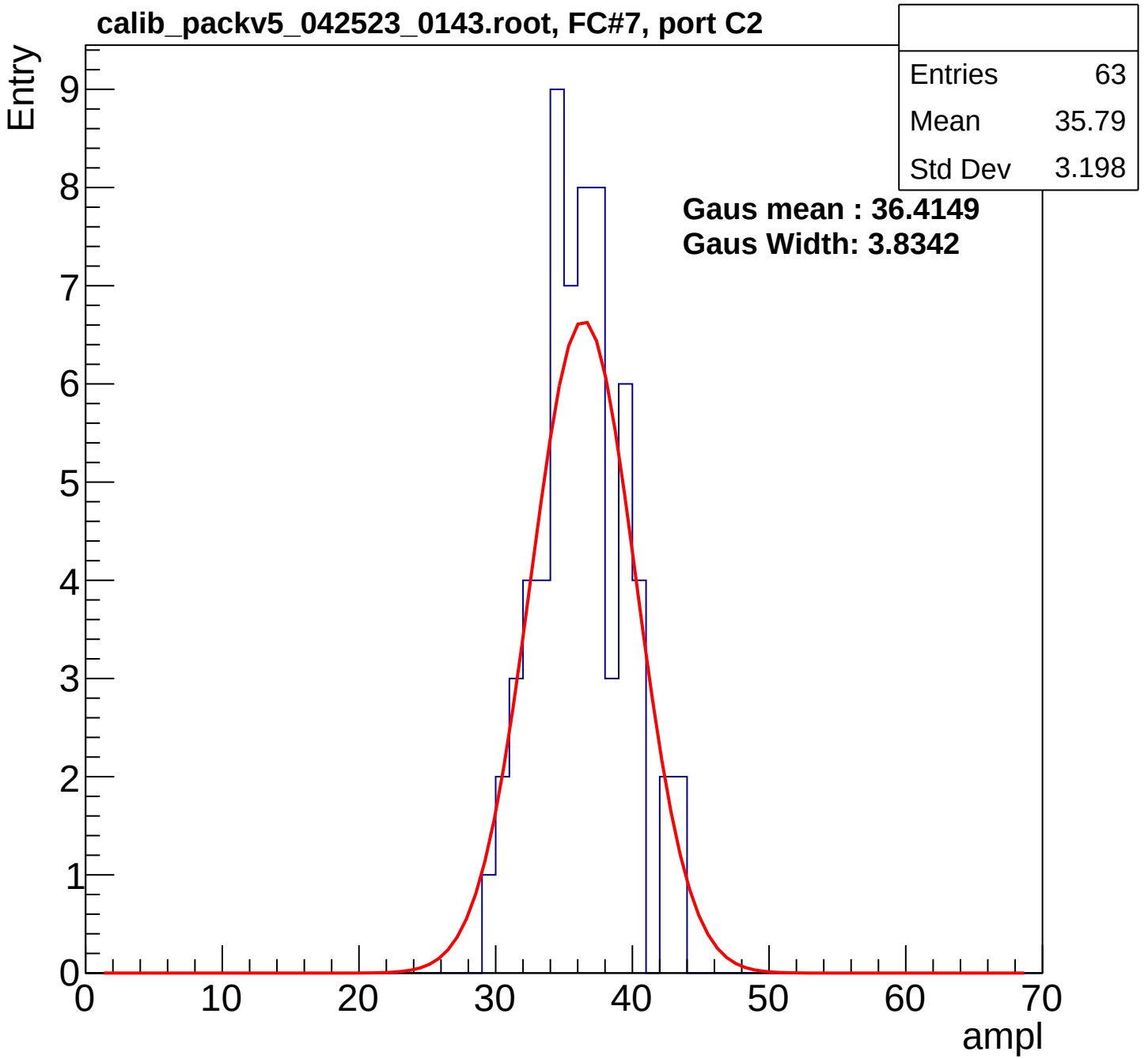
B1L103S, U6-ch105, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.79
Std Dev 3.198
Gaus mean : 36.4149
Gaus Width: 3.8342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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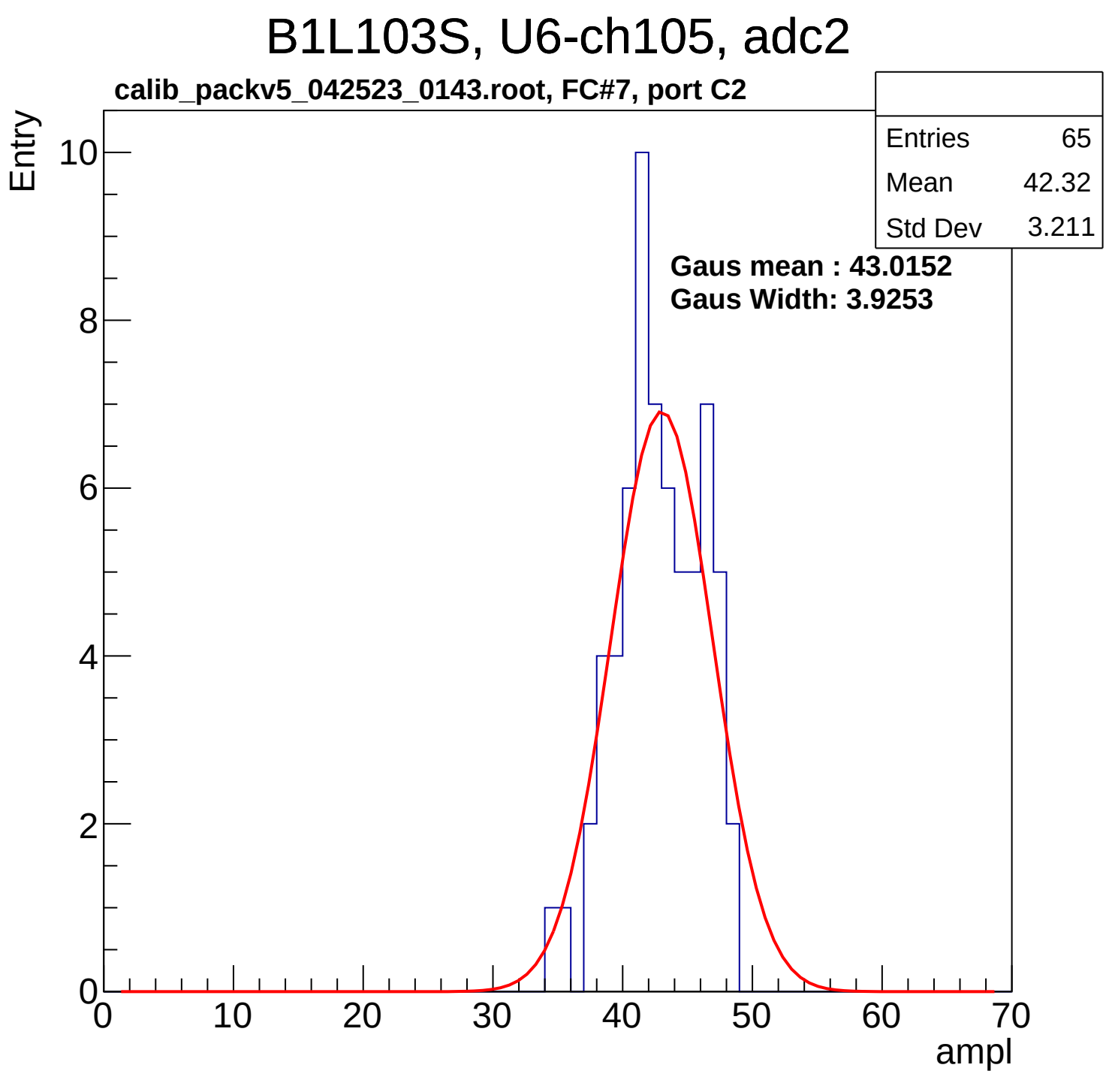
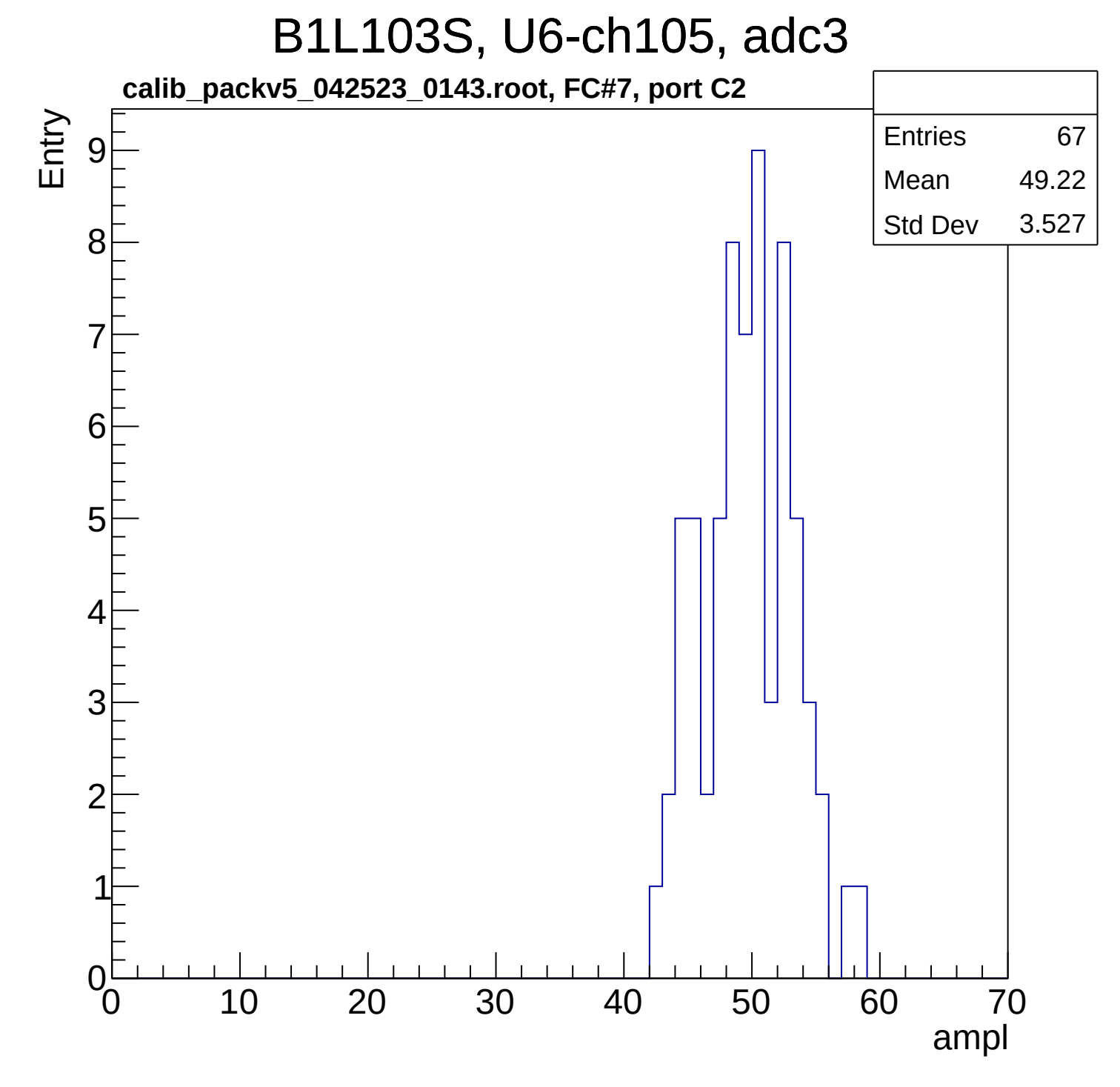
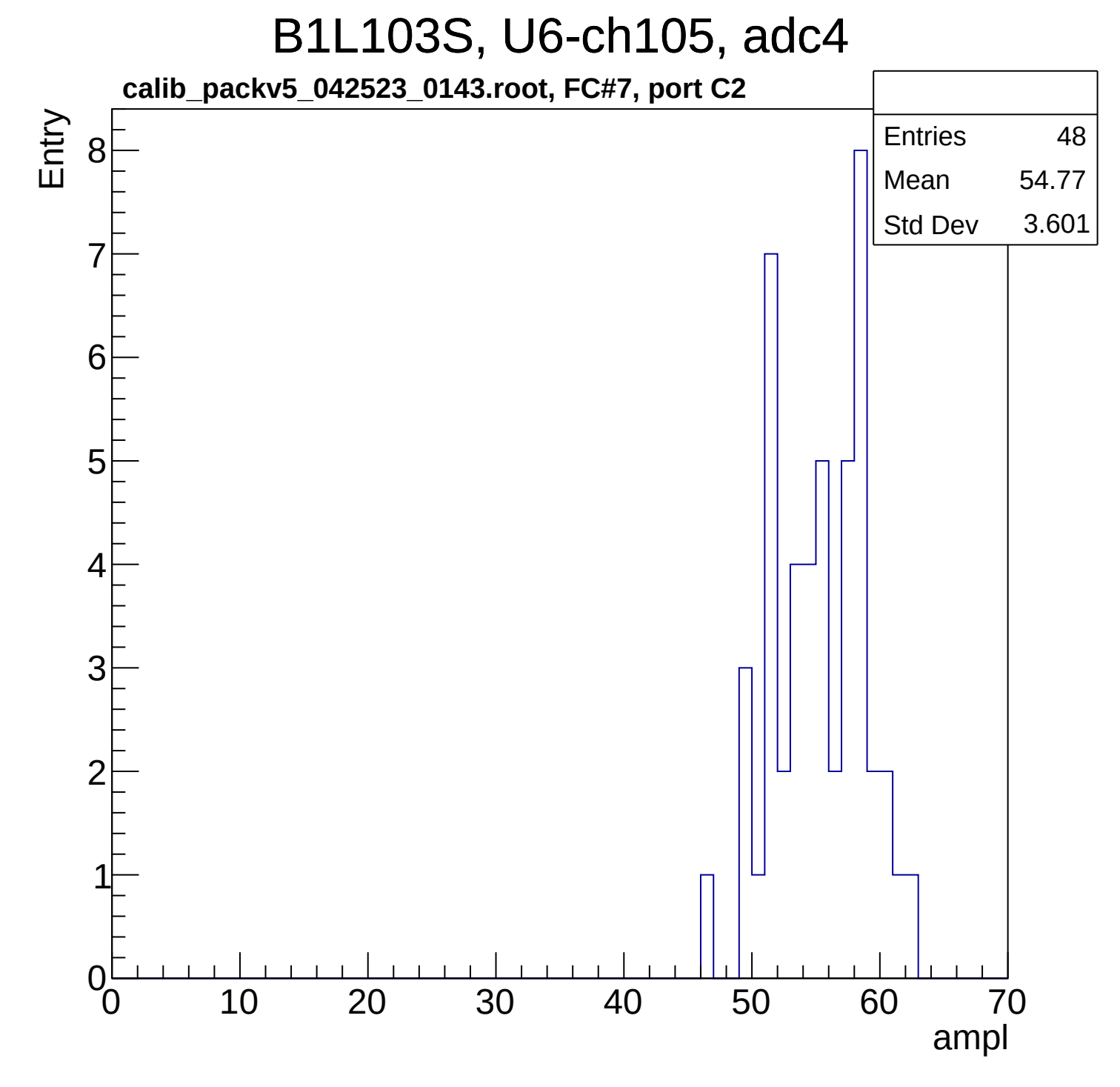
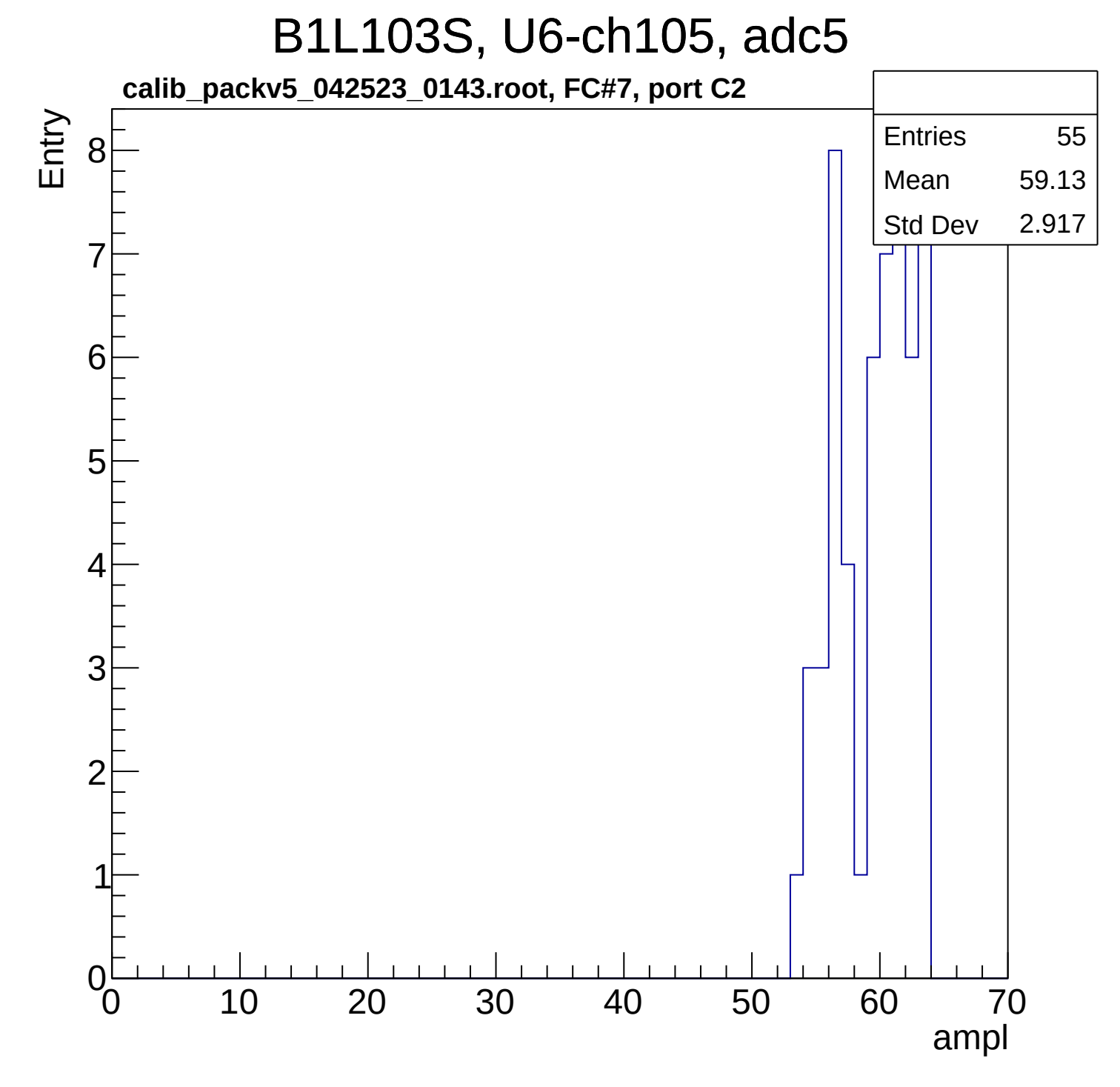

B1L103S, U6-ch105, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.32
Std Dev 3.211
Gaus mean : 43.0152
Gaus Width: 3.9253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch105, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 49.22
Std Dev 3.527
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch105, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.77
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch105, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 59.13
Std Dev 2.917
Entry
ampl
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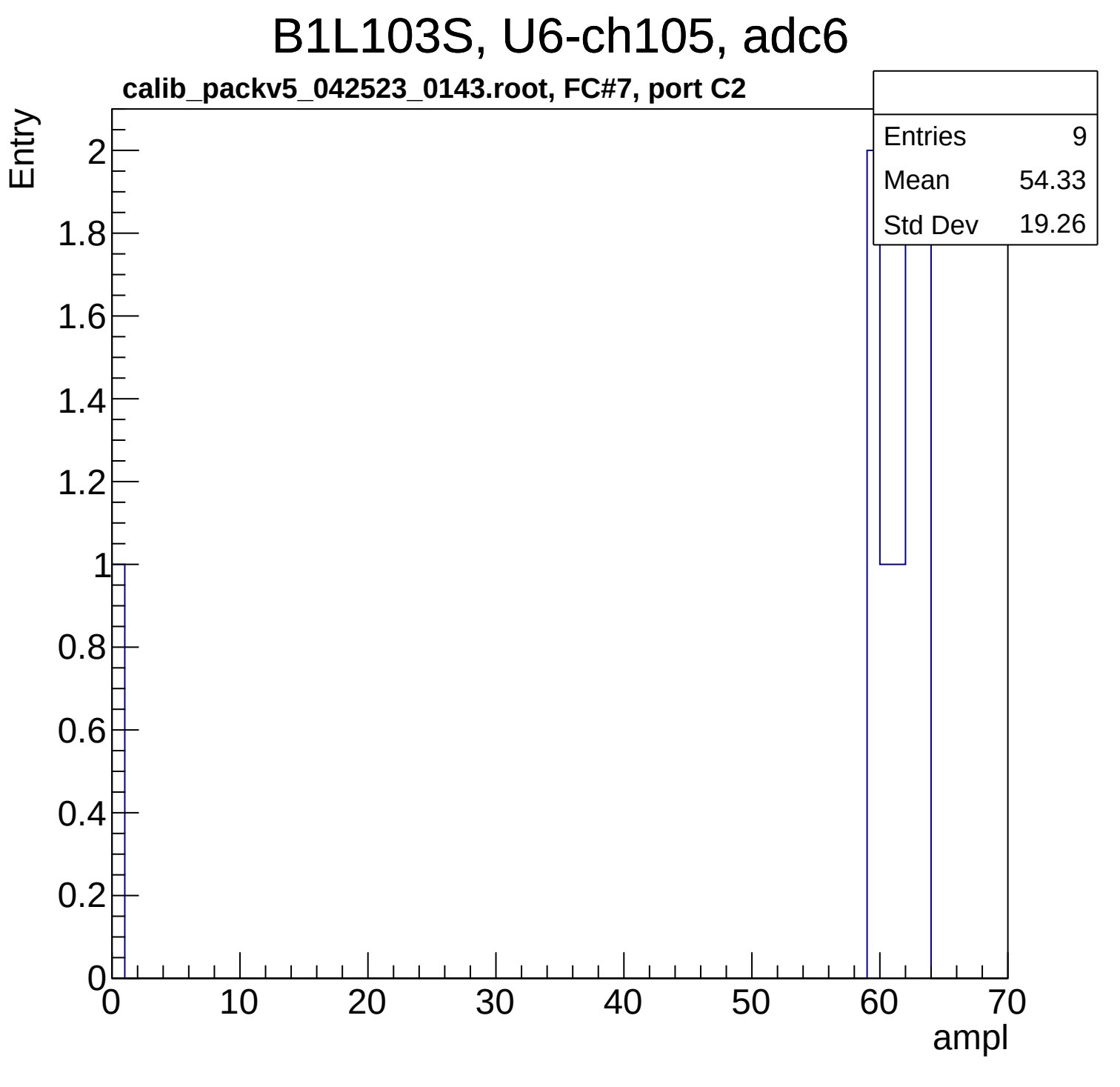
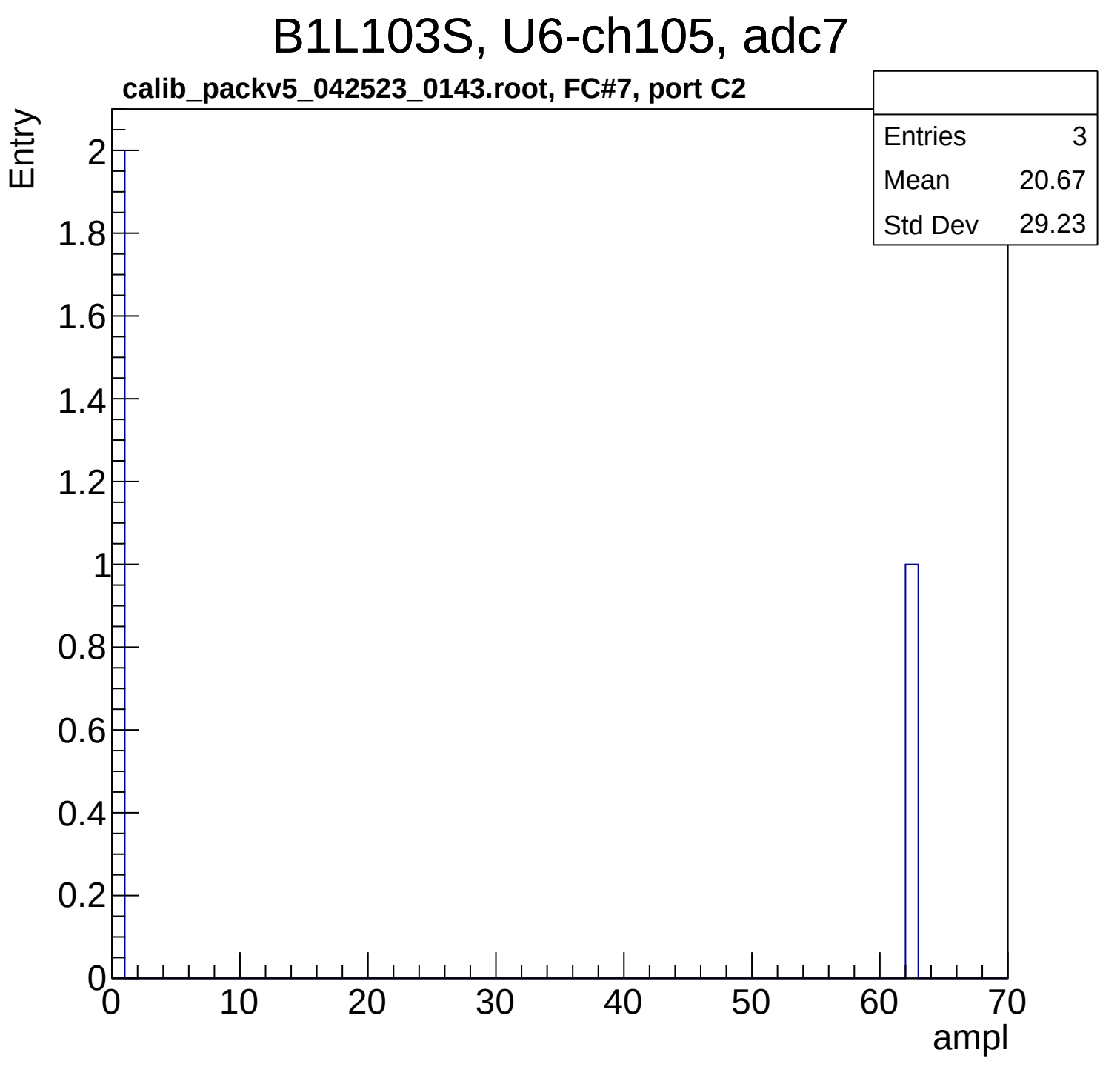
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch105, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 54.33
Std Dev 19.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch105, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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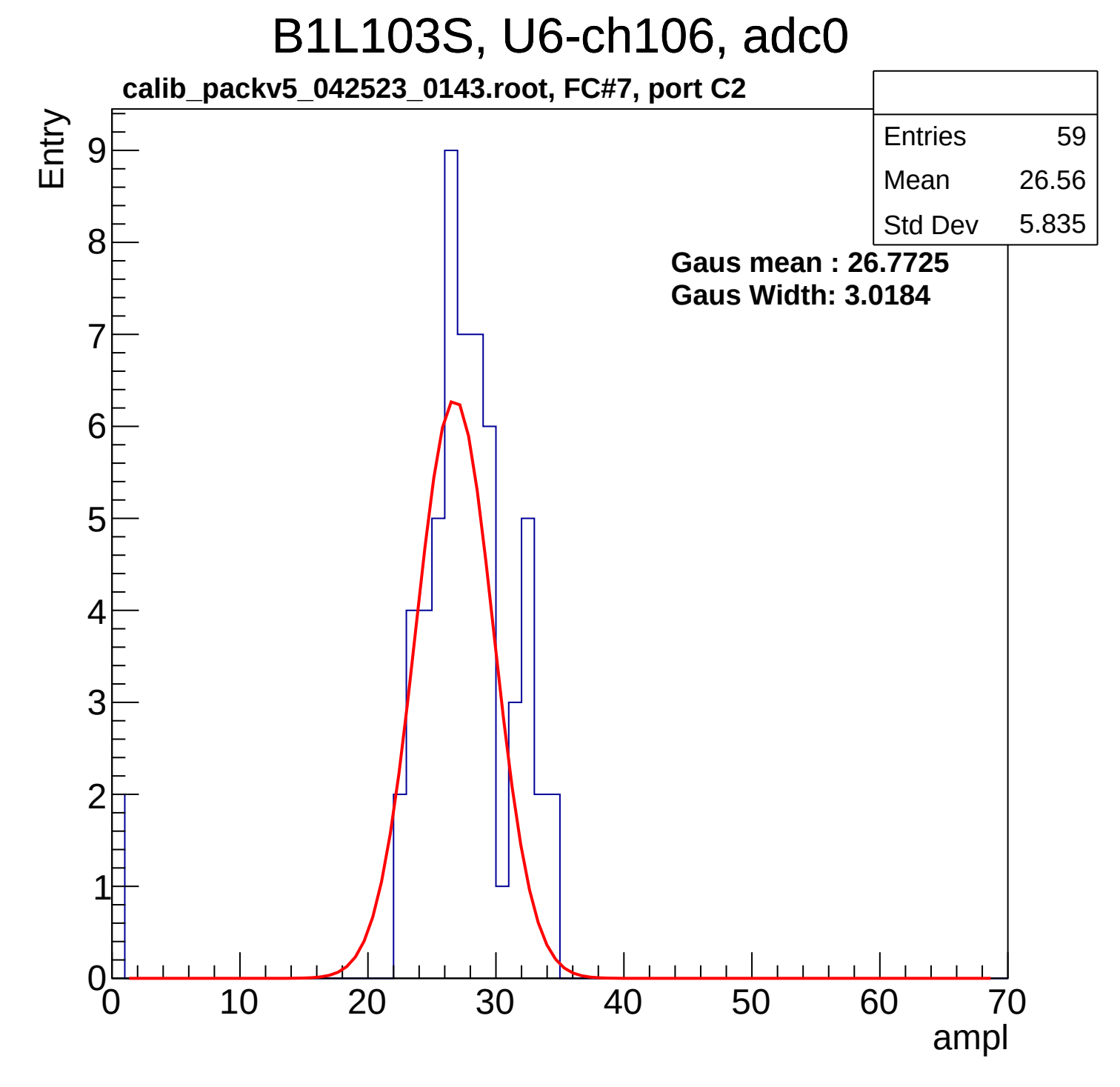
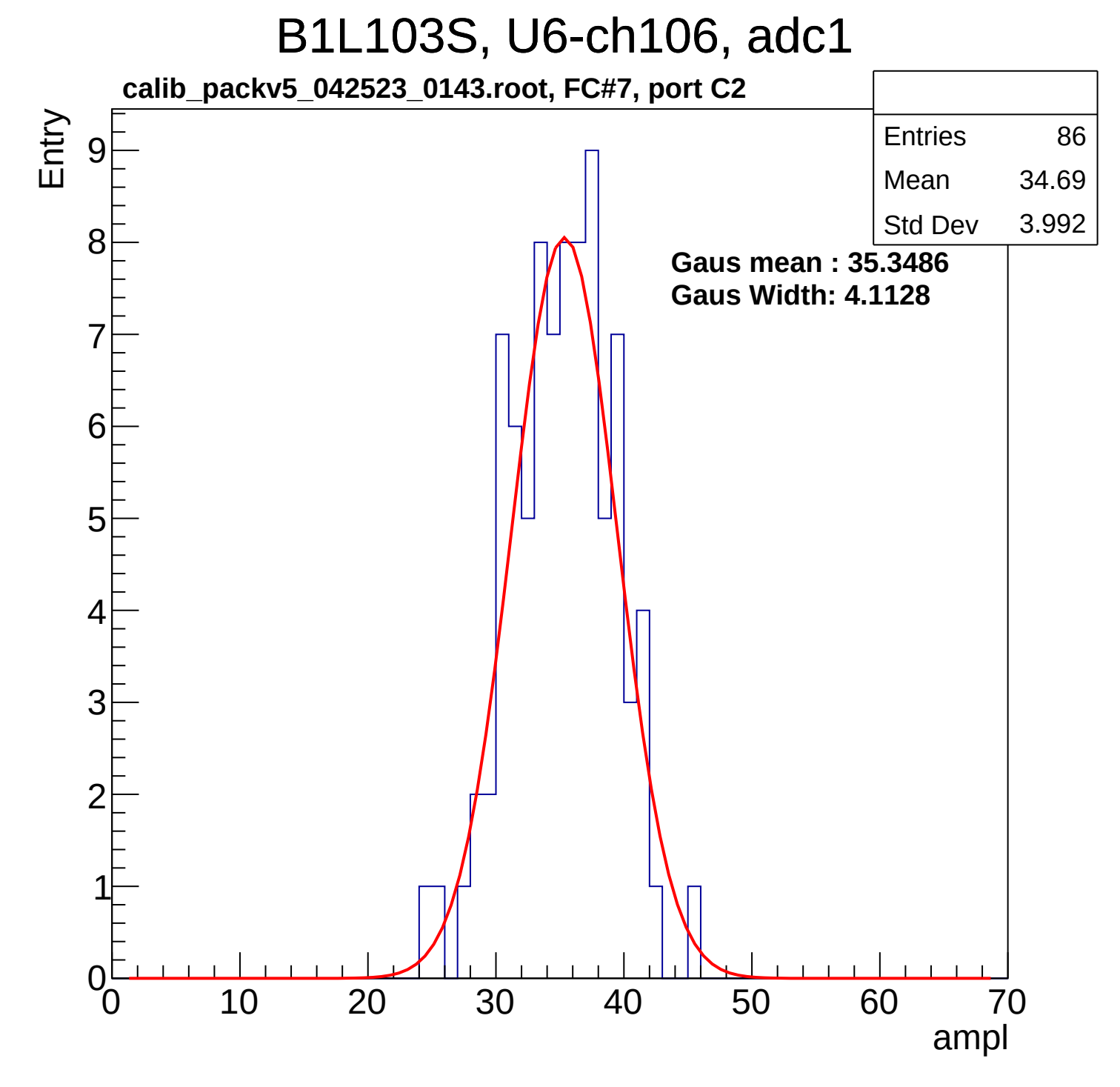
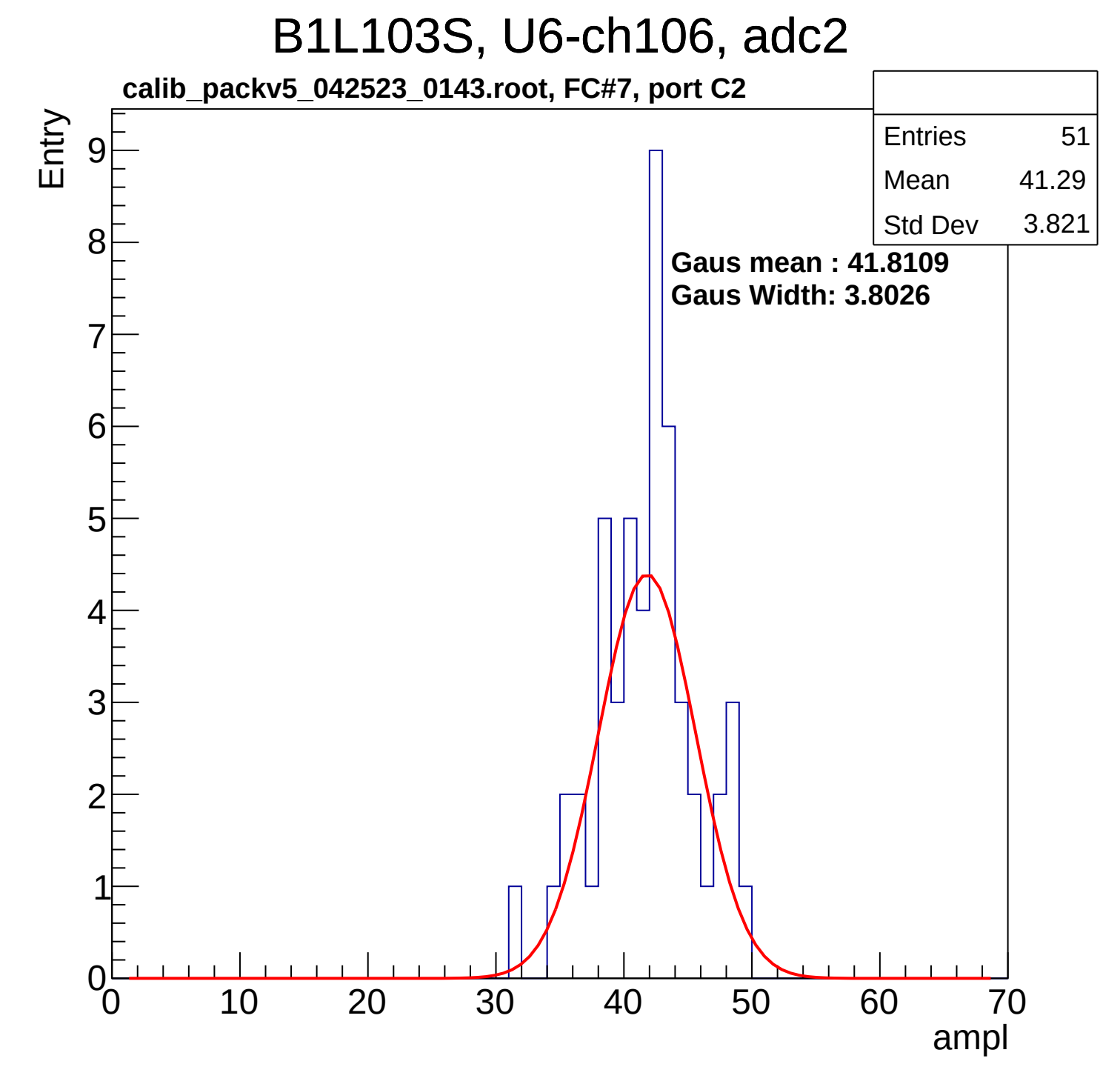
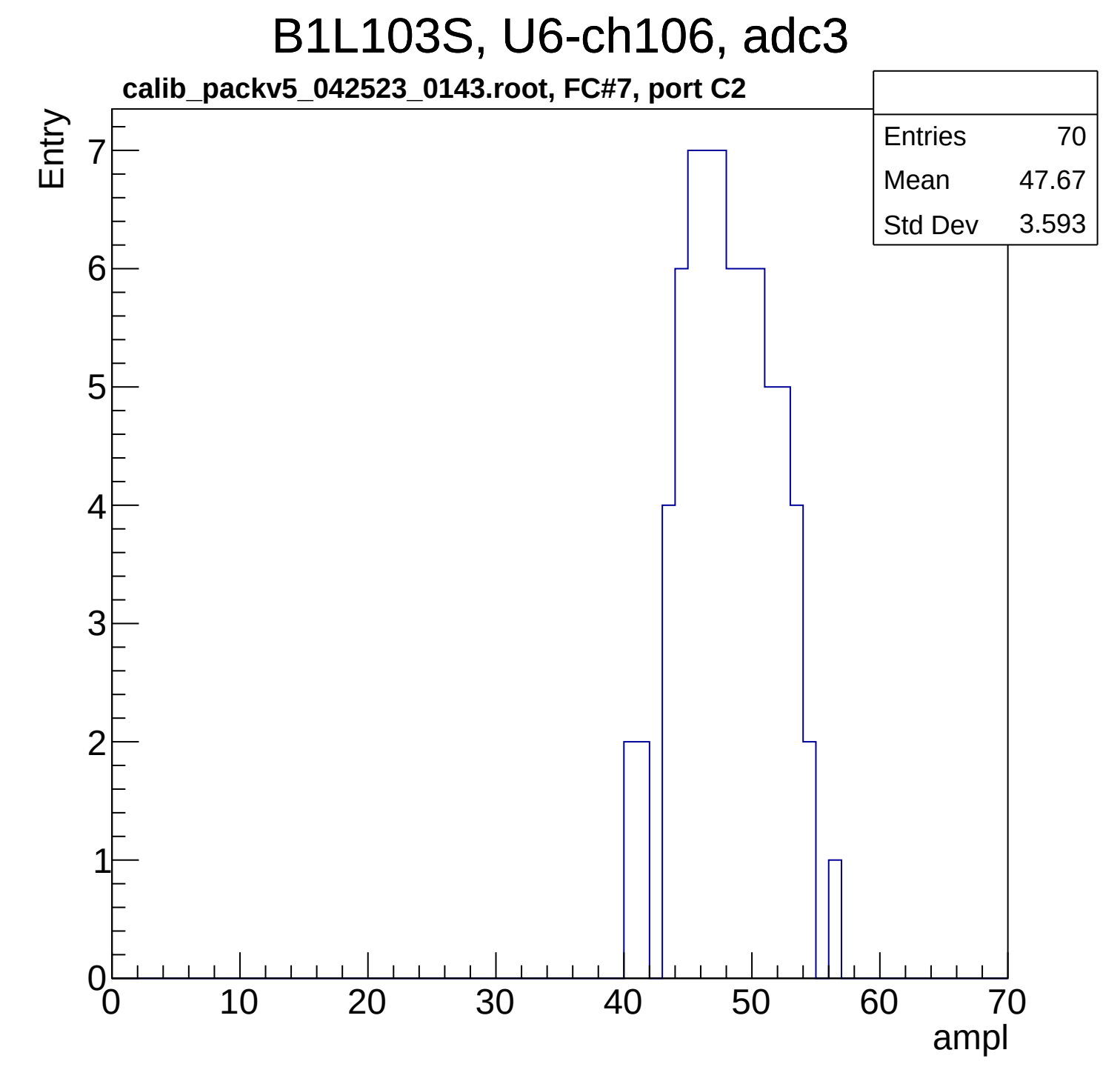
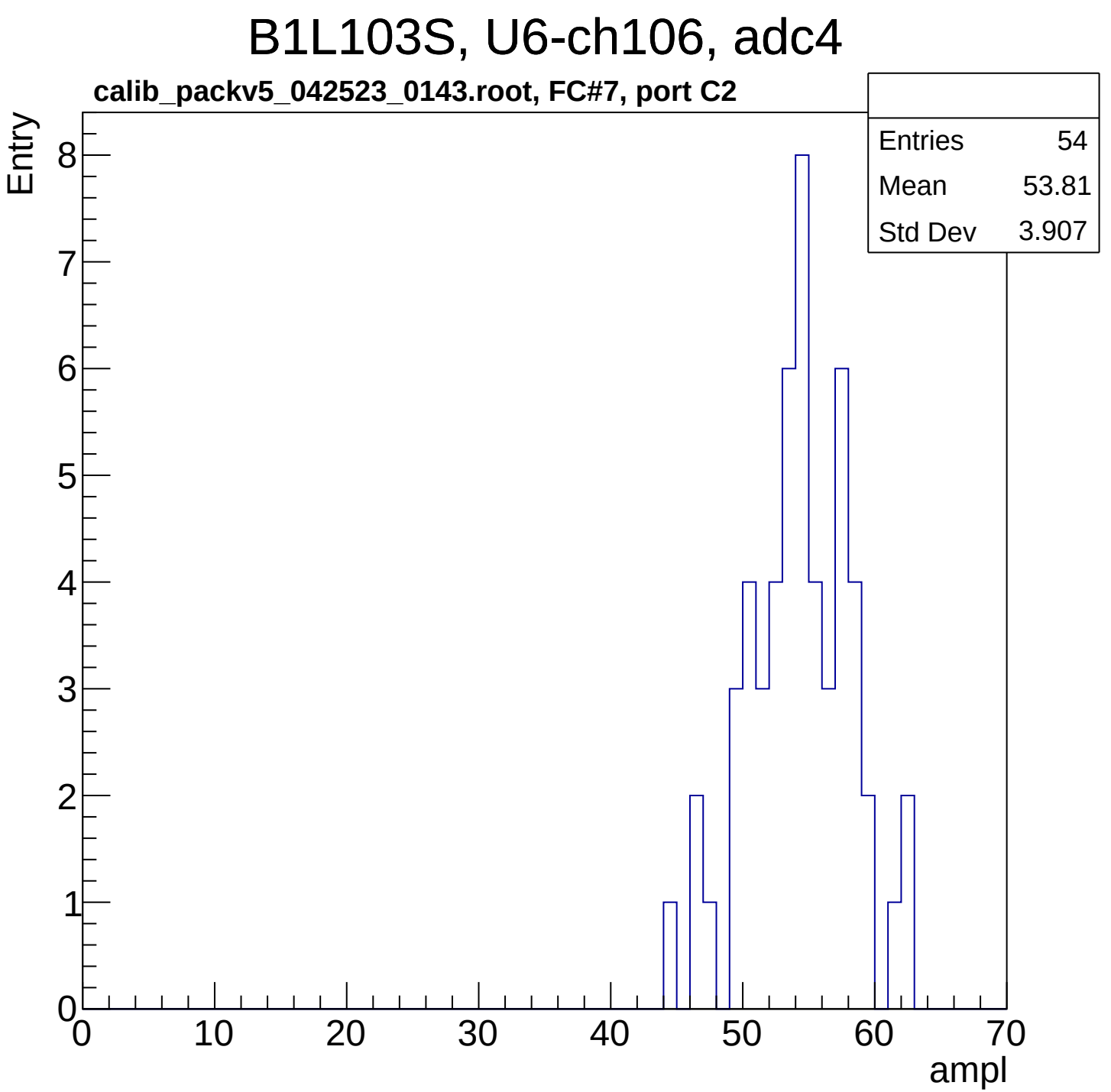
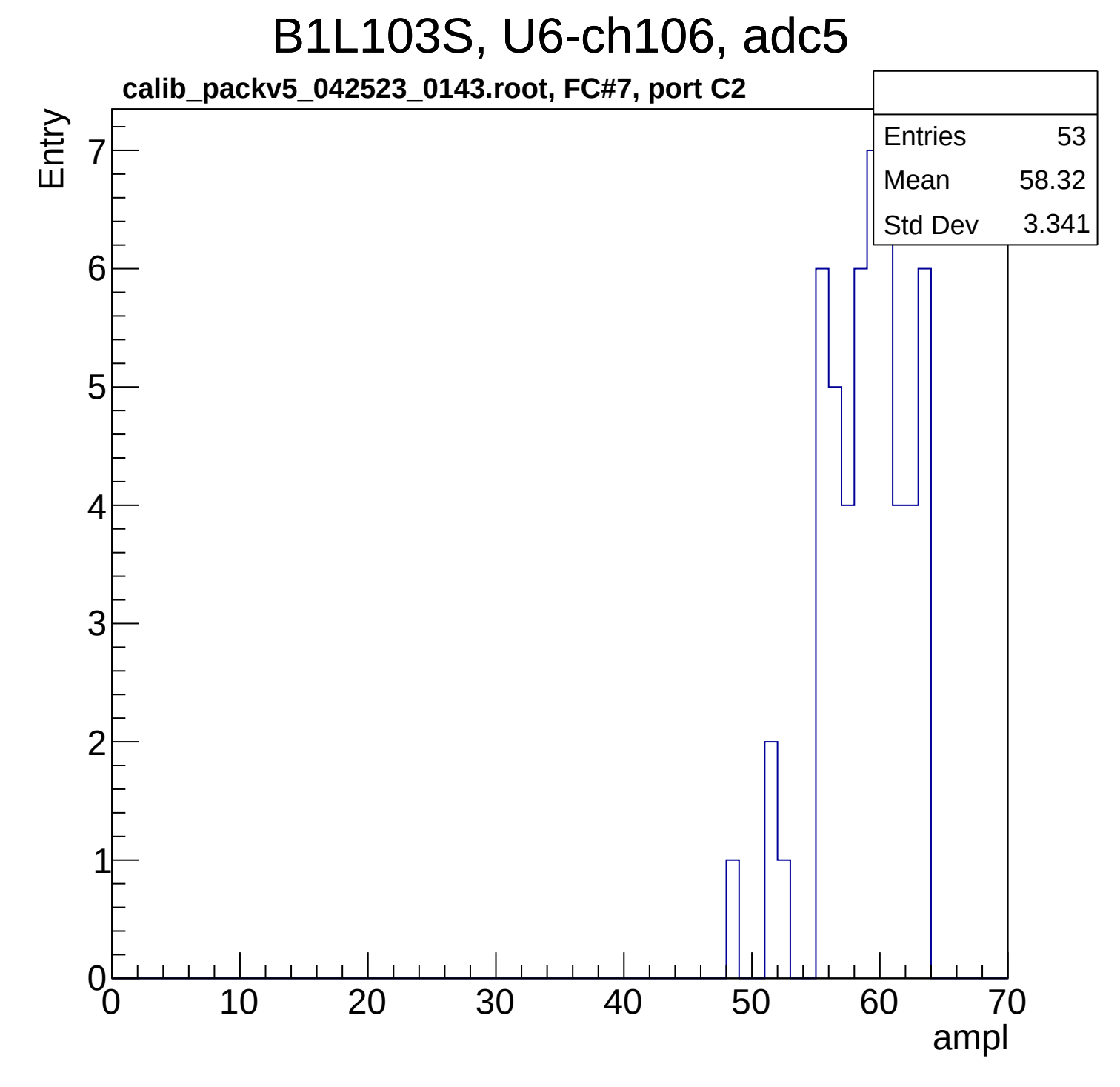

B1L103S, U6-ch106, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 26.56
Std Dev 5.835
Gaus mean : 26.7725
Gaus Width: 3.0184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch106, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 34.69
Std Dev 3.992
Gaus mean : 35.3486
Gaus Width: 4.1128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch106, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 41.29
Std Dev 3.821
Gaus mean : 41.8109
Gaus Width: 3.8026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch106, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 47.67
Std Dev 3.593
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch106, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 53.81
Std Dev 3.907
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch106, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.32
Std Dev 3.341
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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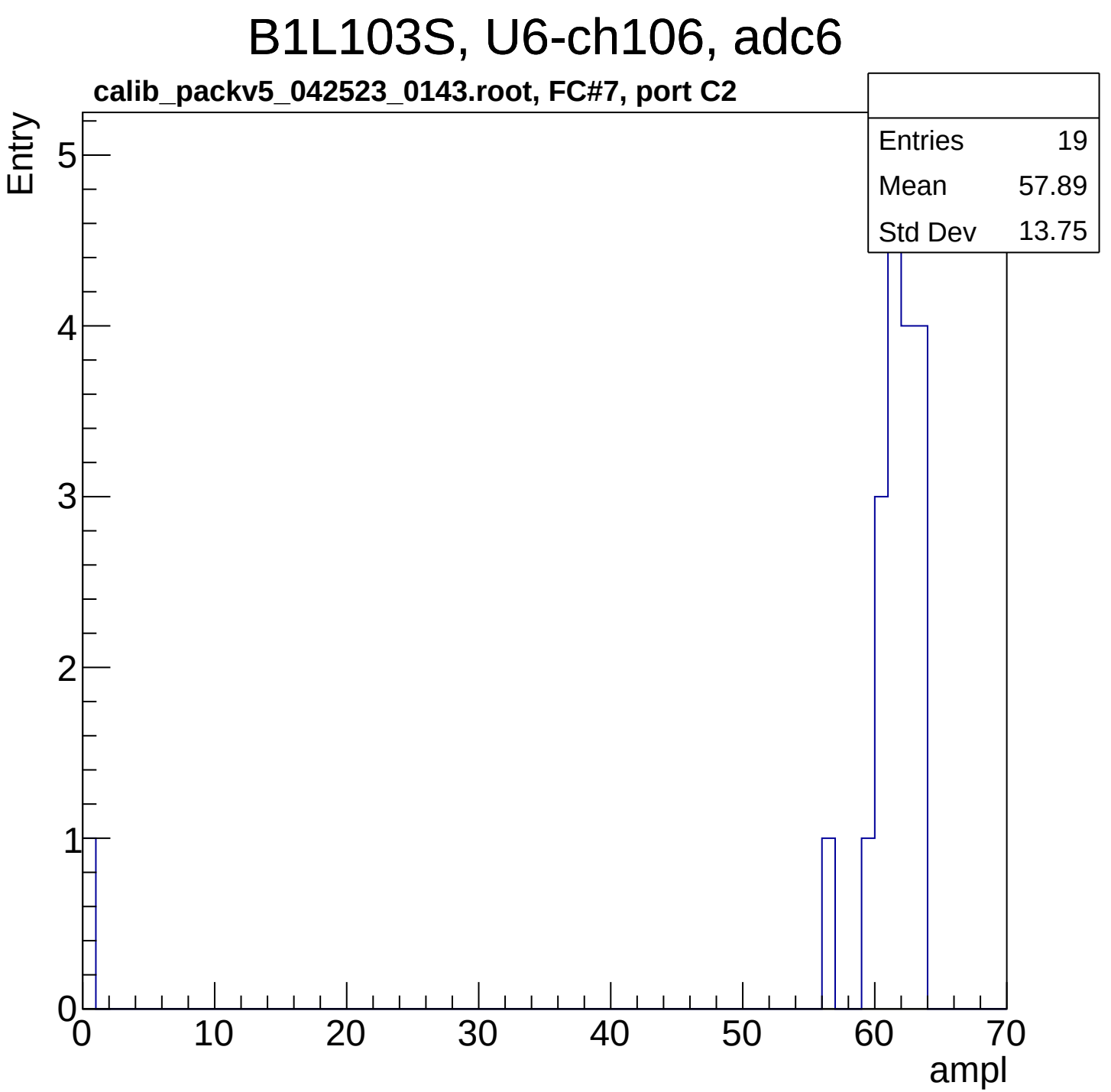
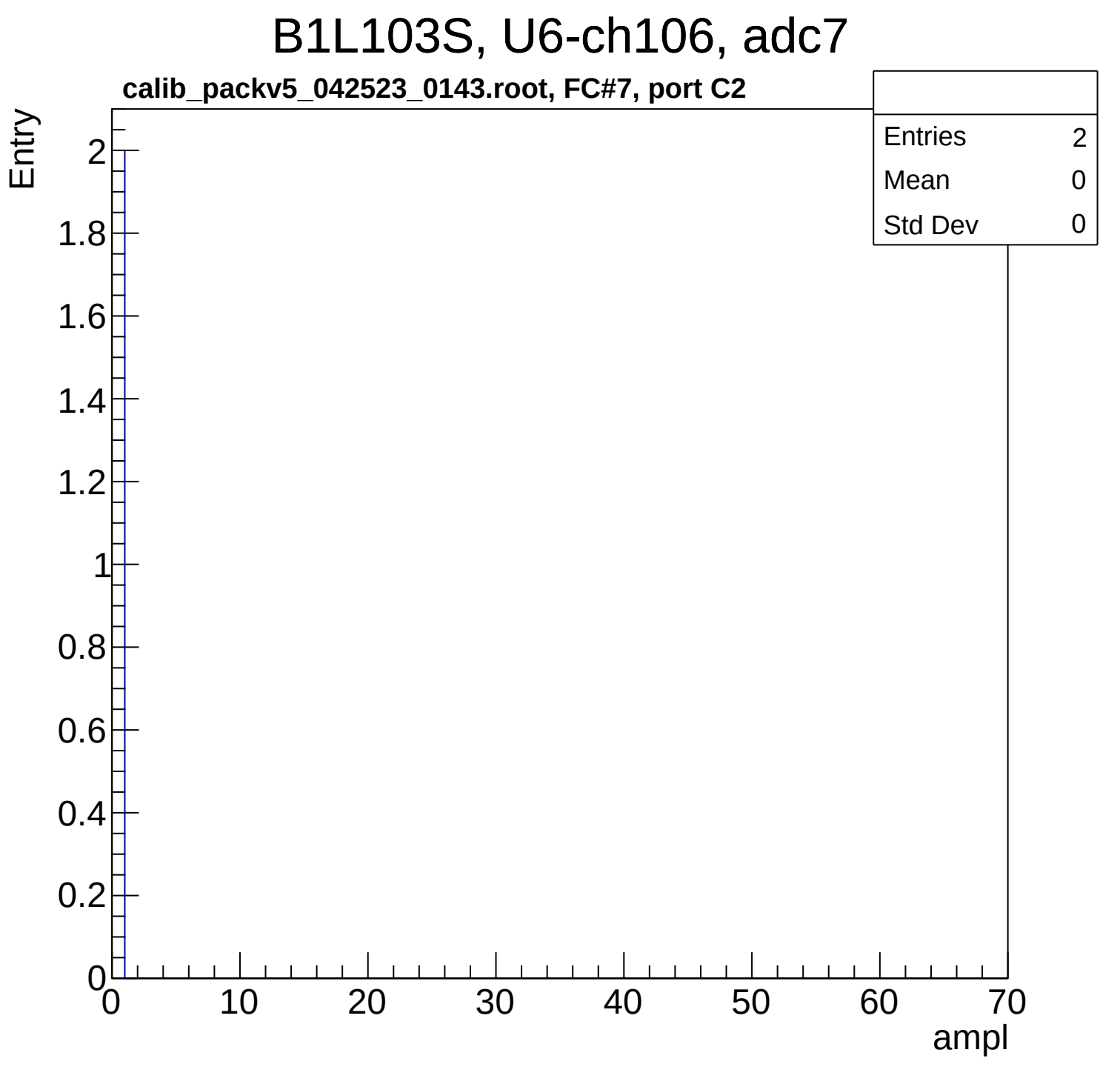
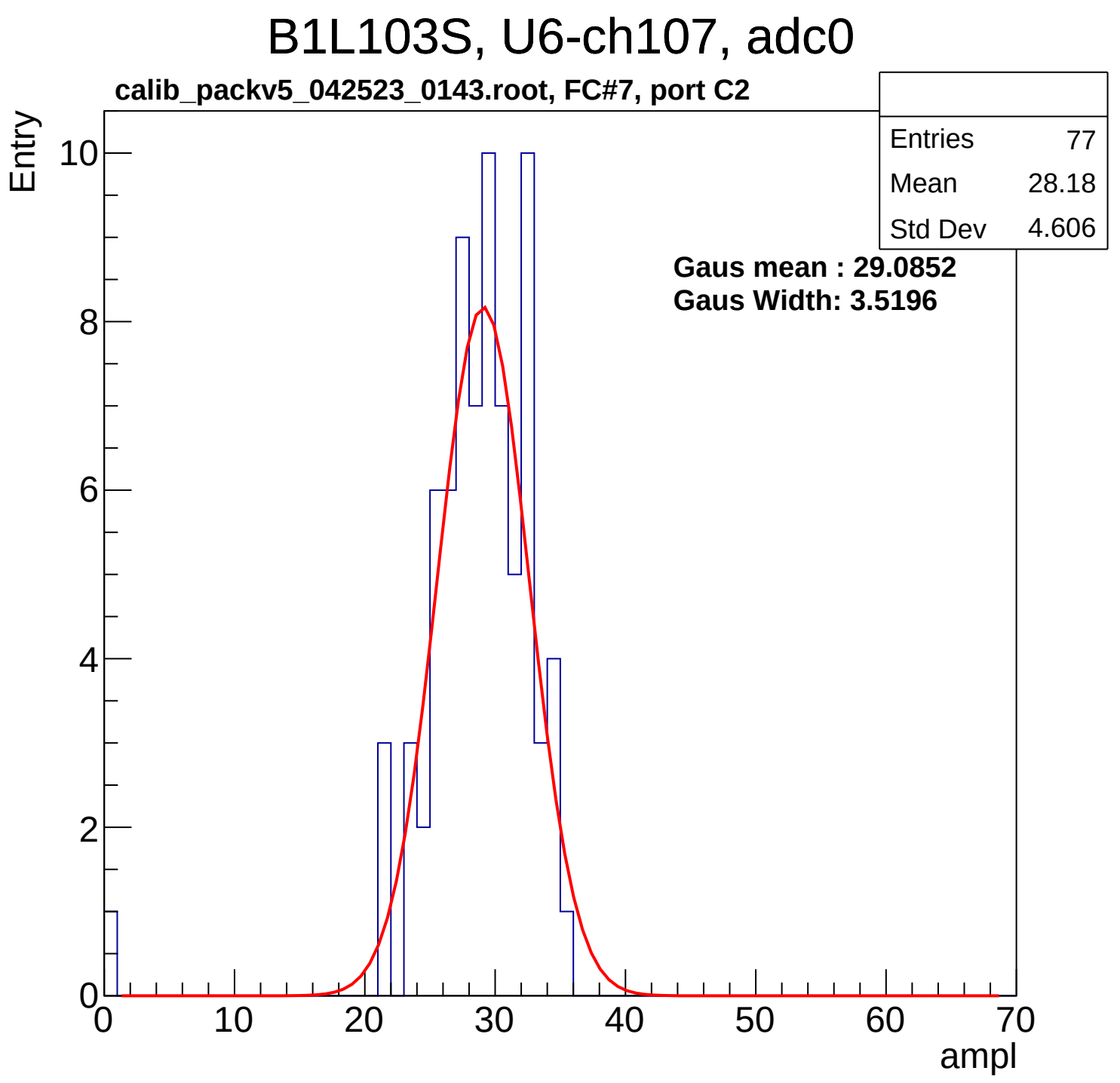
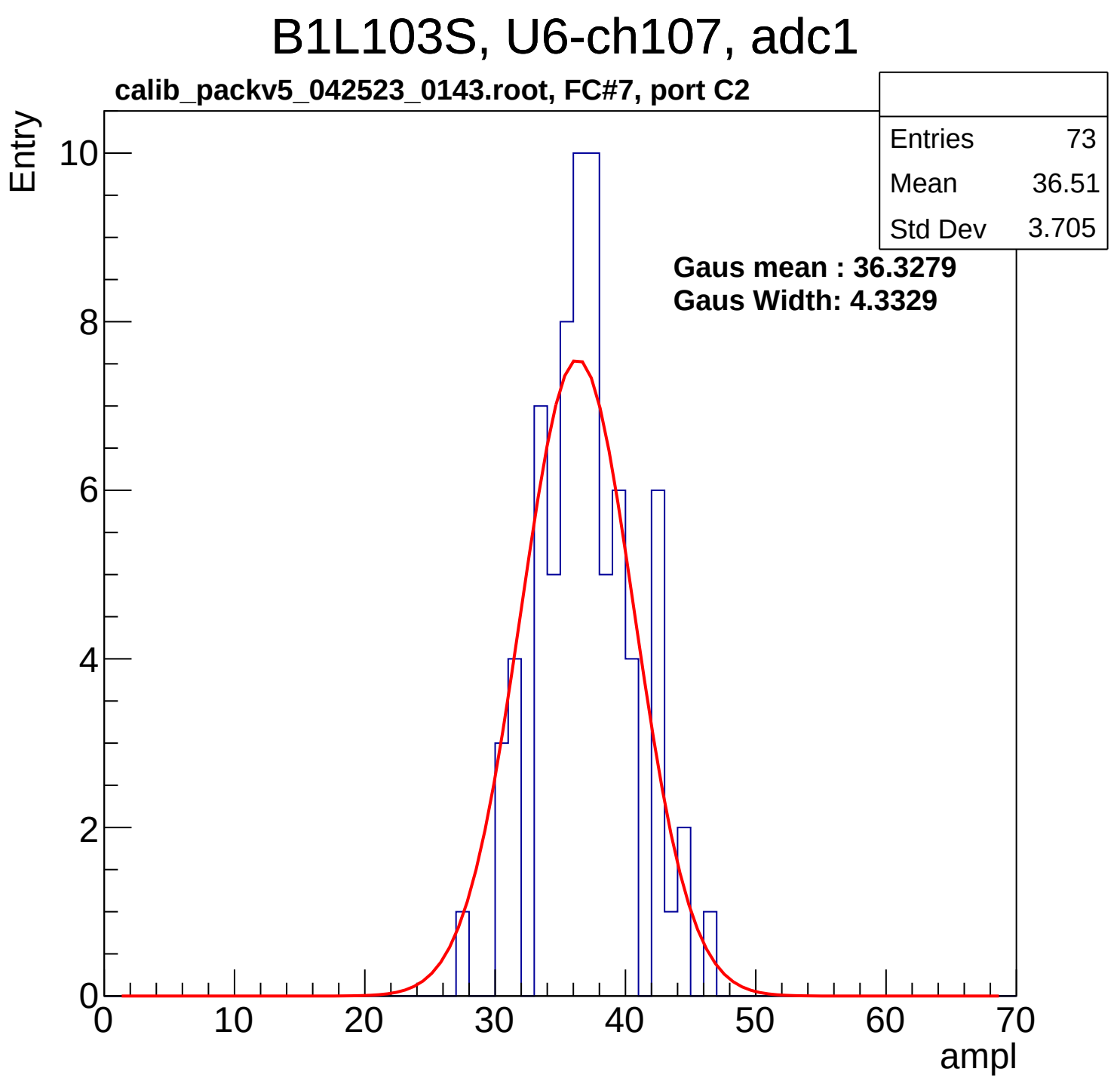
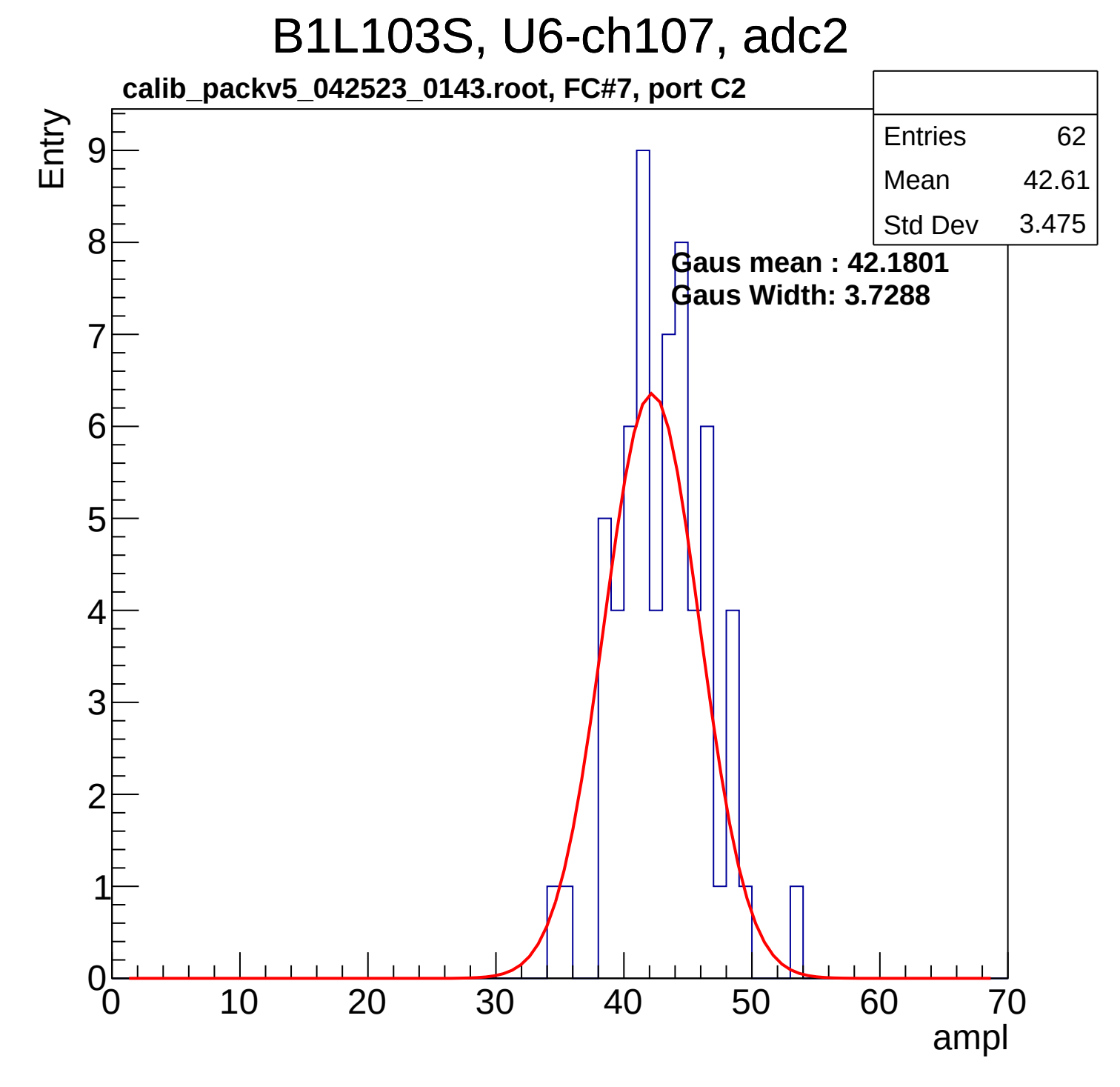

B1L103S, U6-ch106, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 57.89
Std Dev 13.75
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch106, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch107, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 28.18
Std Dev 4.606
Gaus mean : 29.0852
Gaus Width: 3.5196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch107, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.51
Std Dev 3.705
Gaus mean : 36.3279
Gaus Width: 4.3329
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch107, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.61
Std Dev 3.475
Gaus mean : 42.1801
Gaus Width: 3.7288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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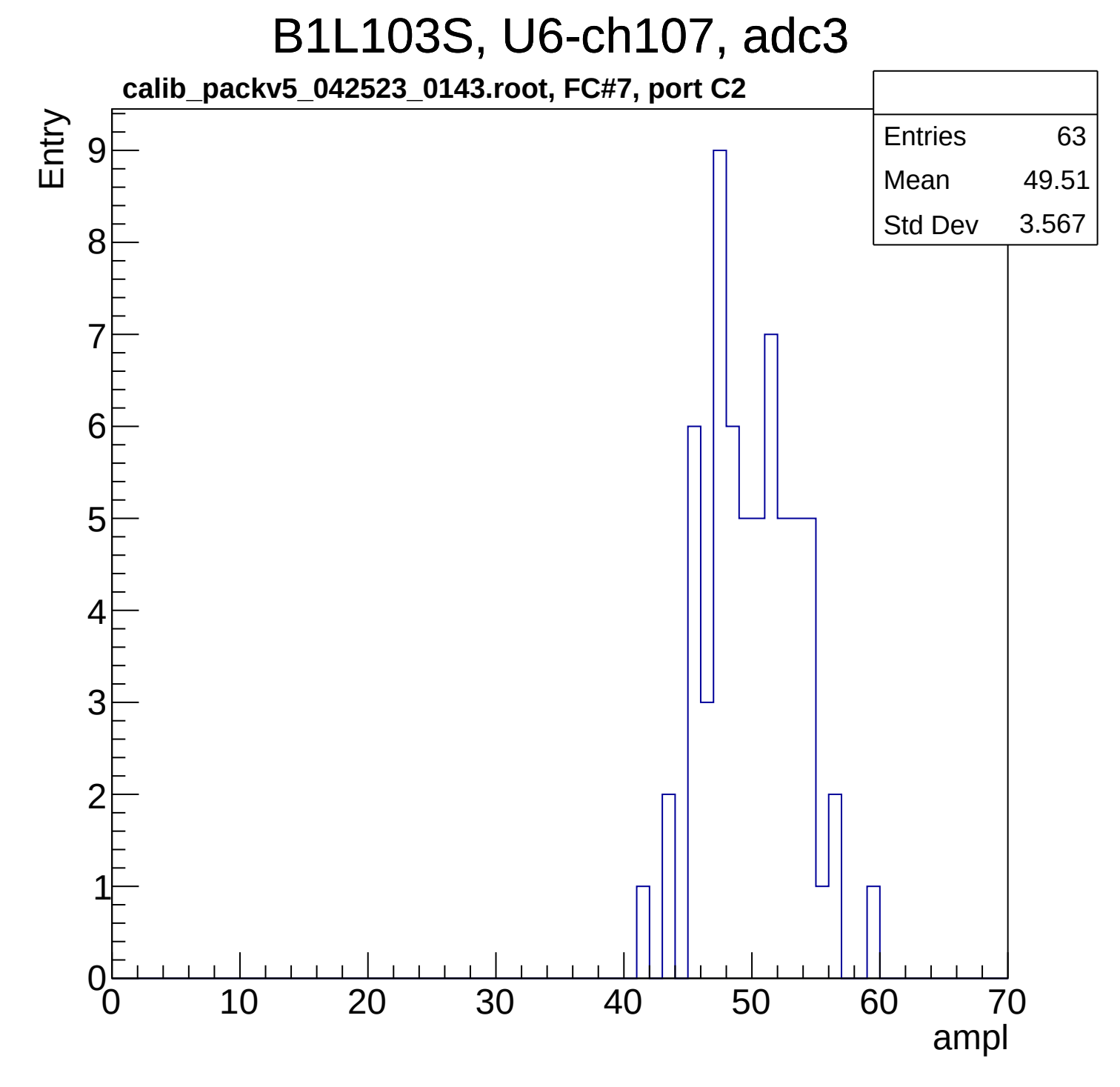
60
70


B1L103S, U6-ch107, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.51
Std Dev 3.567
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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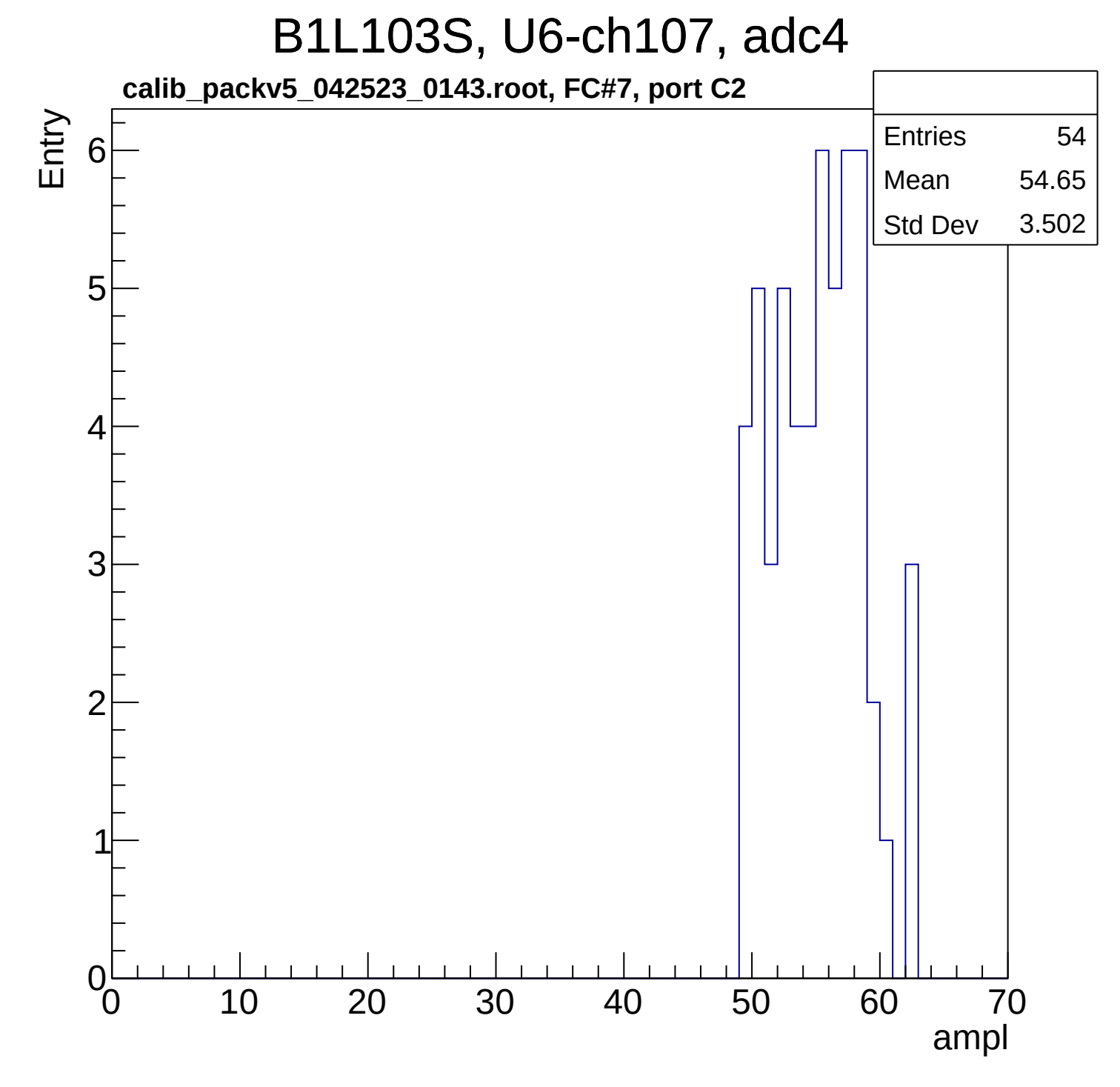
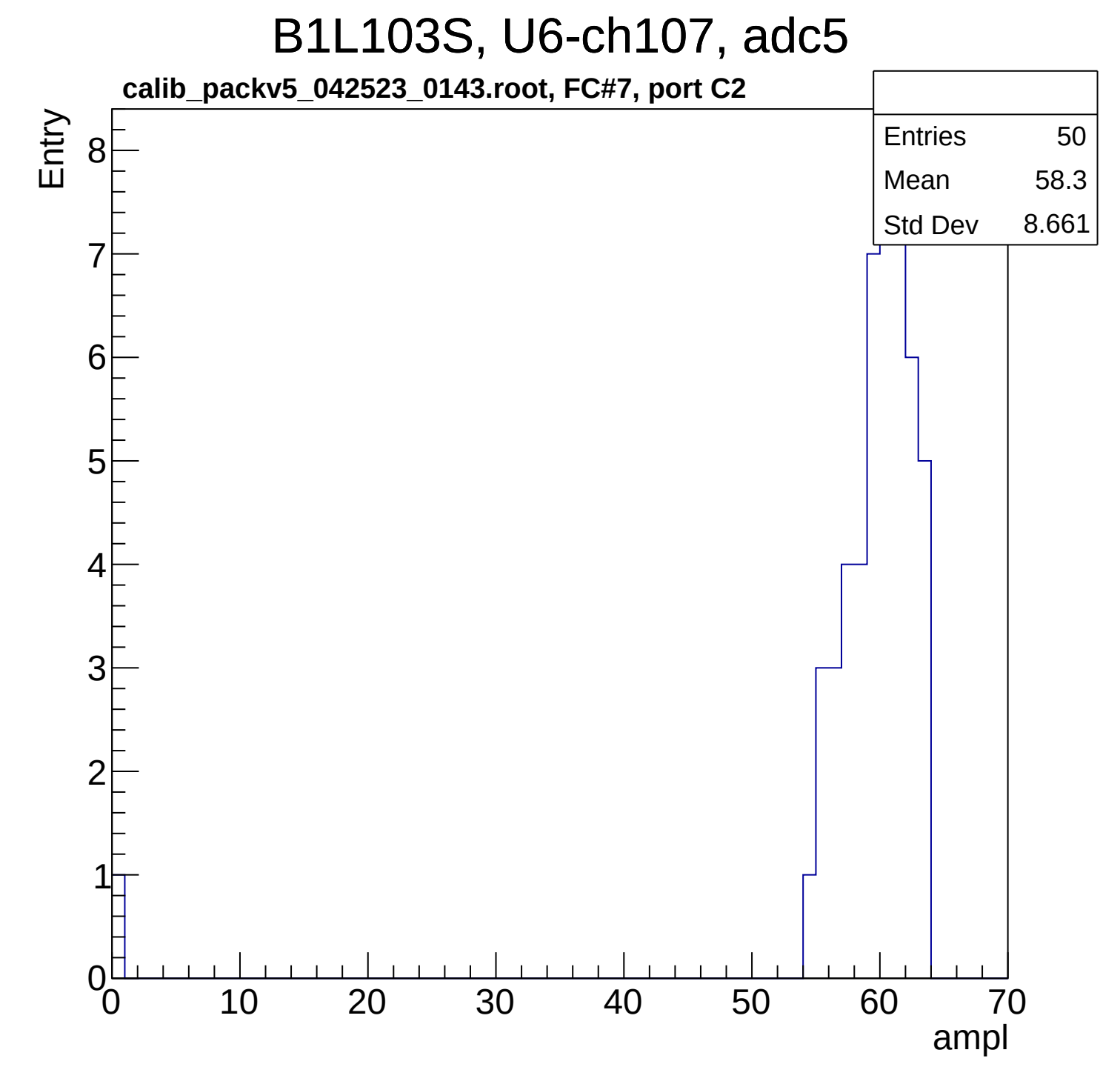

B1L103S, U6-ch107, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 54.65
Std Dev 3.502
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch107, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.3
Std Dev 8.661
Entry
ampl
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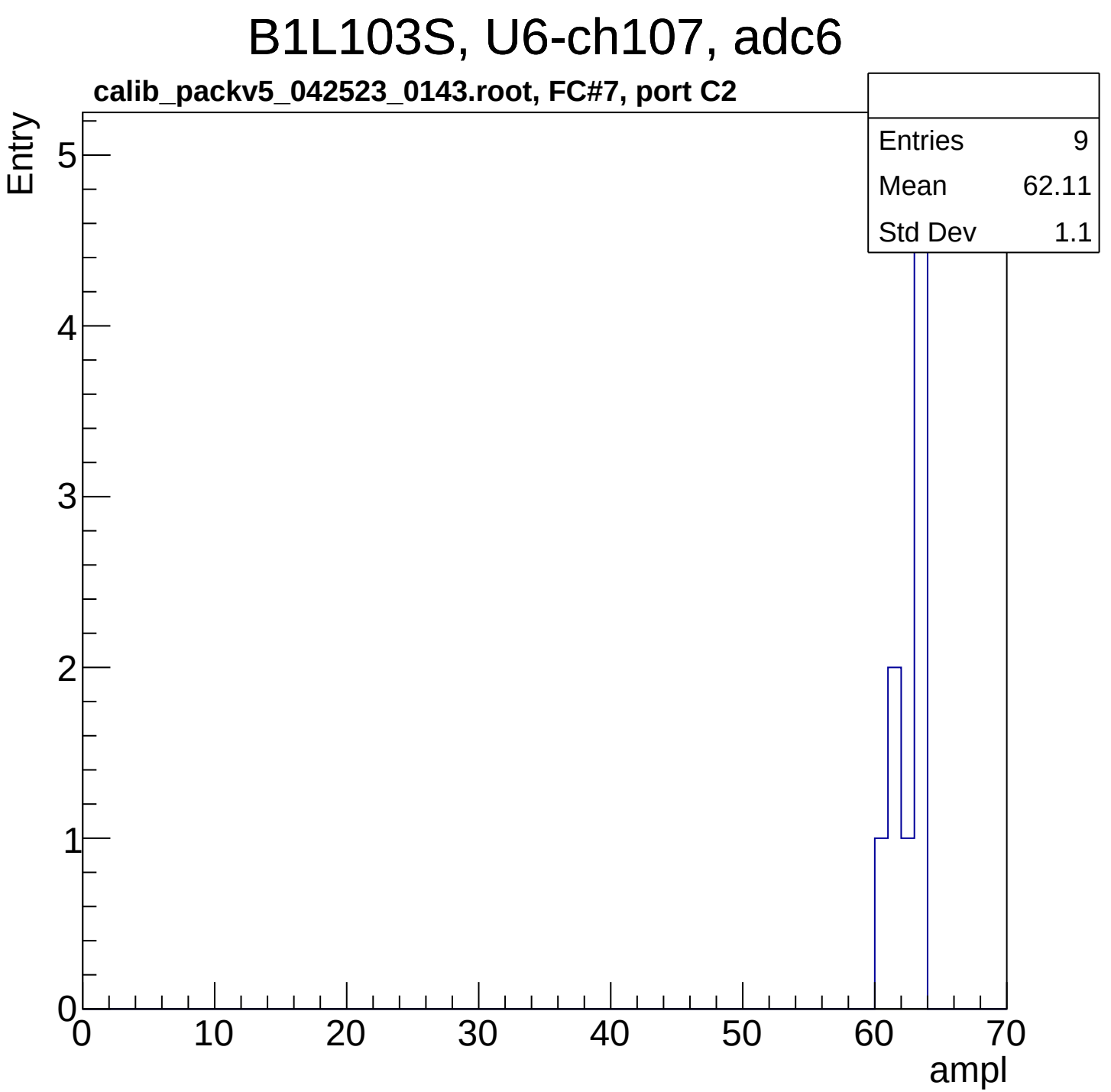
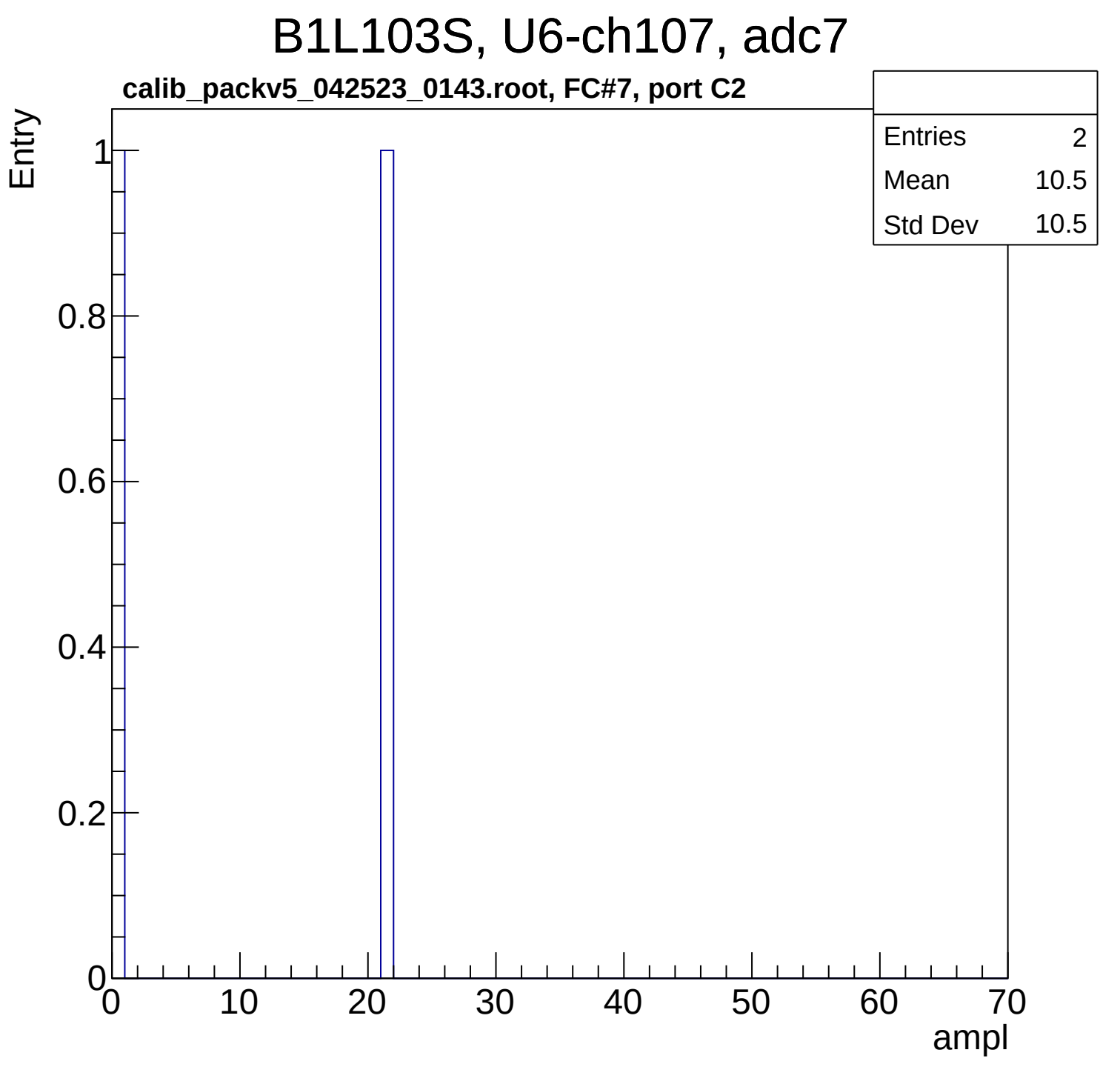
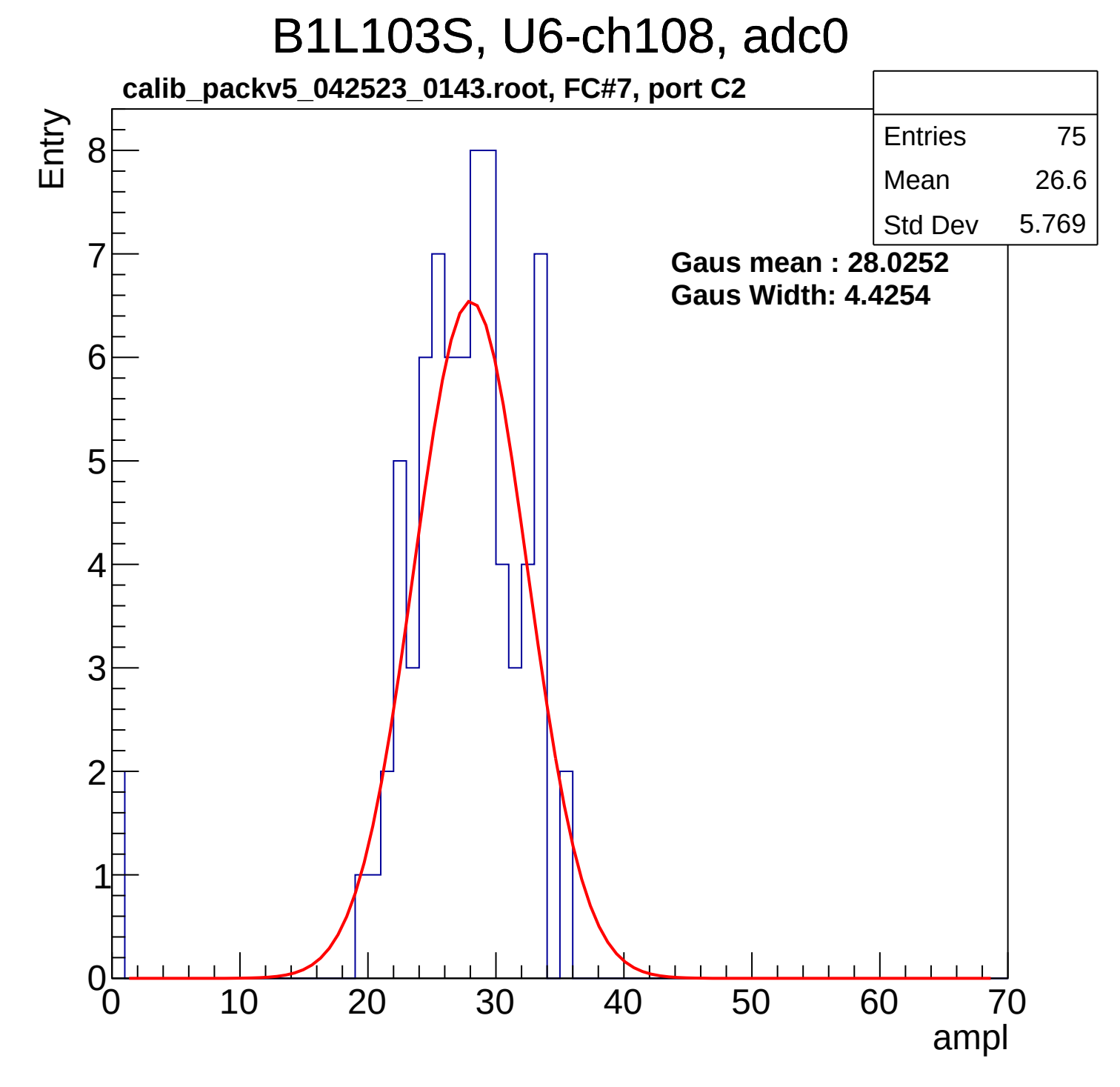
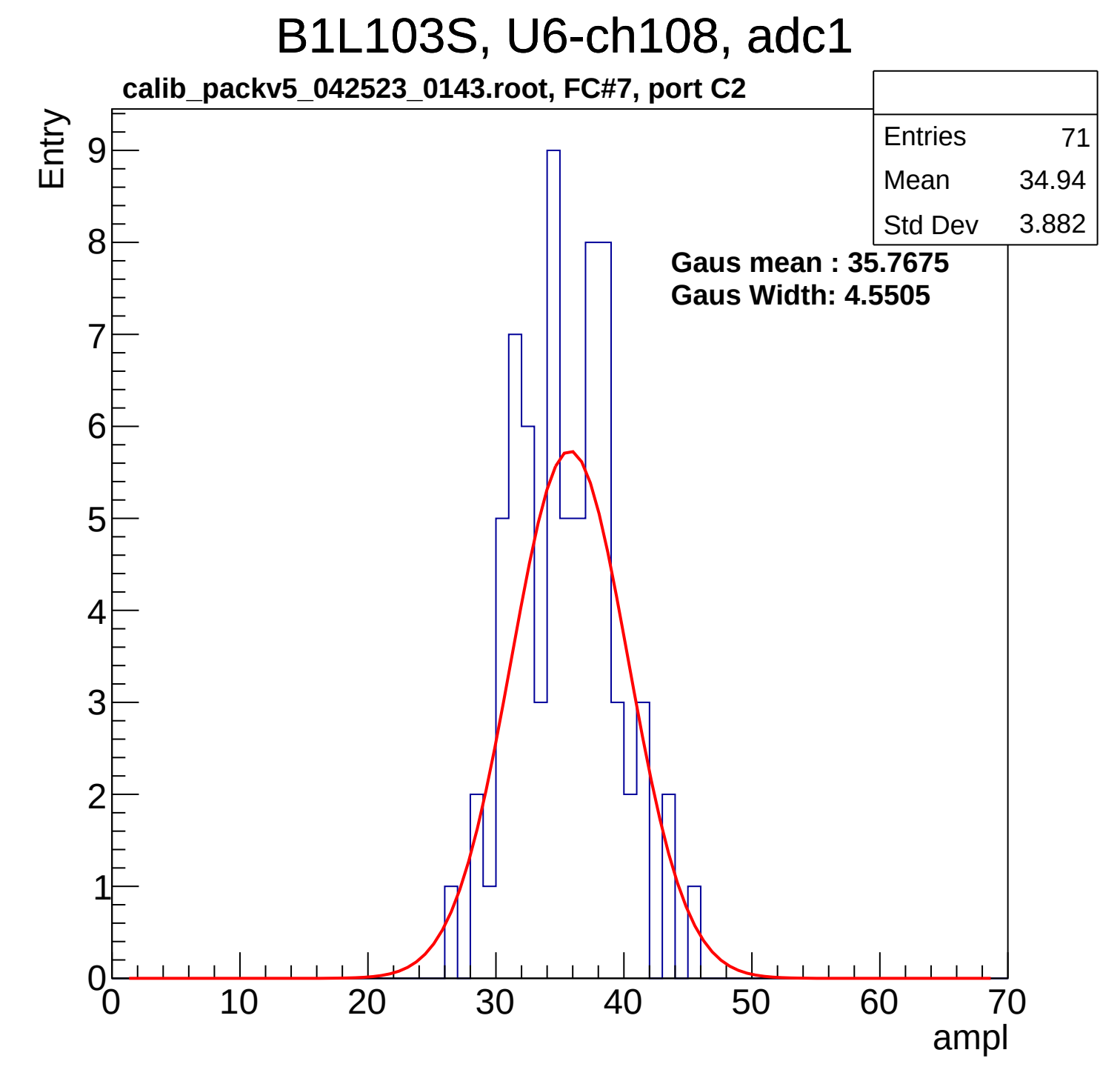
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch107, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 62.11
Std Dev 1.1
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch107, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch108, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 26.6
Std Dev 5.769
Gaus mean : 28.0252
Gaus Width: 4.4254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch108, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 34.94
Std Dev 3.882
Gaus mean : 35.7675
Gaus Width: 4.5505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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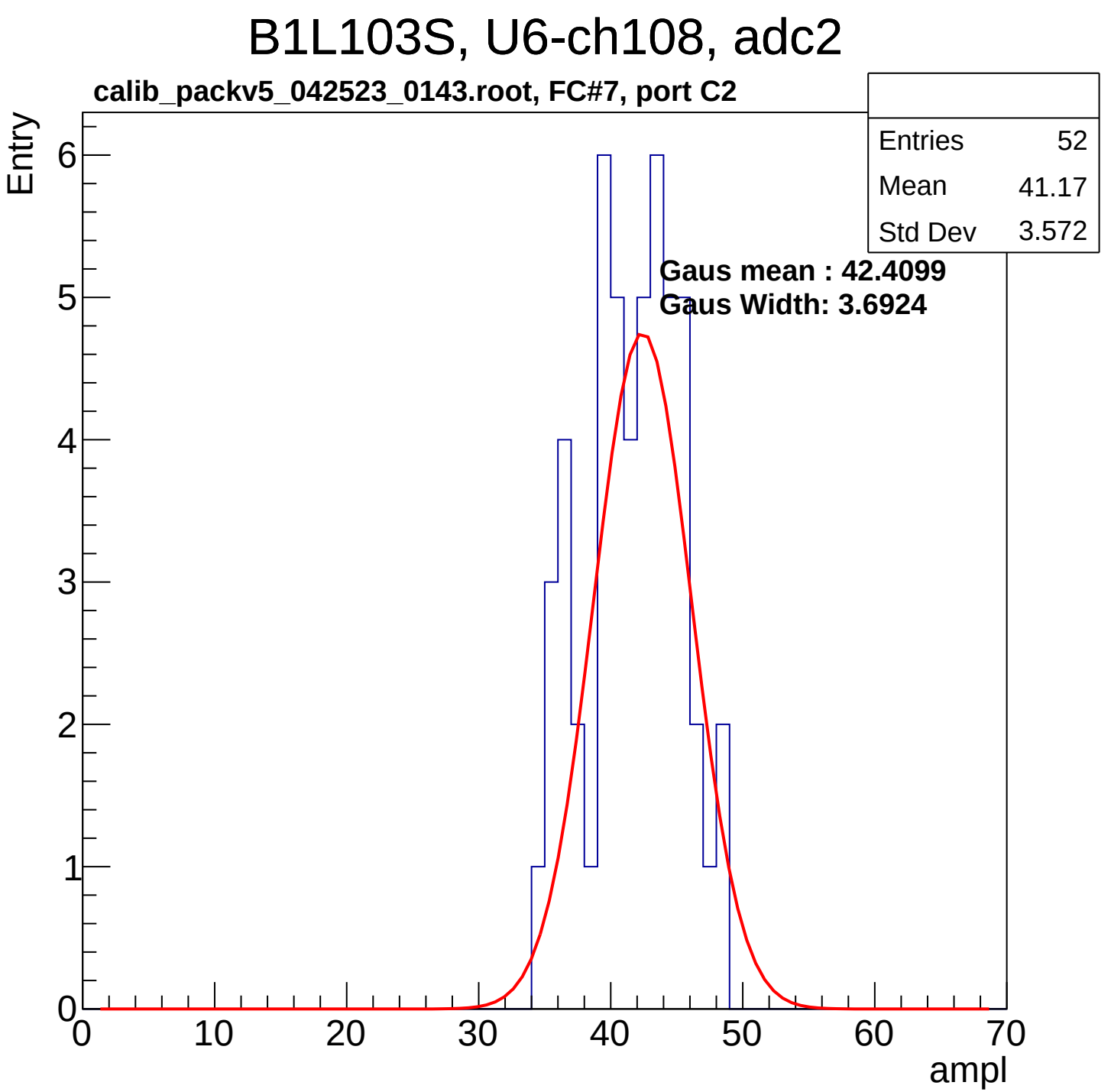
B1L103S, U6-ch108, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 41.17
Std Dev 3.572
Gaus mean : 42.4099
Gaus Width: 3.6924
Entry
ampl
0
1
2
3
4
5
6
0
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20
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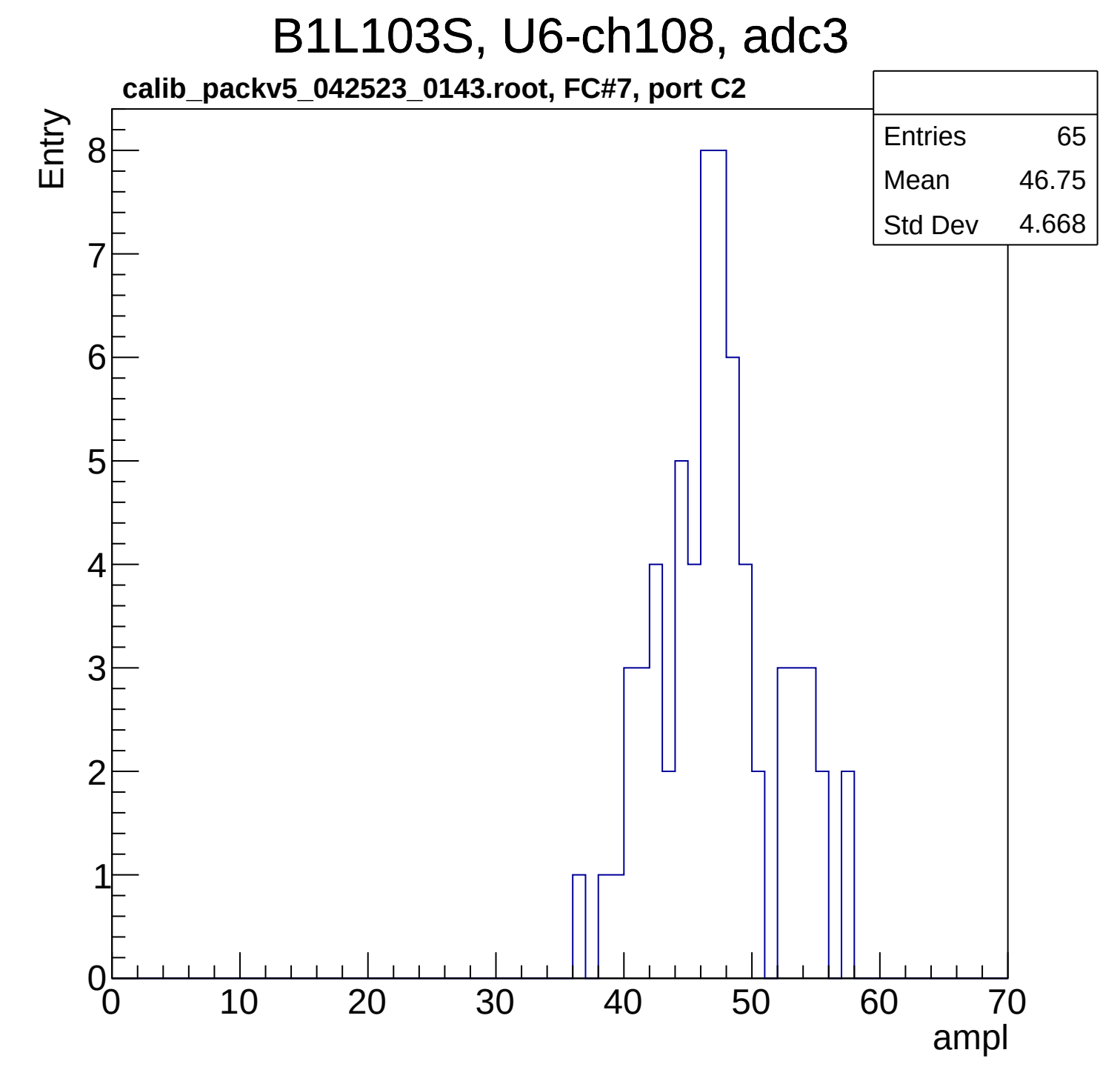
40
50
60
70


B1L103S, U6-ch108, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 46.75
Std Dev 4.668
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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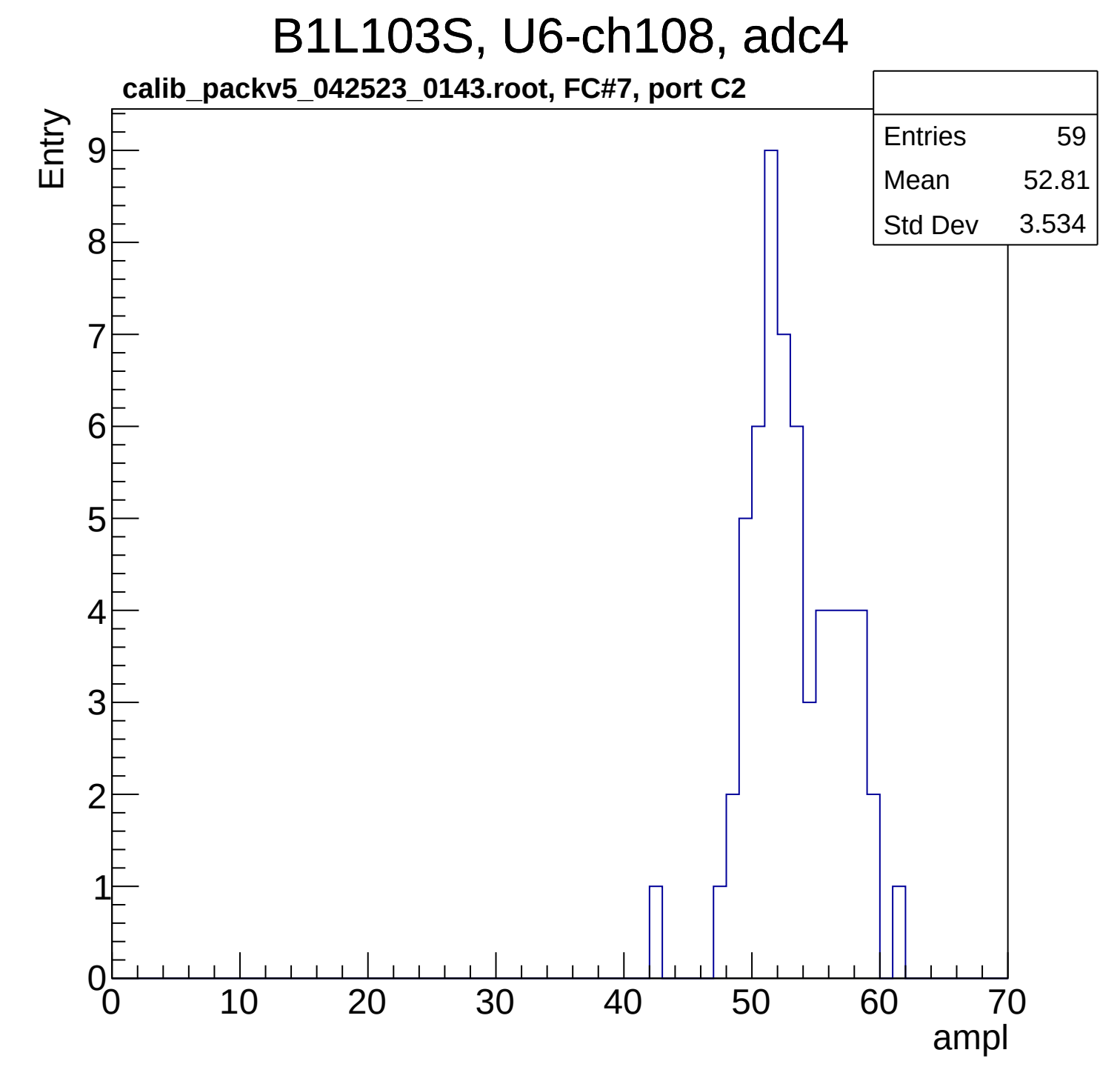
B1L103S, U6-ch108, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 52.81
Std Dev 3.534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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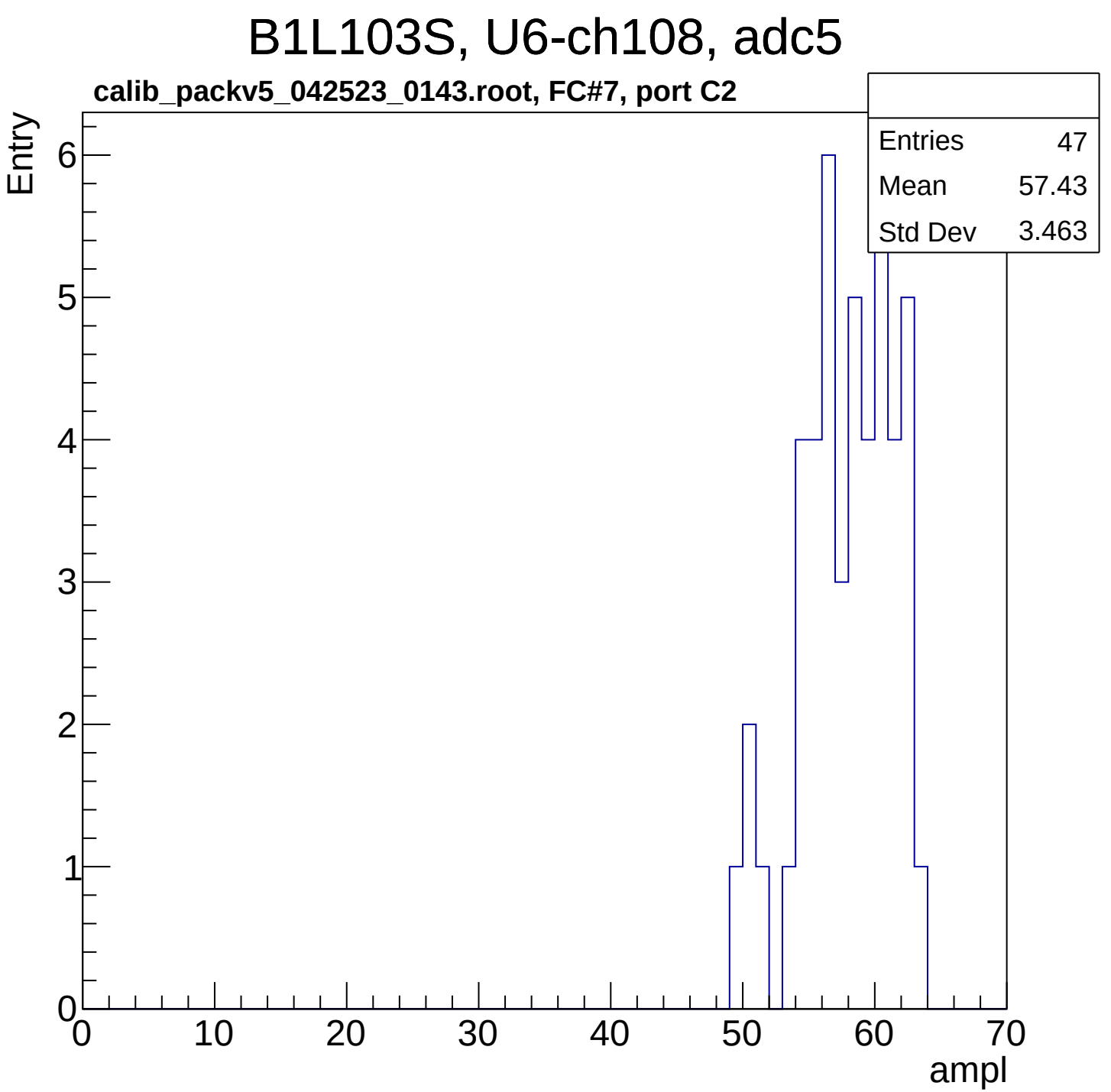
B1L103S, U6-ch108, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.43
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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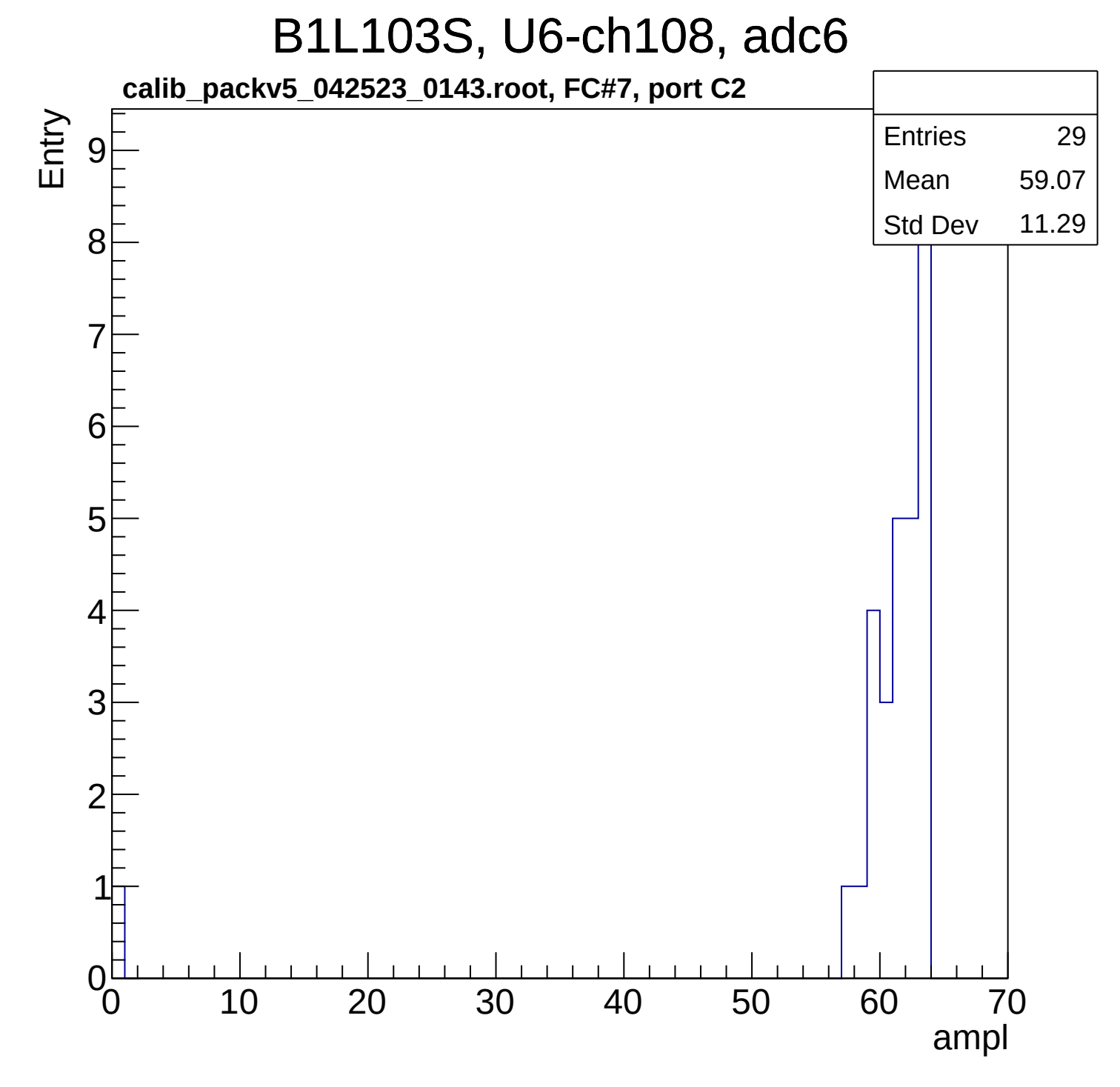
B1L103S, U6-ch108, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 59.07
Std Dev 11.29
Entry
ampl
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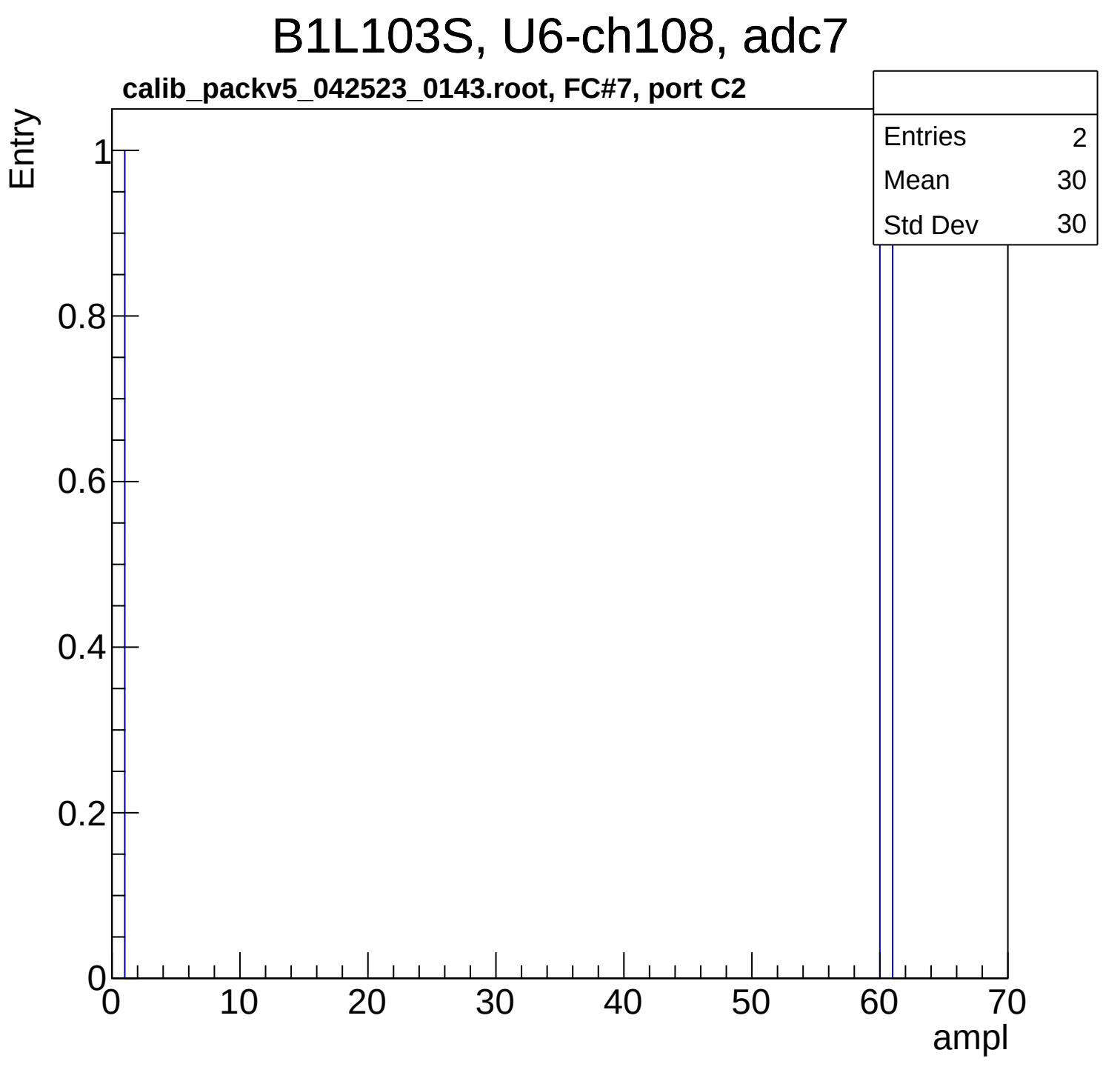
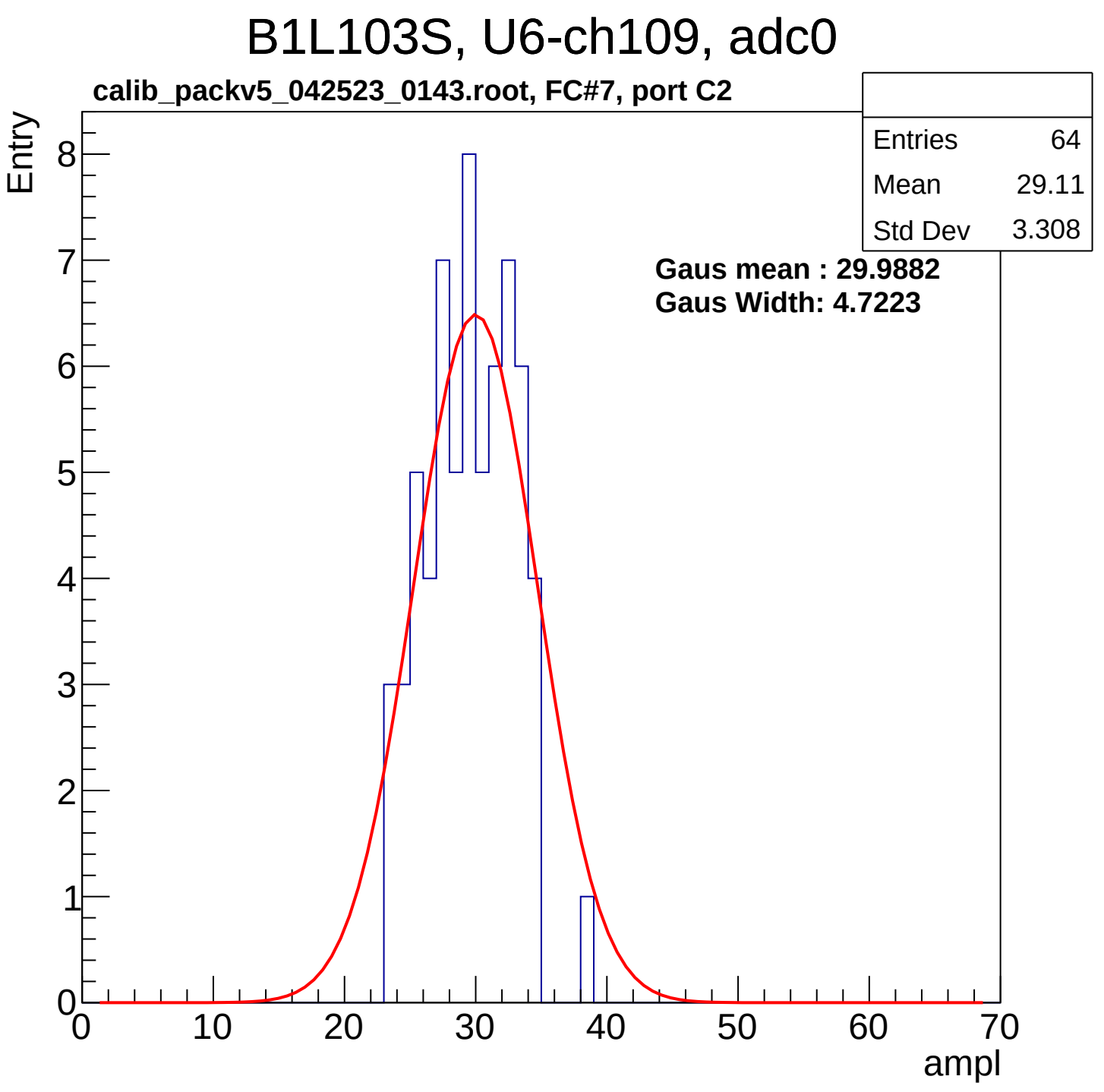
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch108, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 30
Std Dev 30
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch109, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 29.11
Std Dev 3.308
Gaus mean : 29.9882
Gaus Width: 4.7223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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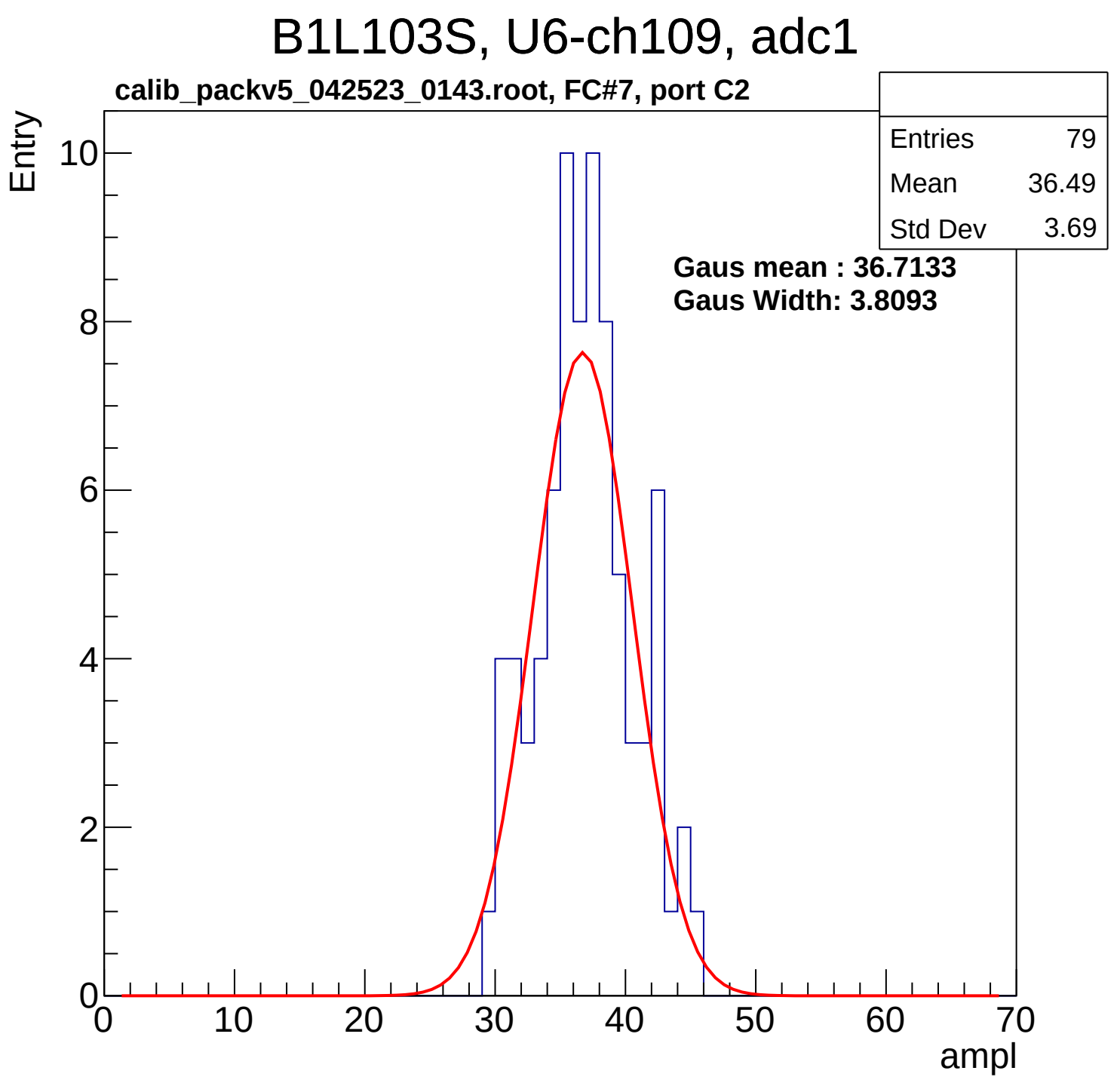
B1L103S, U6-ch109, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 36.49
Std Dev 3.69
Gaus mean : 36.7133
Gaus Width: 3.8093
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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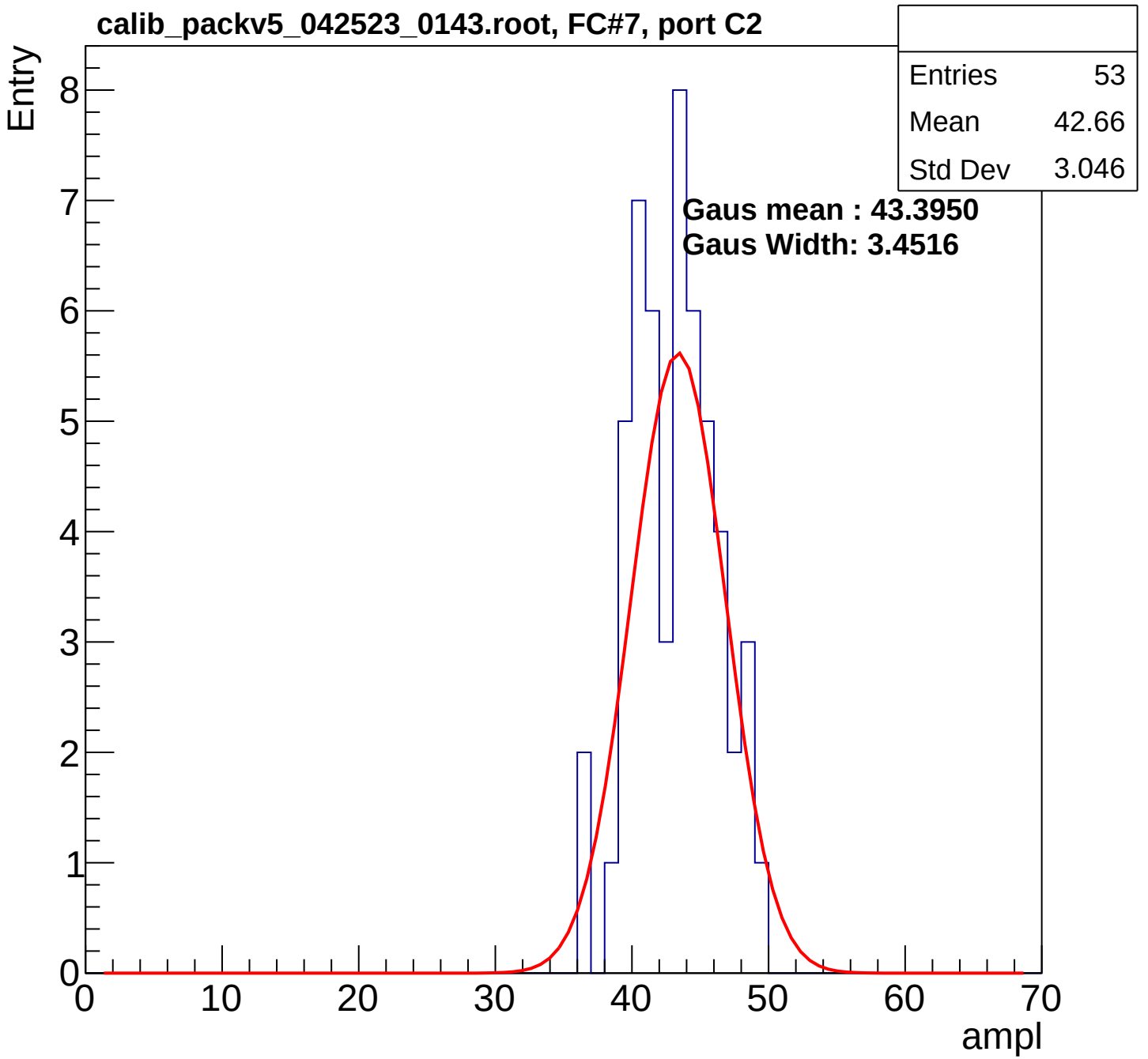
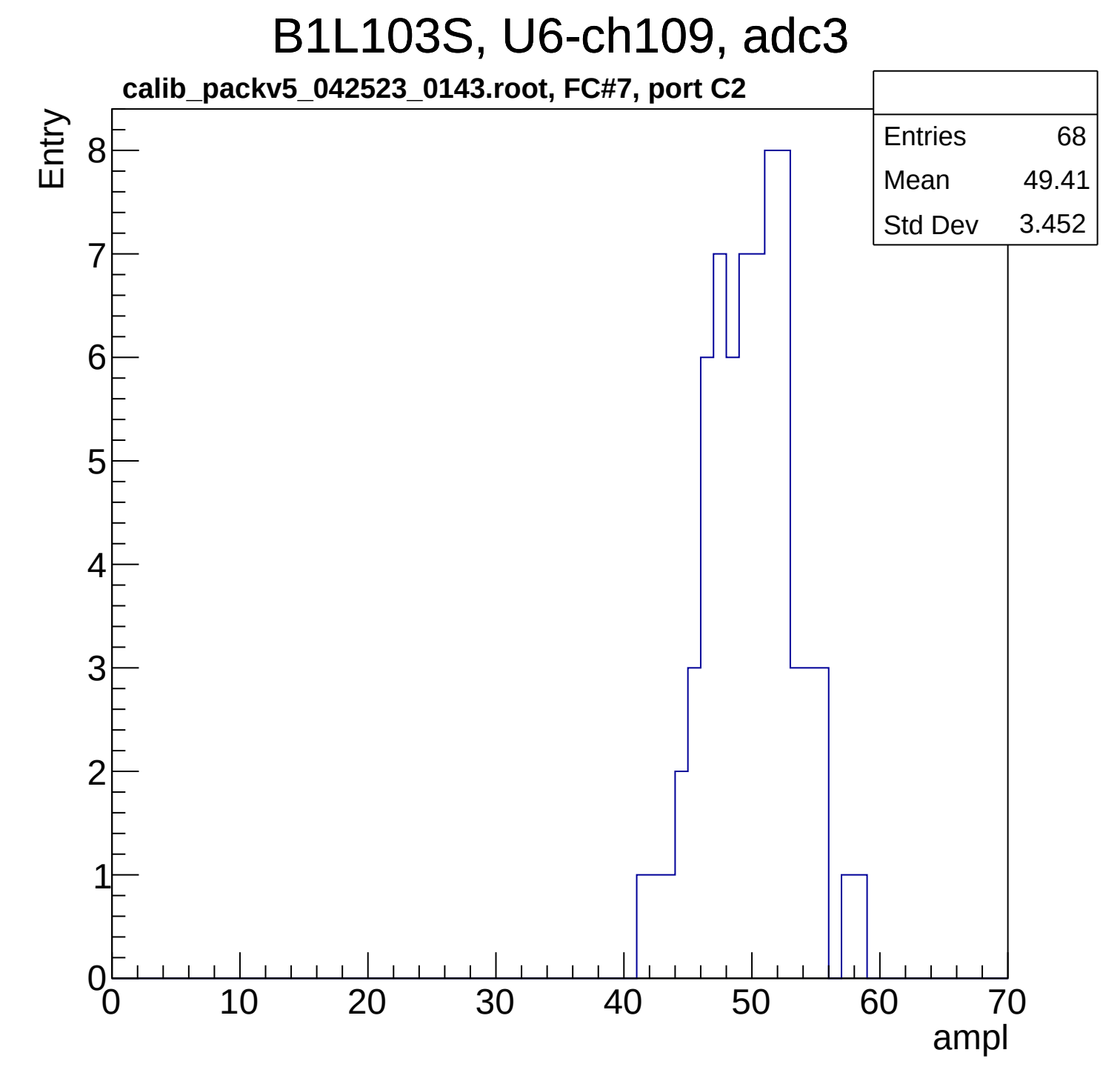

B1L103S, U6-ch109, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 42.66
Std Dev 3.046
Gaus mean : 43.3950
Gaus Width: 3.4516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch109, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 49.41
Std Dev 3.452
Entry
ampl
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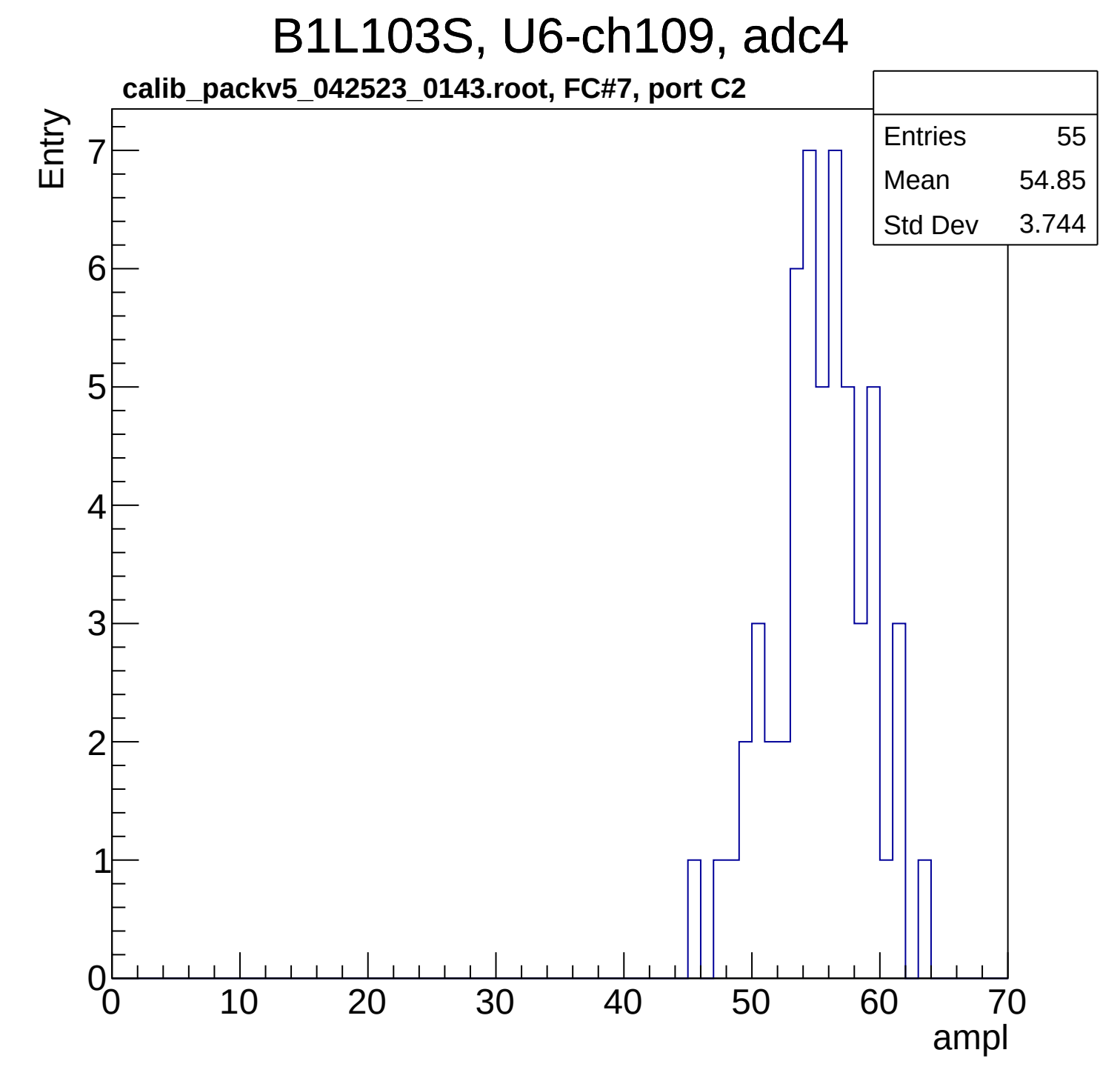
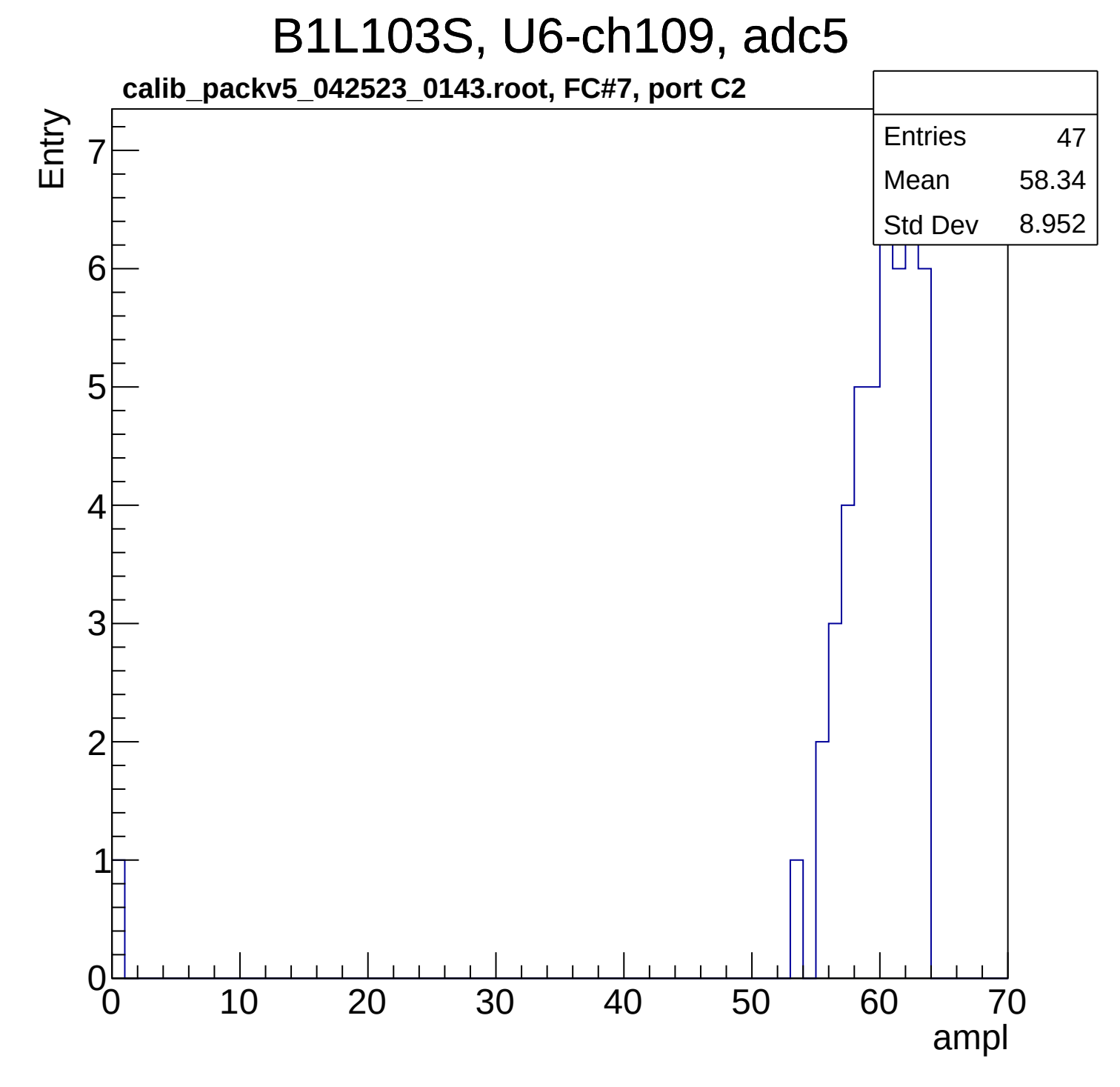
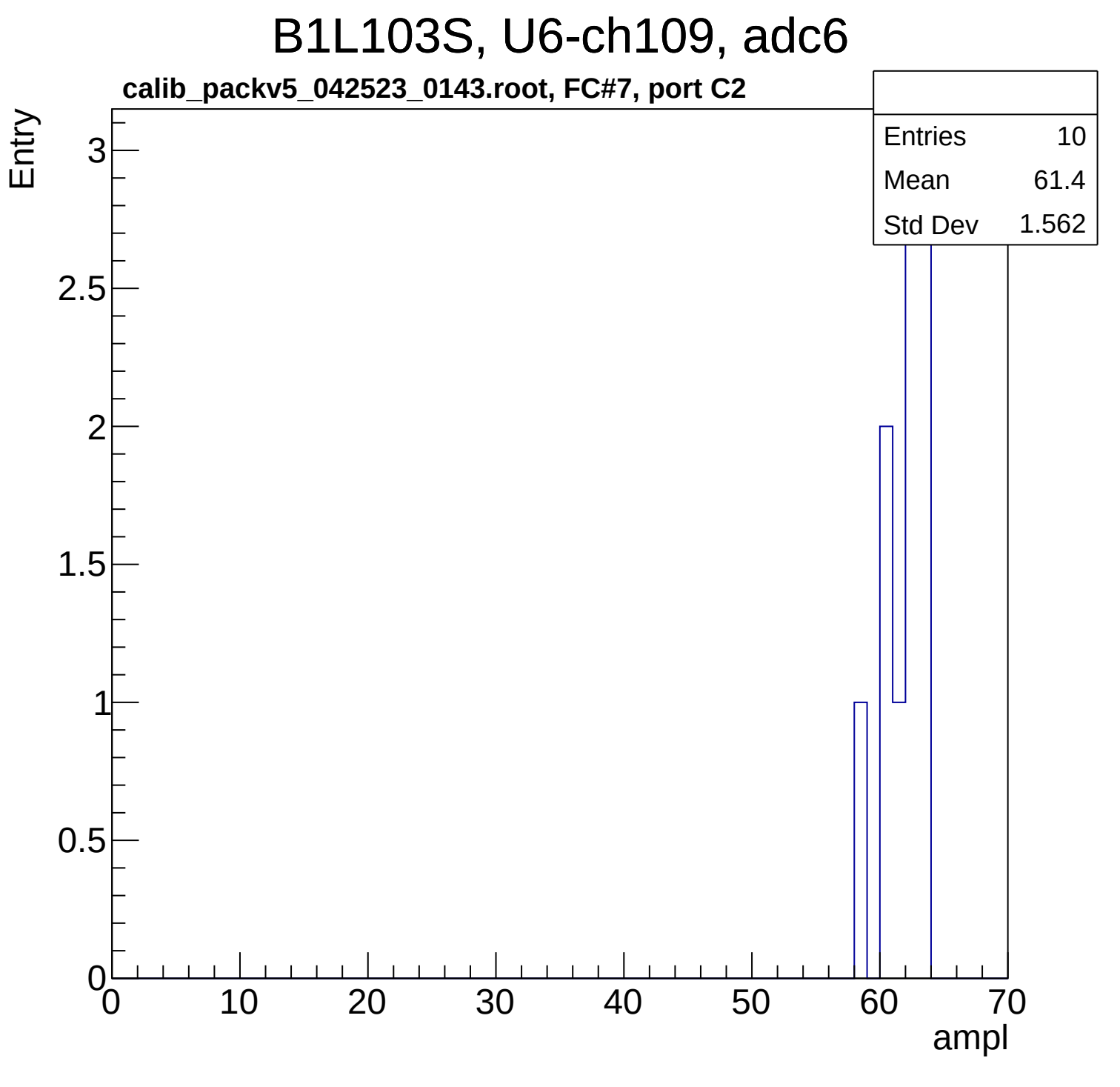
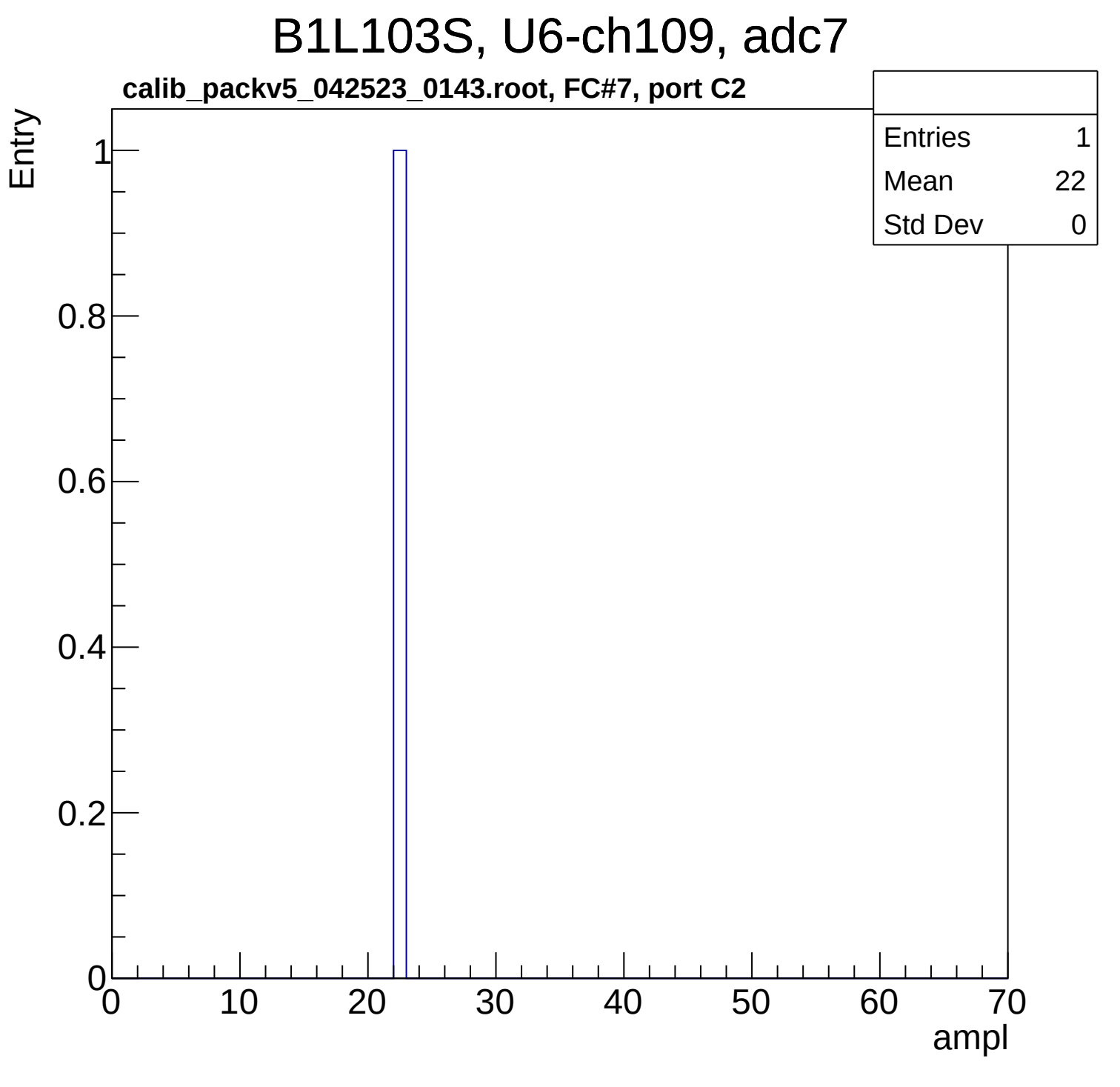
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch109, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.85
Std Dev 3.744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch109, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.34
Std Dev 8.952
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch109, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.4
Std Dev 1.562
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U6-ch109, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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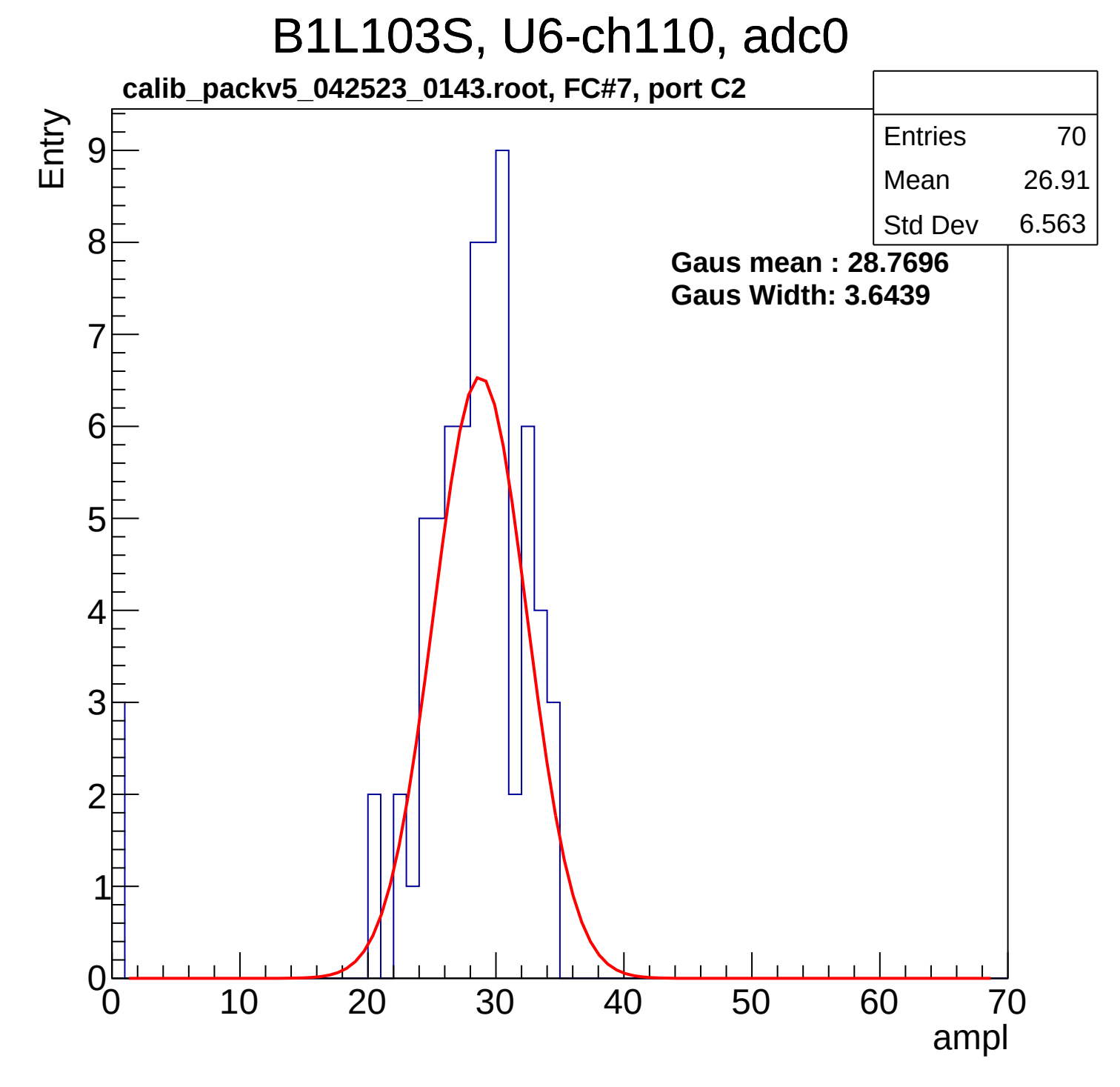
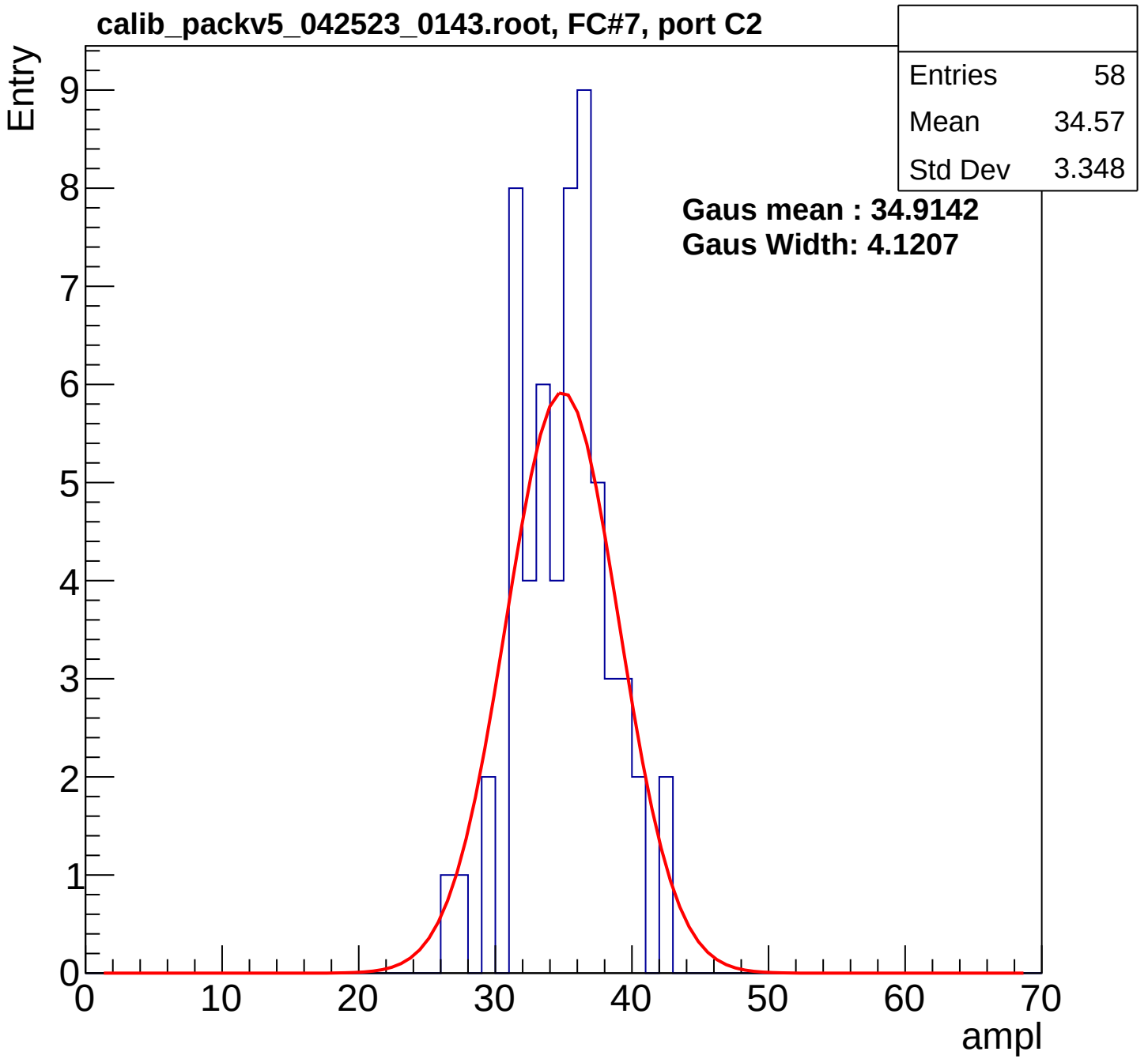
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch110, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 26.91
Std Dev 6.563
Gaus mean : 28.7696
Gaus Width: 3.6439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch110, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 34.57
Std Dev 3.348
Gaus mean : 34.9142
Gaus Width: 4.1207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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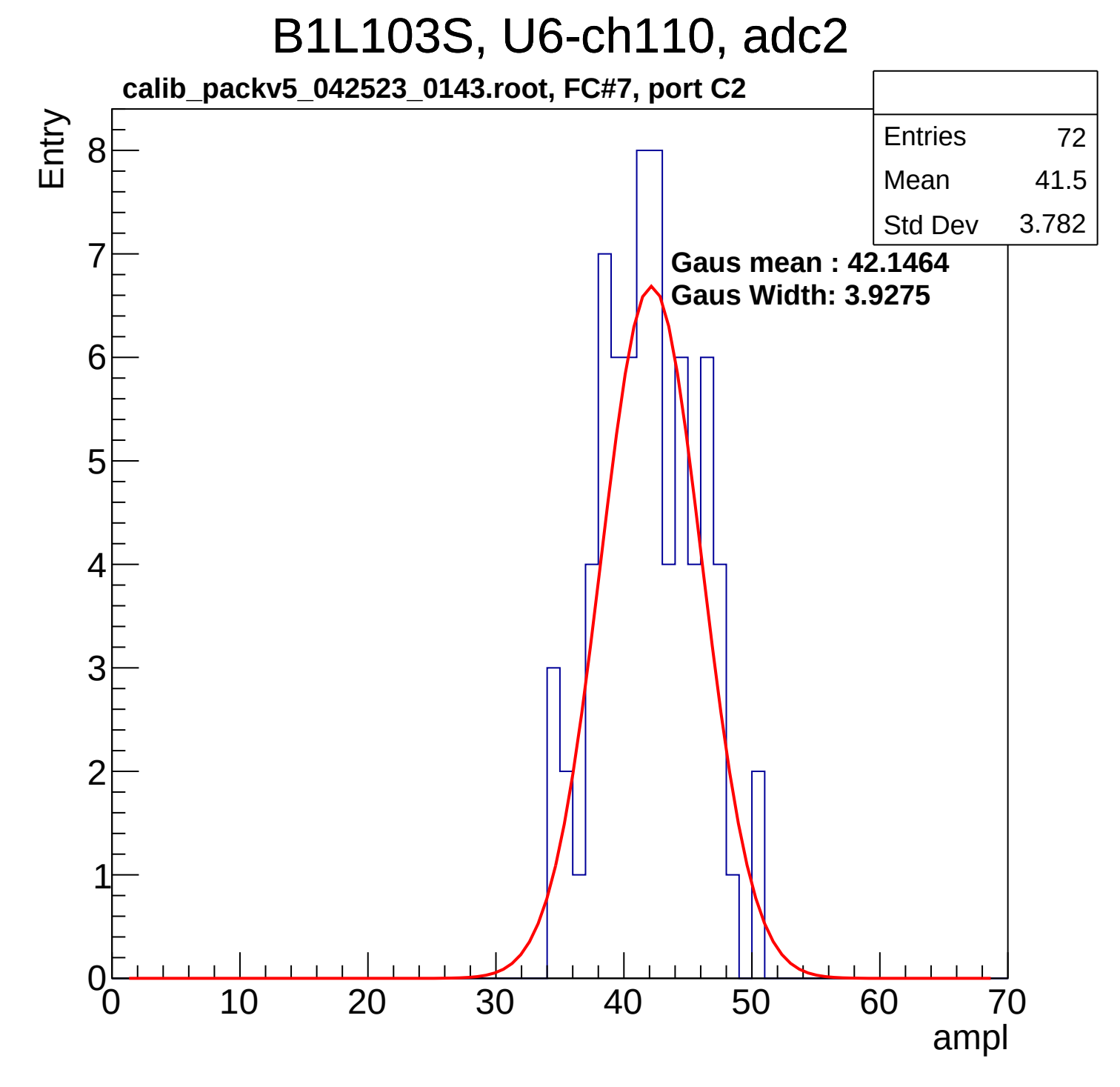
B1L103S, U6-ch110, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 41.5
Std Dev 3.782
Gaus mean : 42.1464
Gaus Width: 3.9275
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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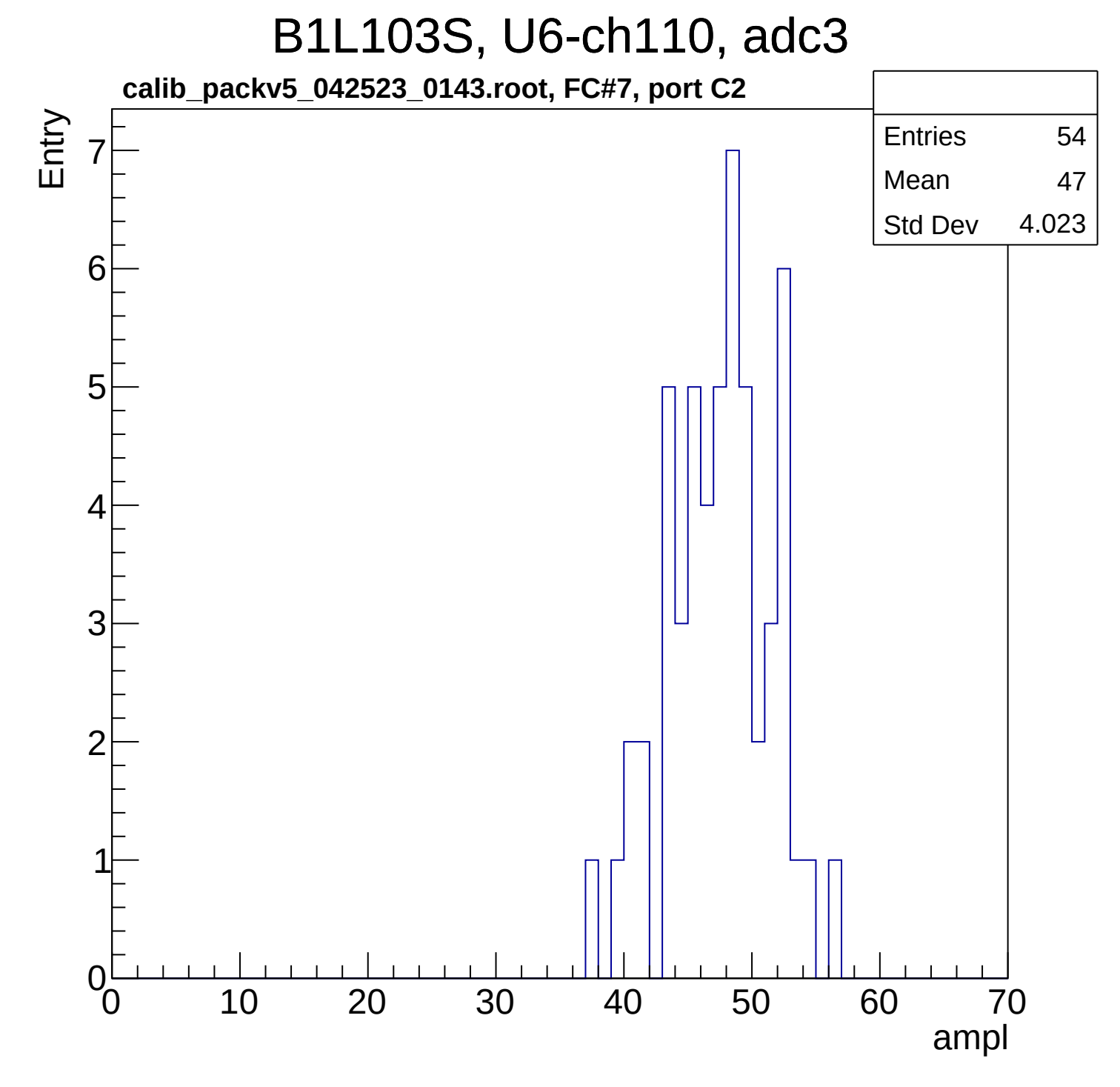
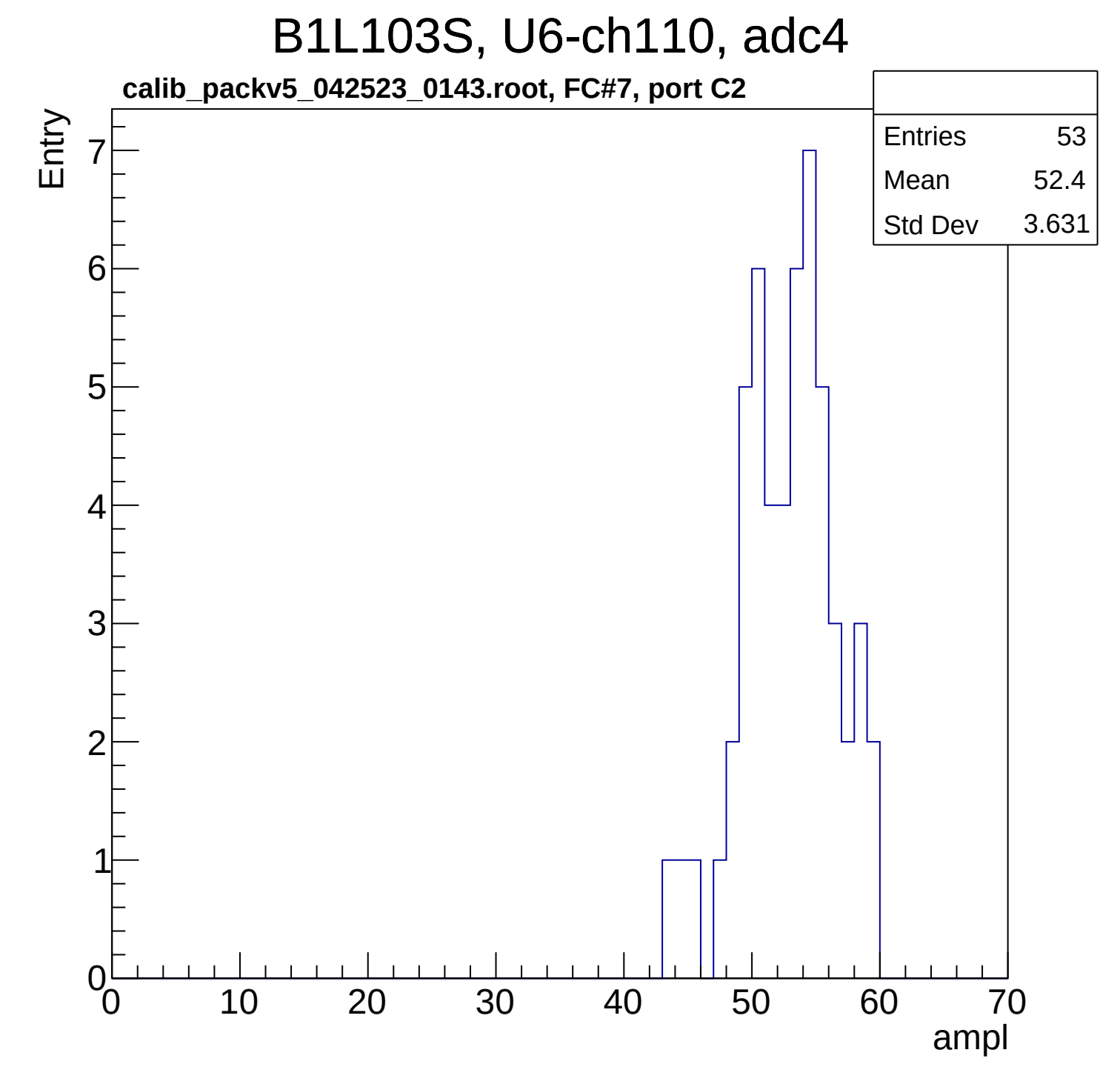

B1L103S, U6-ch110, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 47
Std Dev 4.023
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch110, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 52.4
Std Dev 3.631
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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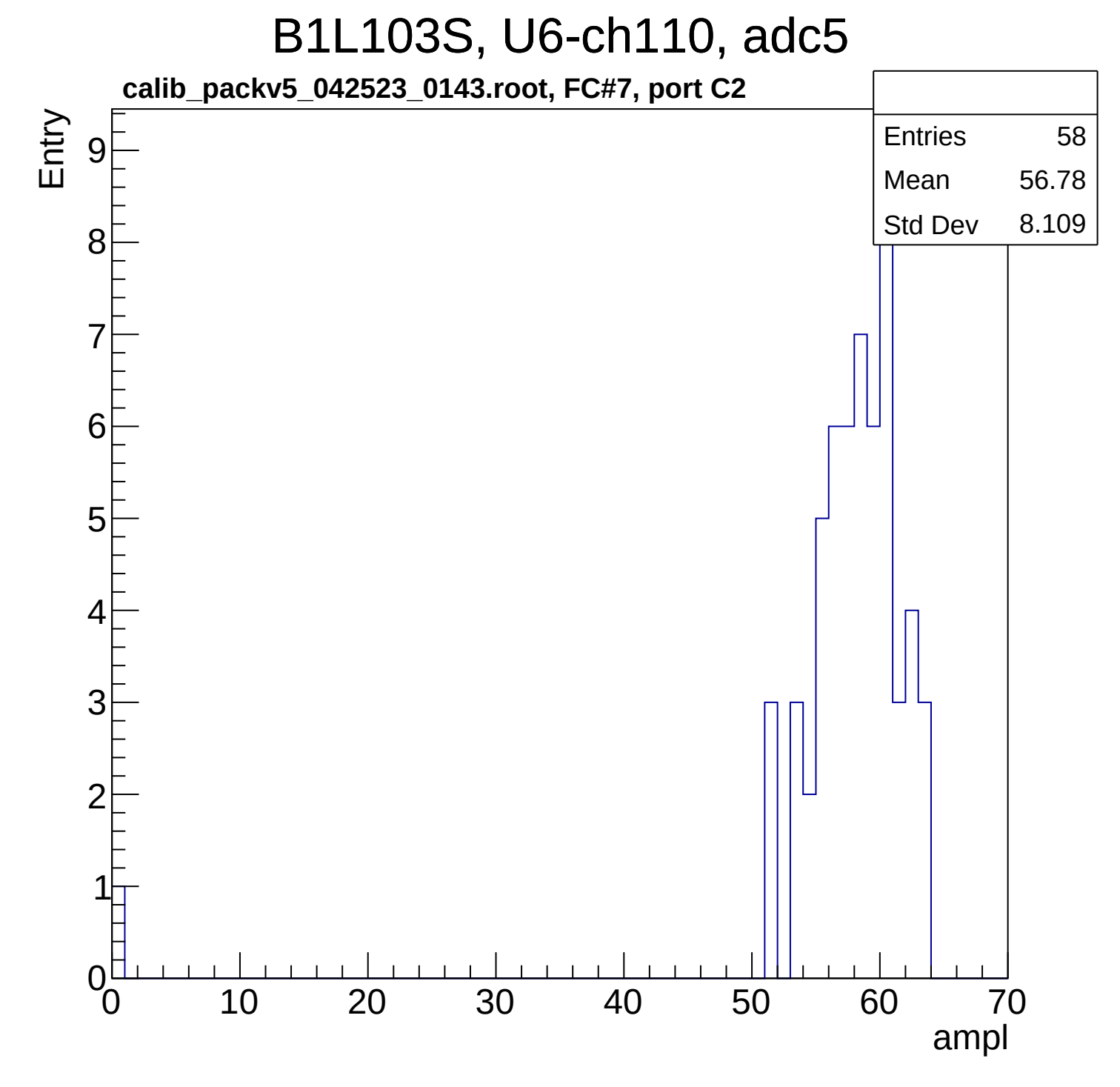
B1L103S, U6-ch110, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.78
Std Dev 8.109
Entry
ampl
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2
3
4
5
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0
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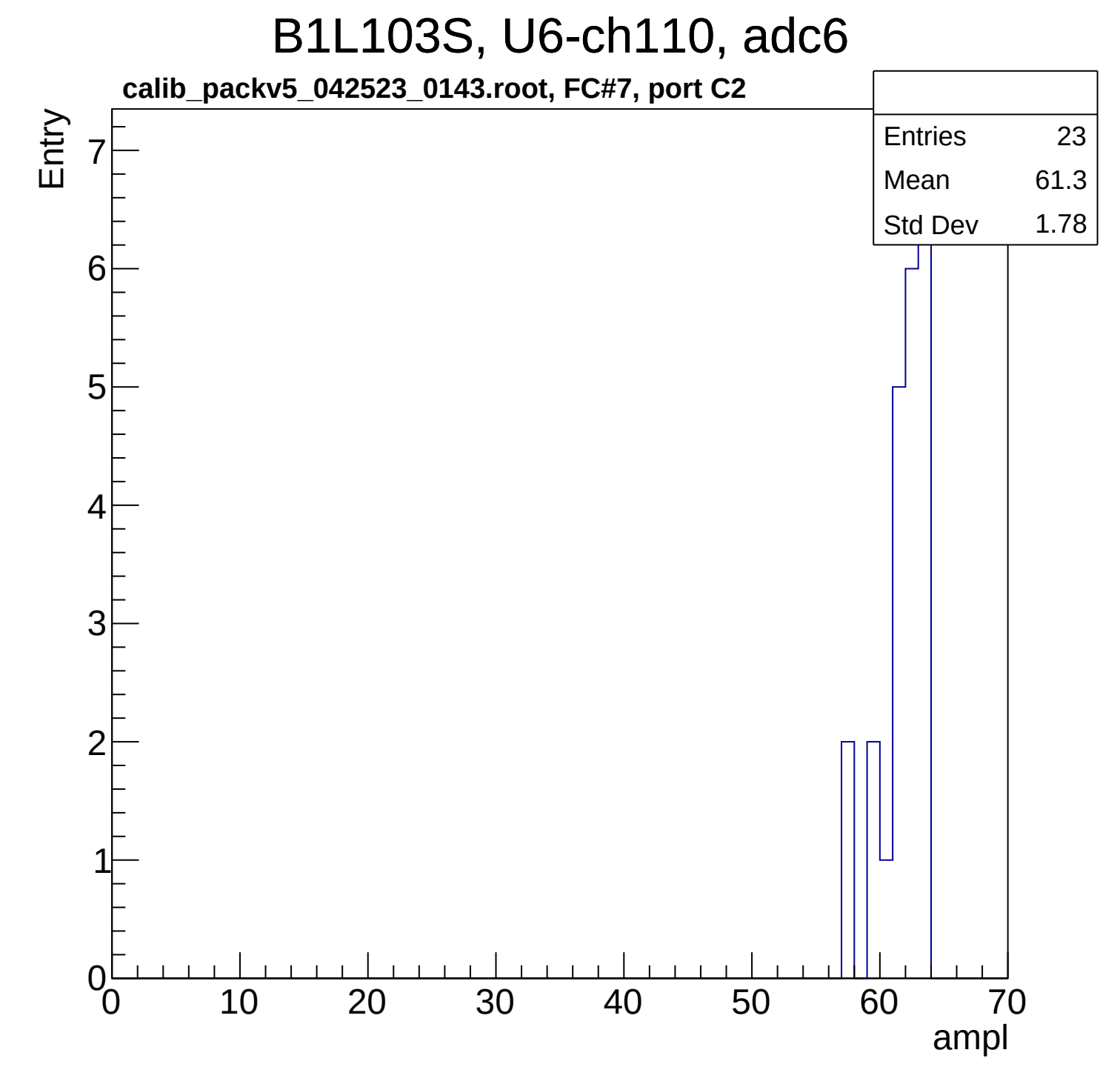
B1L103S, U6-ch110, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 23
Mean 61.3
Std Dev 1.78
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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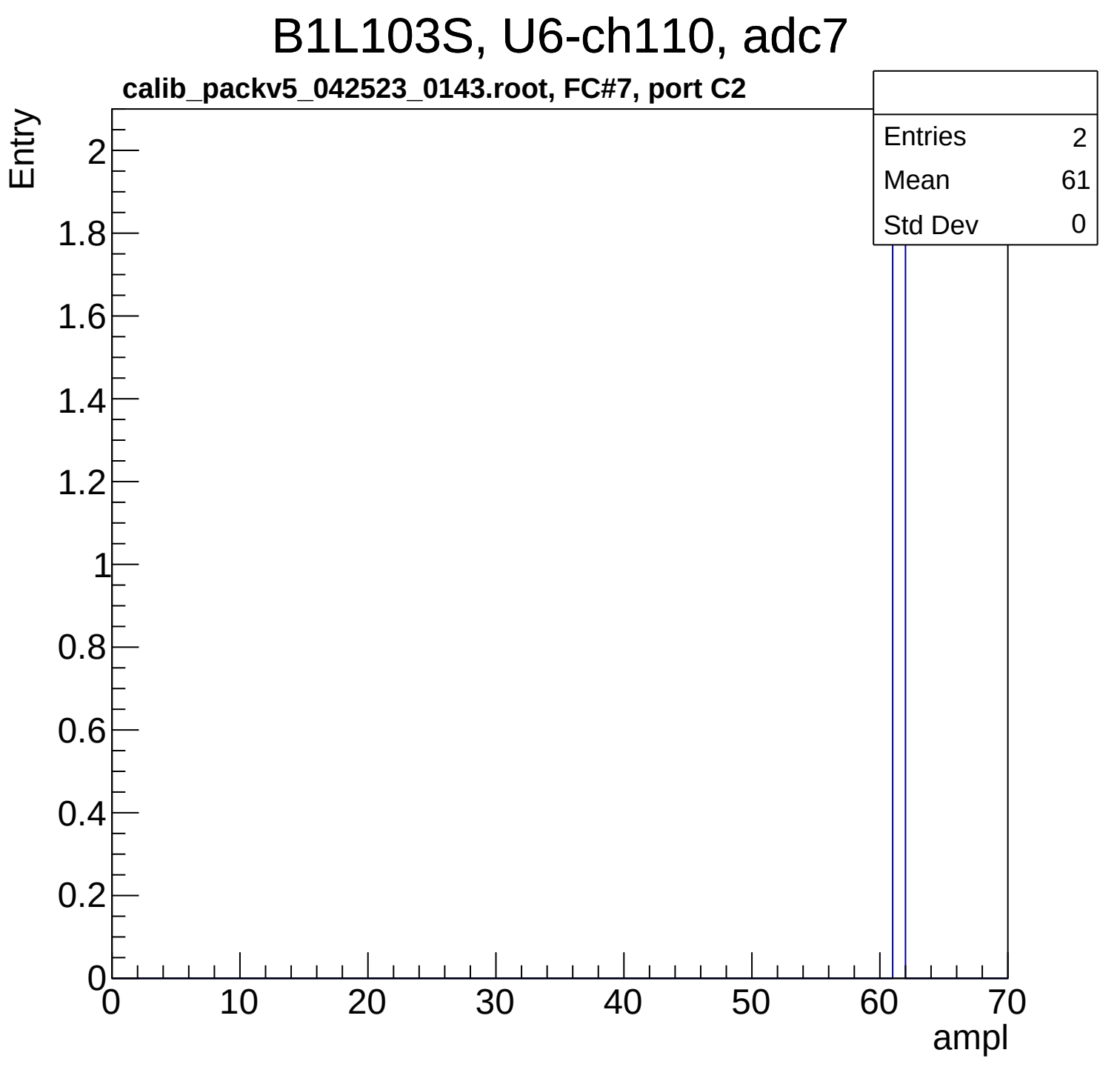
B1L103S, U6-ch110, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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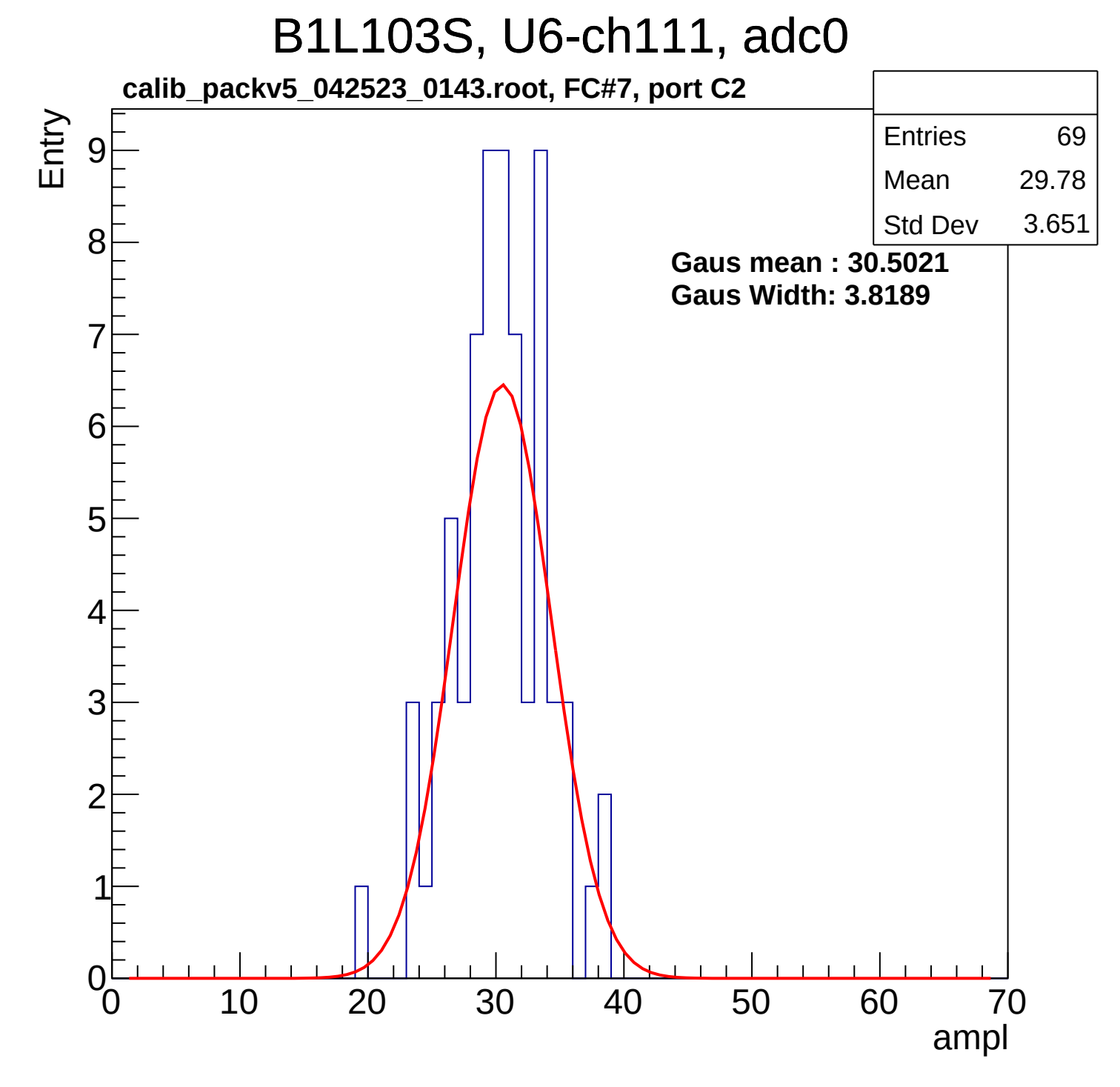
60
70


B1L103S, U6-ch111, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 29.78
Std Dev 3.651
Gaus mean : 30.5021
Gaus Width: 3.8189
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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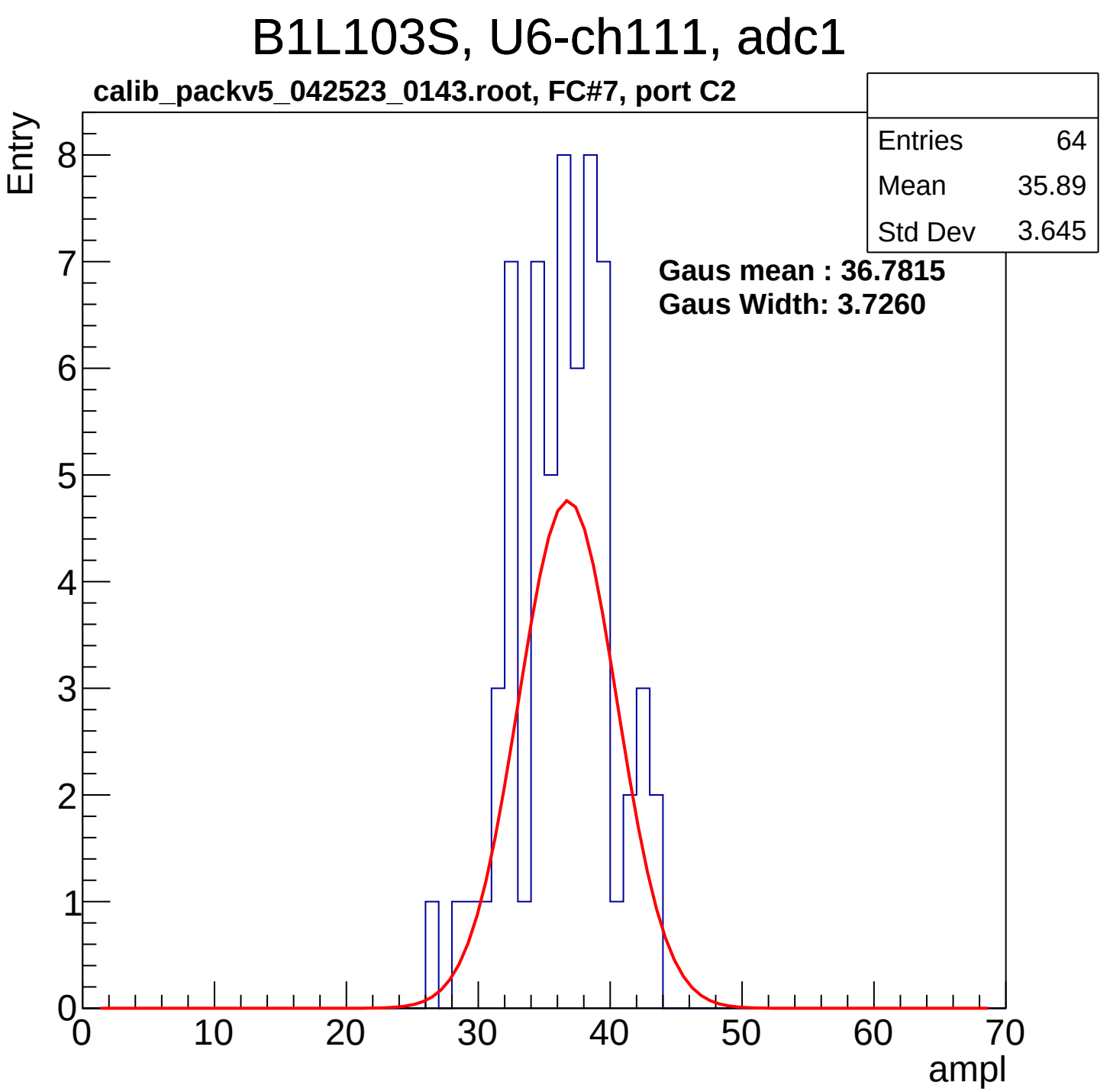
B1L103S, U6-ch111, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 35.89
Std Dev 3.645
Gaus mean : 36.7815
Gaus Width: 3.7260
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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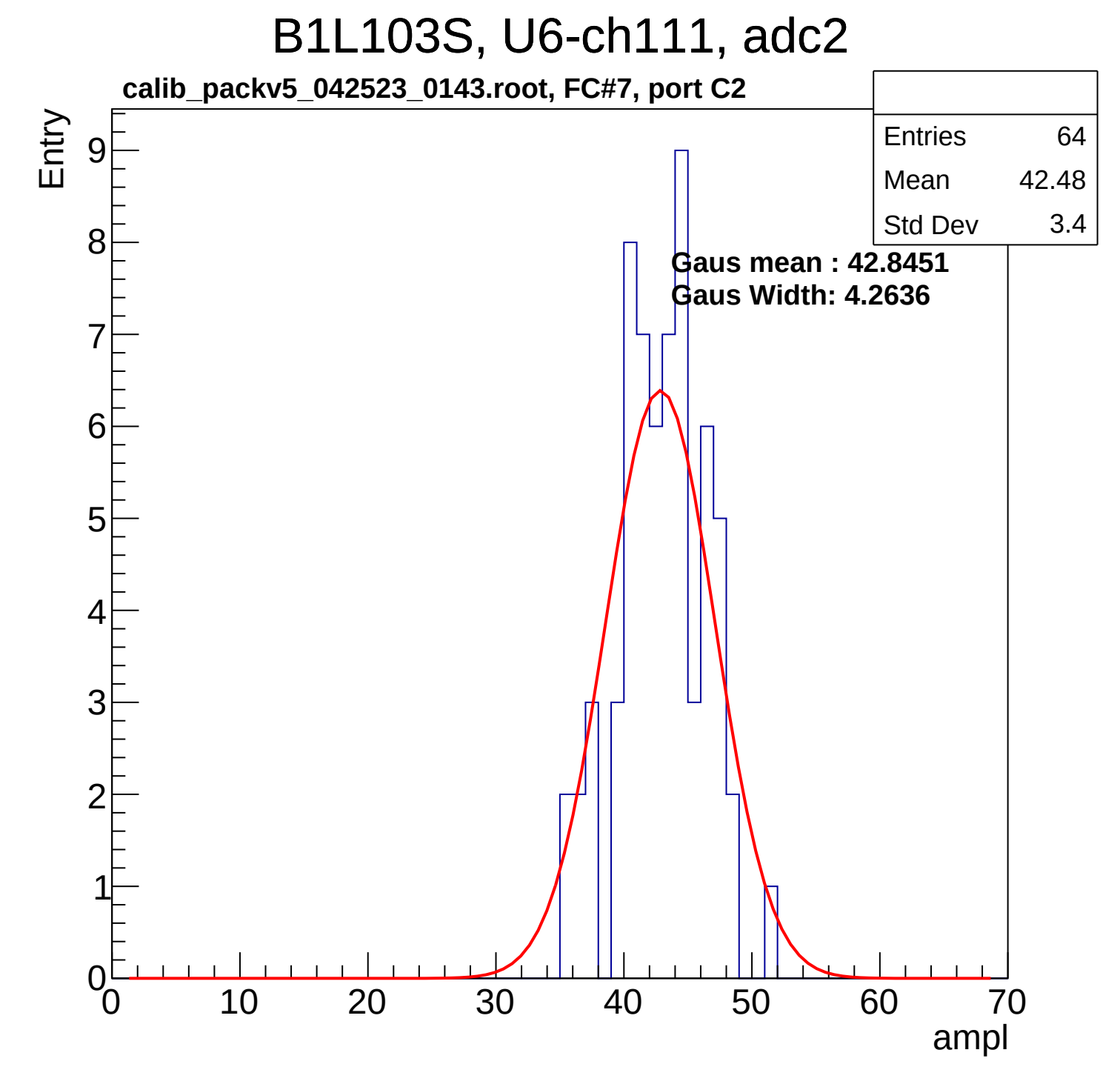
B1L103S, U6-ch111, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 42.48
Std Dev 3.4
Gaus mean : 42.8451
Gaus Width: 4.2636
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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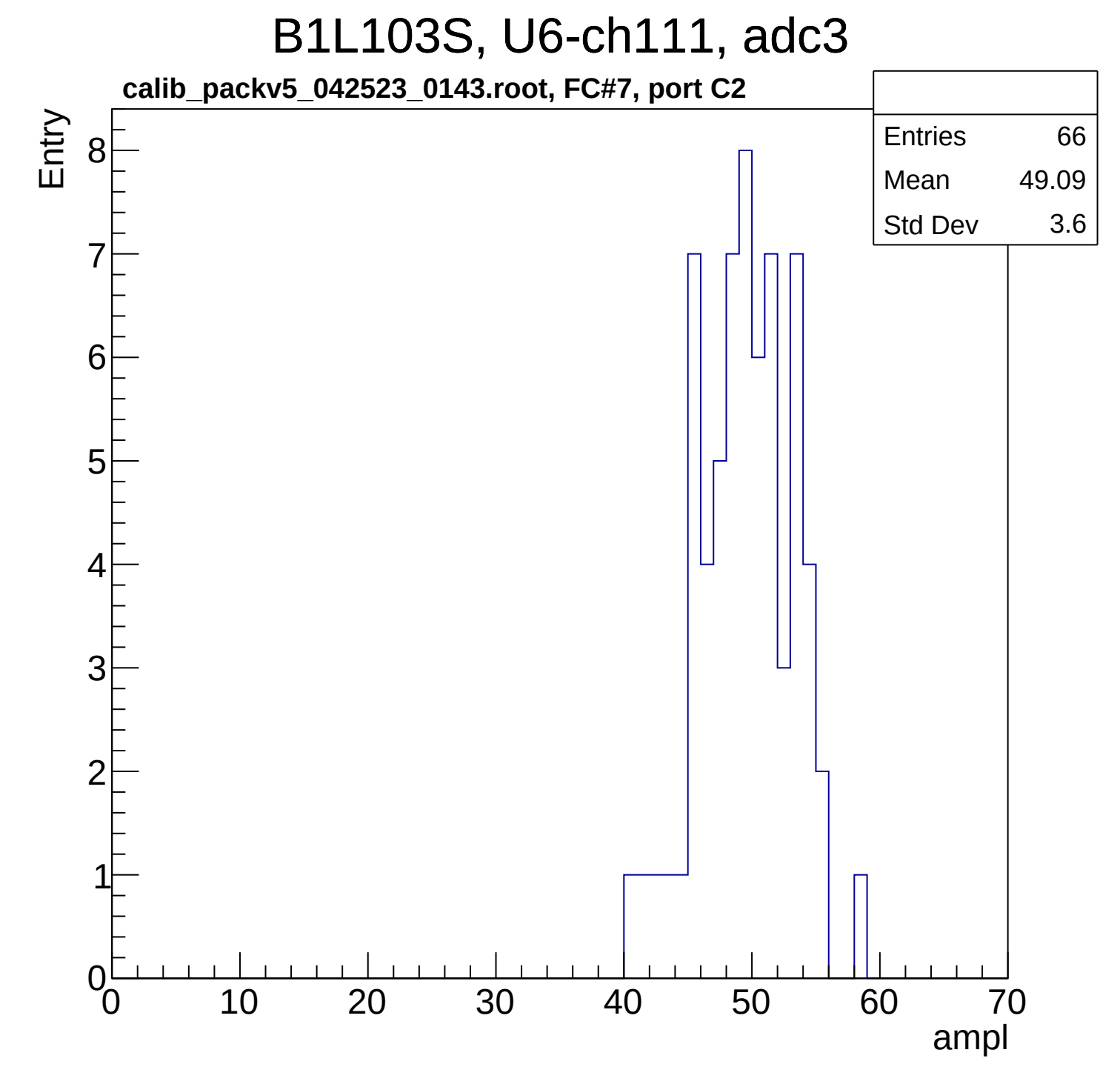
B1L103S, U6-ch111, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 49.09
Std Dev 3.6
Entry
ampl
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3
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0
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60
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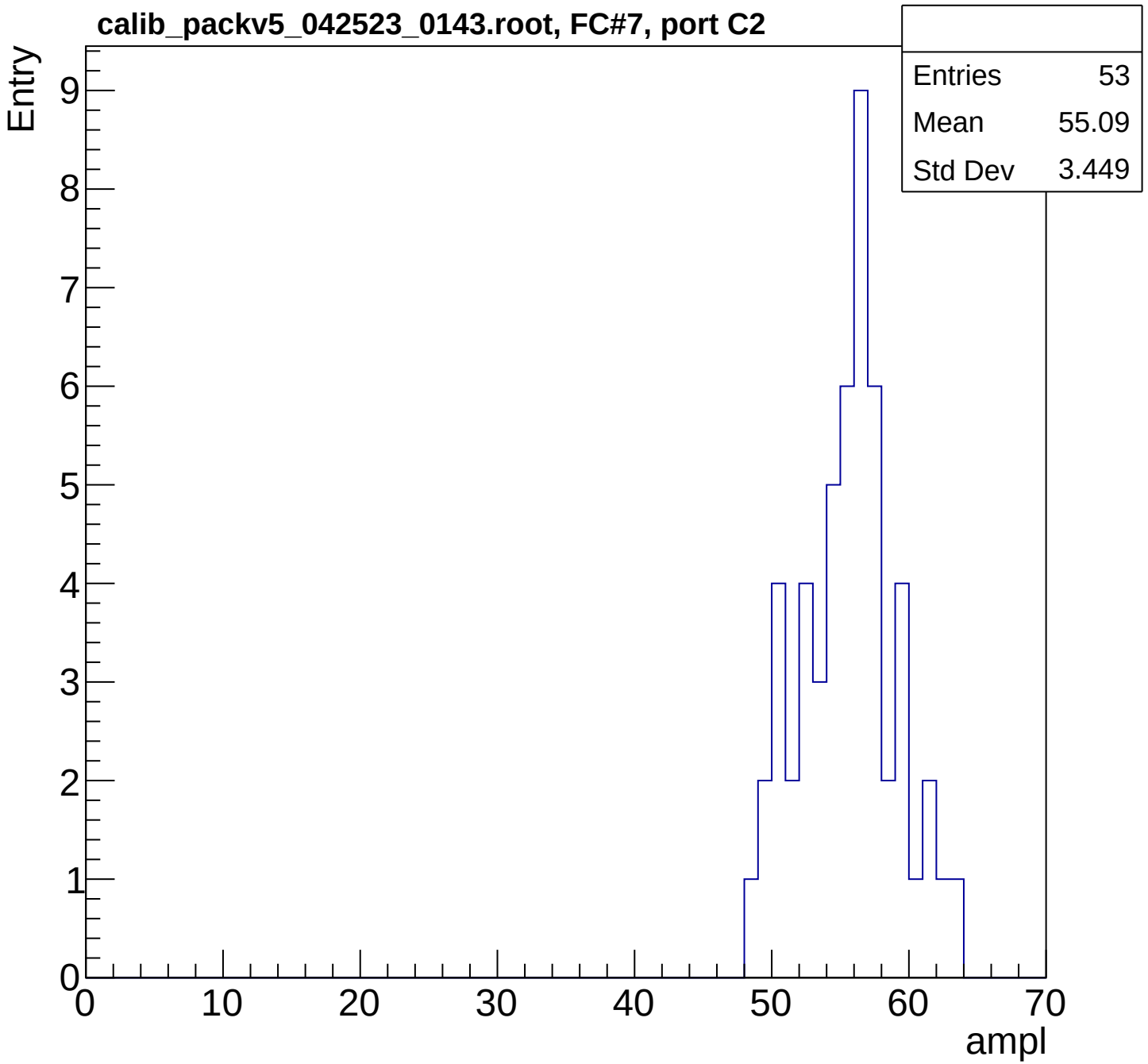
B1L103S, U6-ch111, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.09
Std Dev 3.449
Entry
ampl
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2
3
4
5
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7
8
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0
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40
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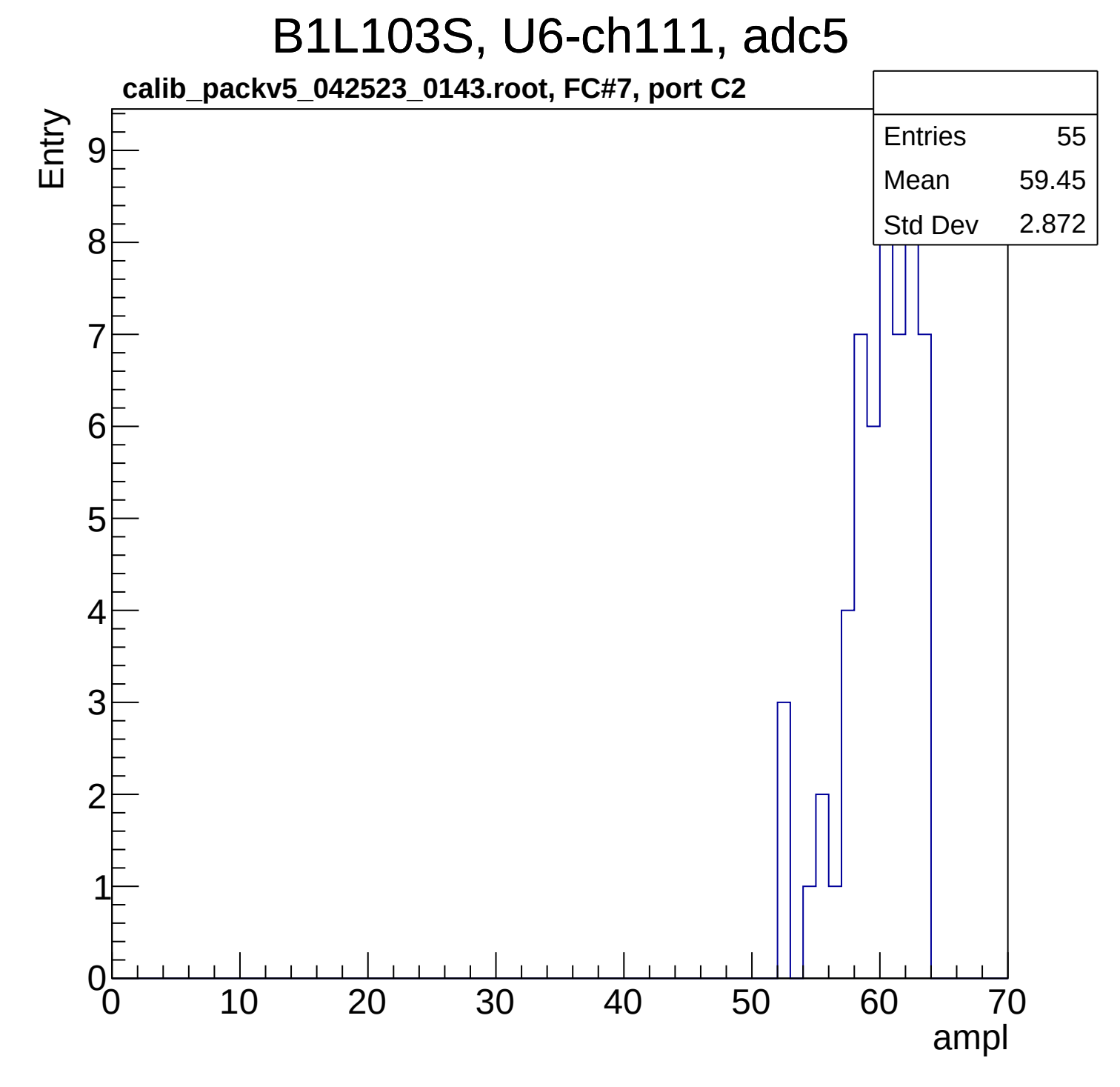
B1L103S, U6-ch111, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 59.45
Std Dev 2.872
Entry
ampl
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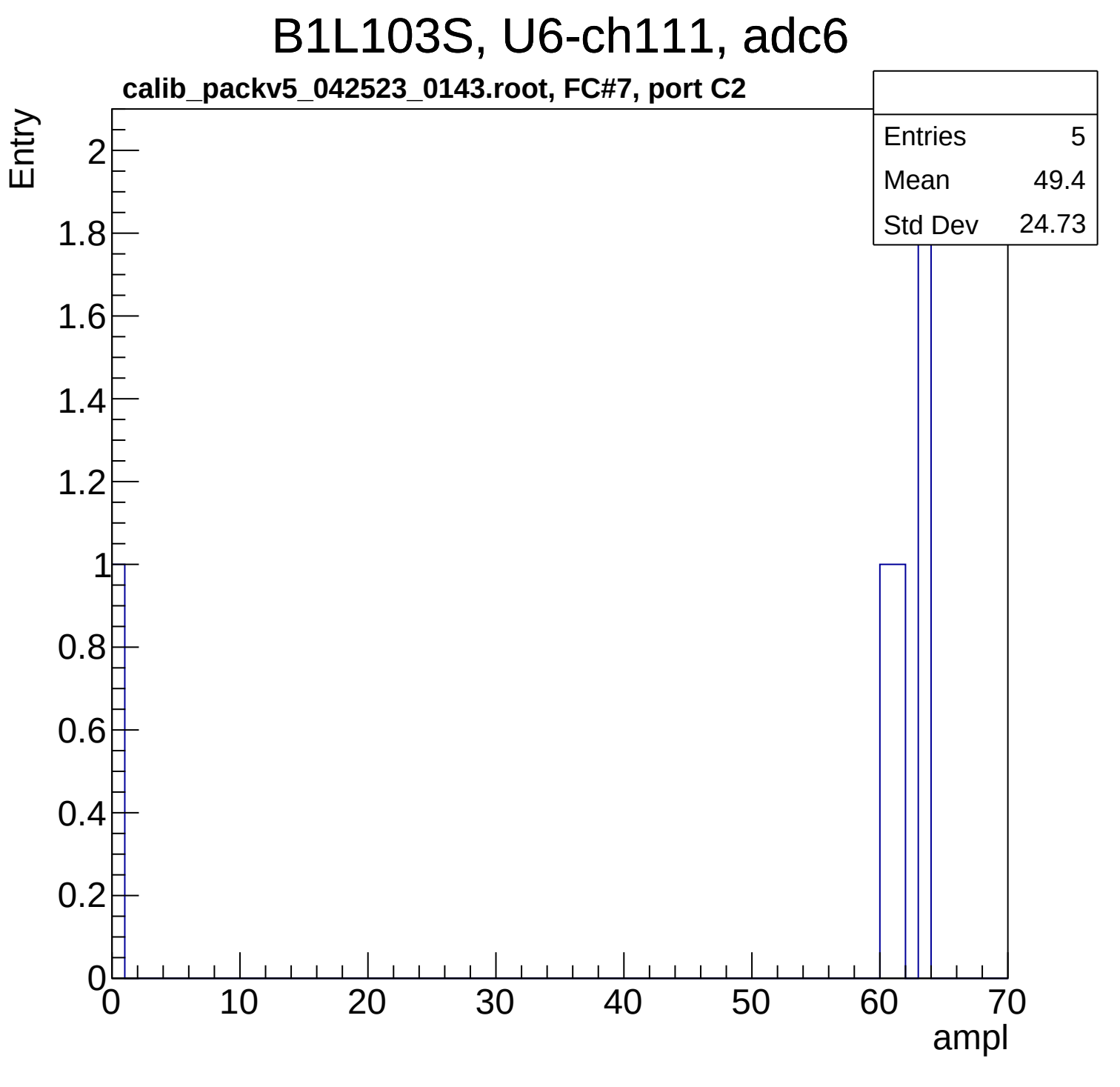
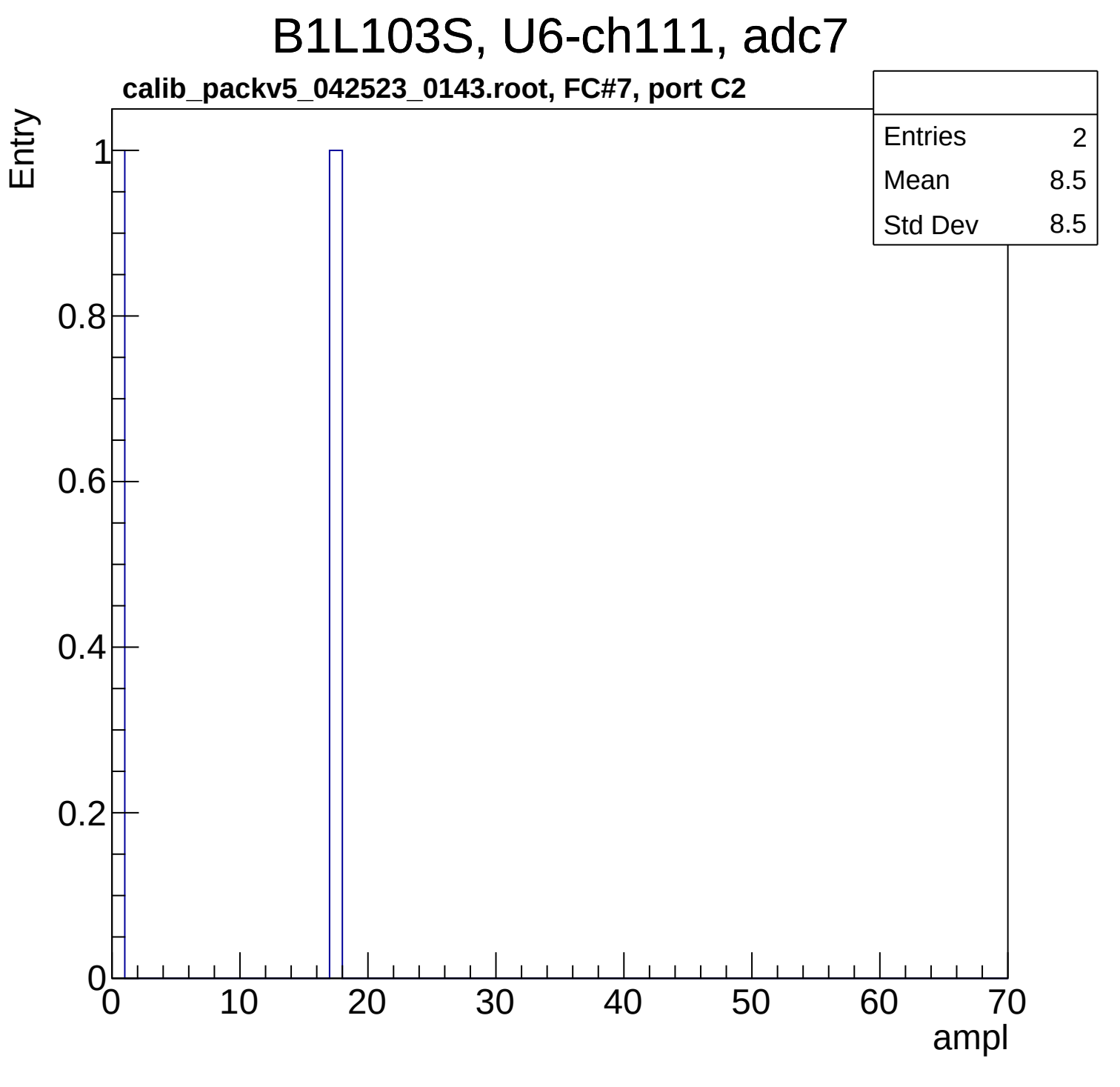
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch111, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.4
Std Dev 24.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch111, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
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0.6
0.4
0.2
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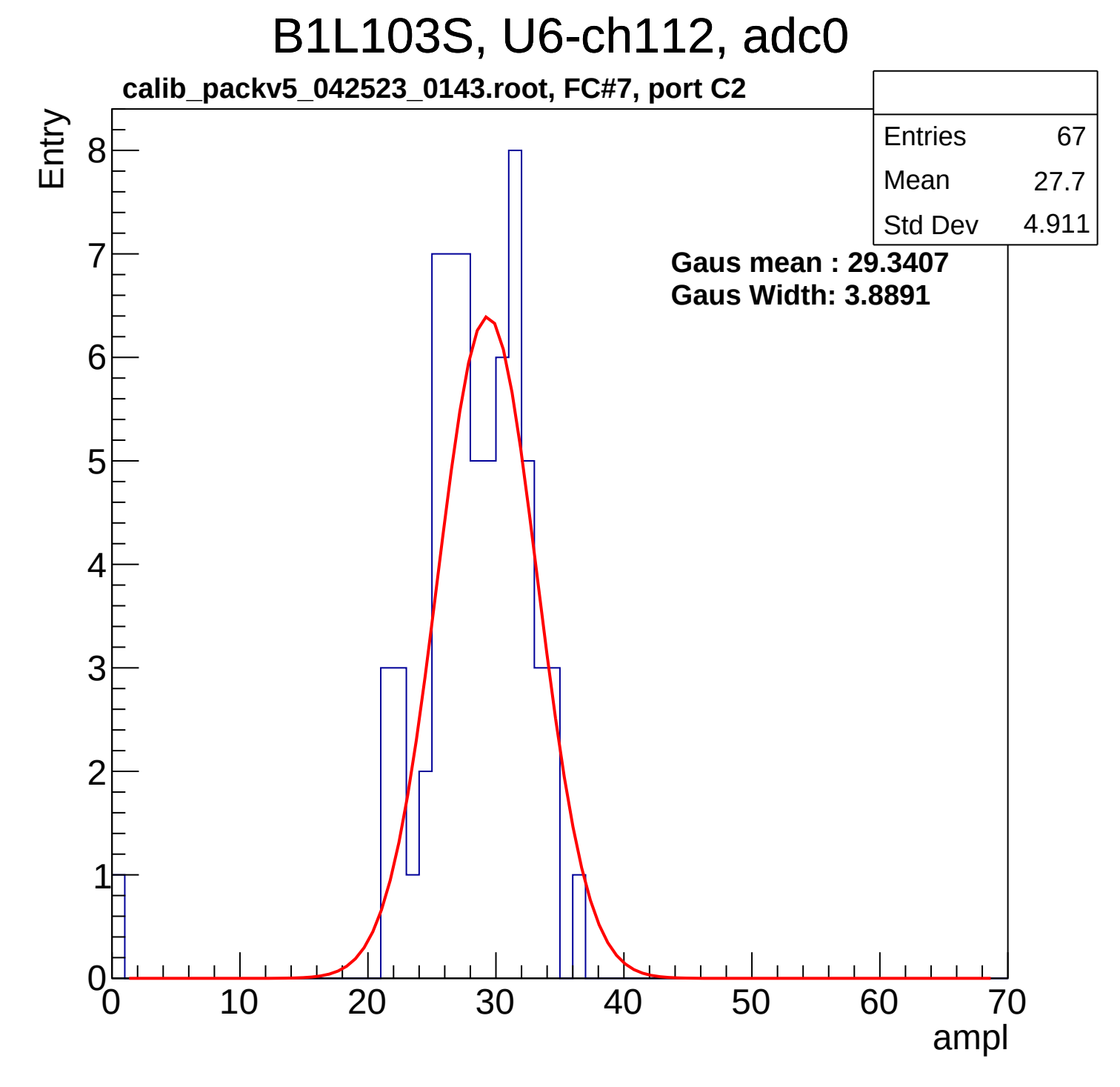
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch112, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 27.7
Std Dev 4.911
Gaus mean : 29.3407
Gaus Width: 3.8891
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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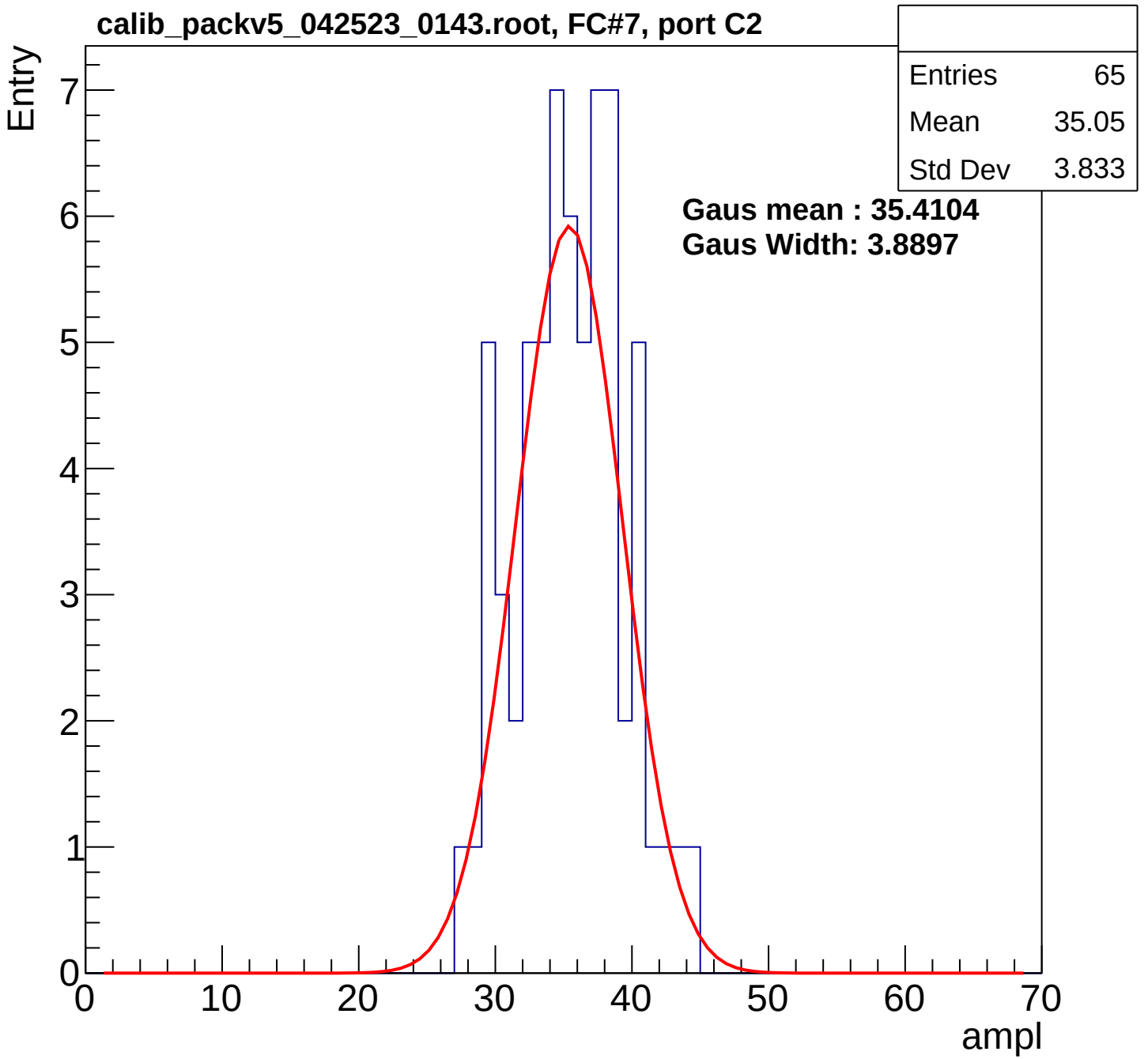
60
70


B1L103S, U6-ch112, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.05
Std Dev 3.833
Gaus mean : 35.4104
Gaus Width: 3.8897
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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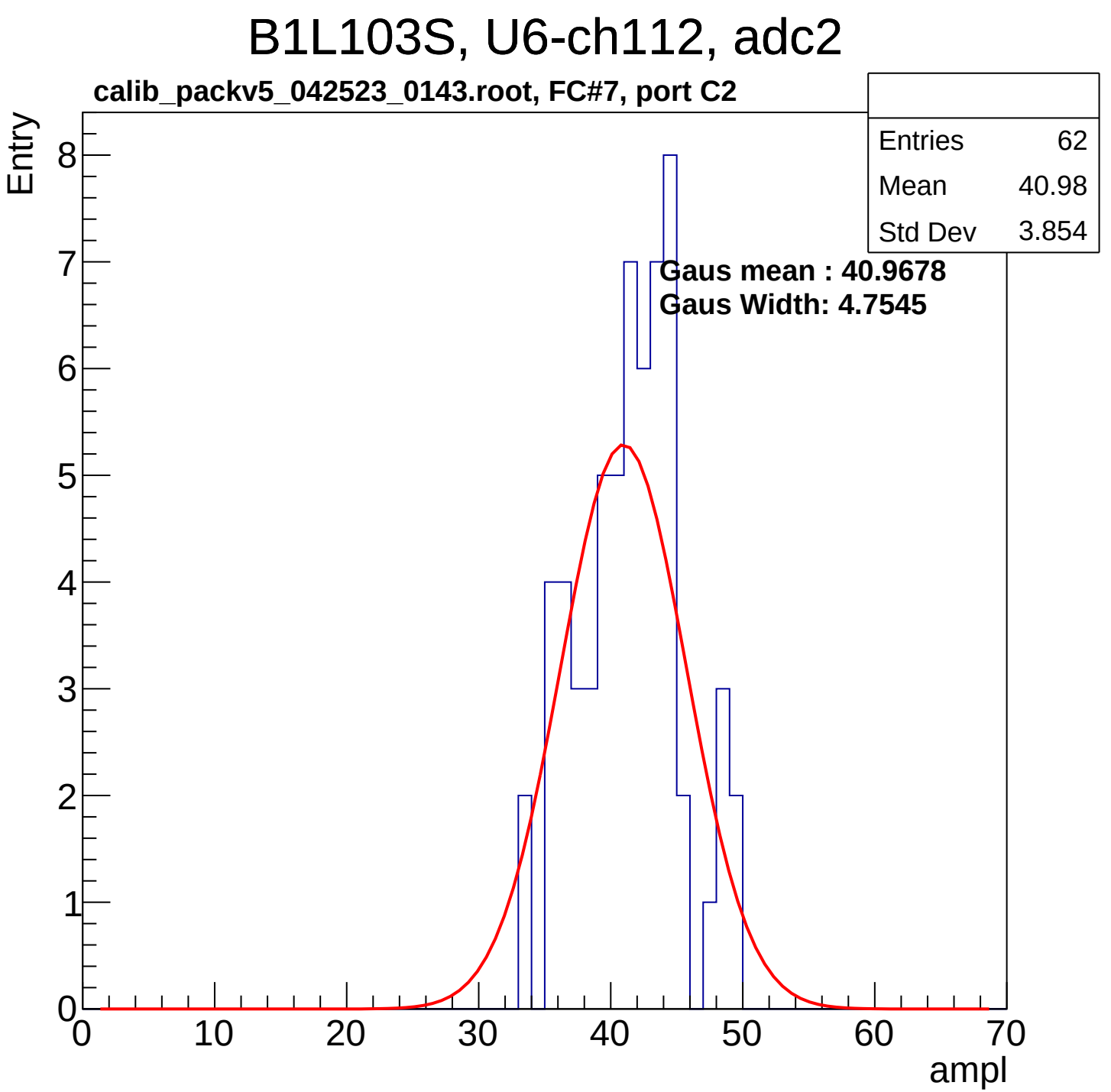
B1L103S, U6-ch112, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 40.98
Std Dev 3.854
Gaus mean : 40.9678
Gaus Width: 4.7545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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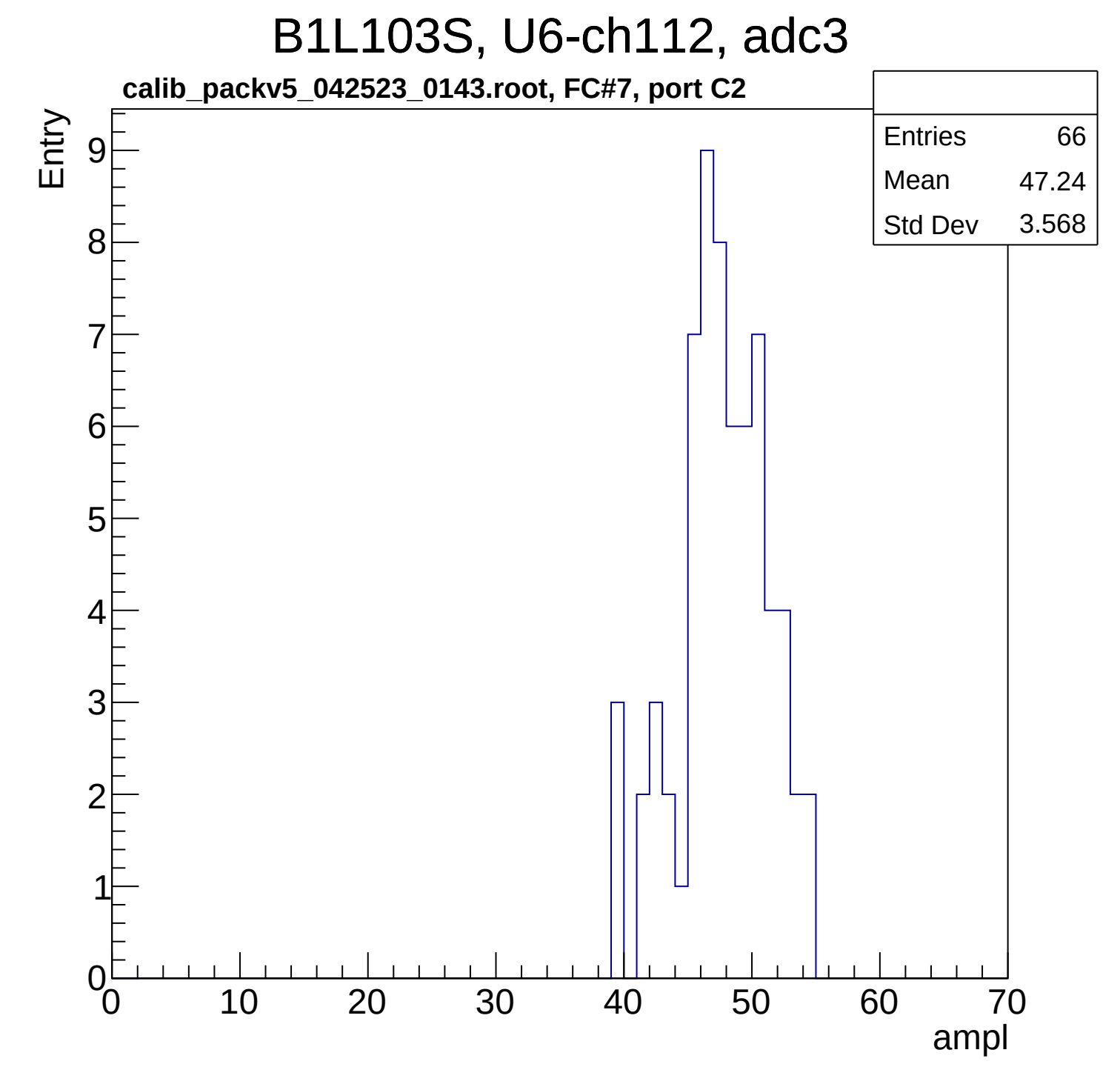
B1L103S, U6-ch112, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 47.24
Std Dev 3.568
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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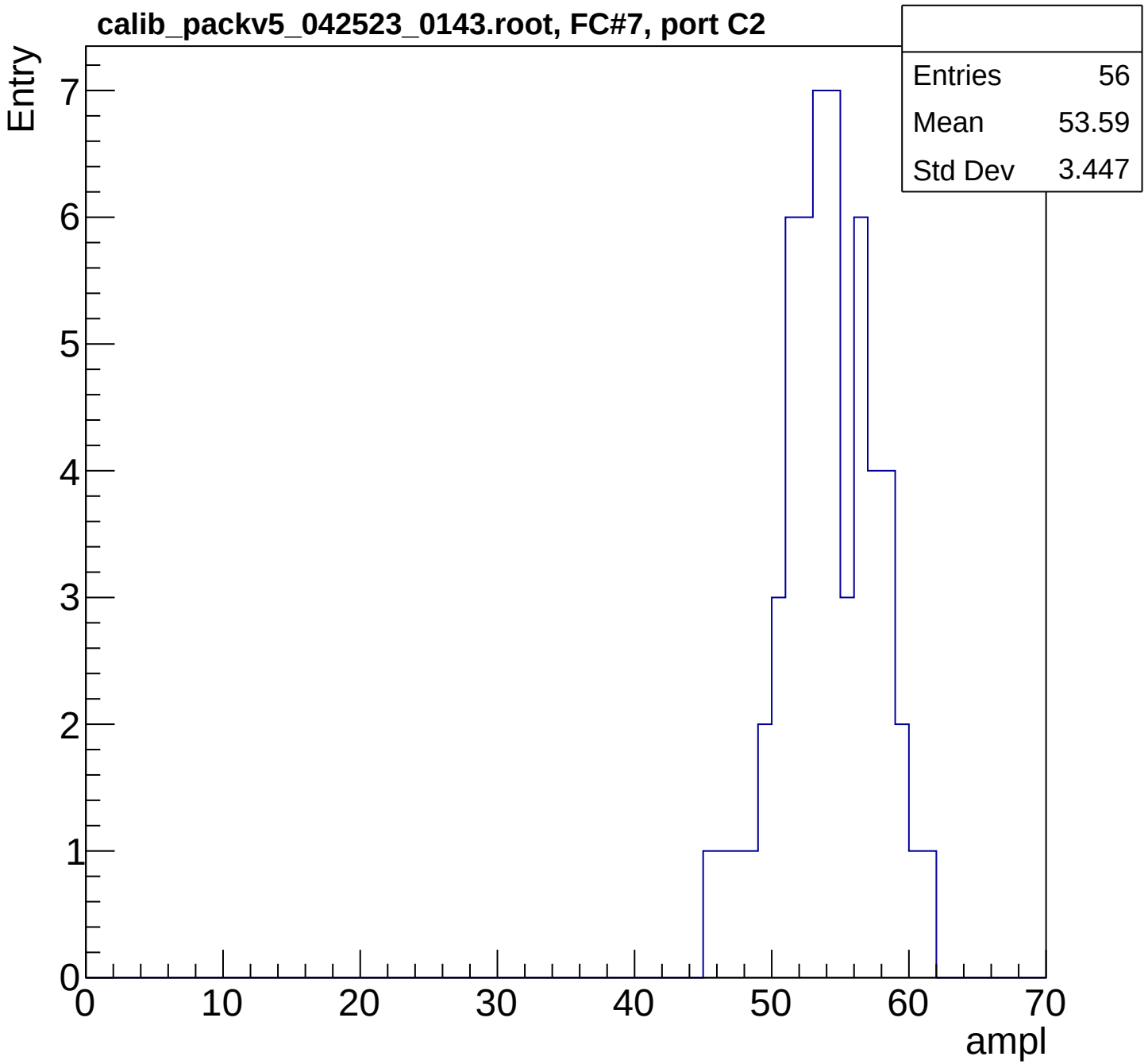
B1L103S, U6-ch112, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.59
Std Dev 3.447
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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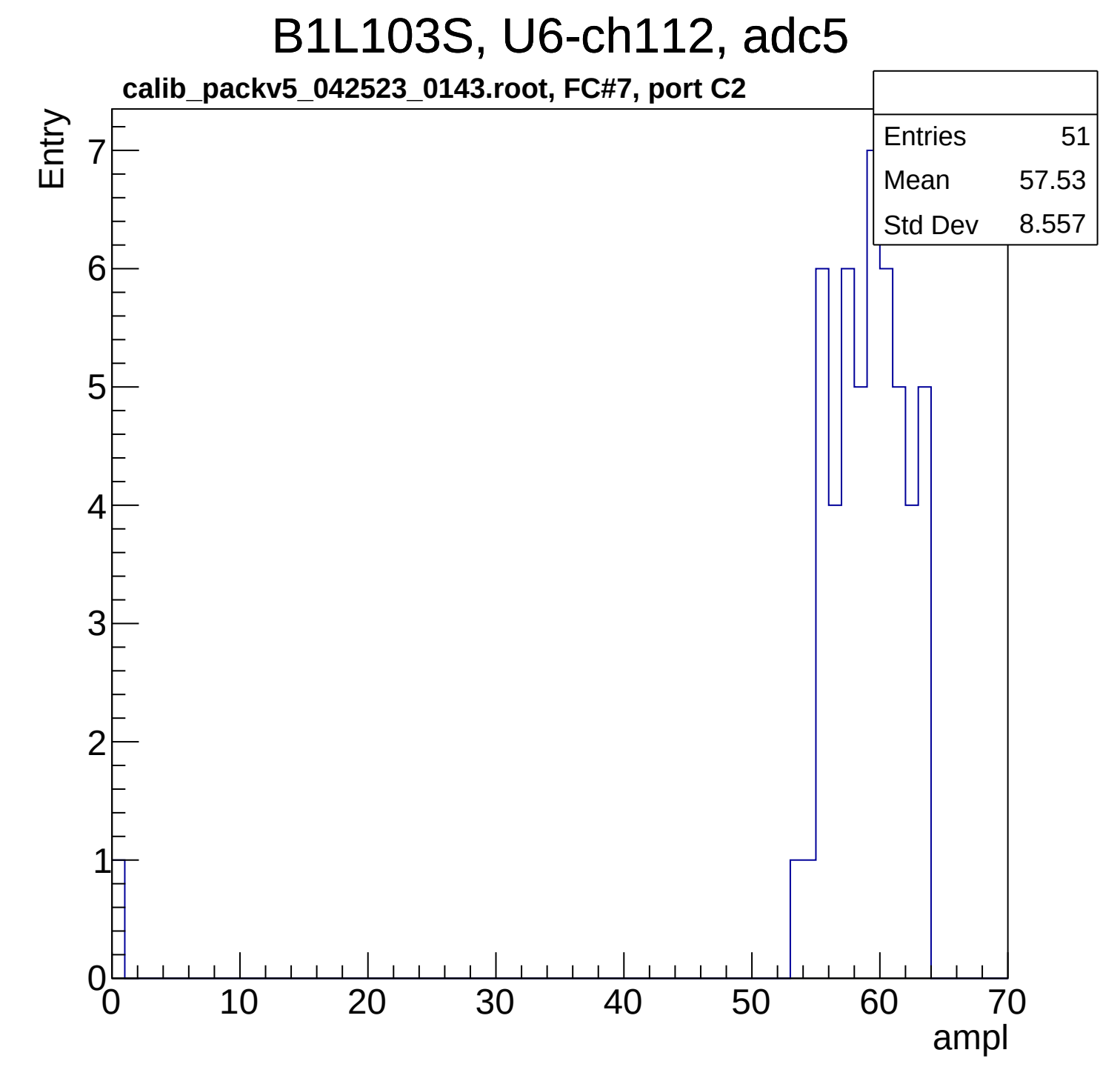
B1L103S, U6-ch112, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.53
Std Dev 8.557
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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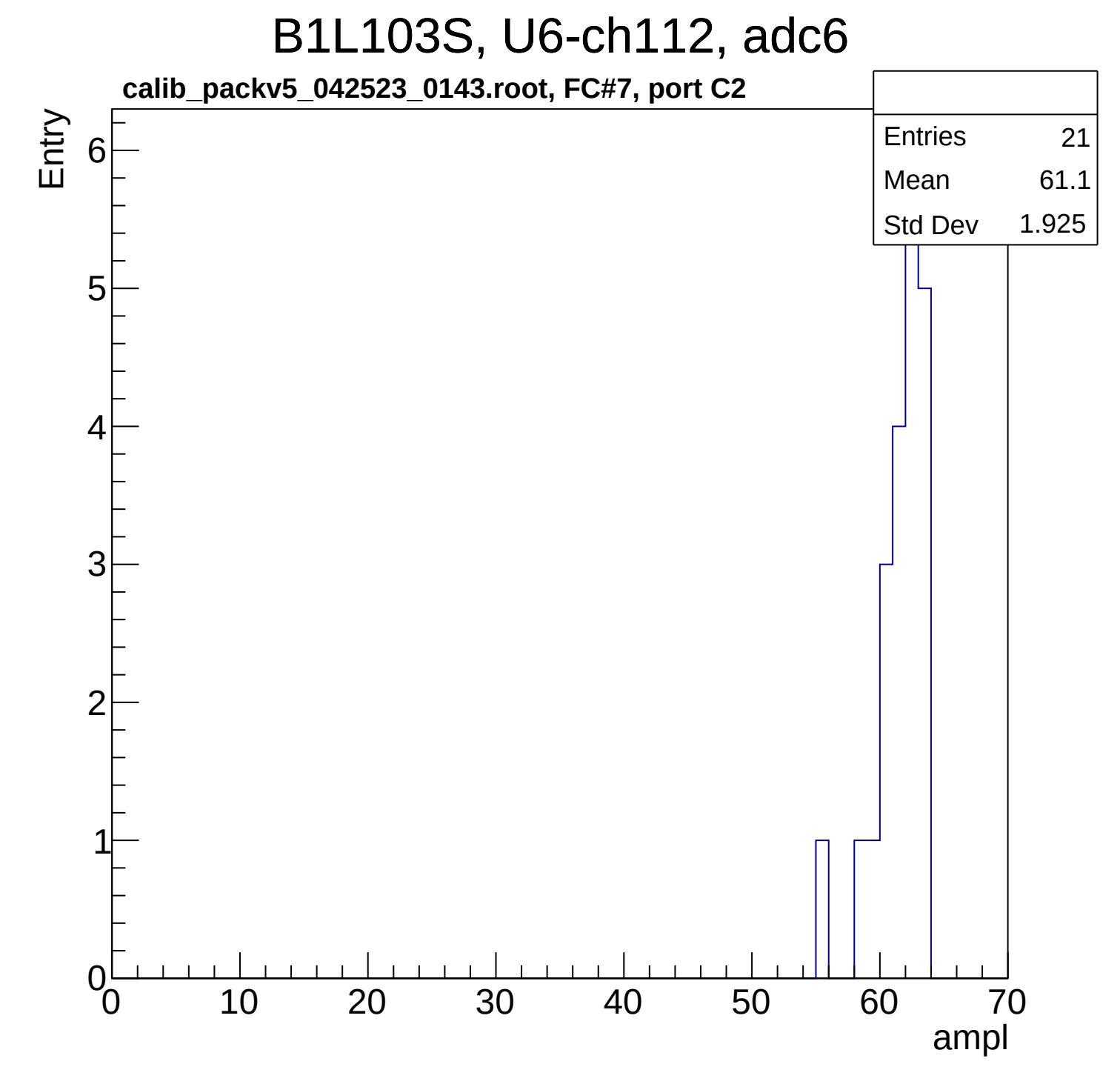
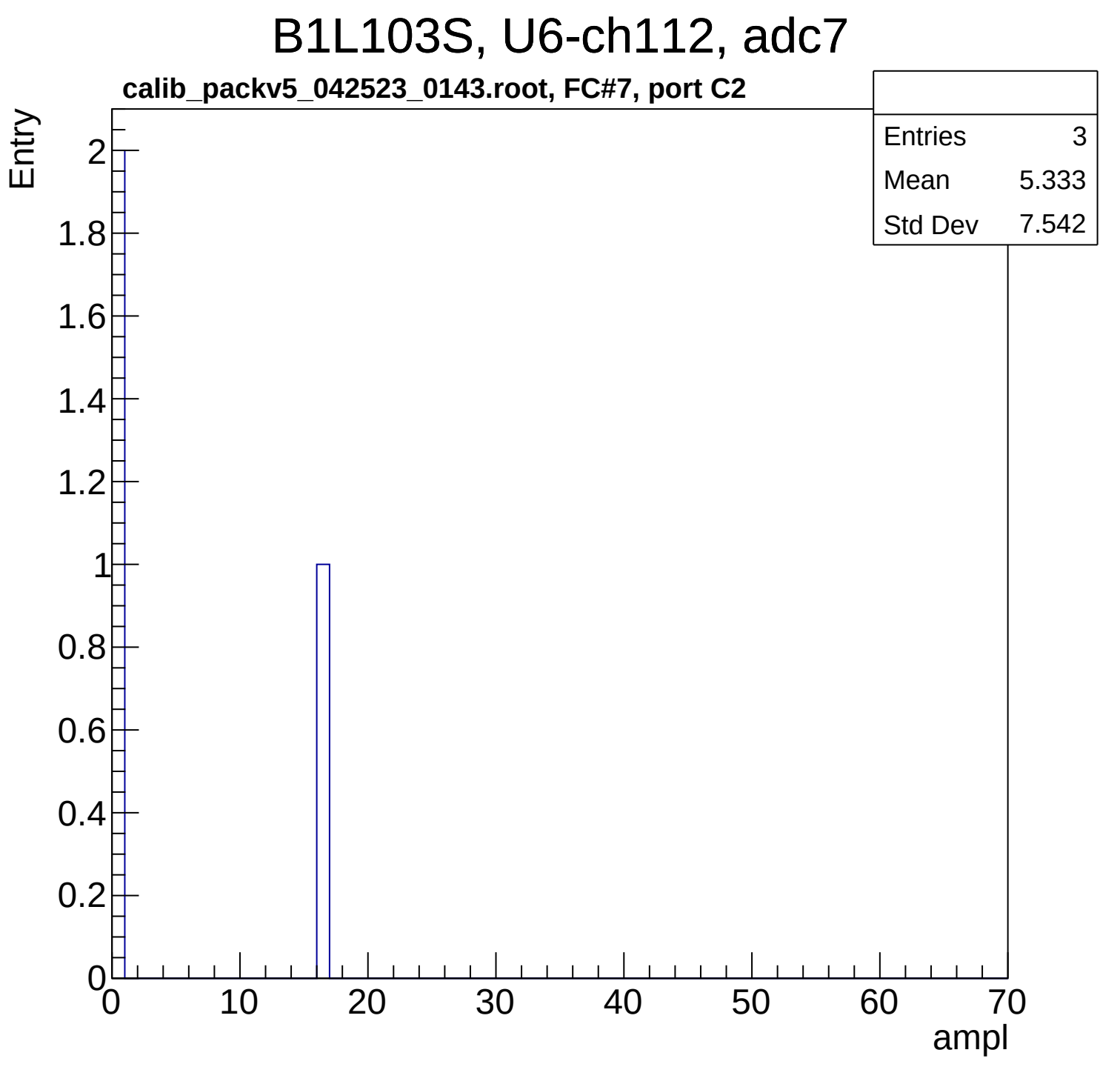
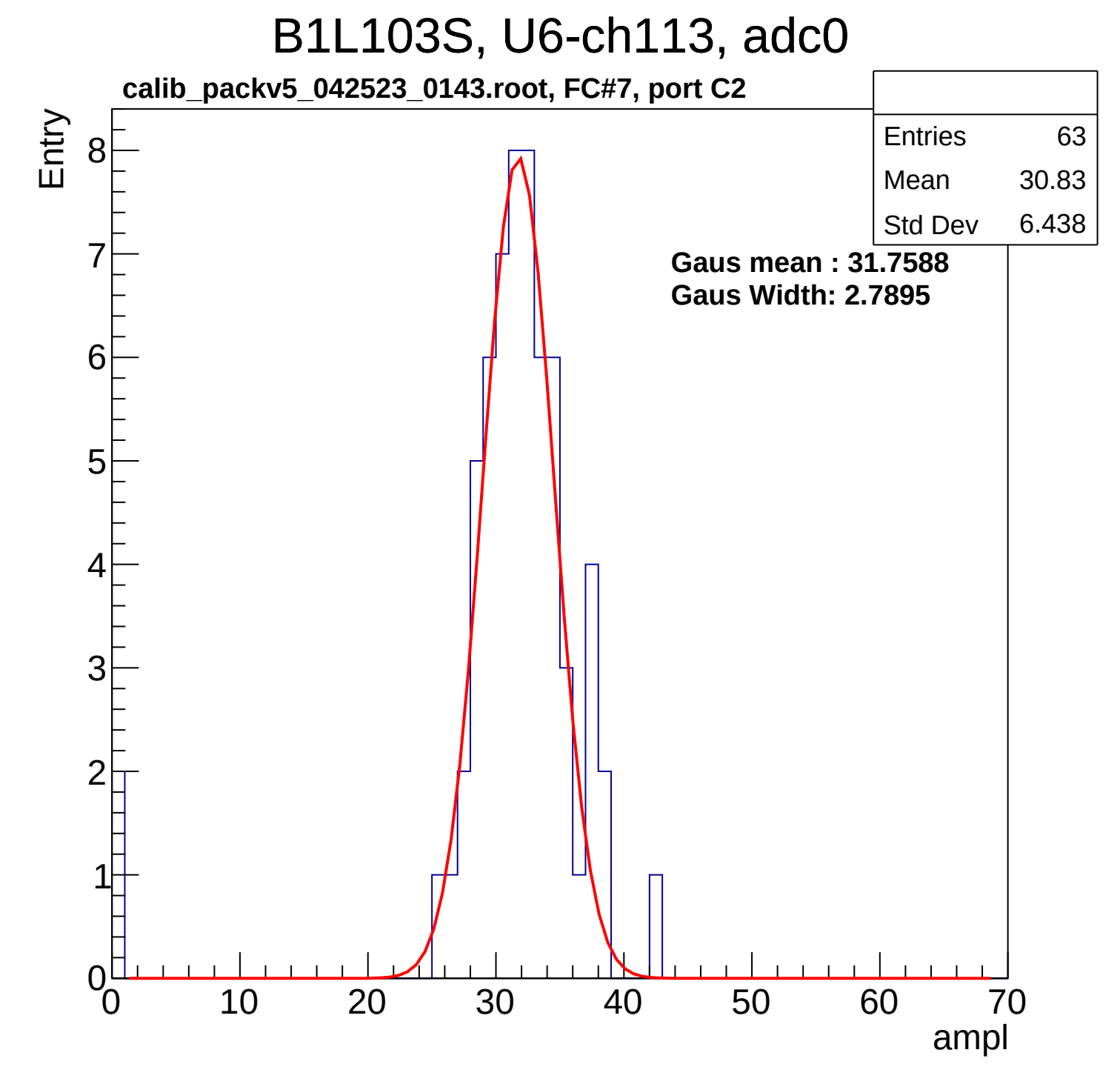

B1L103S, U6-ch112, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 61.1
Std Dev 1.925
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch112, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 5.333
Std Dev 7.542
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch113, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 30.83
Std Dev 6.438
Gaus mean : 31.7588
Gaus Width: 2.7895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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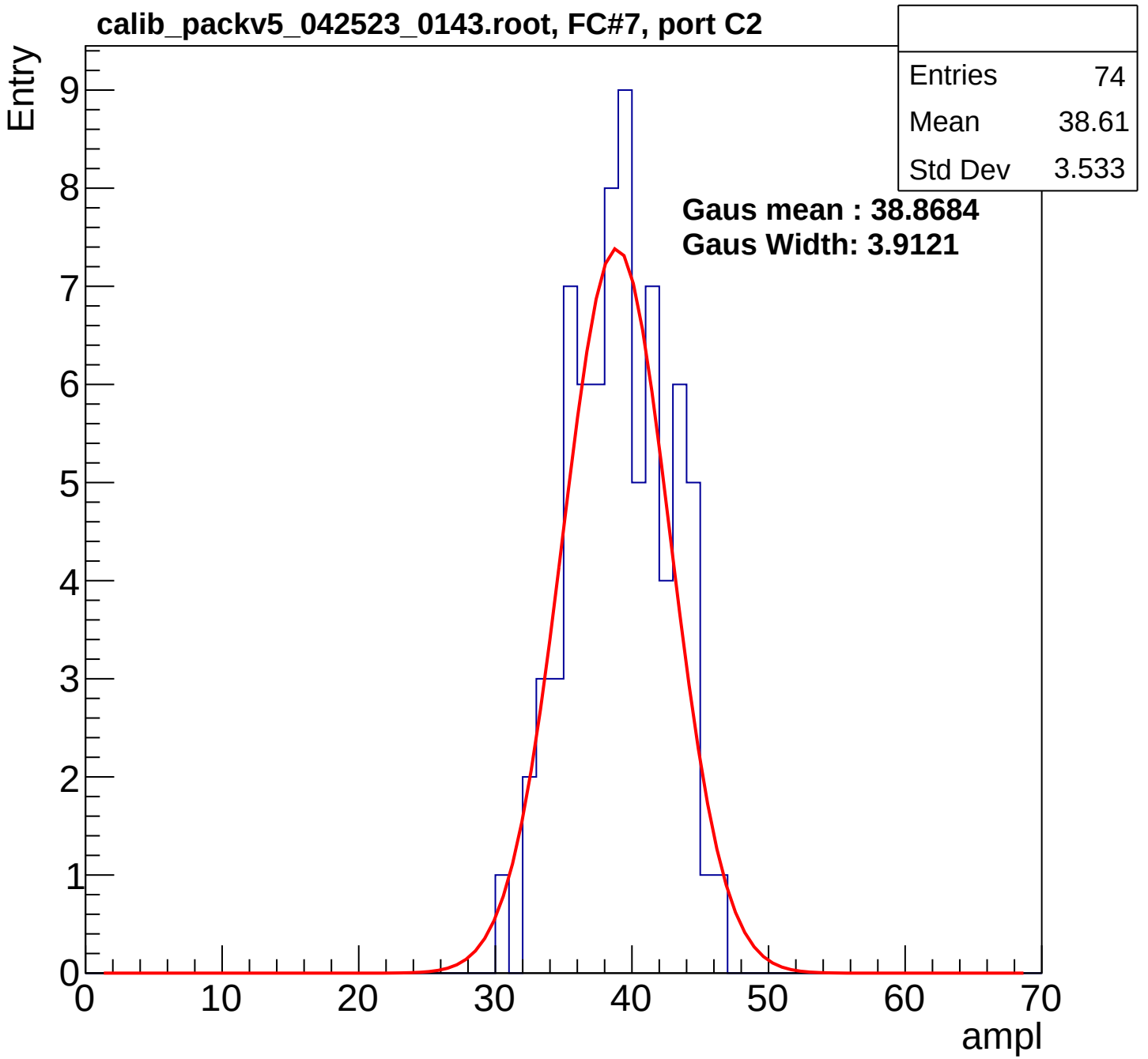
B1L103S, U6-ch113, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 38.61
Std Dev 3.533
Gaus mean : 38.8684
Gaus Width: 3.9121
Entry
ampl
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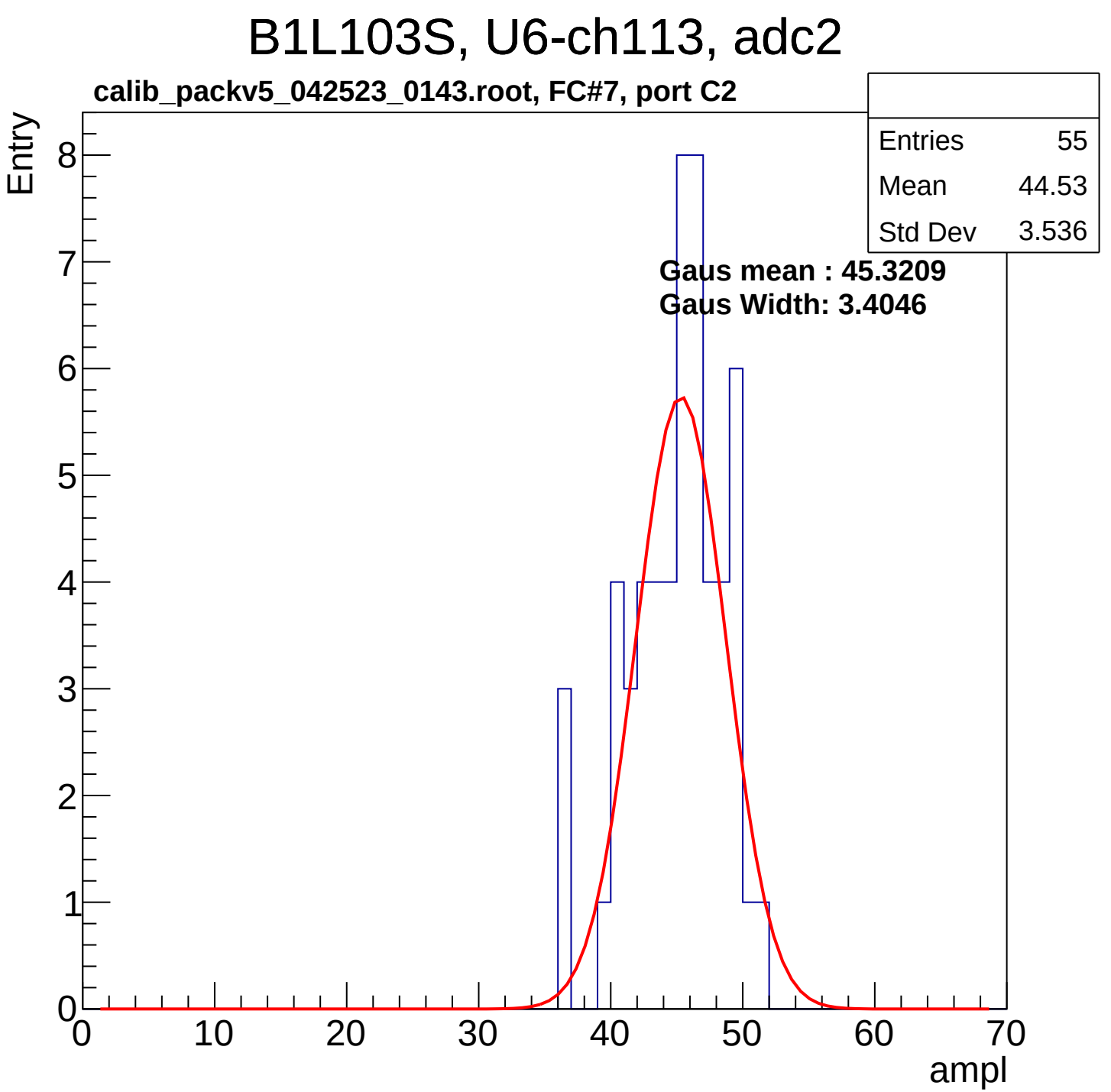
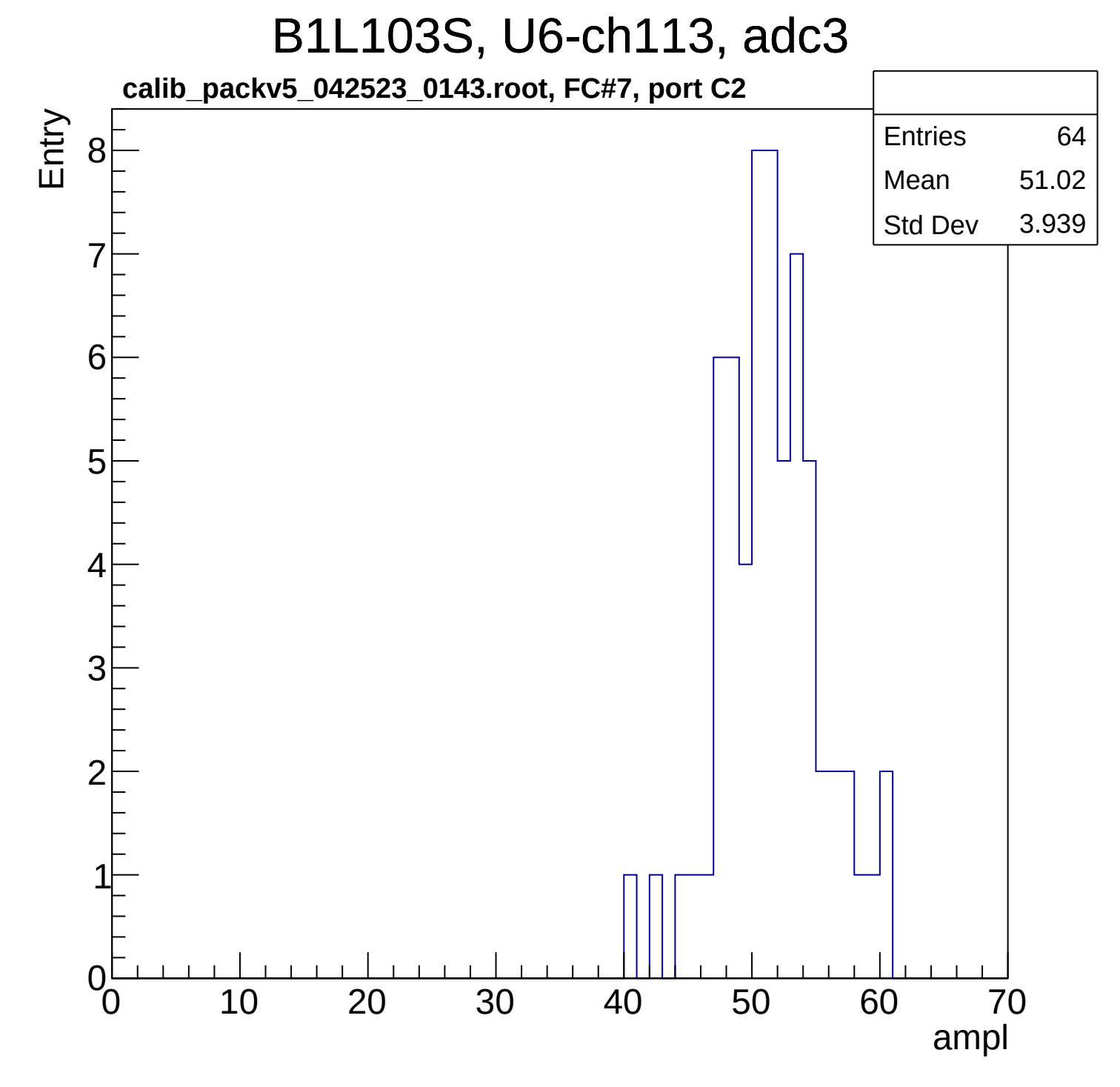
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch113, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 44.53
Std Dev 3.536
Gaus mean : 45.3209
Gaus Width: 3.4046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch113, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 51.02
Std Dev 3.939
Entry
ampl
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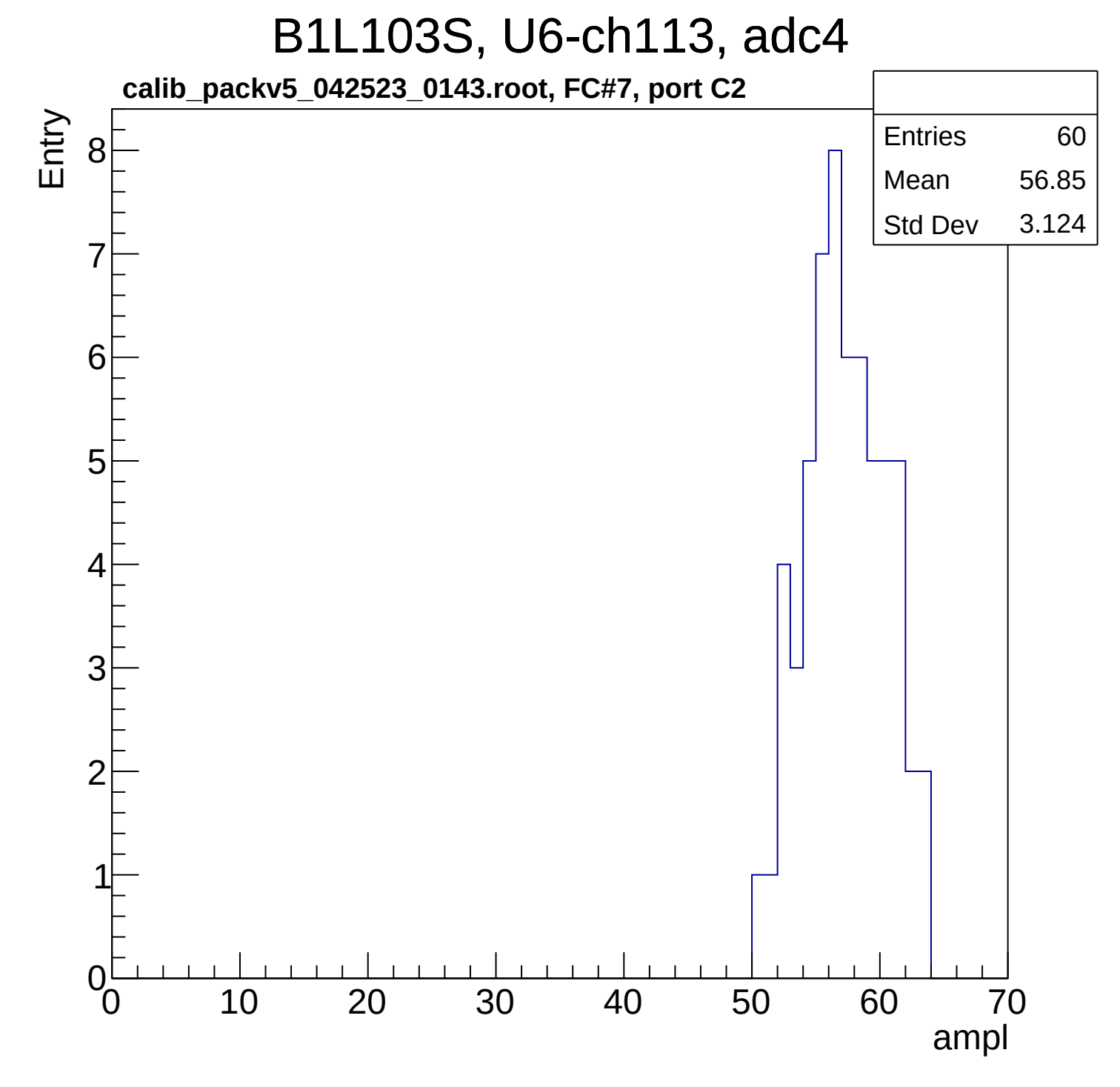
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch113, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.85
Std Dev 3.124
Entry
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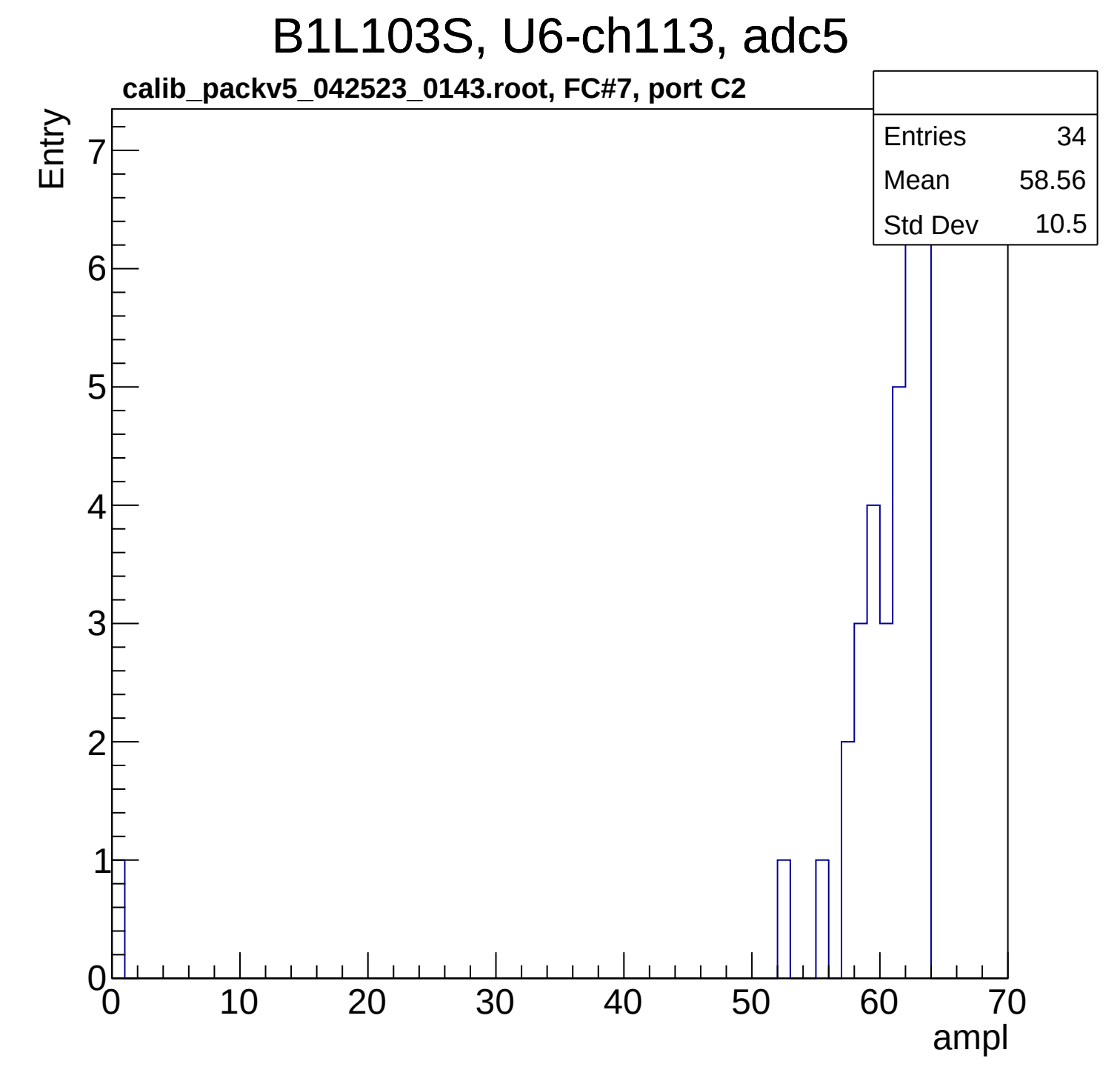
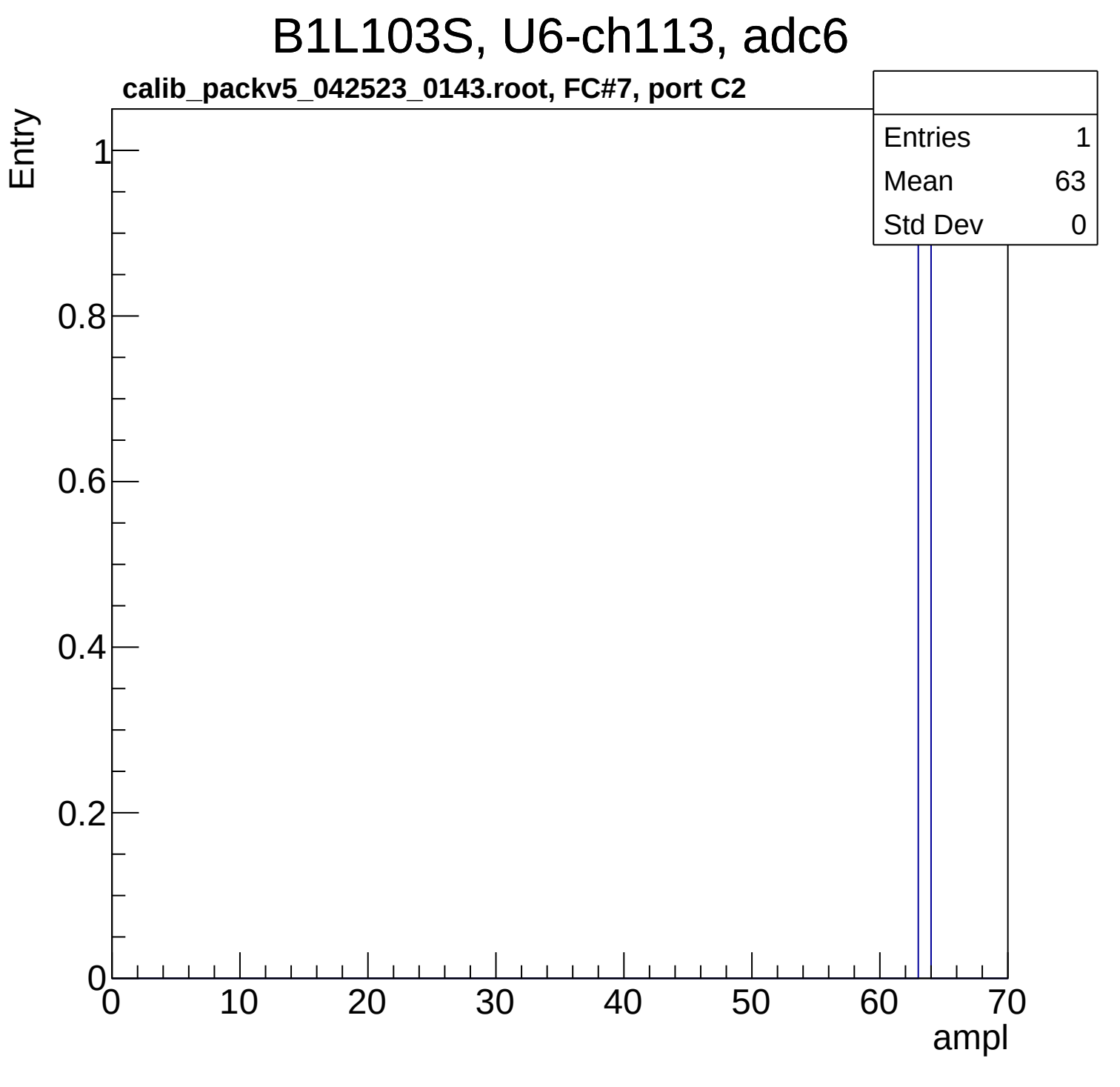
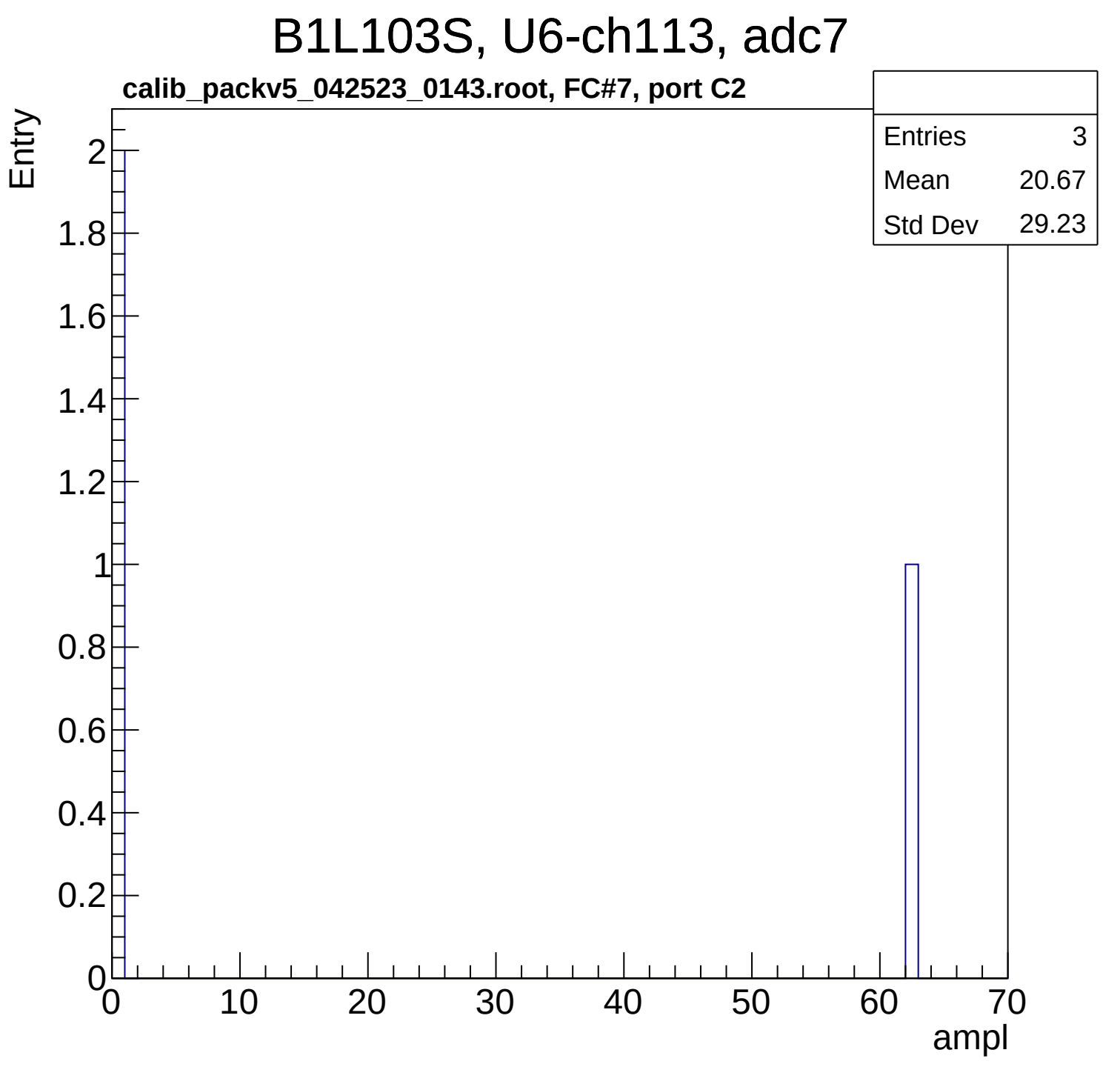
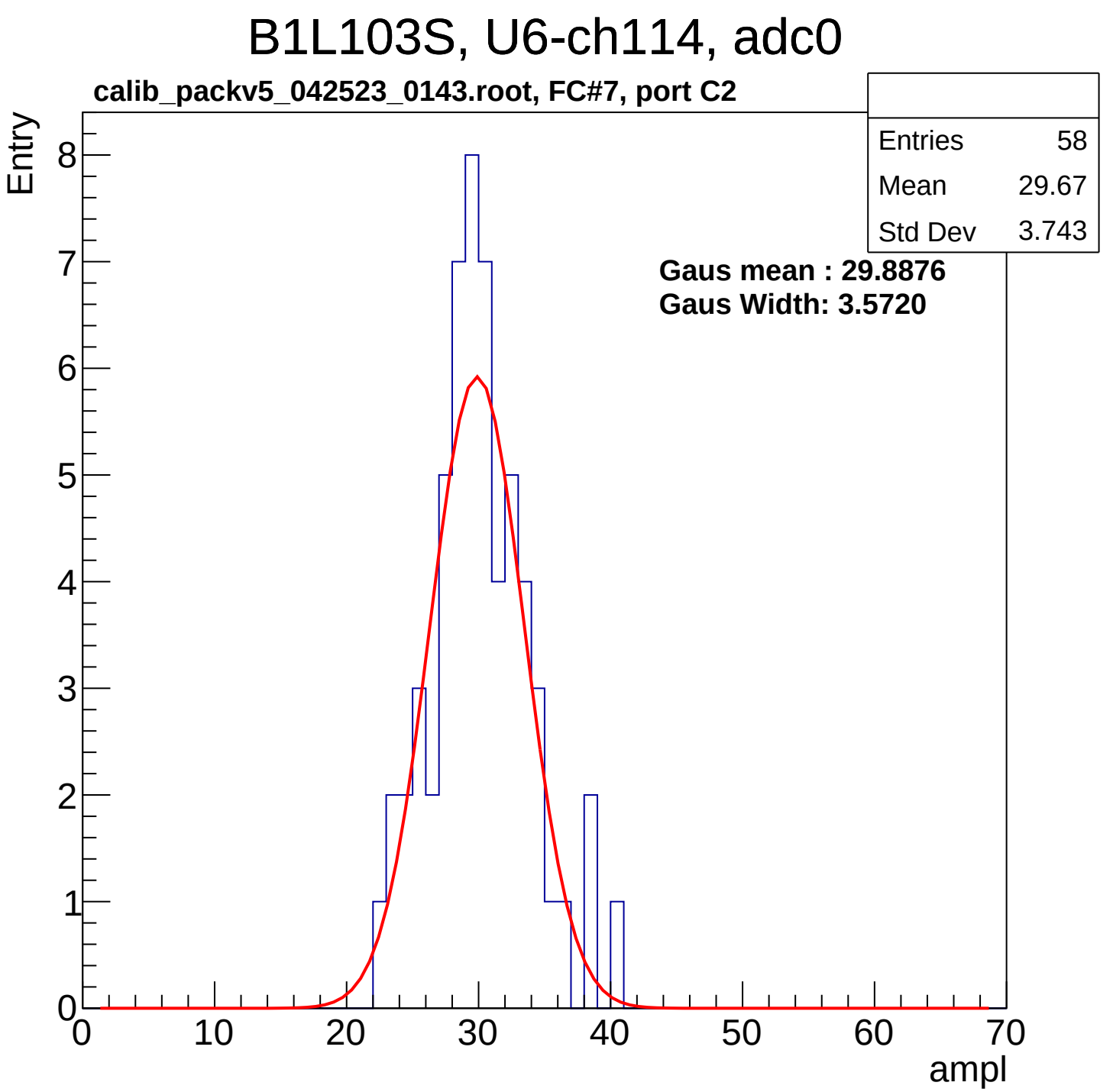
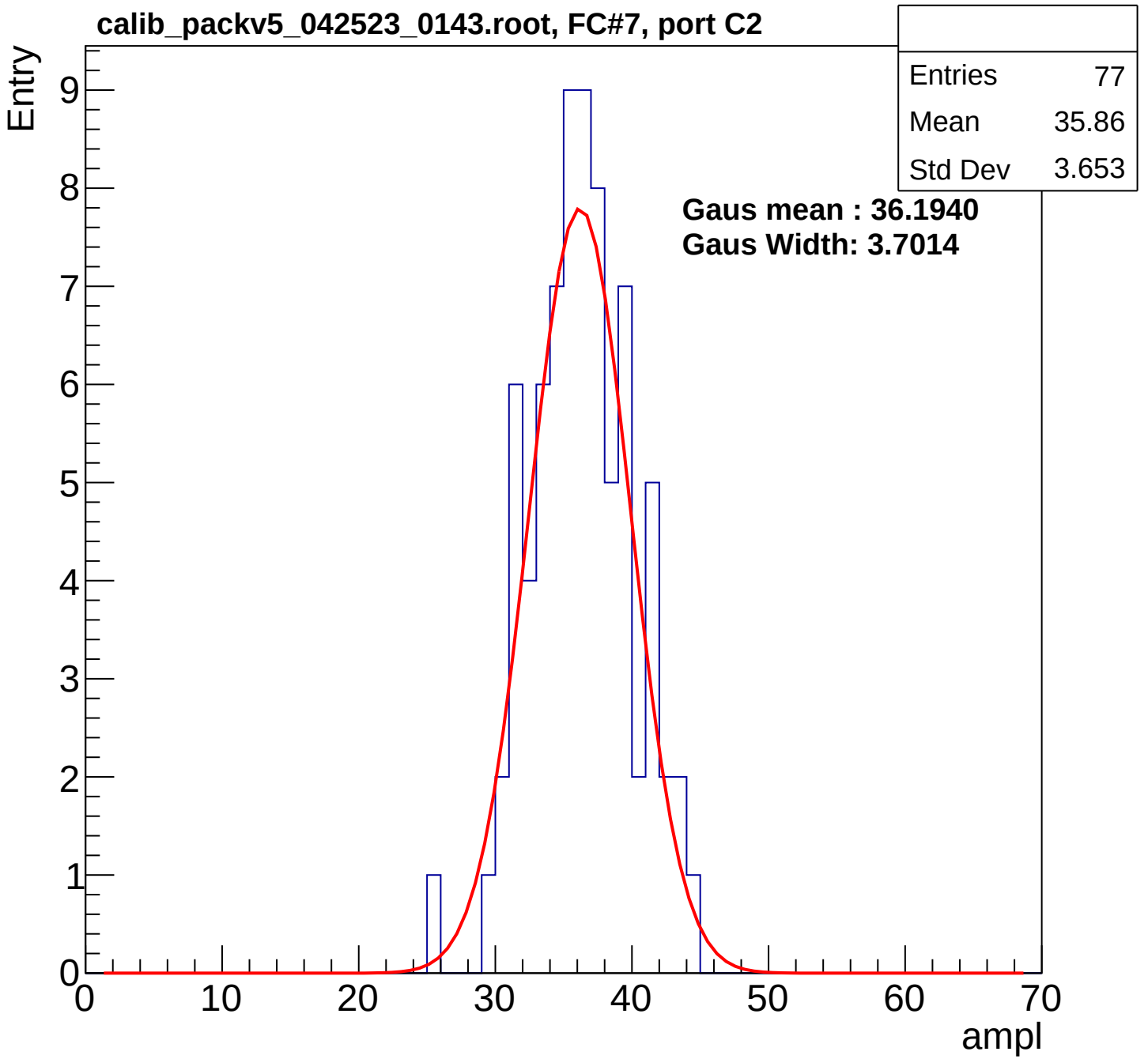
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

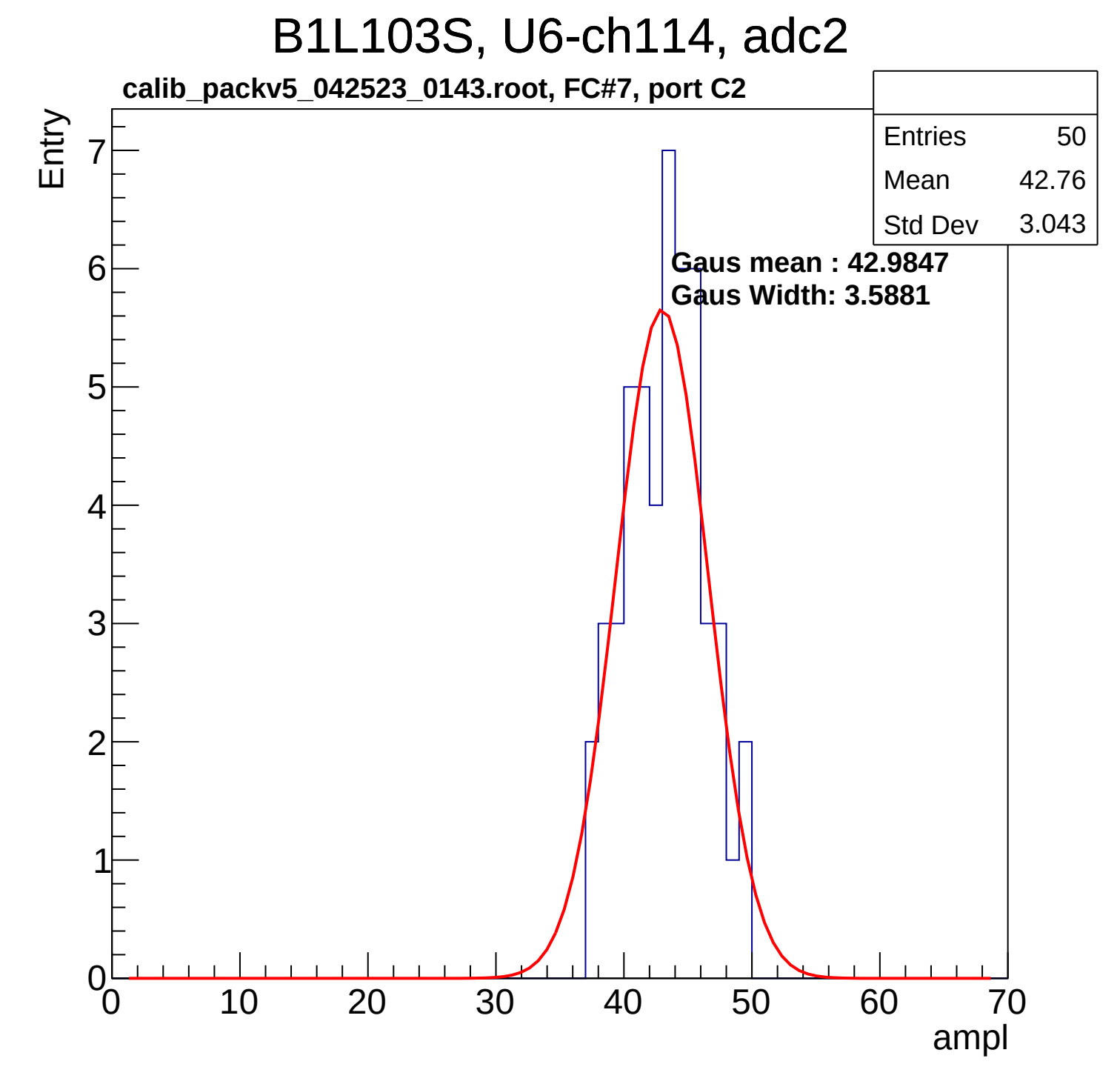

B1L103S, U6-ch113, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.56
Std Dev 10.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

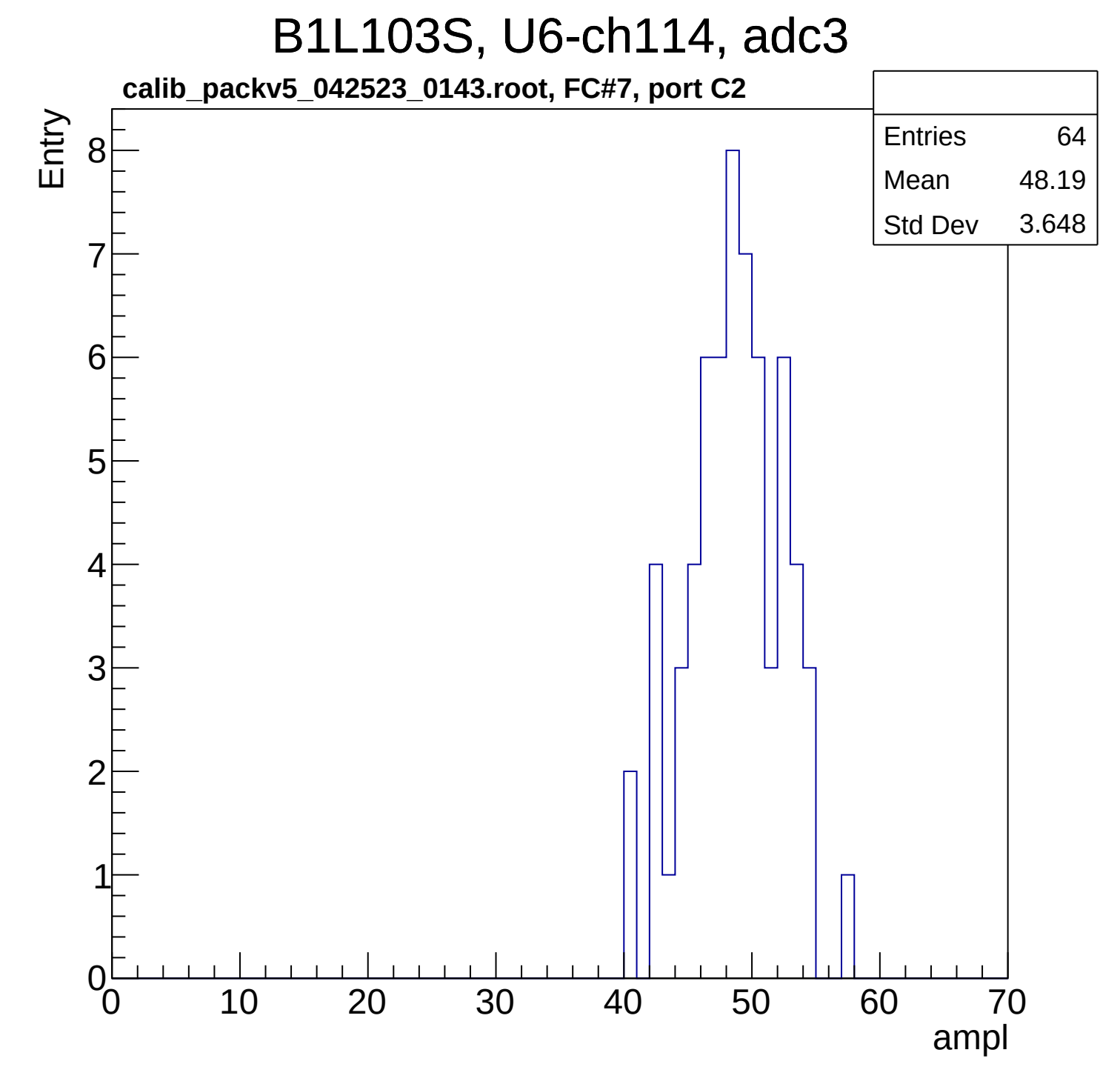

B1L103S, U6-ch113, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

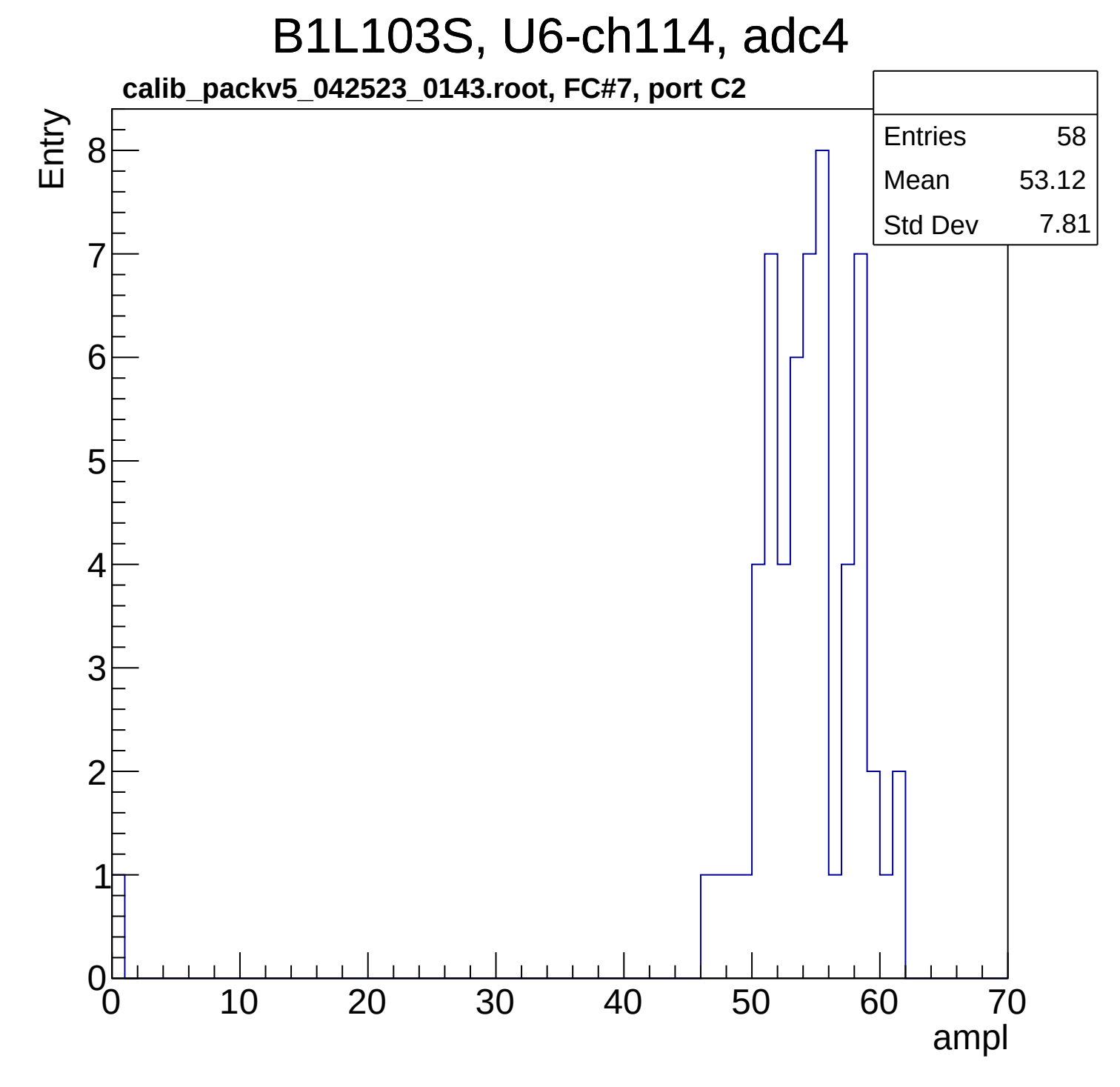

B1L103S, U6-ch113, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 29.67
Std Dev 3.743
Gaus mean : 29.8876
Gaus Width: 3.5720
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 35.86
Std Dev 3.653
Gaus mean : 36.1940
Gaus Width: 3.7014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 42.76
Std Dev 3.043
Gaus mean : 42.9847
Gaus Width: 3.5881
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.19
Std Dev 3.648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53.12
Std Dev 7.81
Entry
ampl
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1
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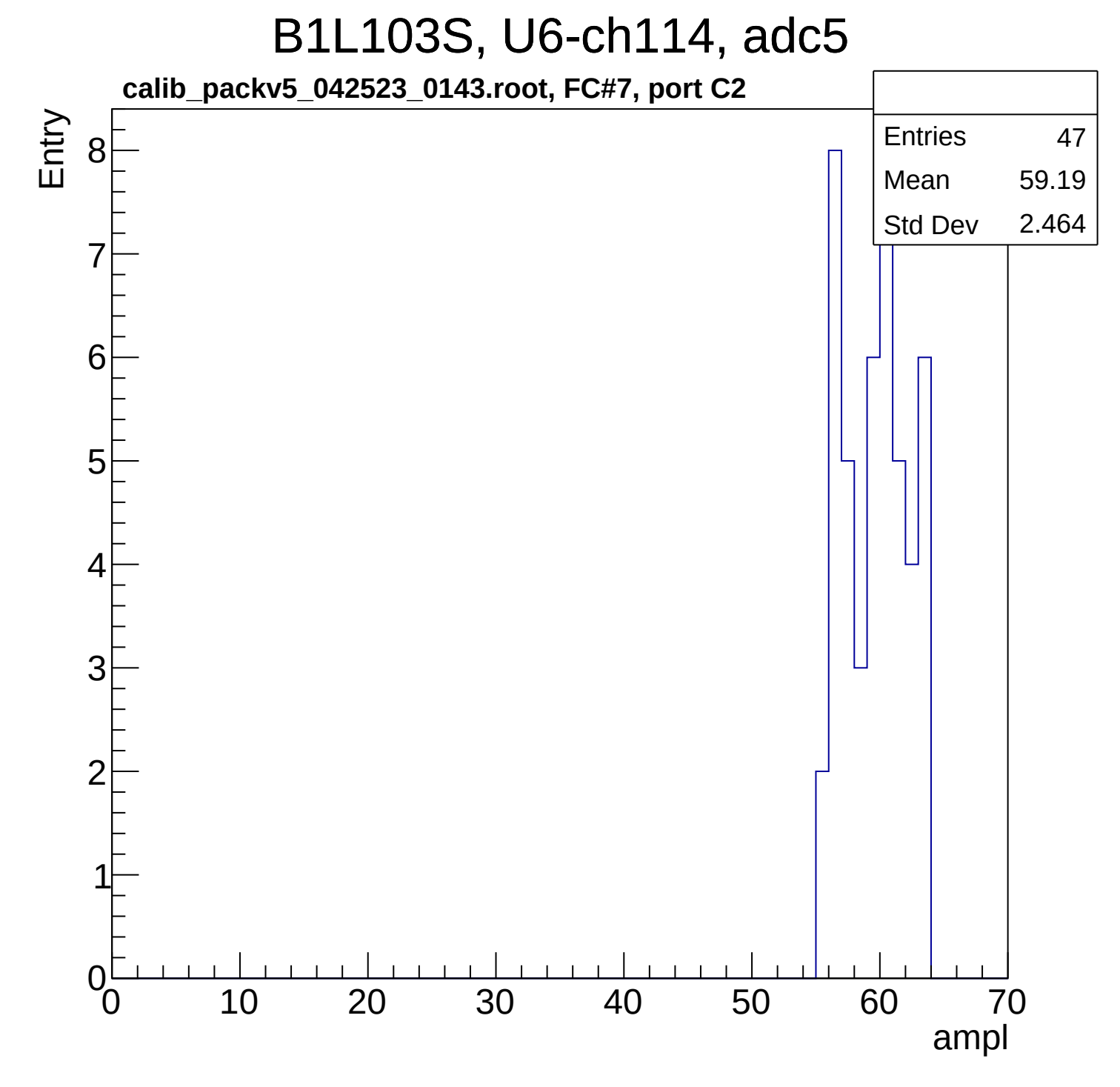
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.19
Std Dev 2.464
Entry
ampl
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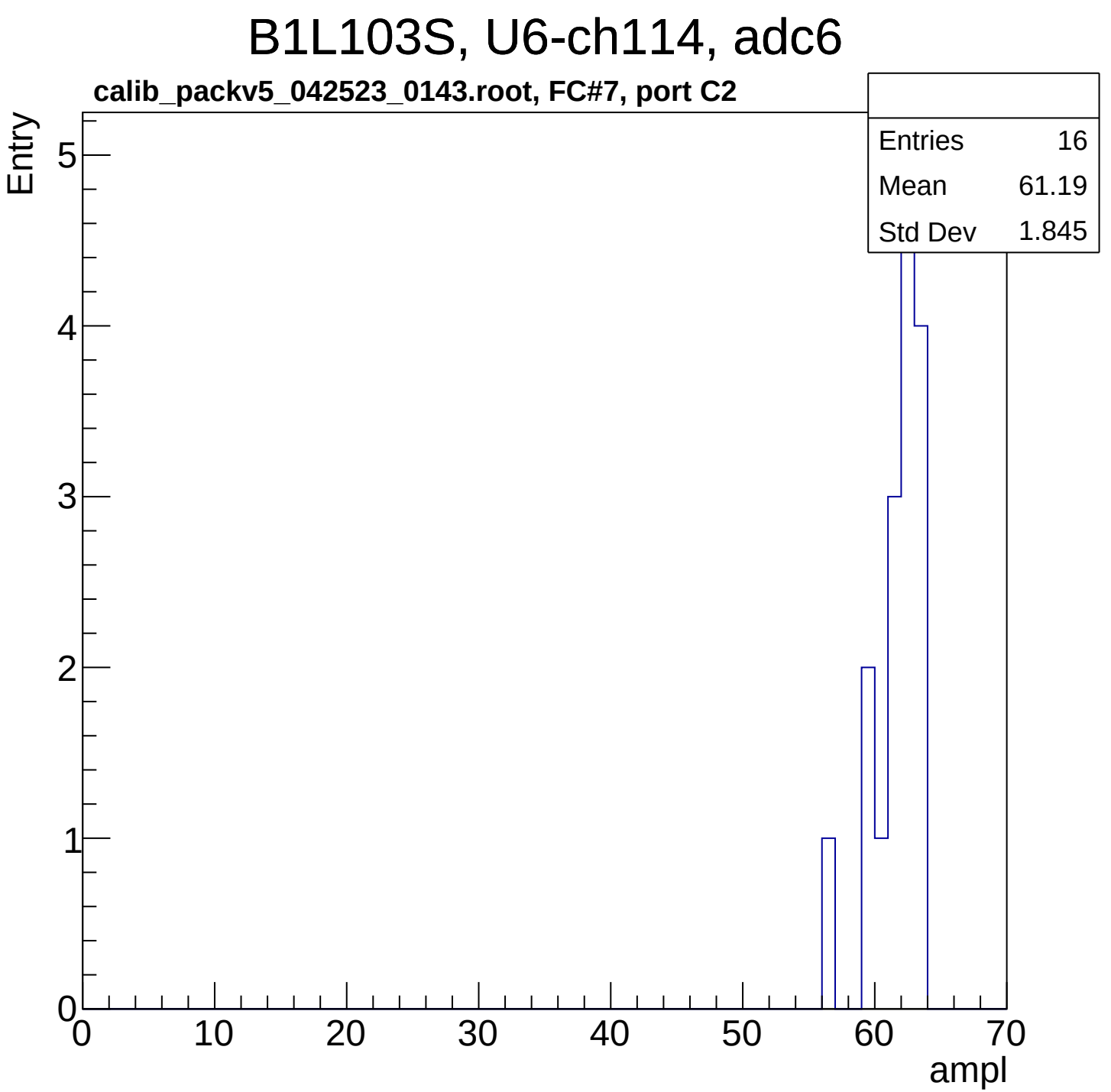
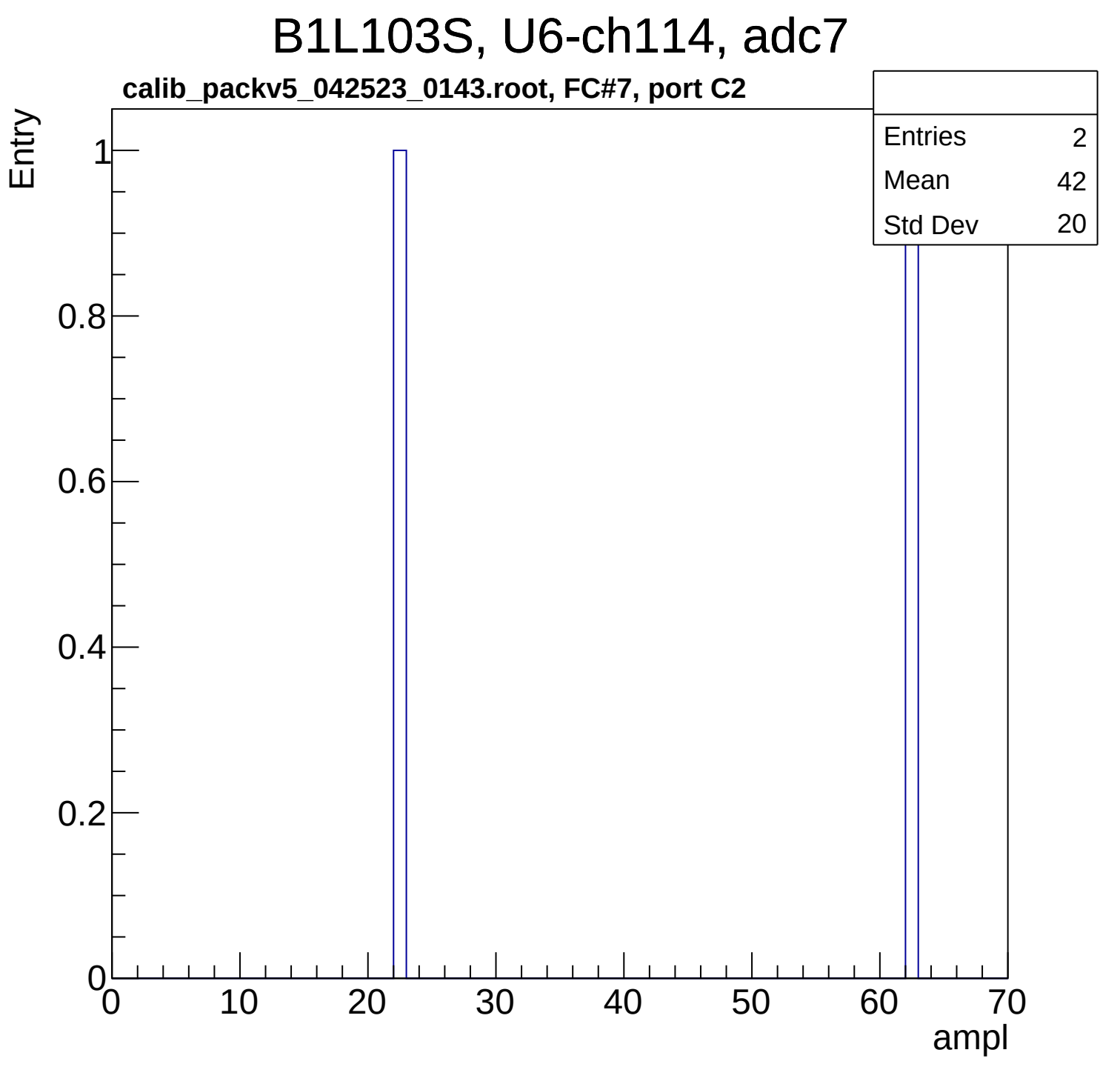
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch114, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.19
Std Dev 1.845
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch114, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 42
Std Dev 20
Entry
1
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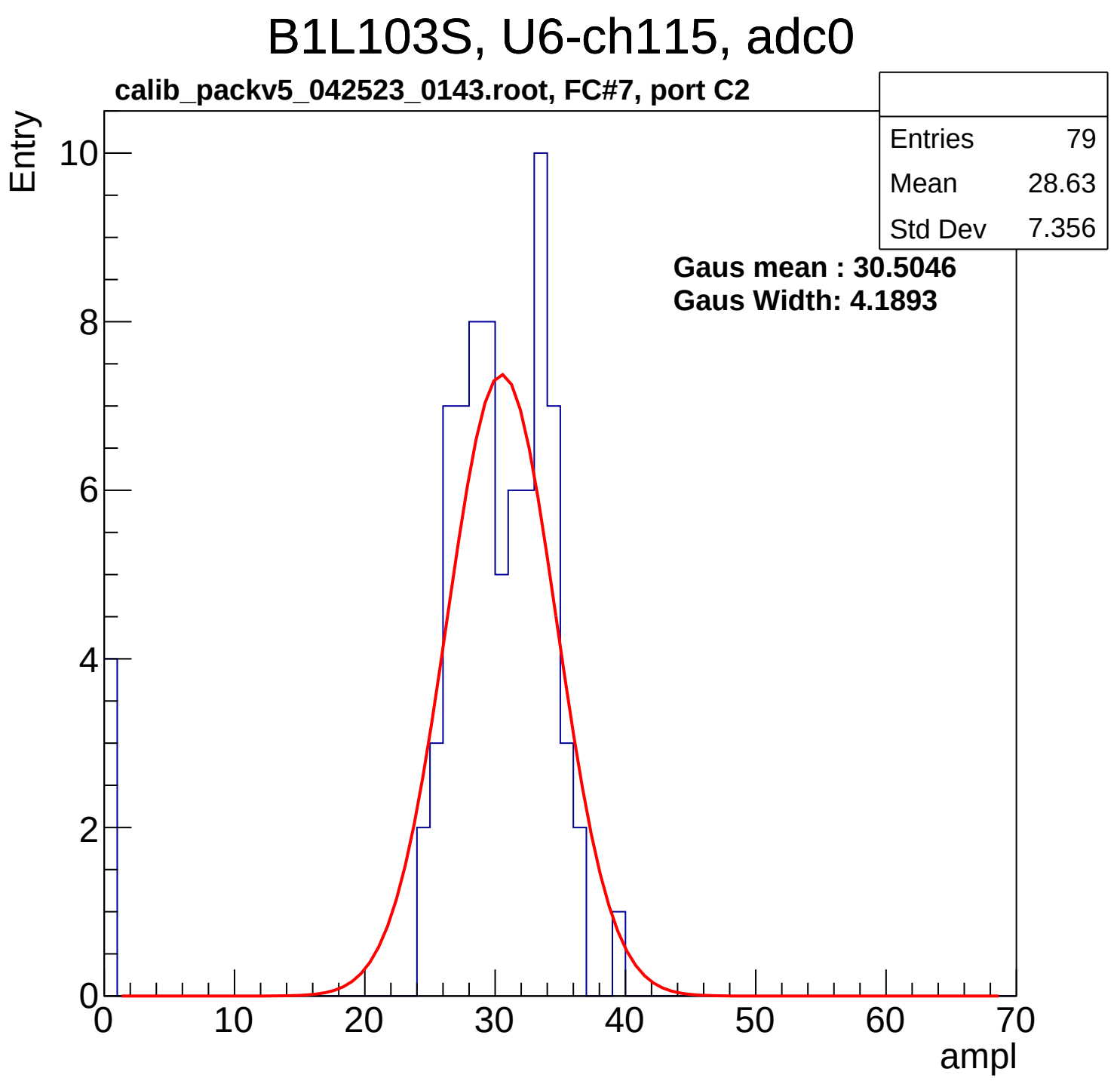
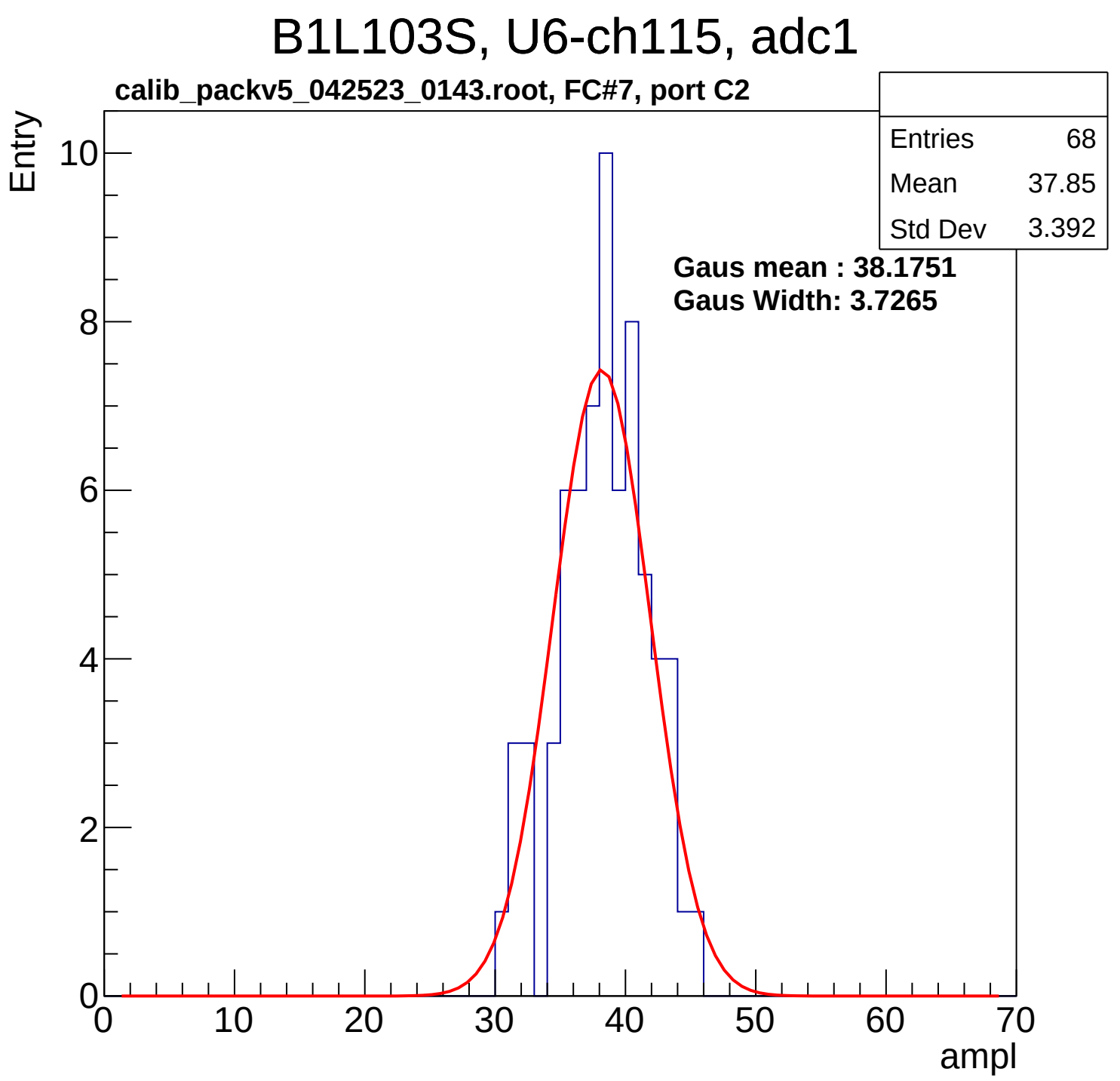
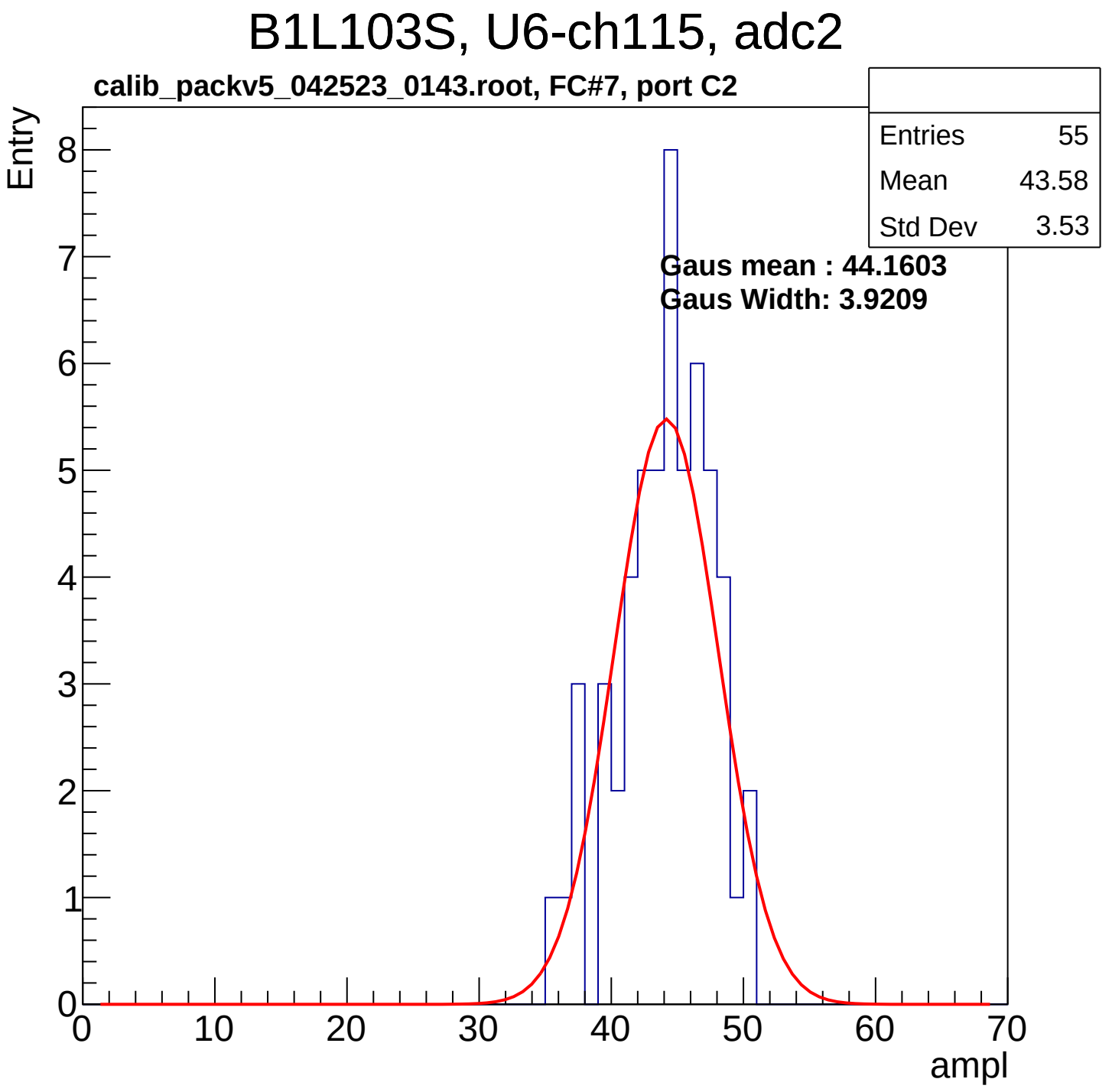
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch115, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 28.63
Std Dev 7.356
Gaus mean : 30.5046
Gaus Width: 4.1893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch115, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 37.85
Std Dev 3.392
Gaus mean : 38.1751
Gaus Width: 3.7265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch115, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 43.58
Std Dev 3.53
Gaus mean : 44.1603
Gaus Width: 3.9209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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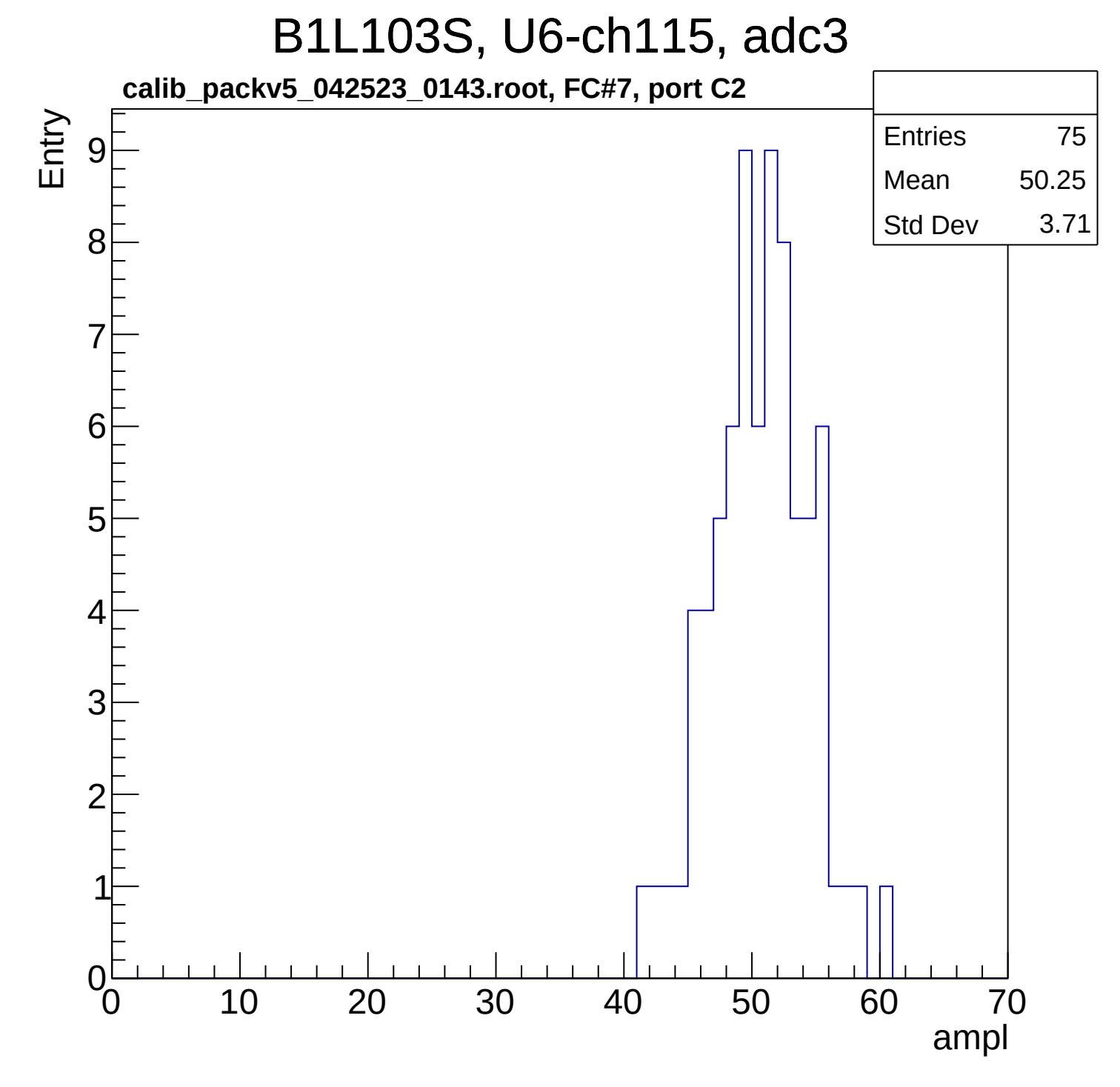
B1L103S, U6-ch115, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 50.25
Std Dev 3.71
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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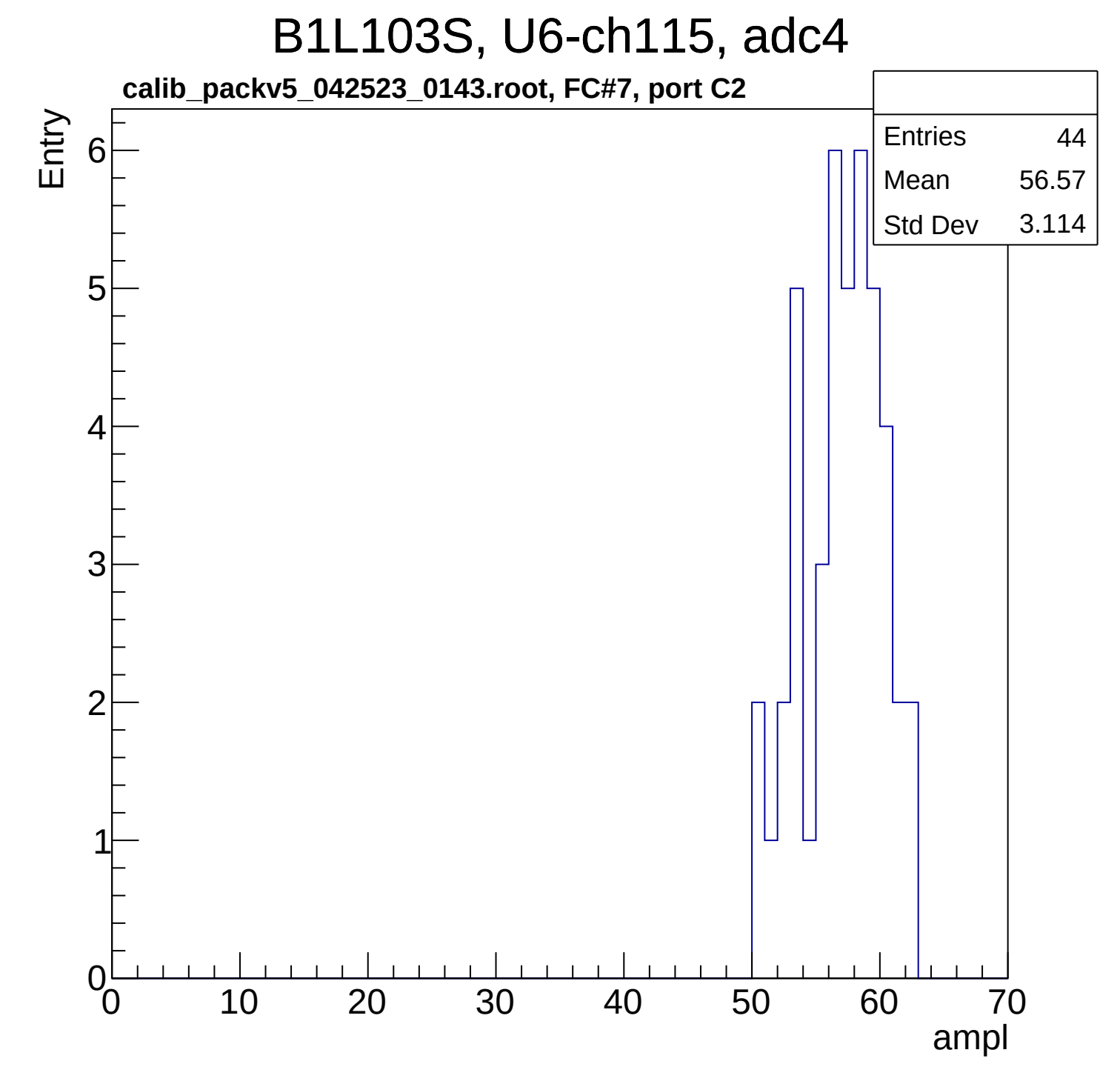
B1L103S, U6-ch115, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 56.57
Std Dev 3.114
Entry
0
1
2
3
4
5
6
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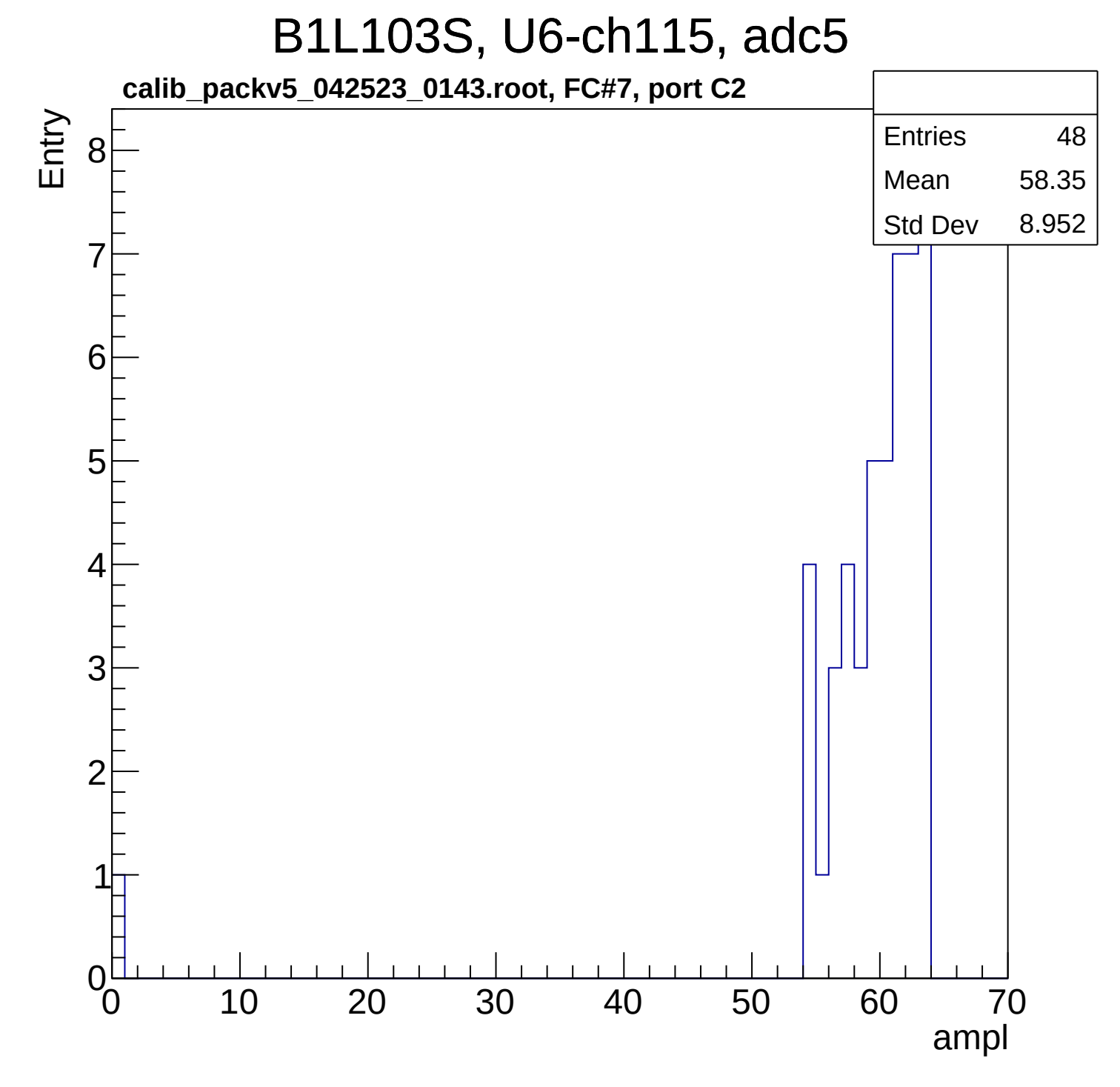
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch115, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.35
Std Dev 8.952
Entry
ampl
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1
2
3
4
5
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0
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40
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60
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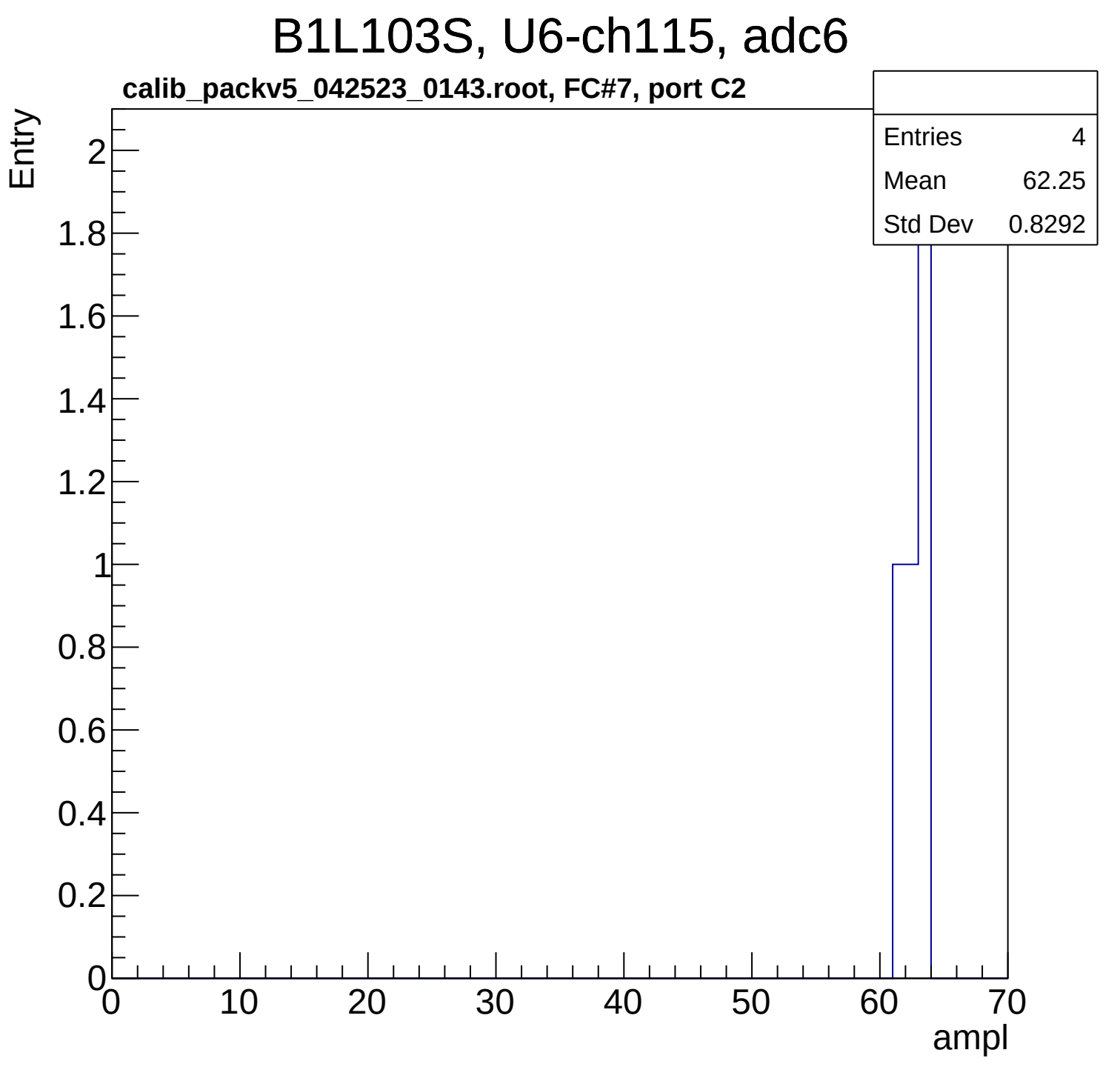
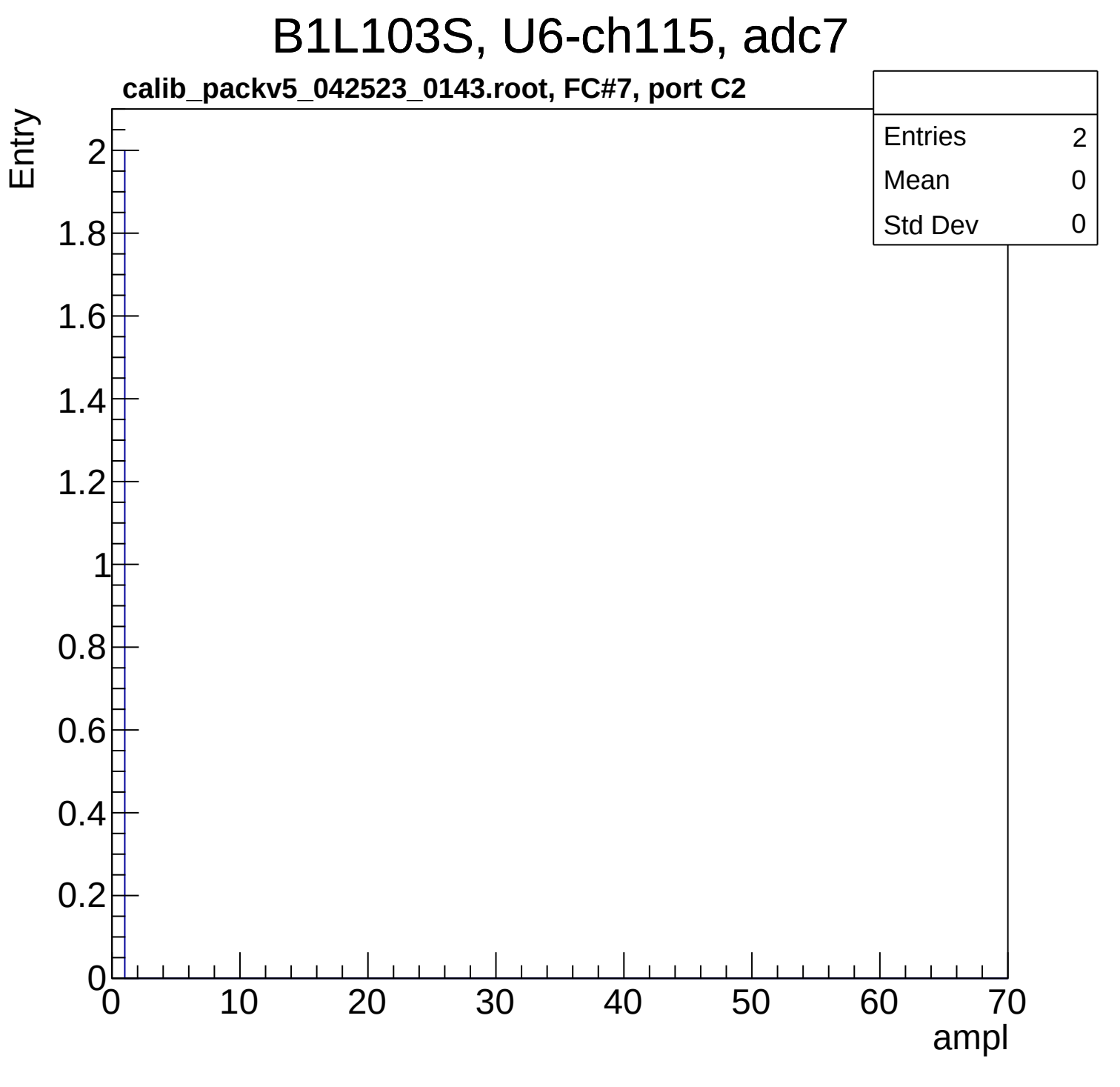

B1L103S, U6-ch115, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch115, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
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0
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60
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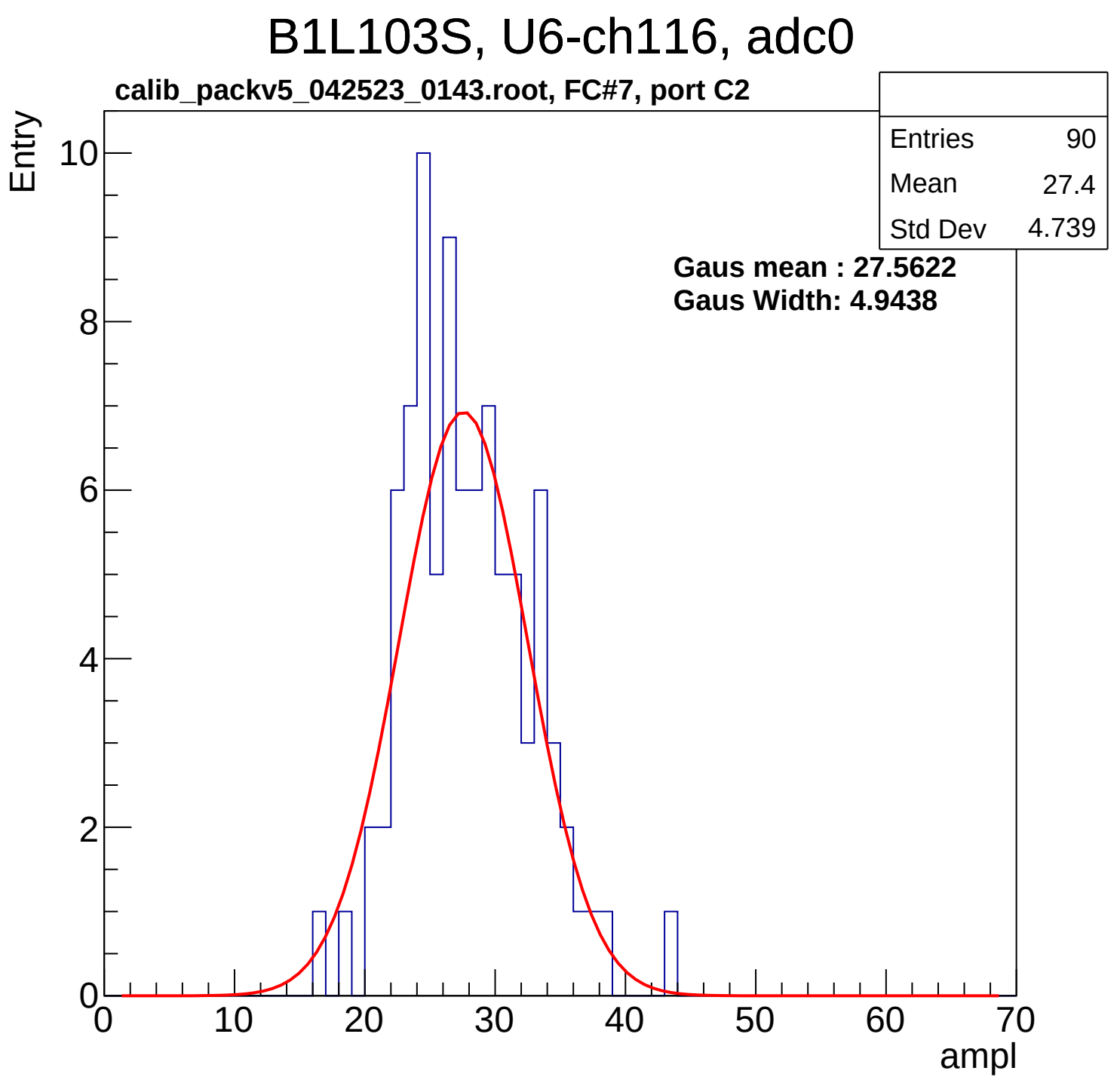
B1L103S, U6-ch116, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 27.4
Std Dev 4.739
Gaus mean : 27.5622
Gaus Width: 4.9438
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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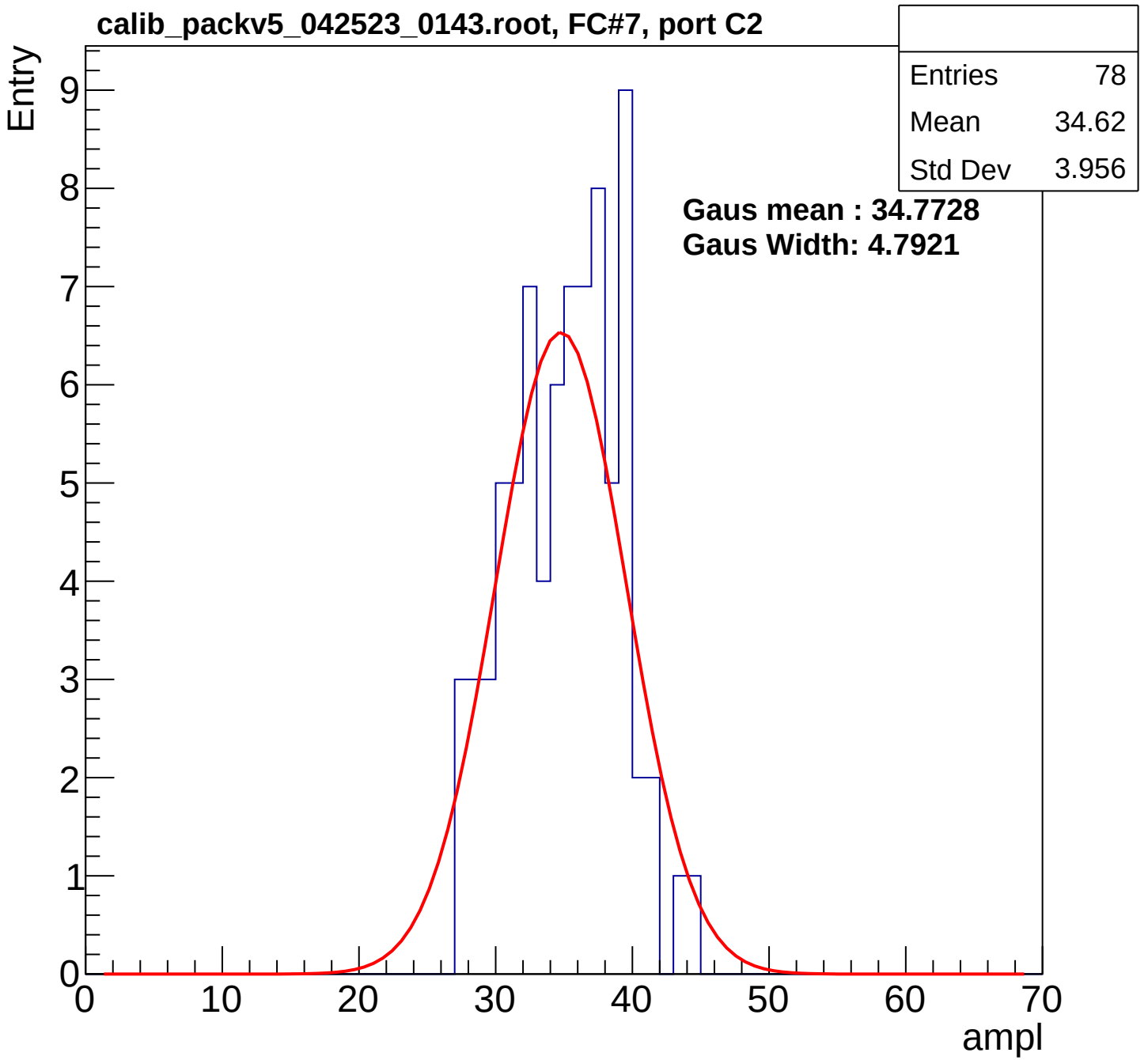
B1L103S, U6-ch116, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 34.62
Std Dev 3.956
Gaus mean : 34.7728
Gaus Width: 4.7921
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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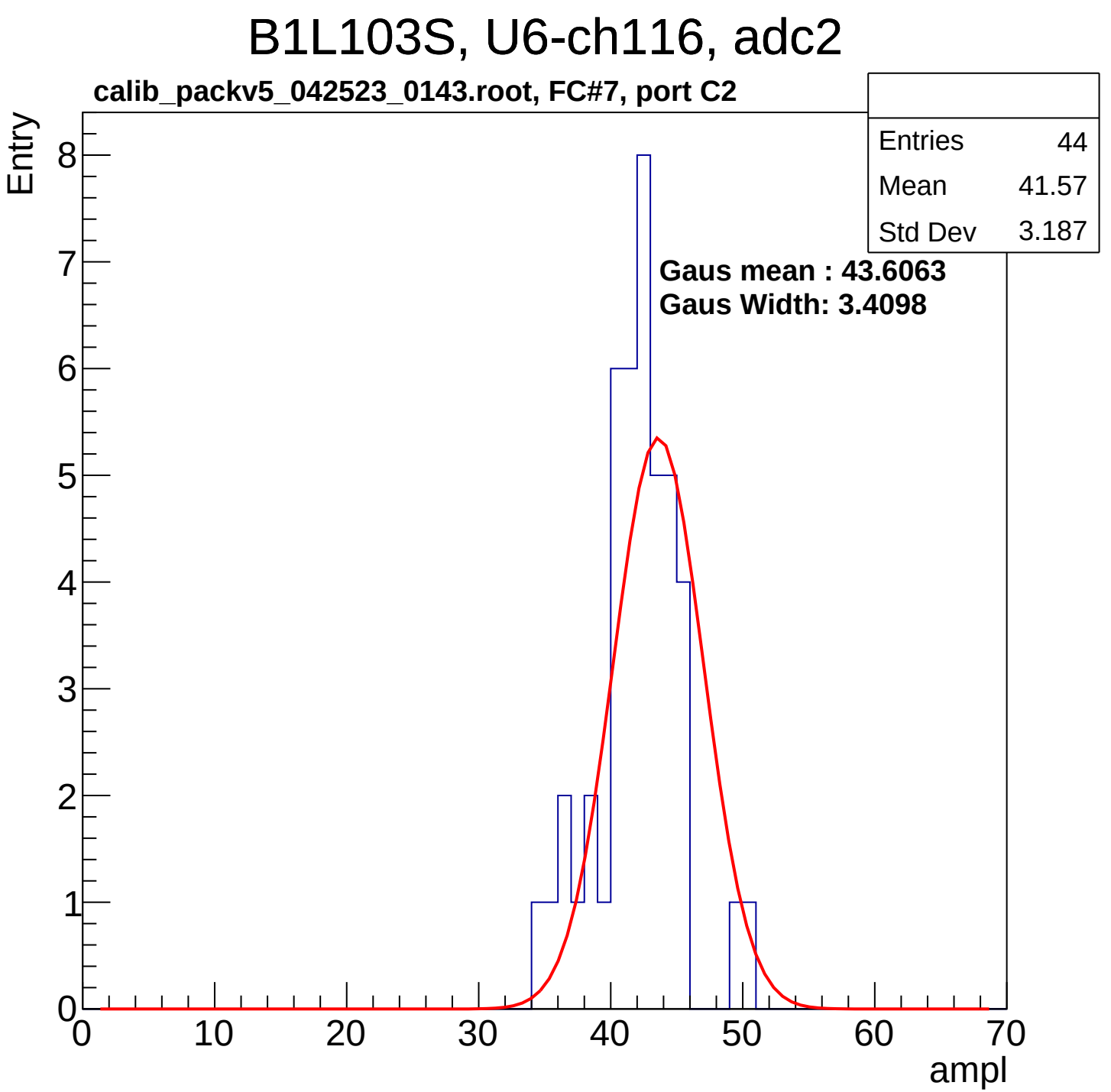
B1L103S, U6-ch116, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 41.57
Std Dev 3.187
Gaus mean : 43.6063
Gaus Width: 3.4098
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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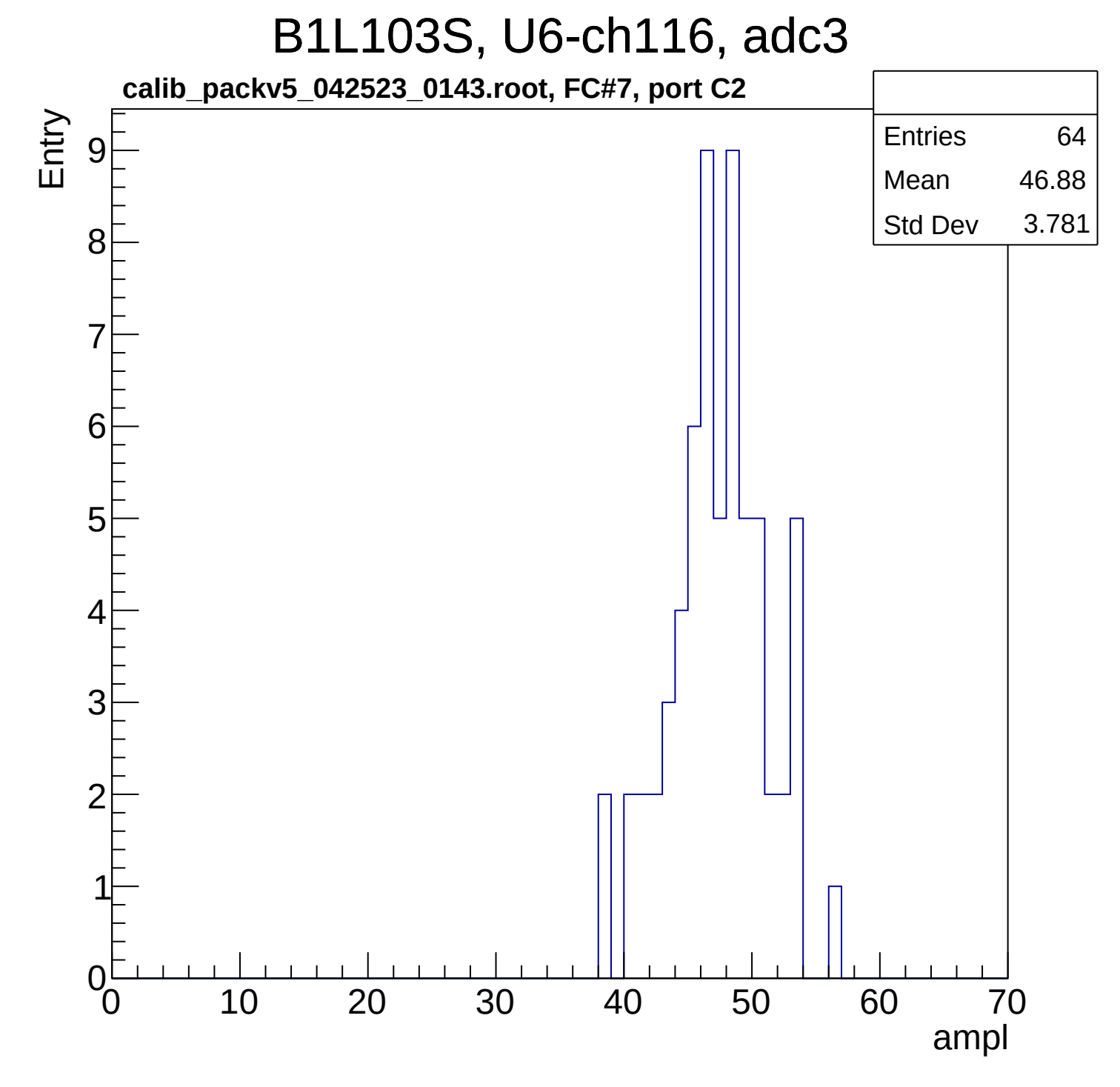
B1L103S, U6-ch116, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 46.88
Std Dev 3.781
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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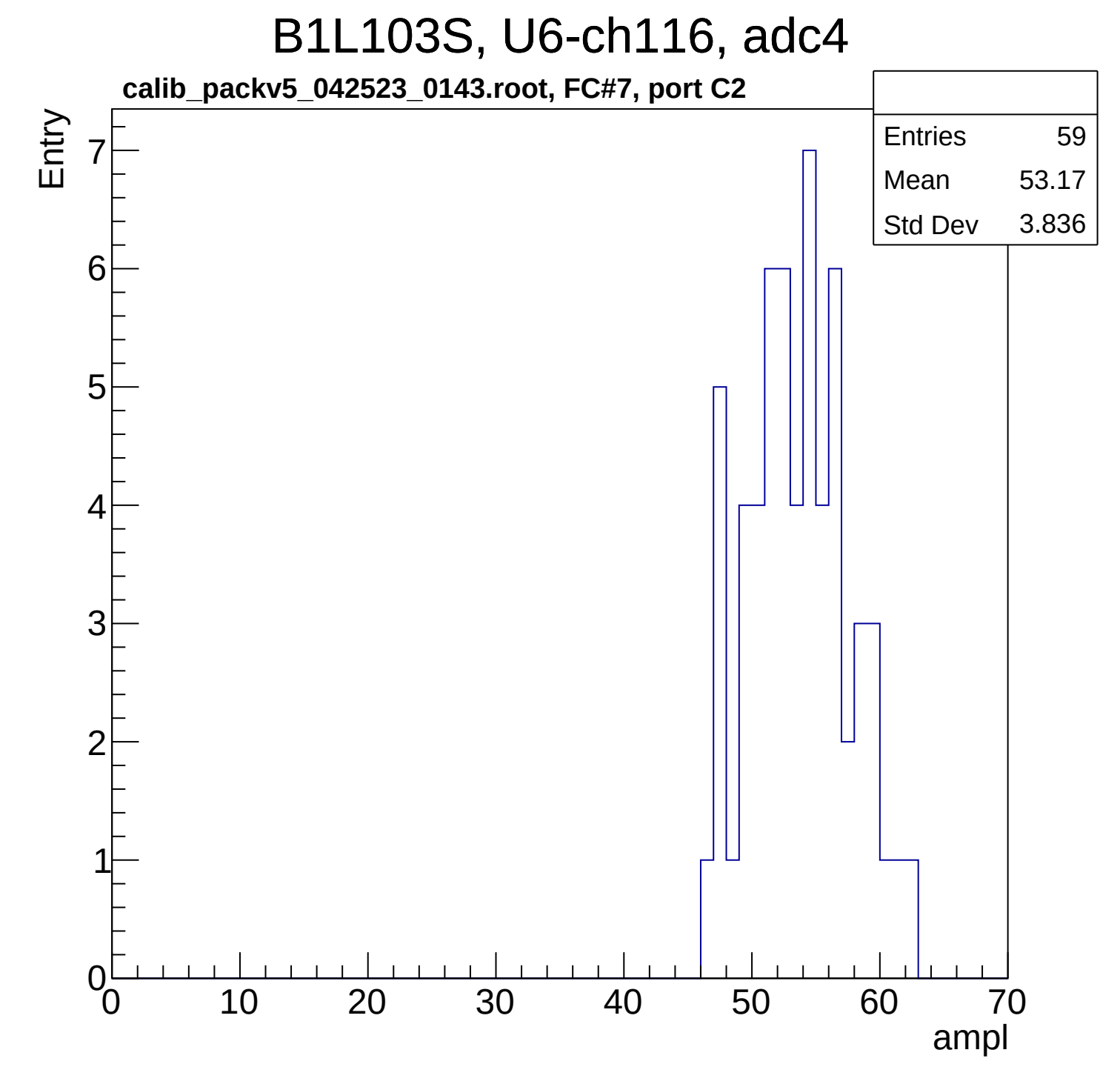
B1L103S, U6-ch116, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 53.17
Std Dev 3.836
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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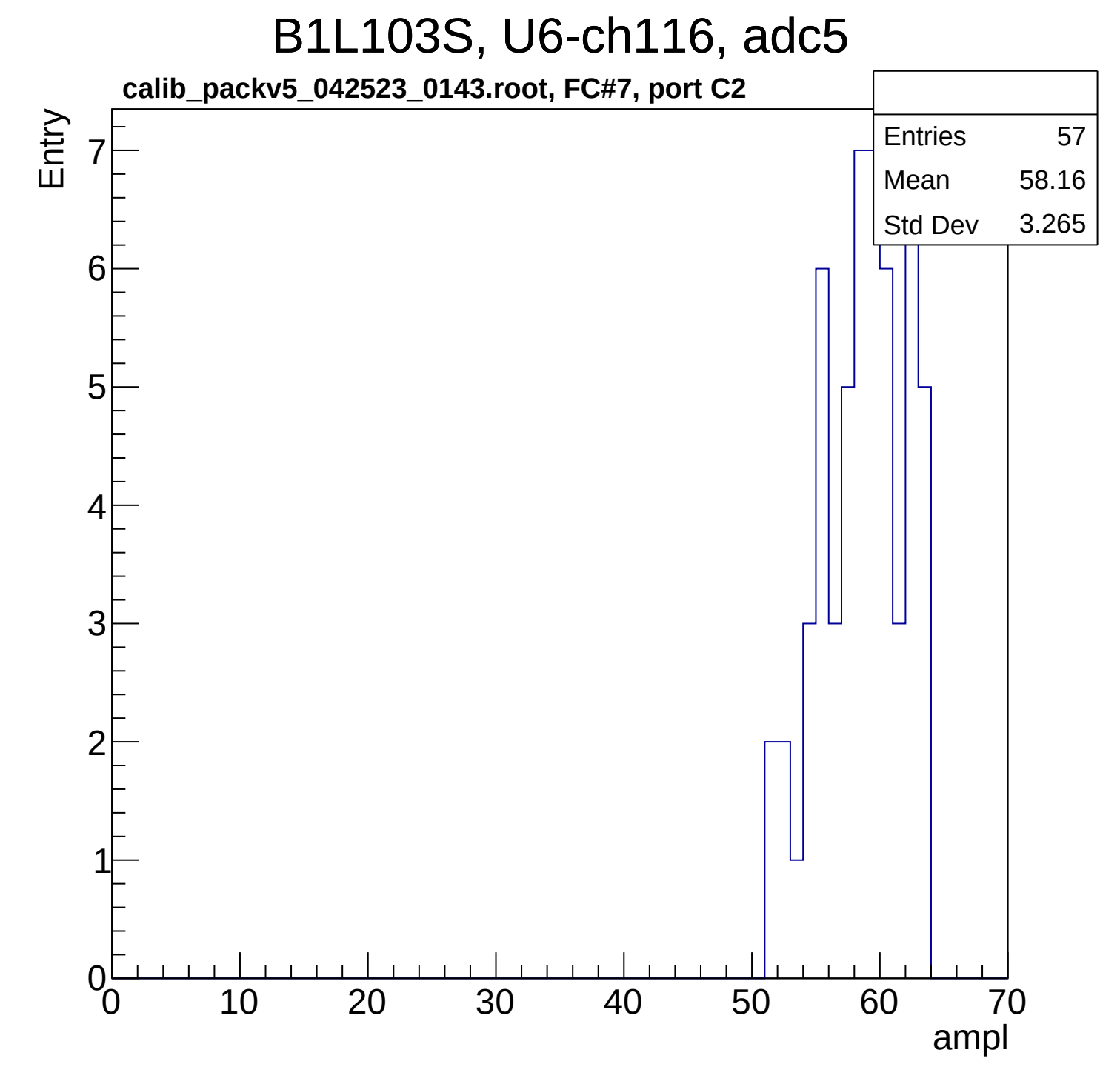
B1L103S, U6-ch116, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 58.16
Std Dev 3.265
Entry
ampl
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1
2
3
4
5
6
7
0
10
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60
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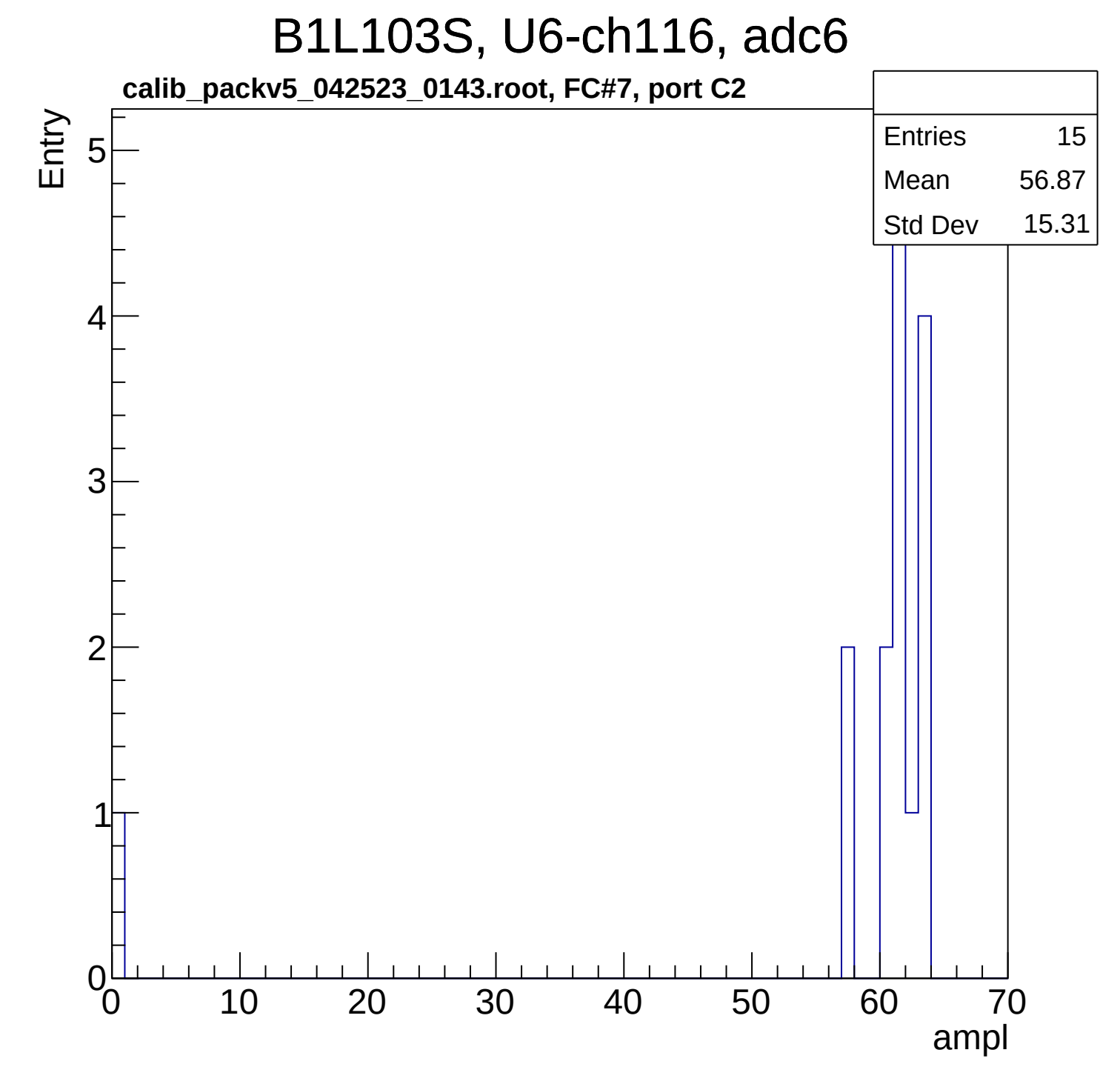
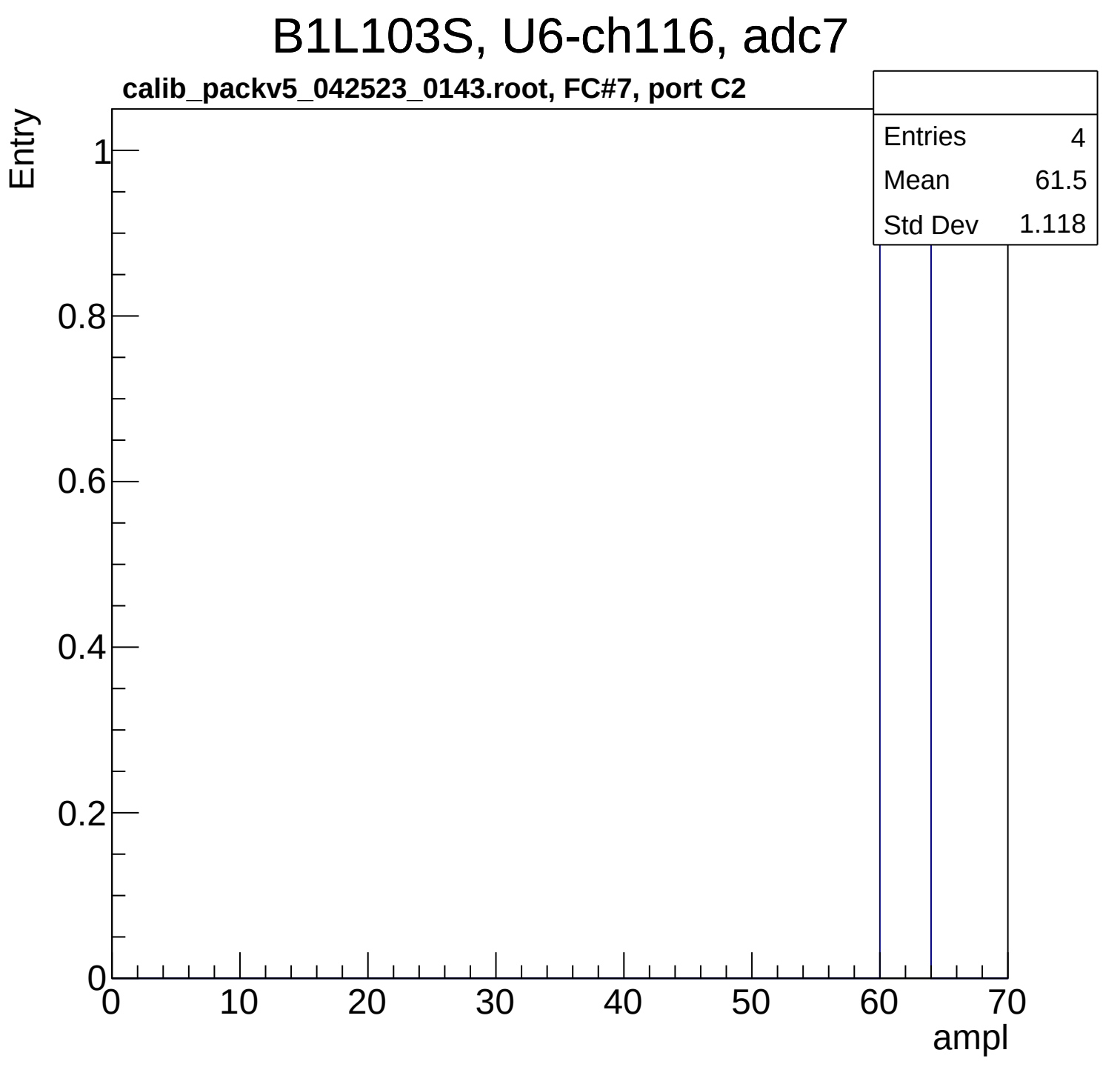

B1L103S, U6-ch116, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 56.87
Std Dev 15.31
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch116, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
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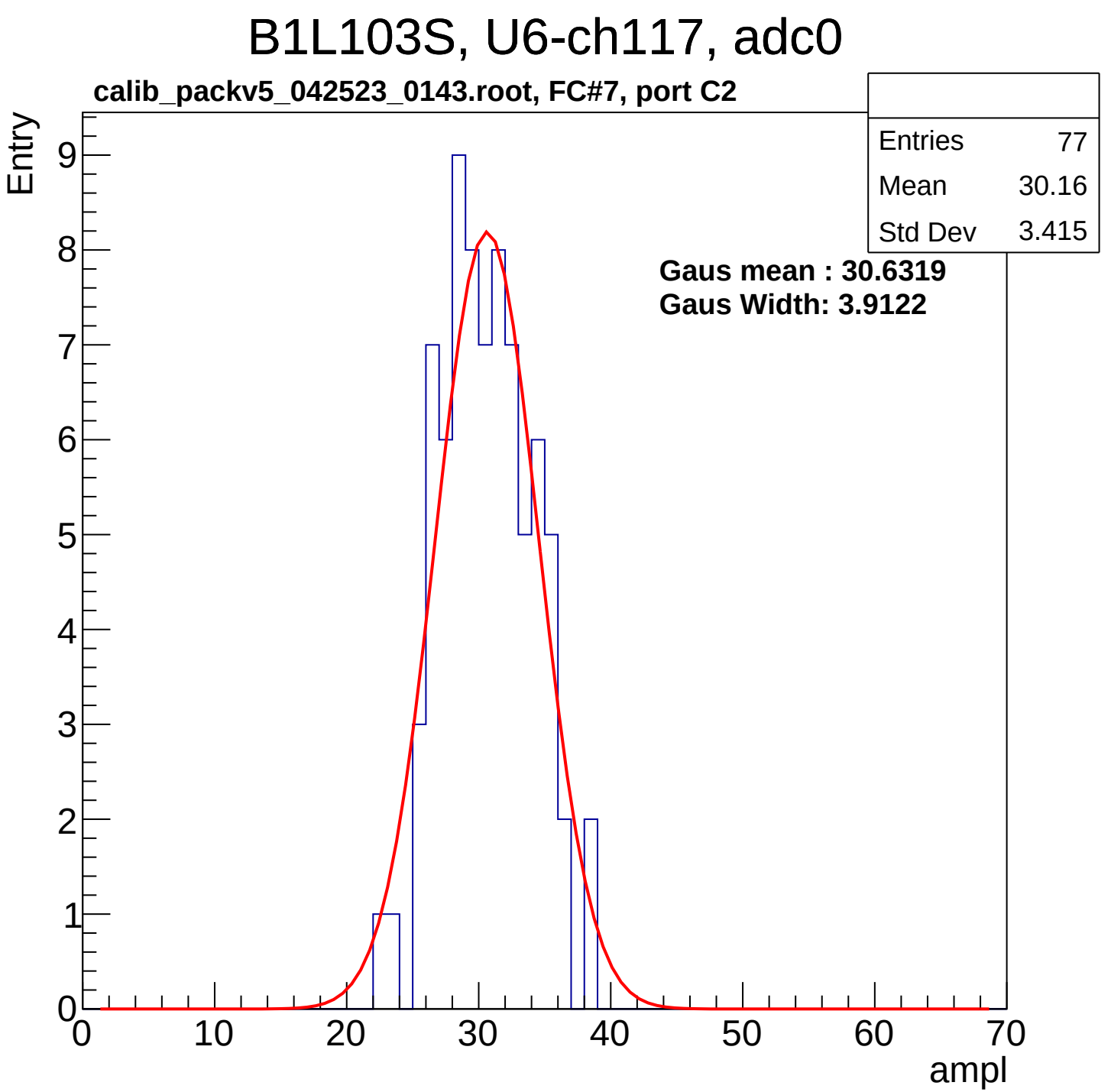
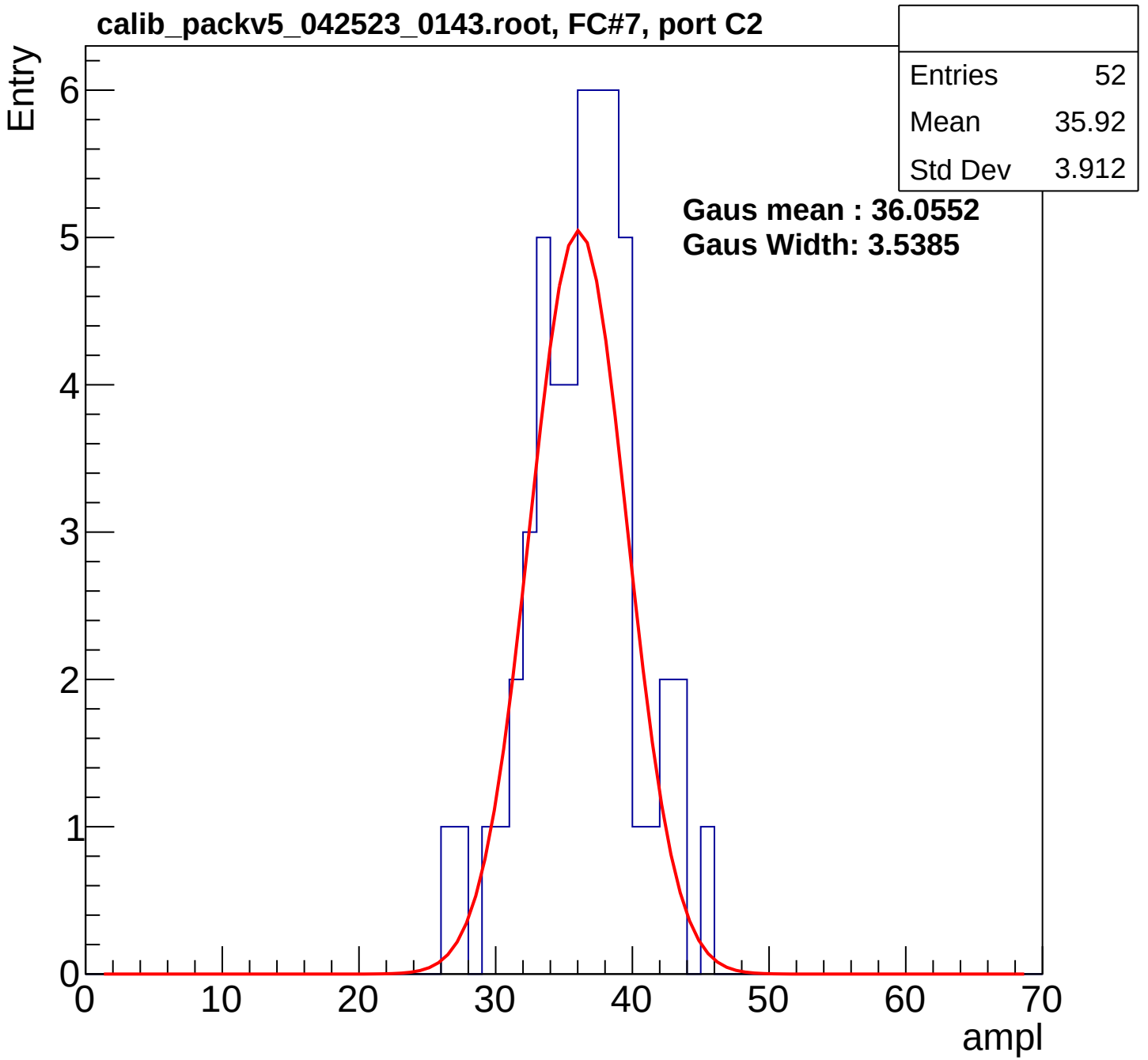
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch117, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.16
Std Dev 3.415
Gaus mean : 30.6319
Gaus Width: 3.9122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch117, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 35.92
Std Dev 3.912
Gaus mean : 36.0552
Gaus Width: 3.5385
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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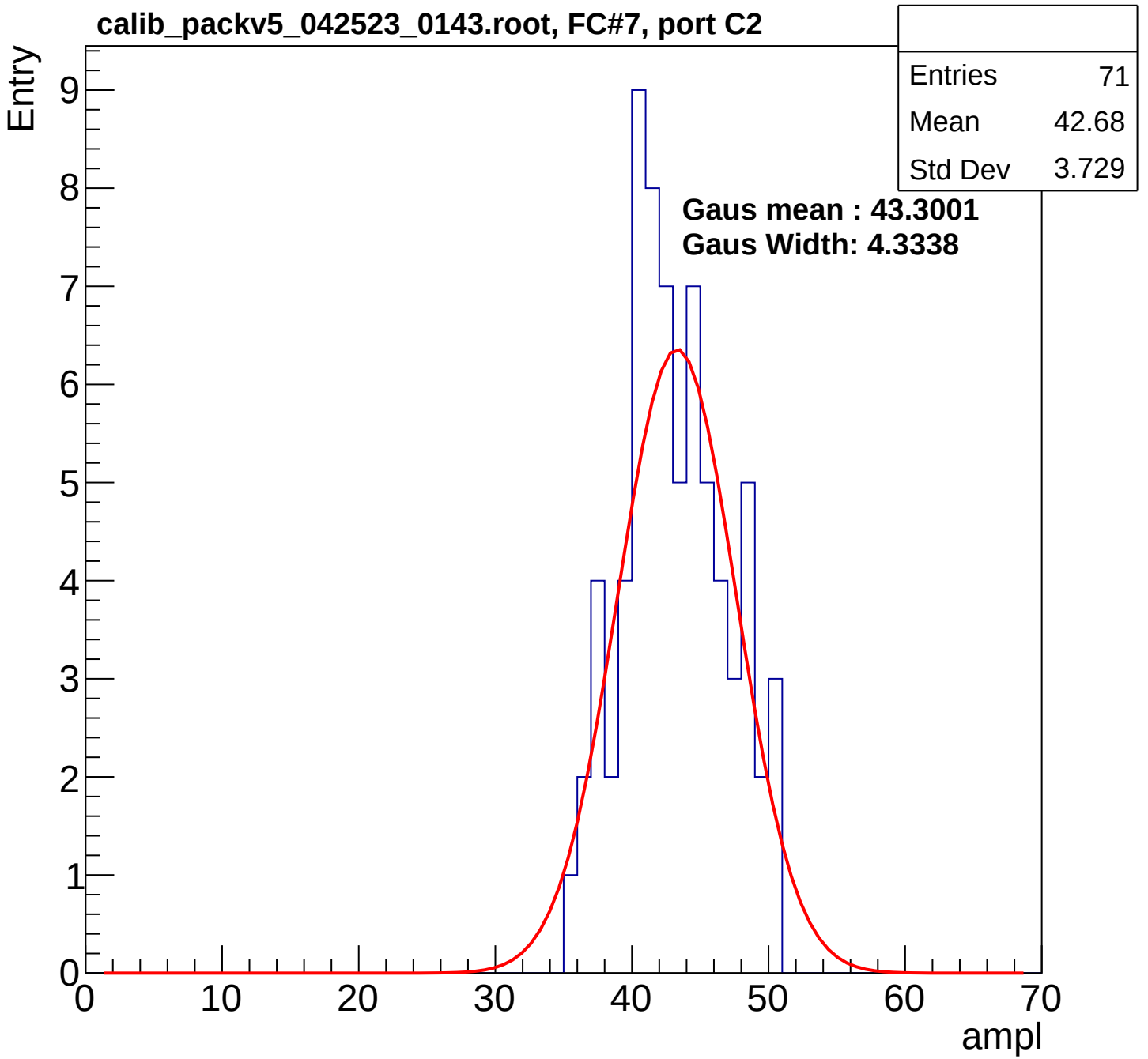
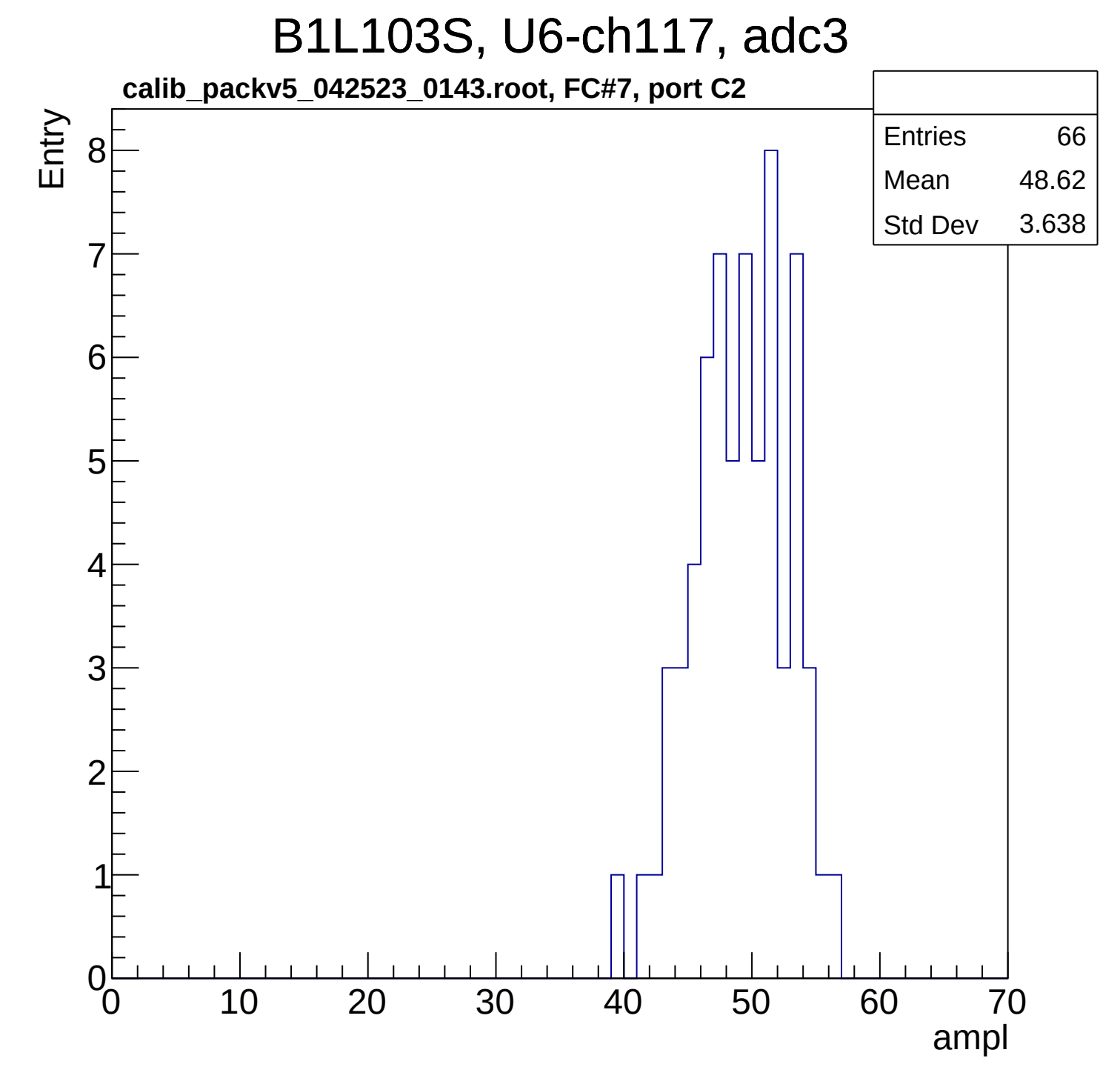

B1L103S, U6-ch117, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.68
Std Dev 3.729
Gaus mean : 43.3001
Gaus Width: 4.3338
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U6-ch117, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.62
Std Dev 3.638
Entry
ampl
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1
2
3
4
5
6
7
8
0
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30
40
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60
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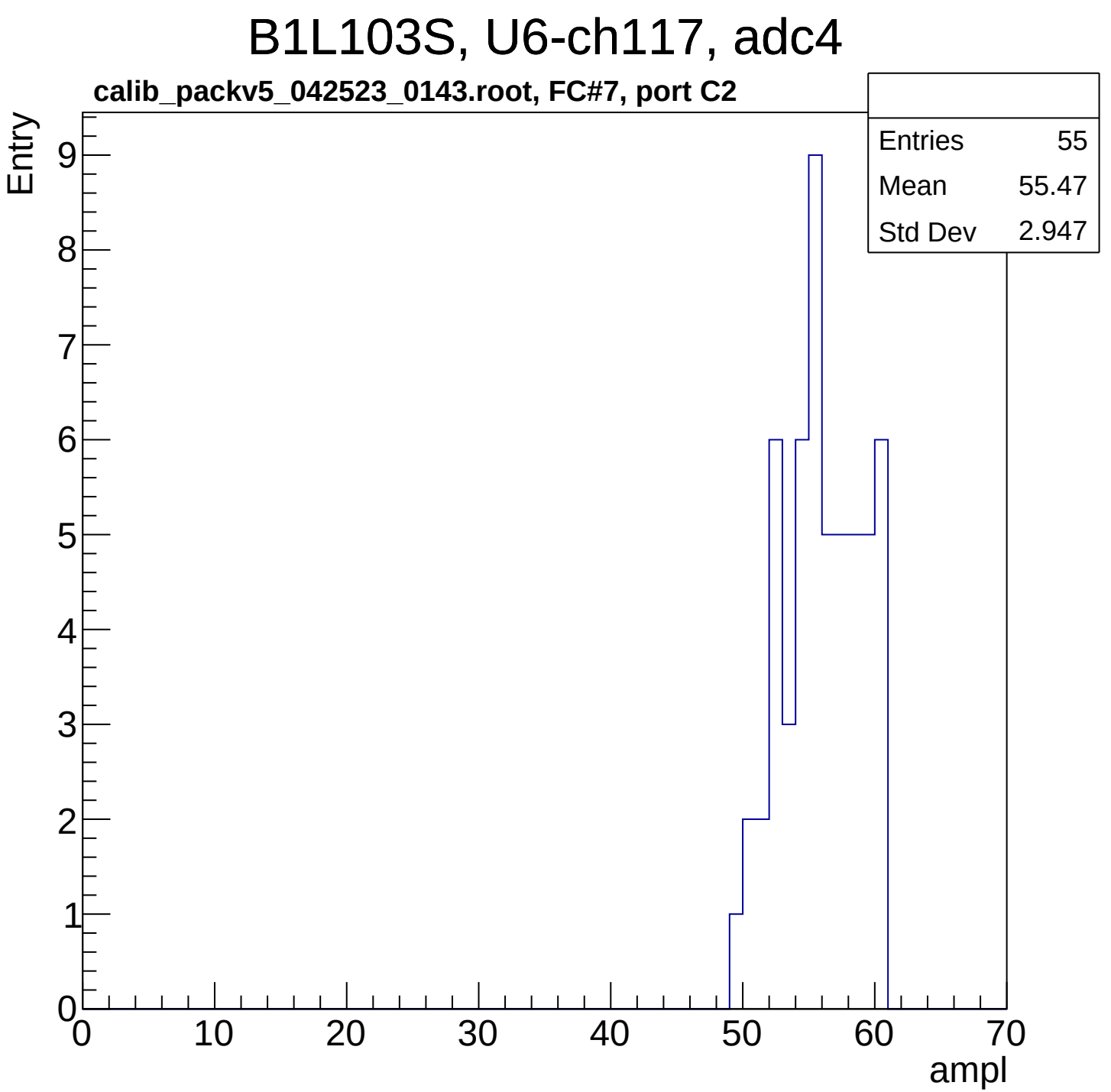
B1L103S, U6-ch117, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.47
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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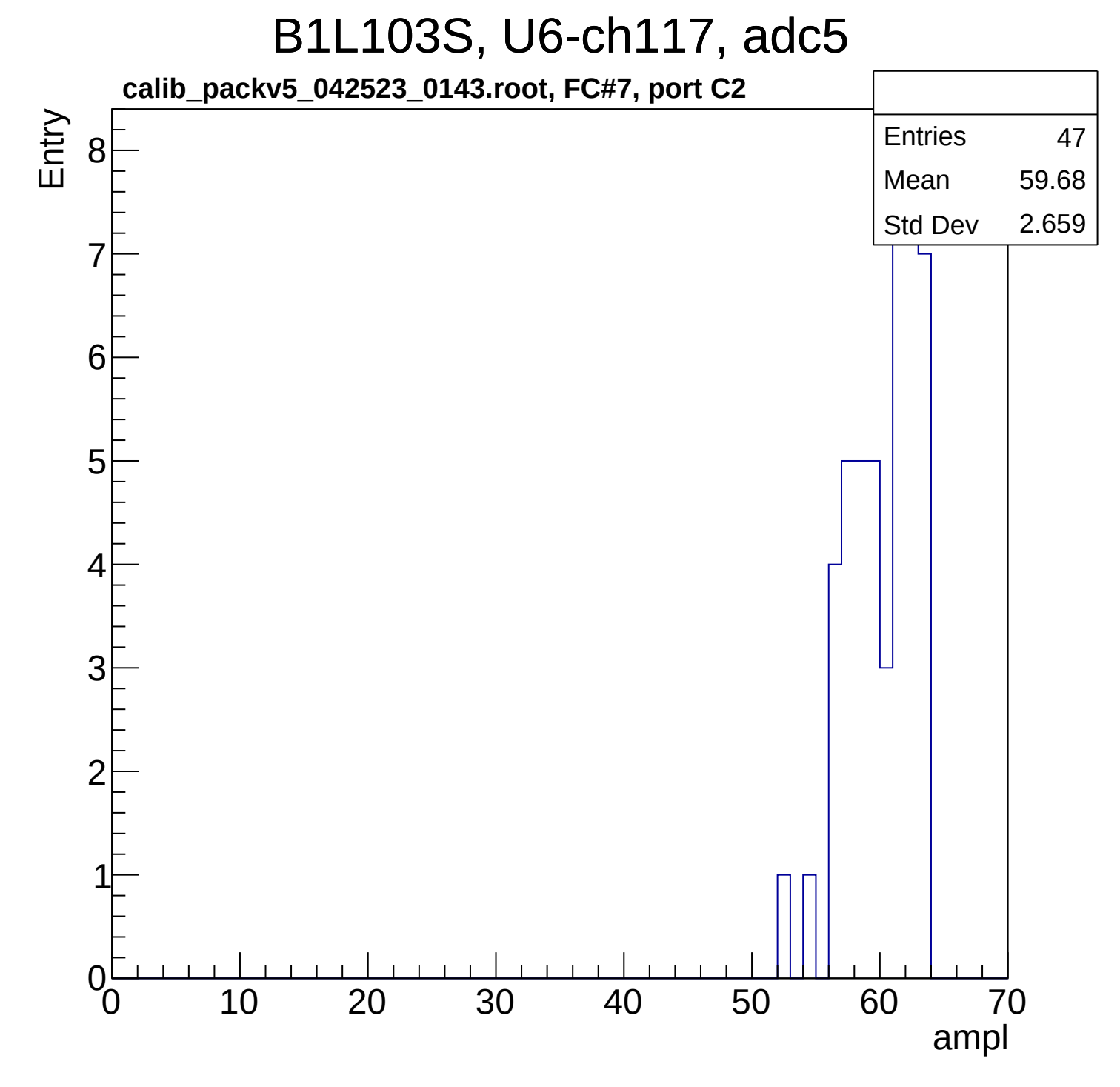
B1L103S, U6-ch117, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.68
Std Dev 2.659
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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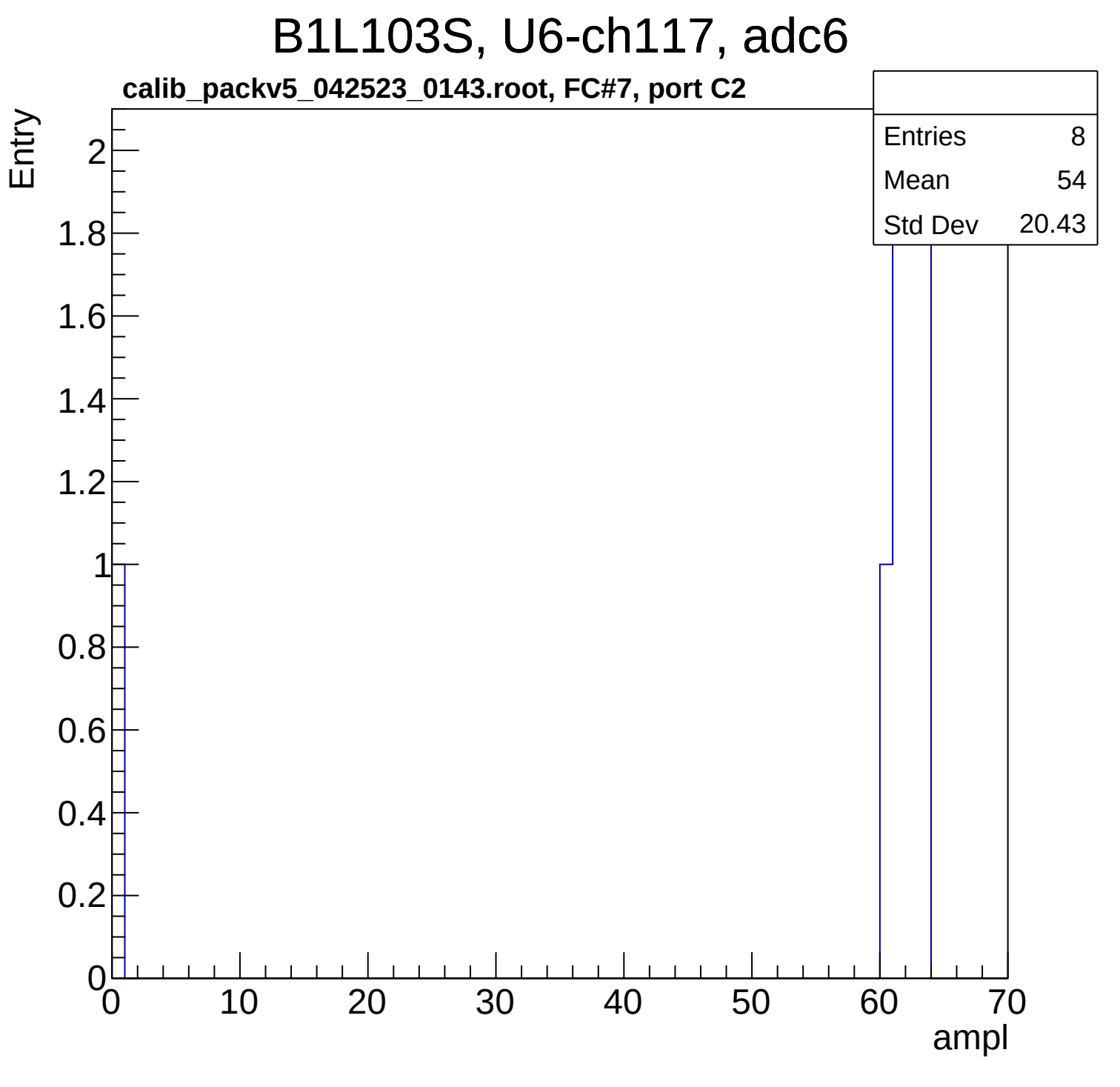
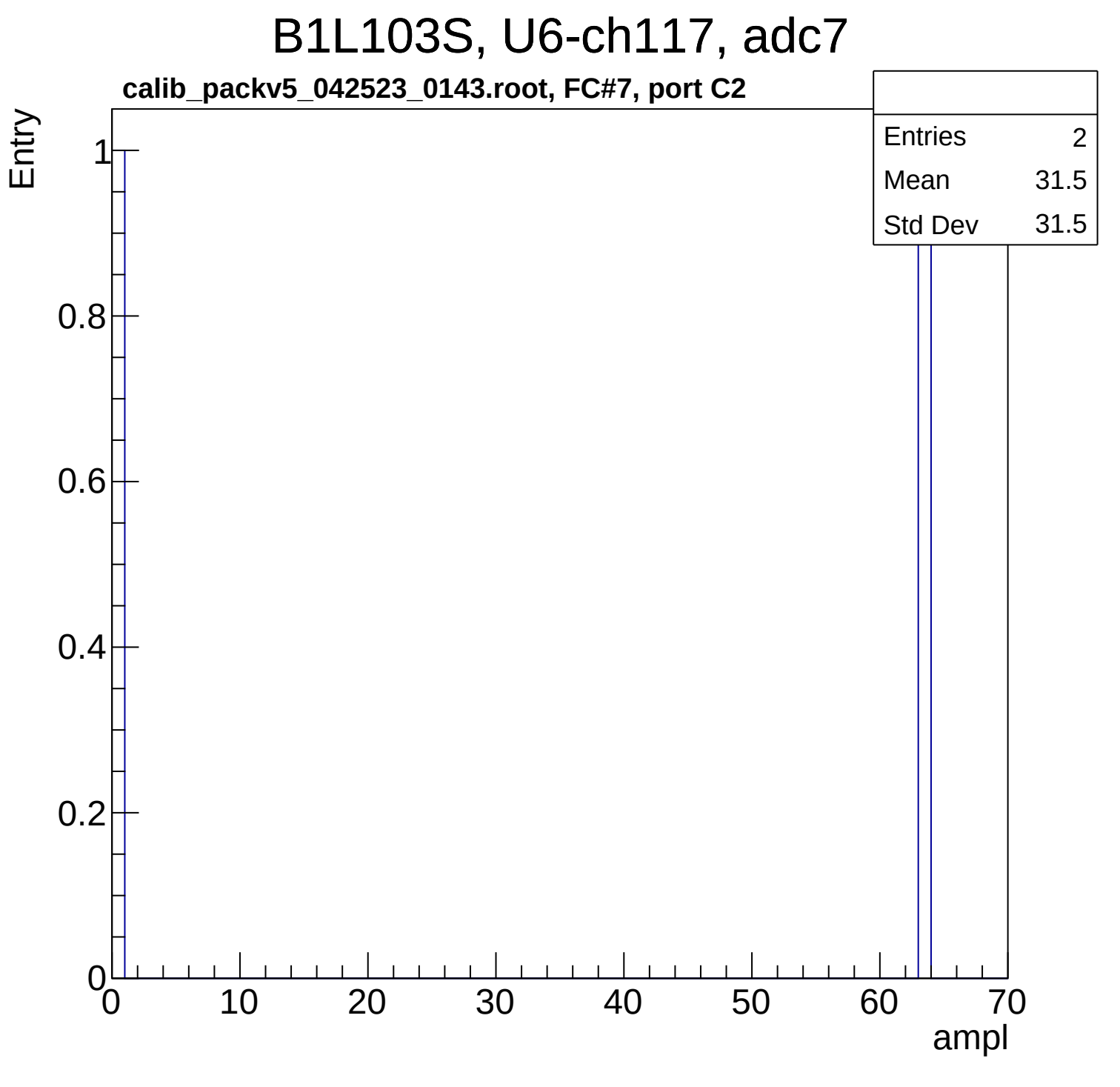
40
50
60
70


B1L103S, U6-ch117, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 54
Std Dev 20.43
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch117, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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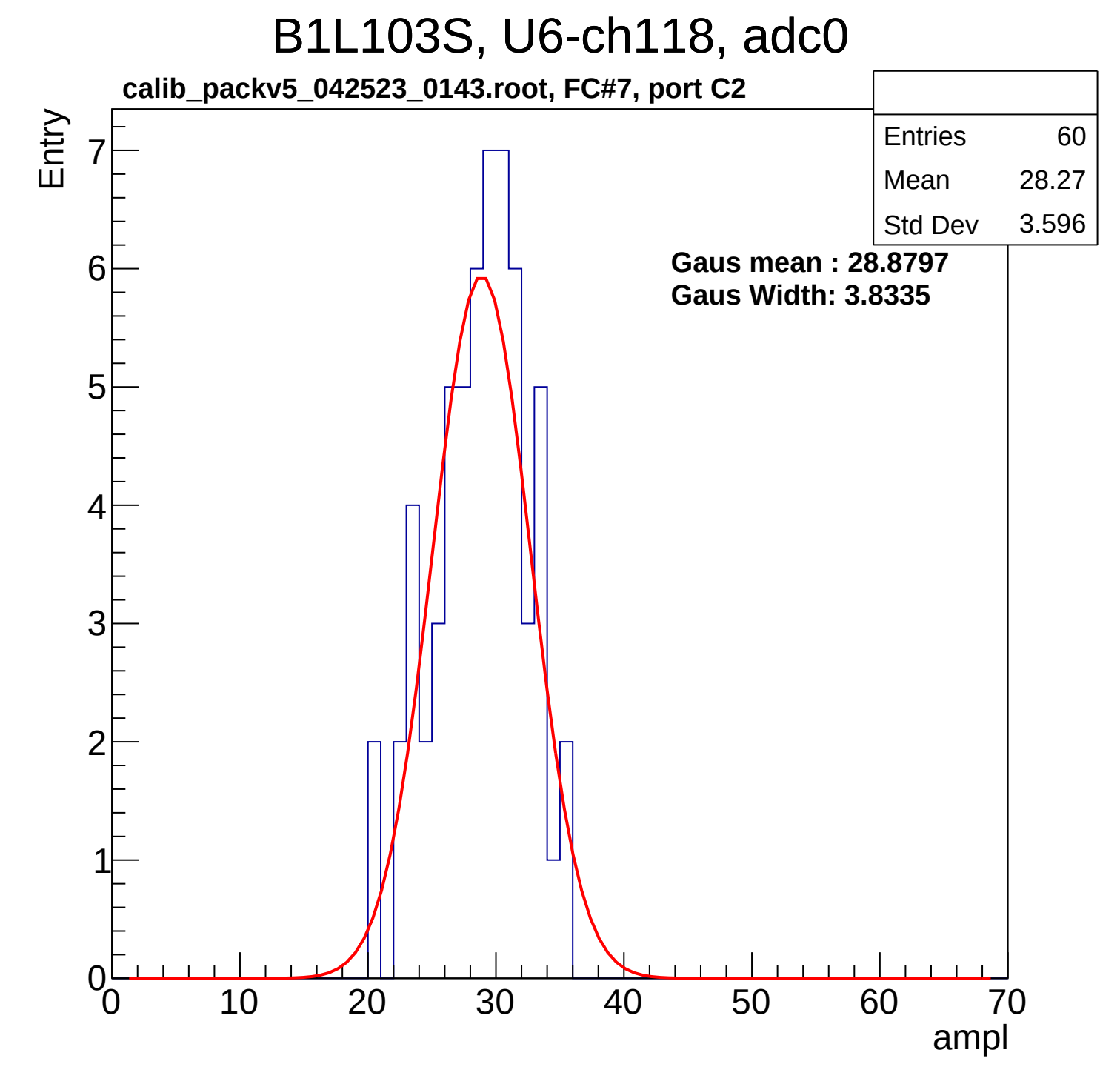
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch118, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 28.27
Std Dev 3.596
Gaus mean : 28.8797
Gaus Width: 3.8335
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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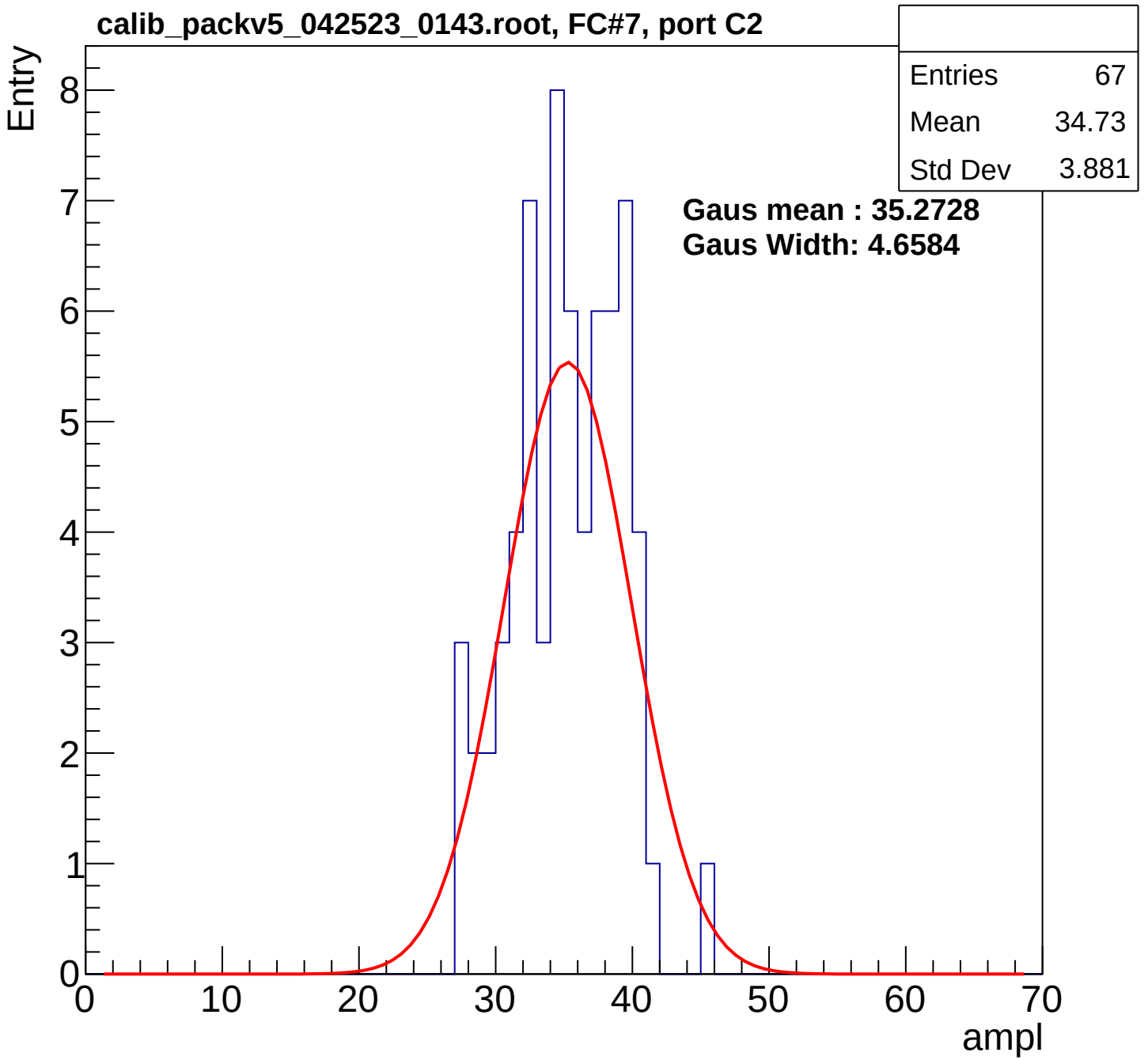
B1L103S, U6-ch118, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 34.73
Std Dev 3.881
Gaus mean : 35.2728
Gaus Width: 4.6584
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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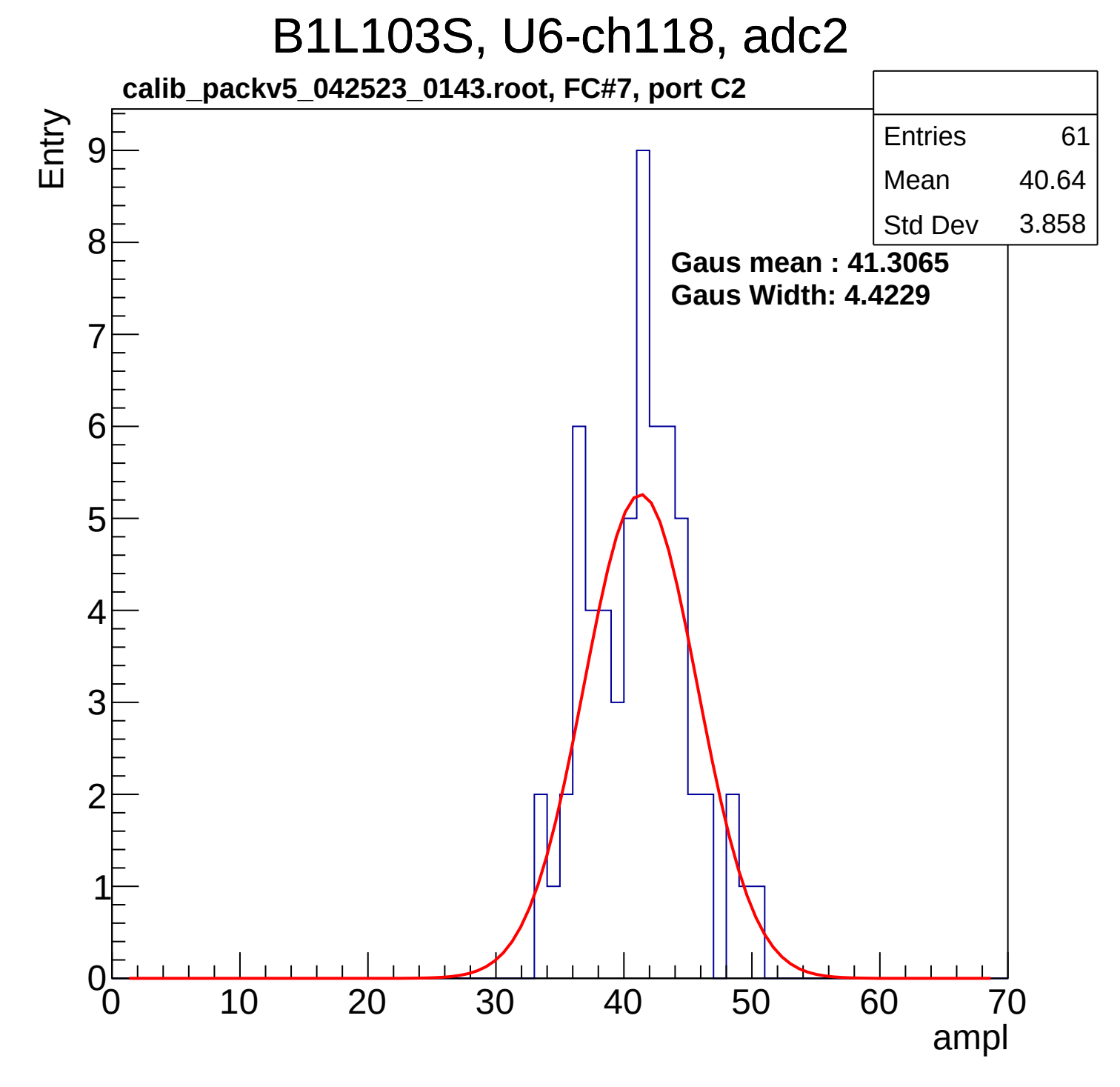
B1L103S, U6-ch118, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 40.64
Std Dev 3.858
Gaus mean : 41.3065
Gaus Width: 4.4229
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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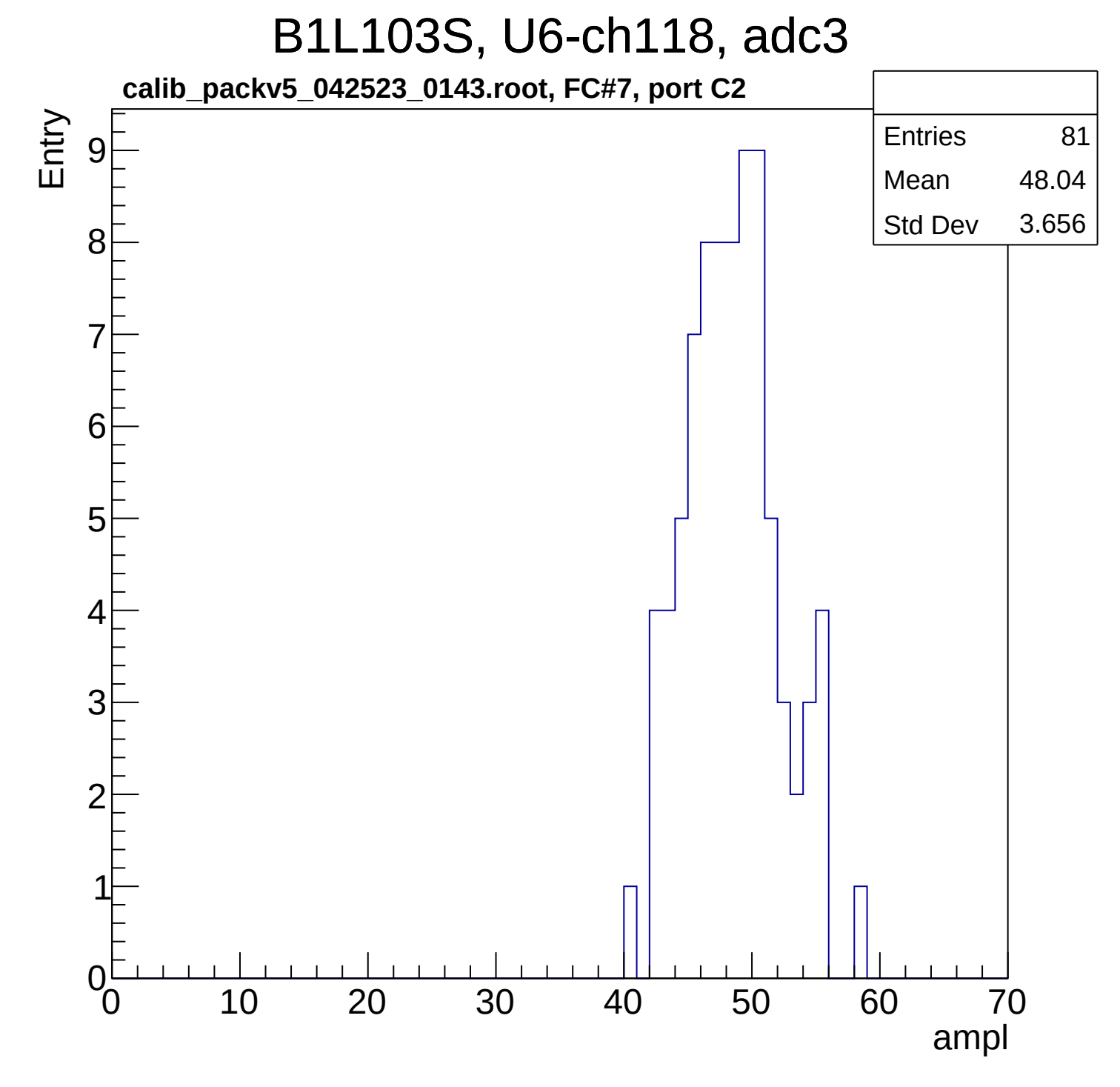
B1L103S, U6-ch118, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 48.04
Std Dev 3.656
Entry
ampl
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0
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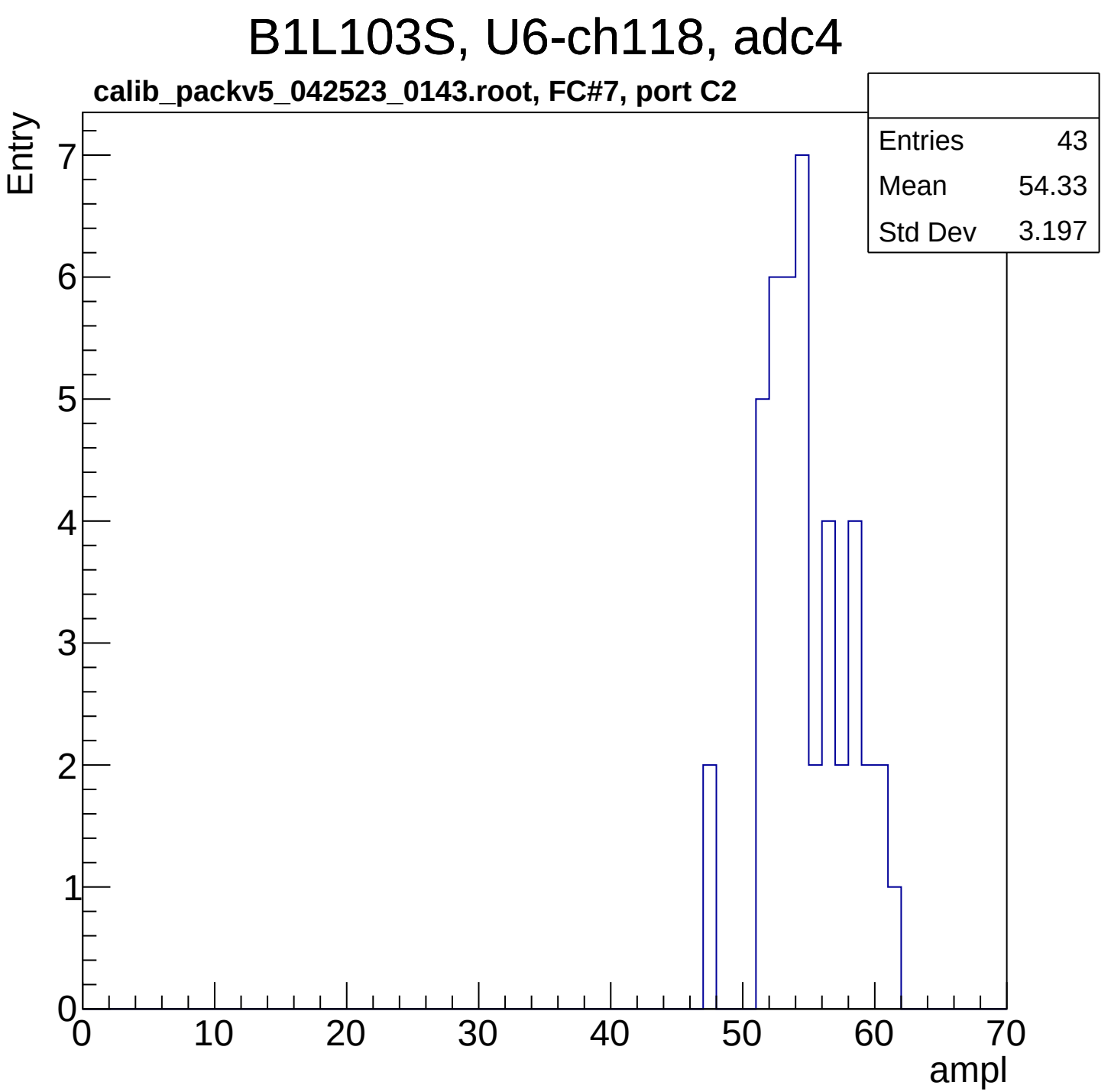
B1L103S, U6-ch118, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 54.33
Std Dev 3.197
Entry
ampl
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1
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3
4
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0
10
20
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40
50
60
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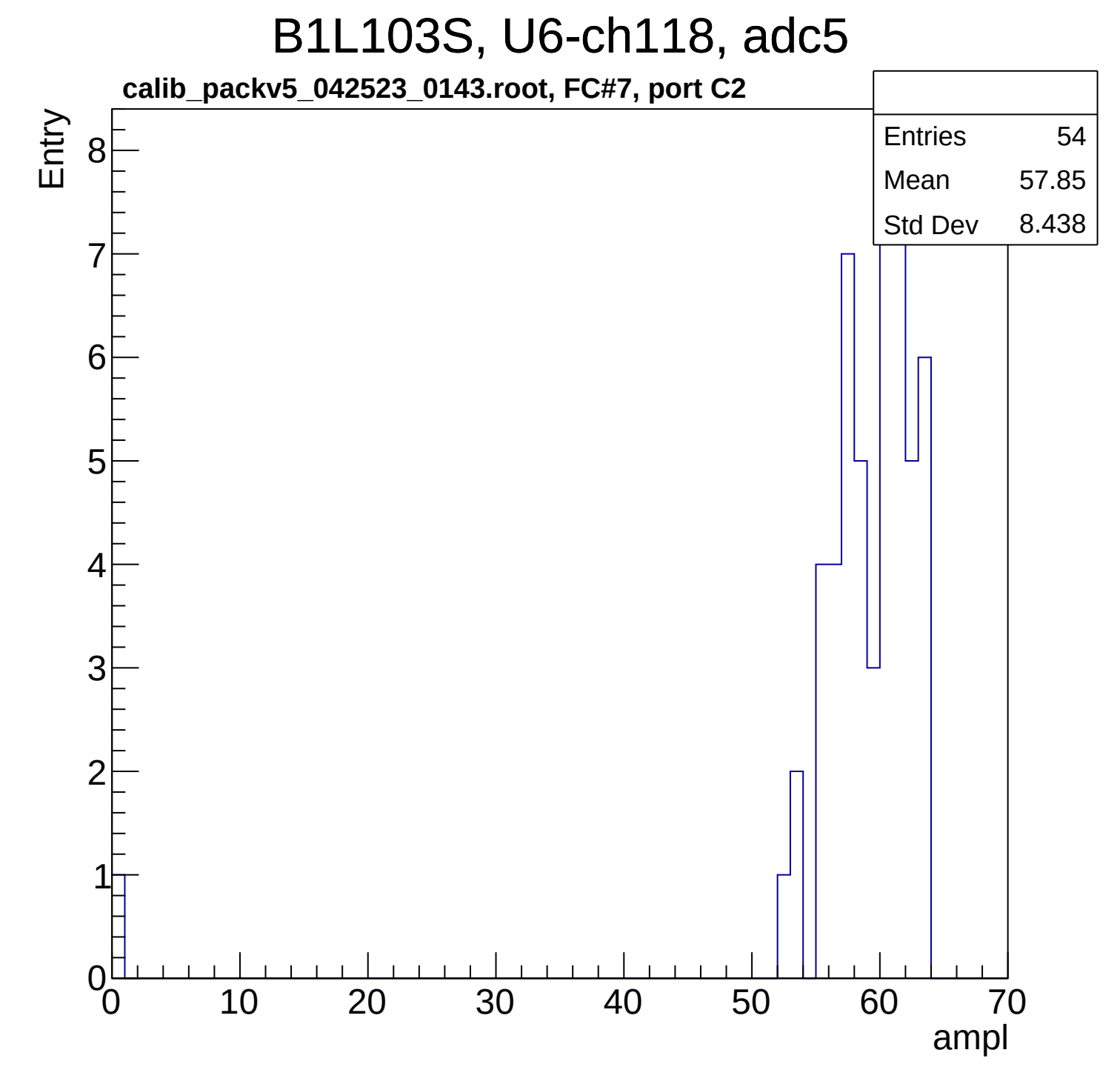
B1L103S, U6-ch118, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.85
Std Dev 8.438
Entry
ampl
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3
4
5
6
7
8
0
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20
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60
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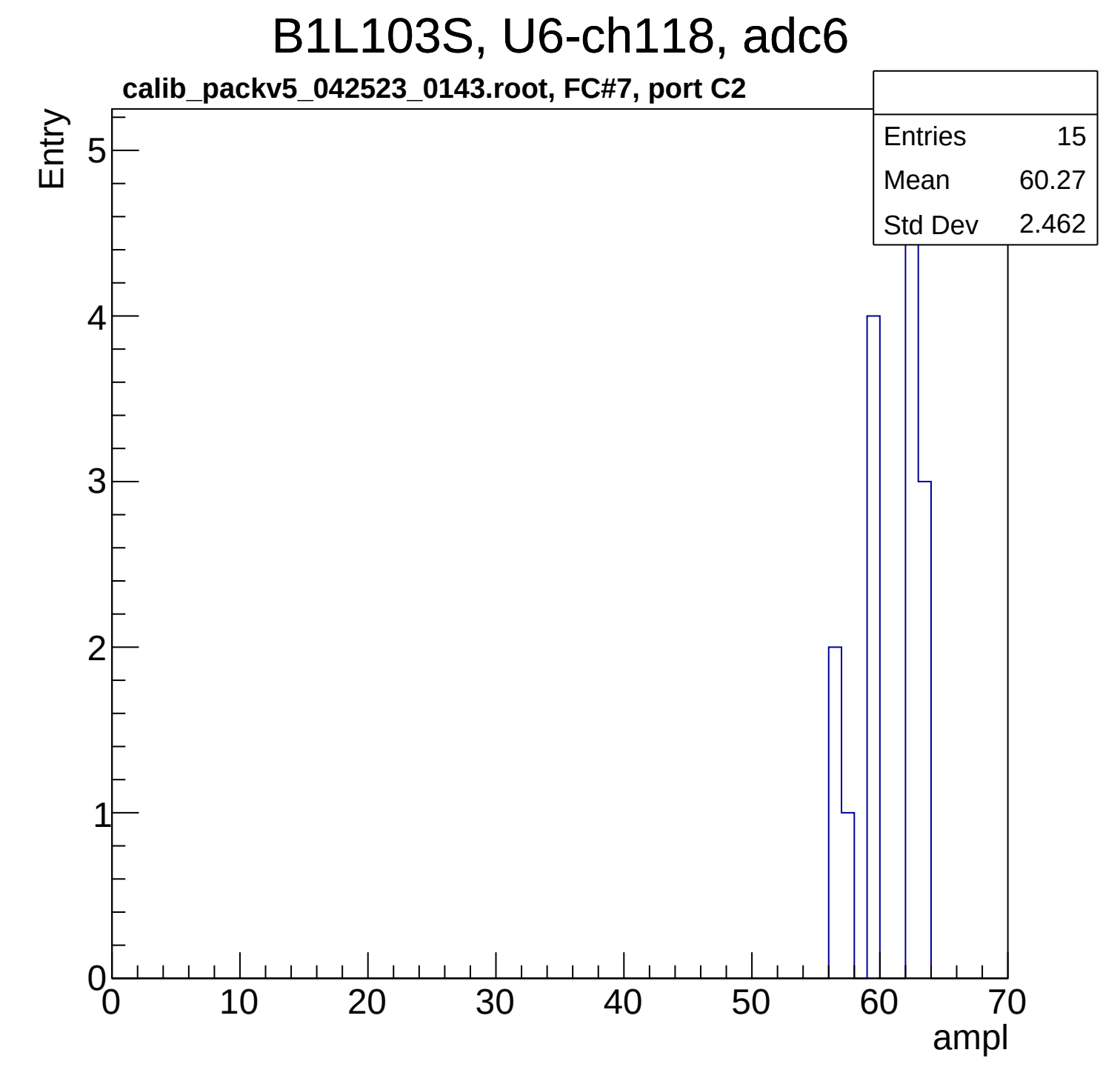
B1L103S, U6-ch118, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 60.27
Std Dev 2.462
Entry
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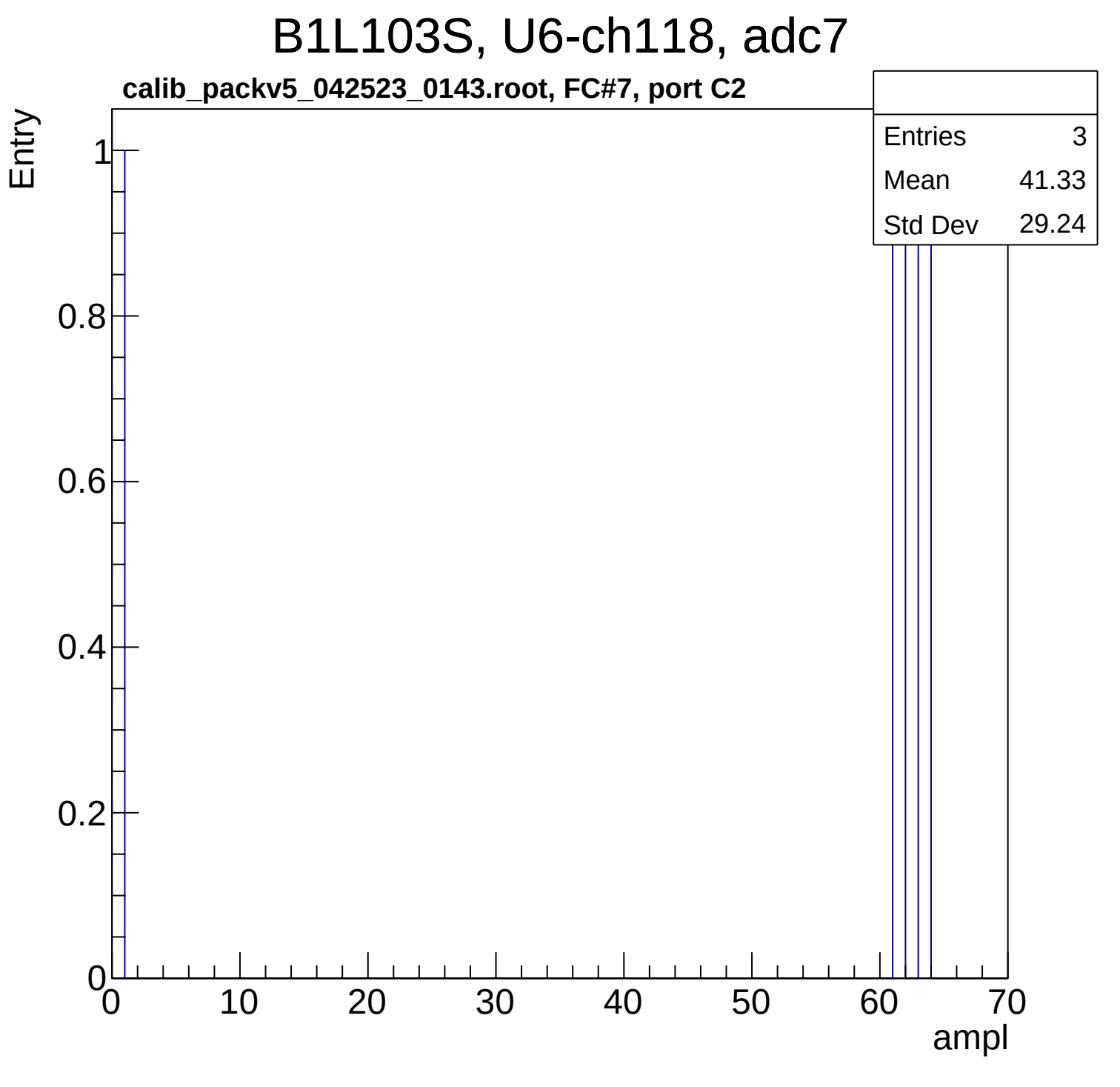
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch118, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
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0.6
0.4
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10
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30
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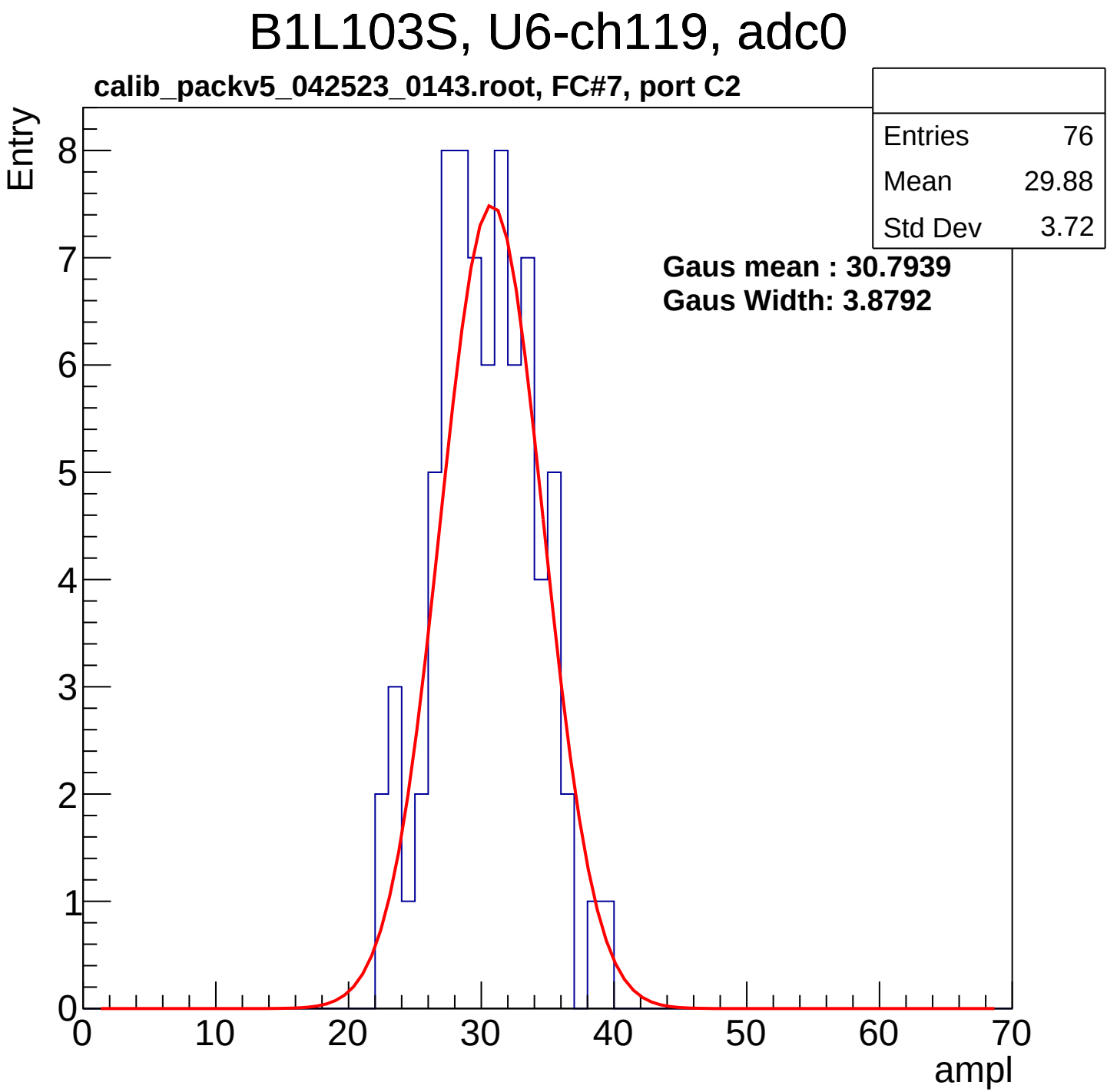
50
60
70
ampl


B1L103S, U6-ch119, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.88
Std Dev 3.72
Gaus mean : 30.7939
Gaus Width: 3.8792
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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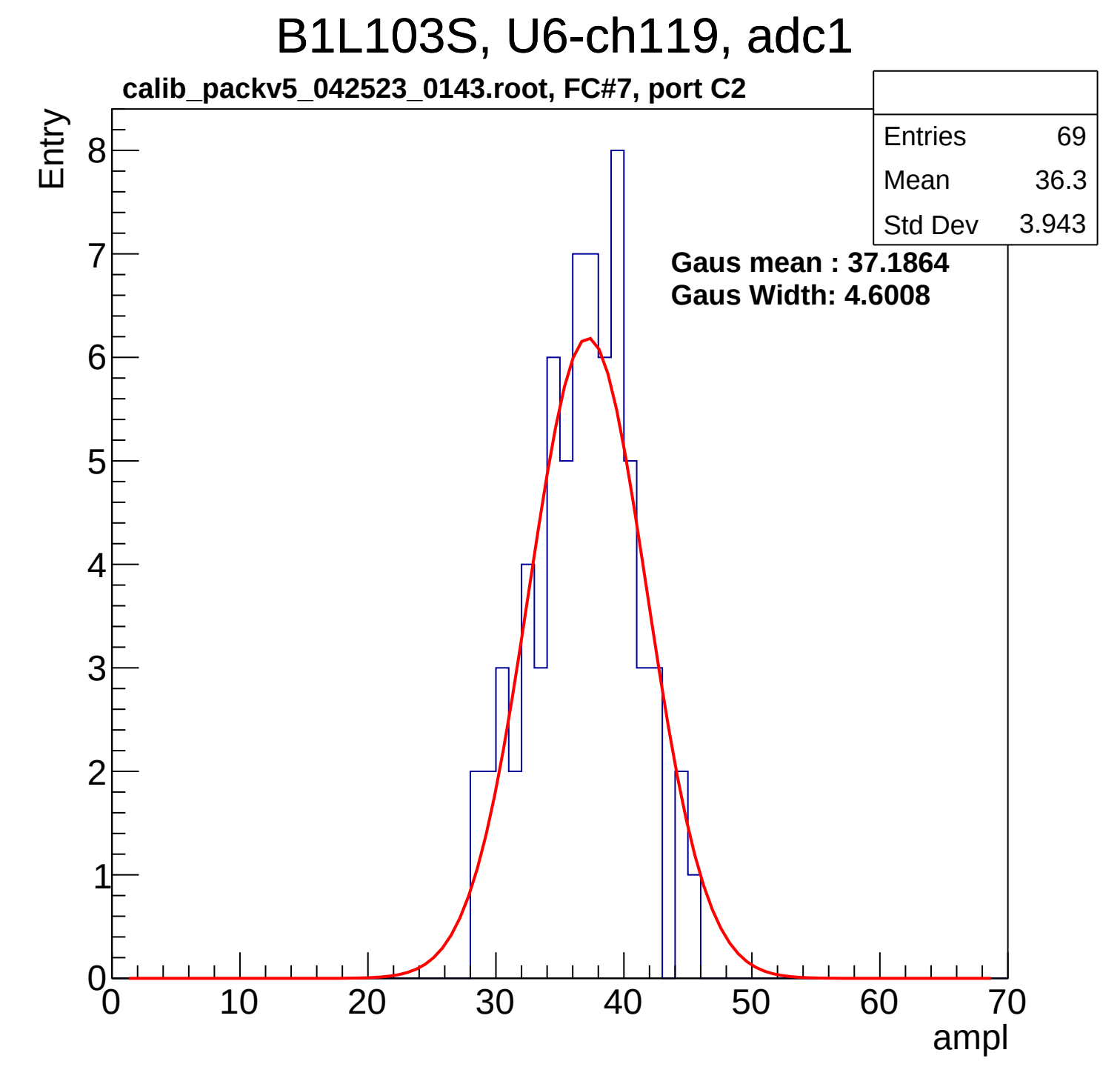
B1L103S, U6-ch119, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.3
Std Dev 3.943
Gaus mean : 37.1864
Gaus Width: 4.6008
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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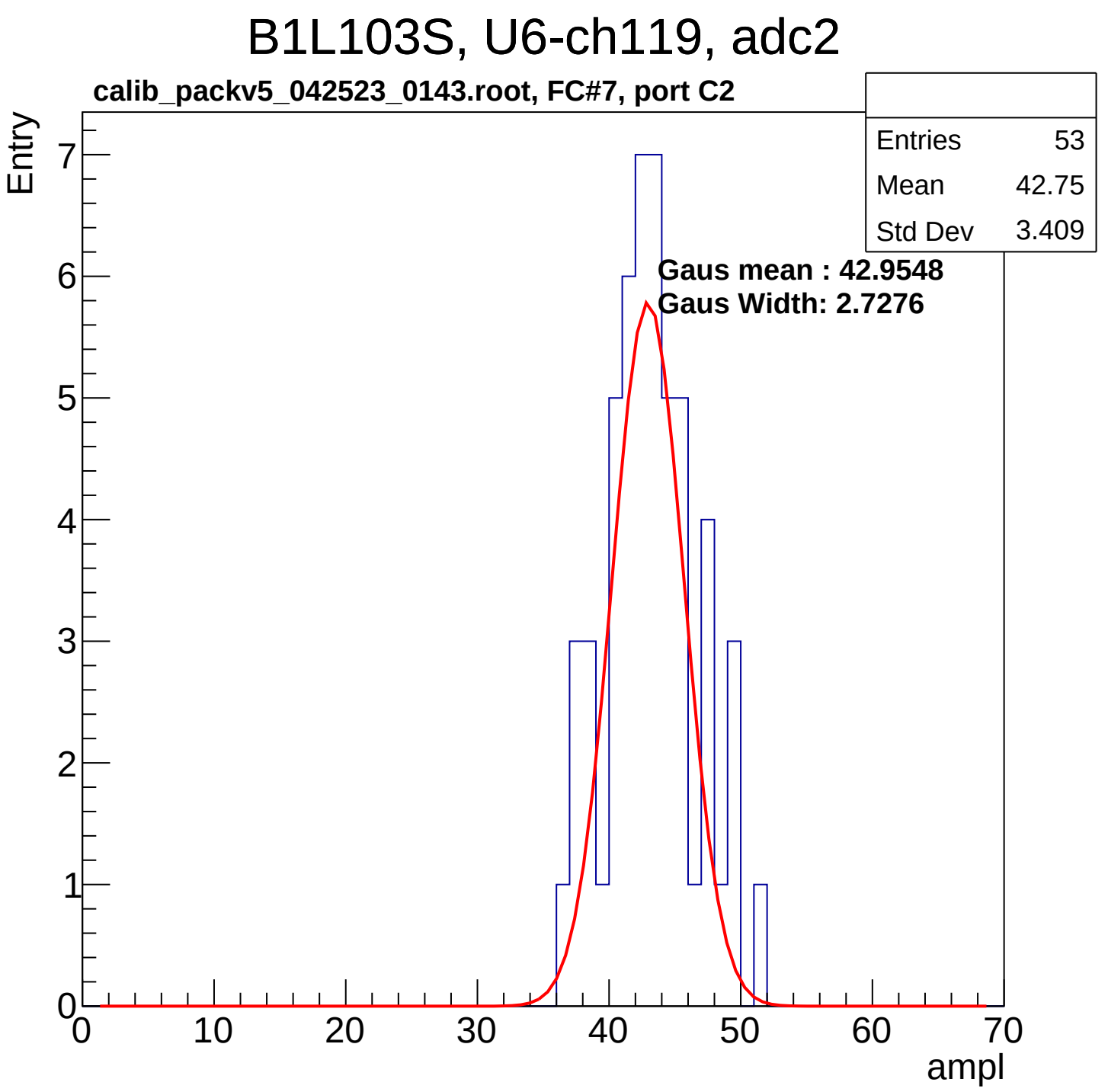
B1L103S, U6-ch119, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 42.75
Std Dev 3.409
Gaus mean : 42.9548
Gaus Width: 2.7276
Entry
ampl
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2
3
4
5
6
7
0
10
20
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60
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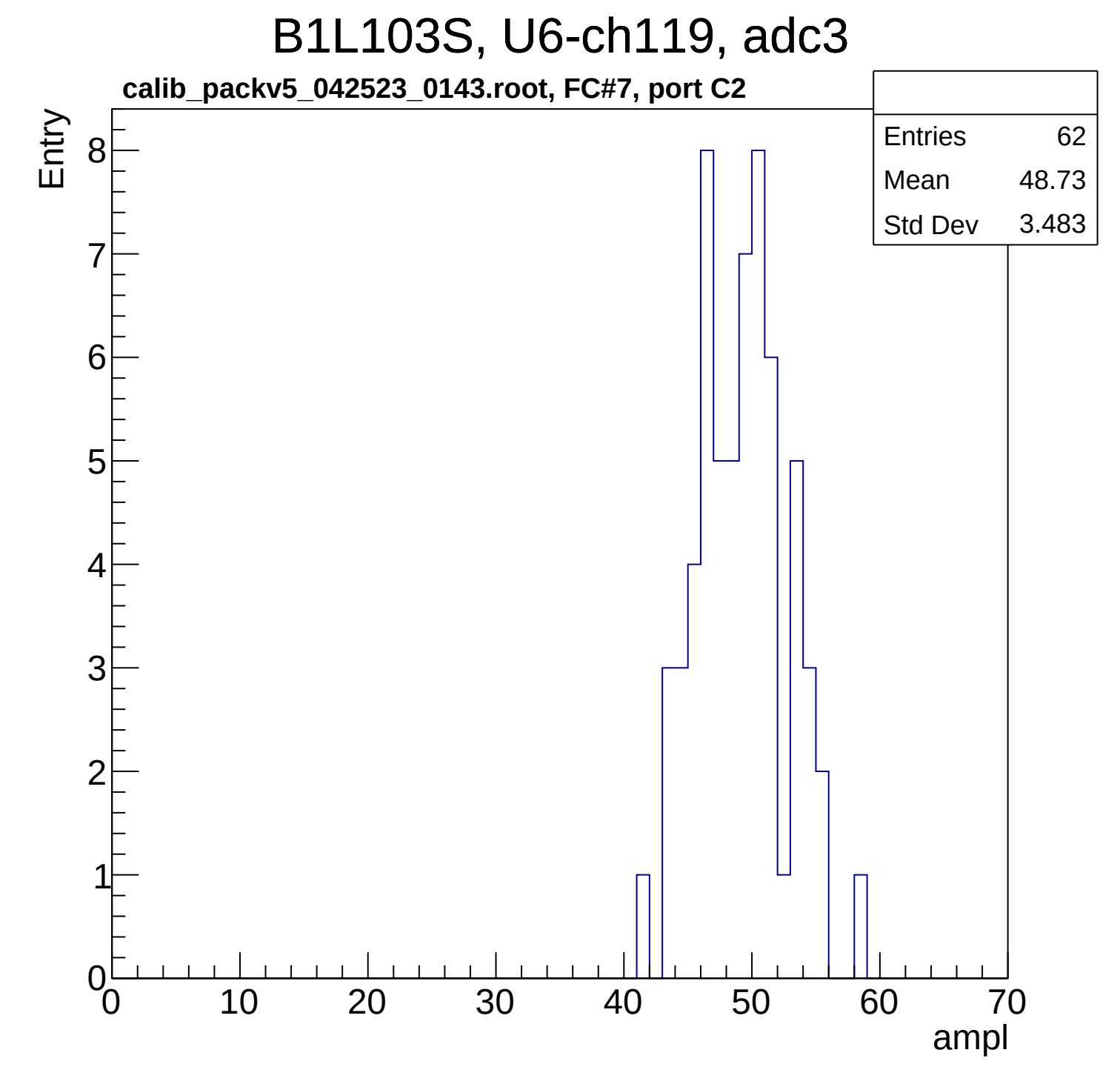
B1L103S, U6-ch119, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 48.73
Std Dev 3.483
Entry
ampl
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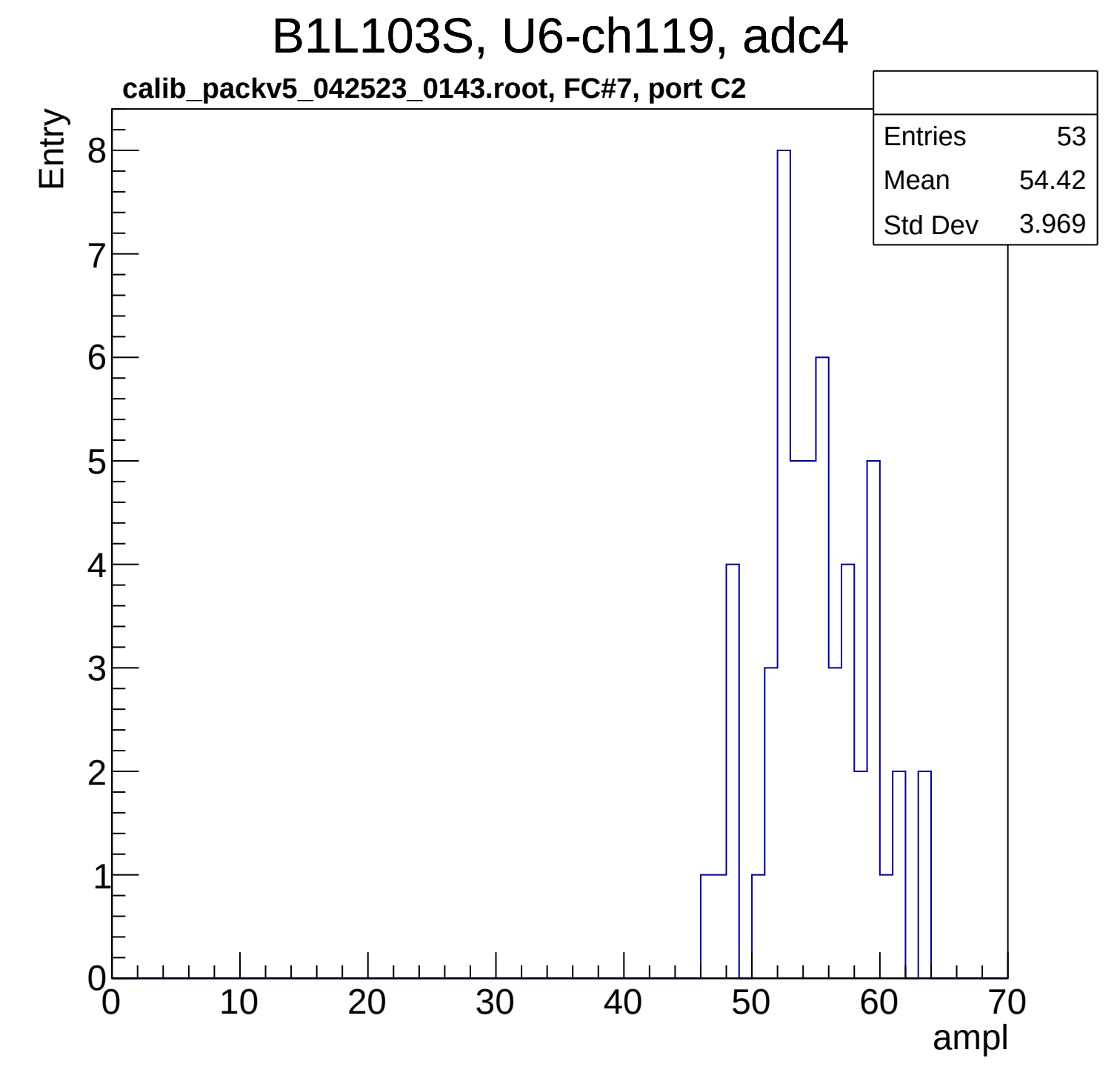
B1L103S, U6-ch119, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 54.42
Std Dev 3.969
Entry
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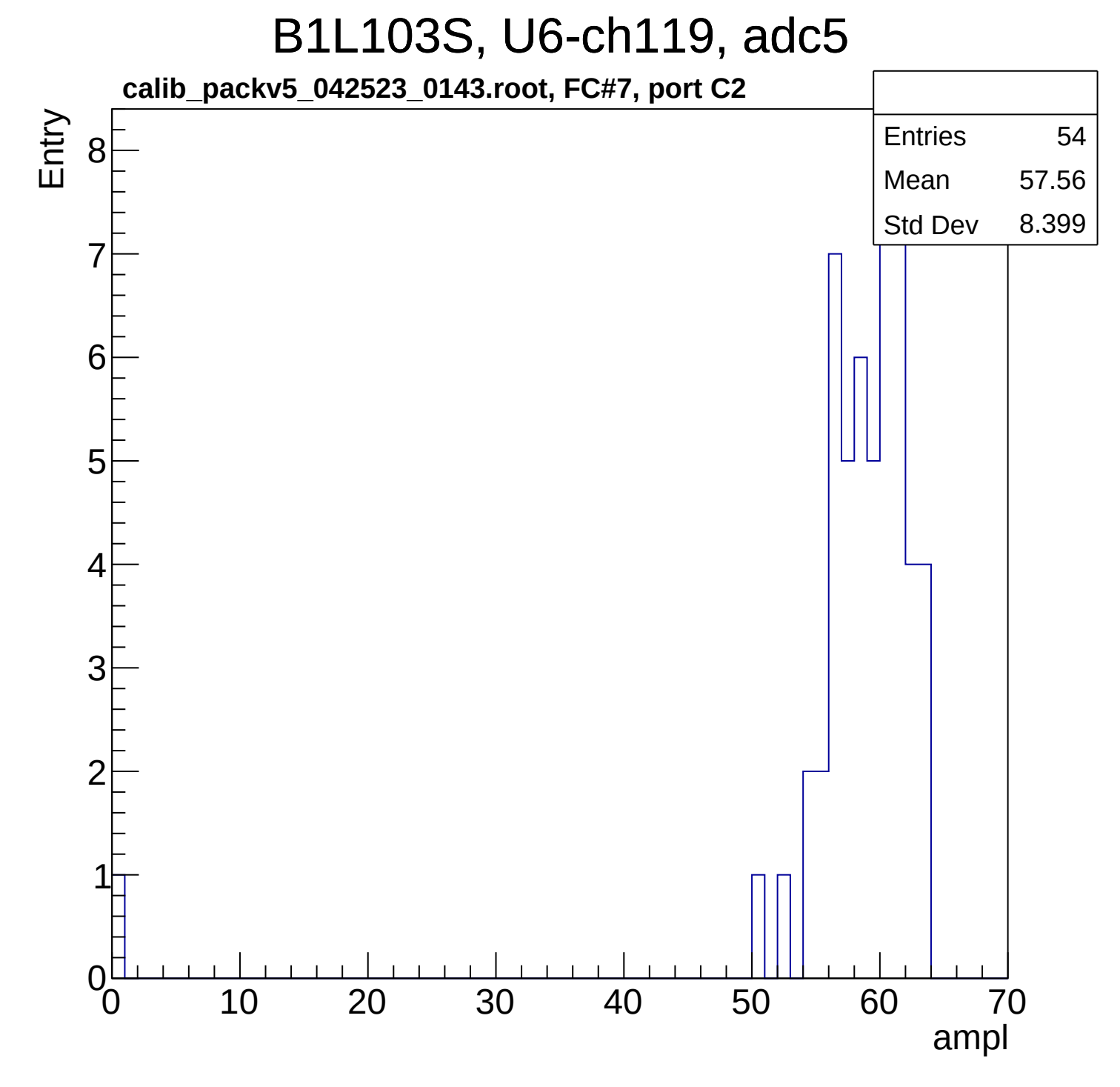
50
60
70
ampl


B1L103S, U6-ch119, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.56
Std Dev 8.399
Entry
ampl
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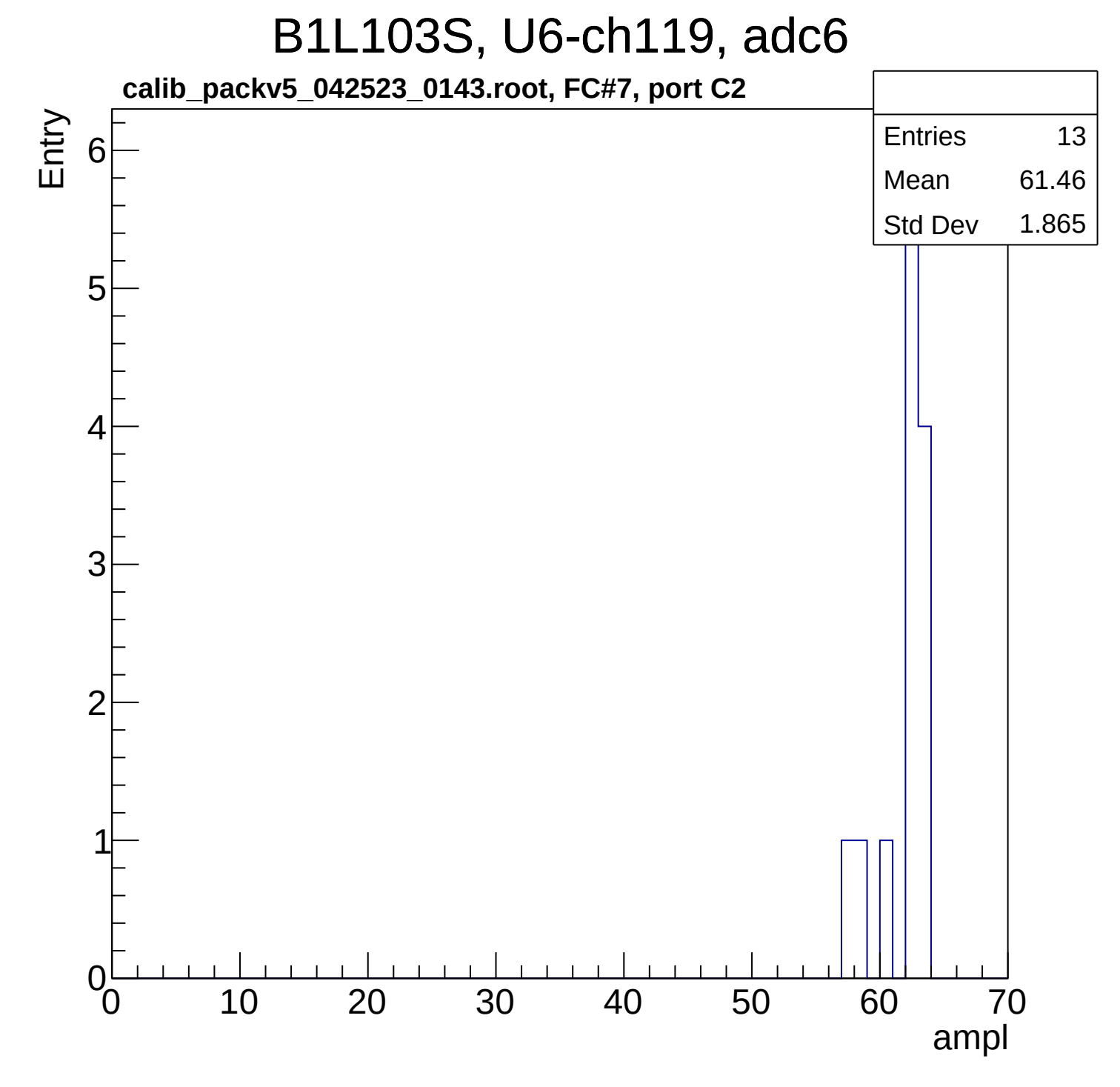
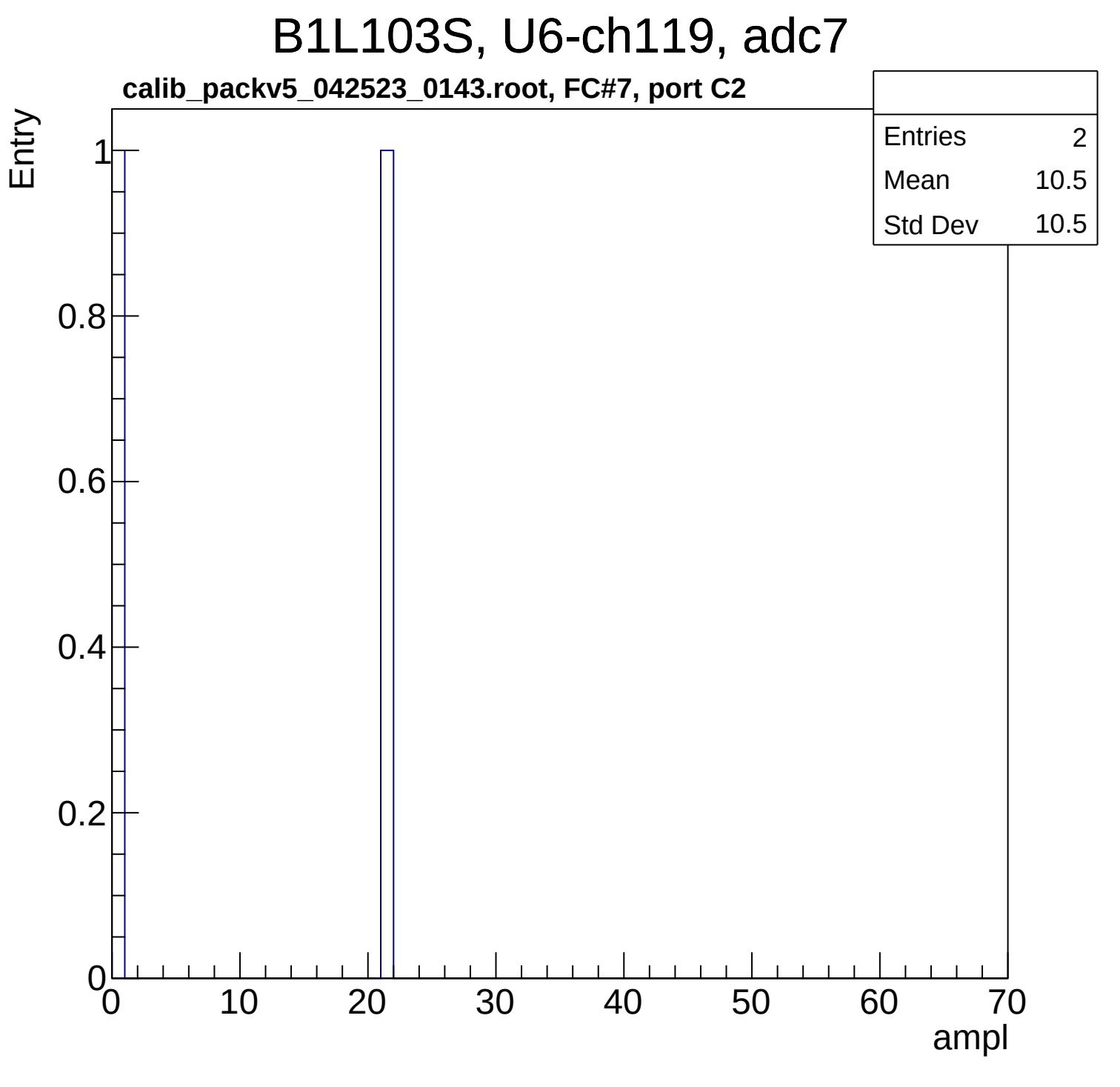

B1L103S, U6-ch119, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.46
Std Dev 1.865
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch119, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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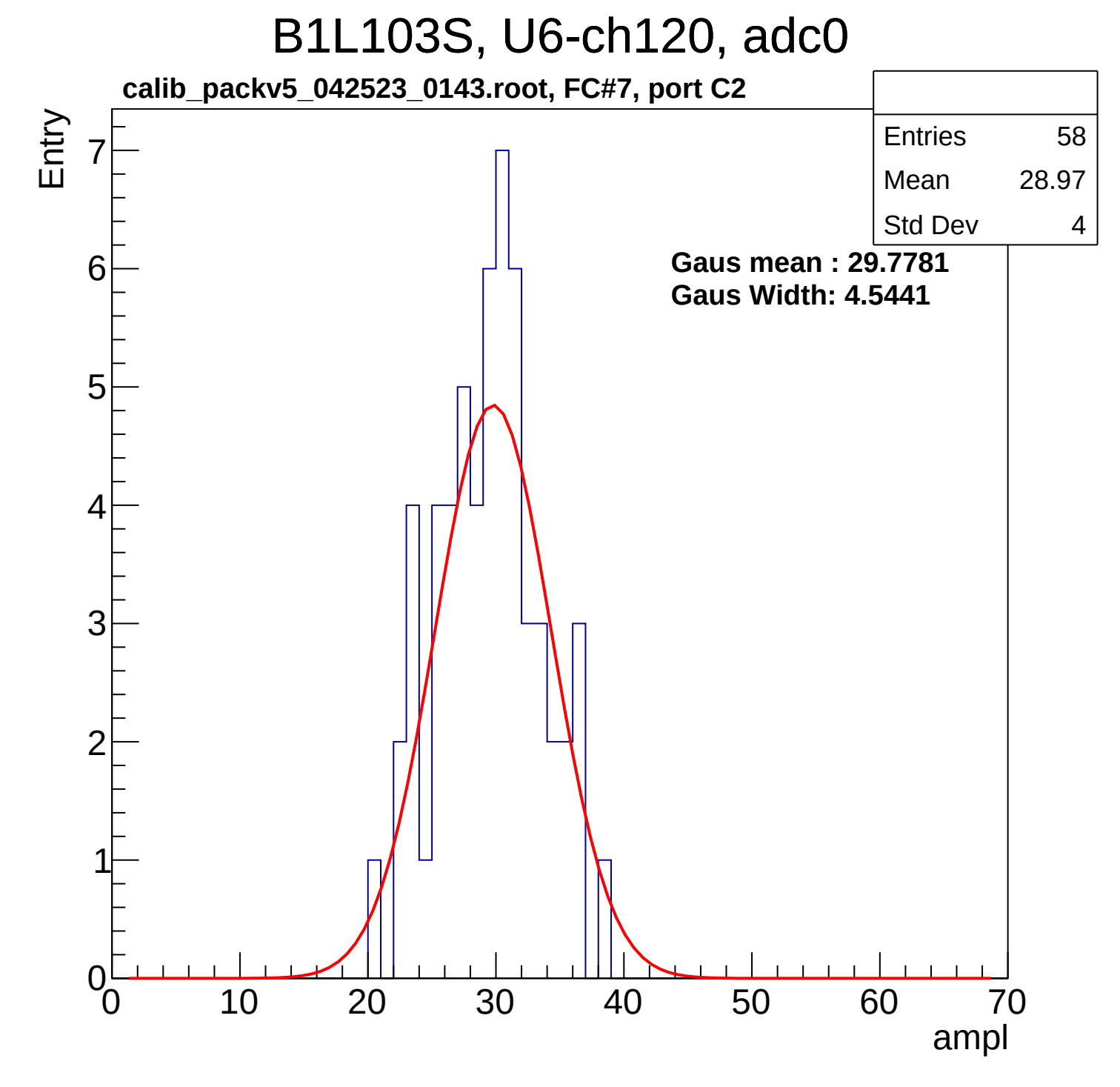
50
60
70
ampl


B1L103S, U6-ch120, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 28.97
Std Dev 4
Gaus mean : 29.7781
Gaus Width: 4.5441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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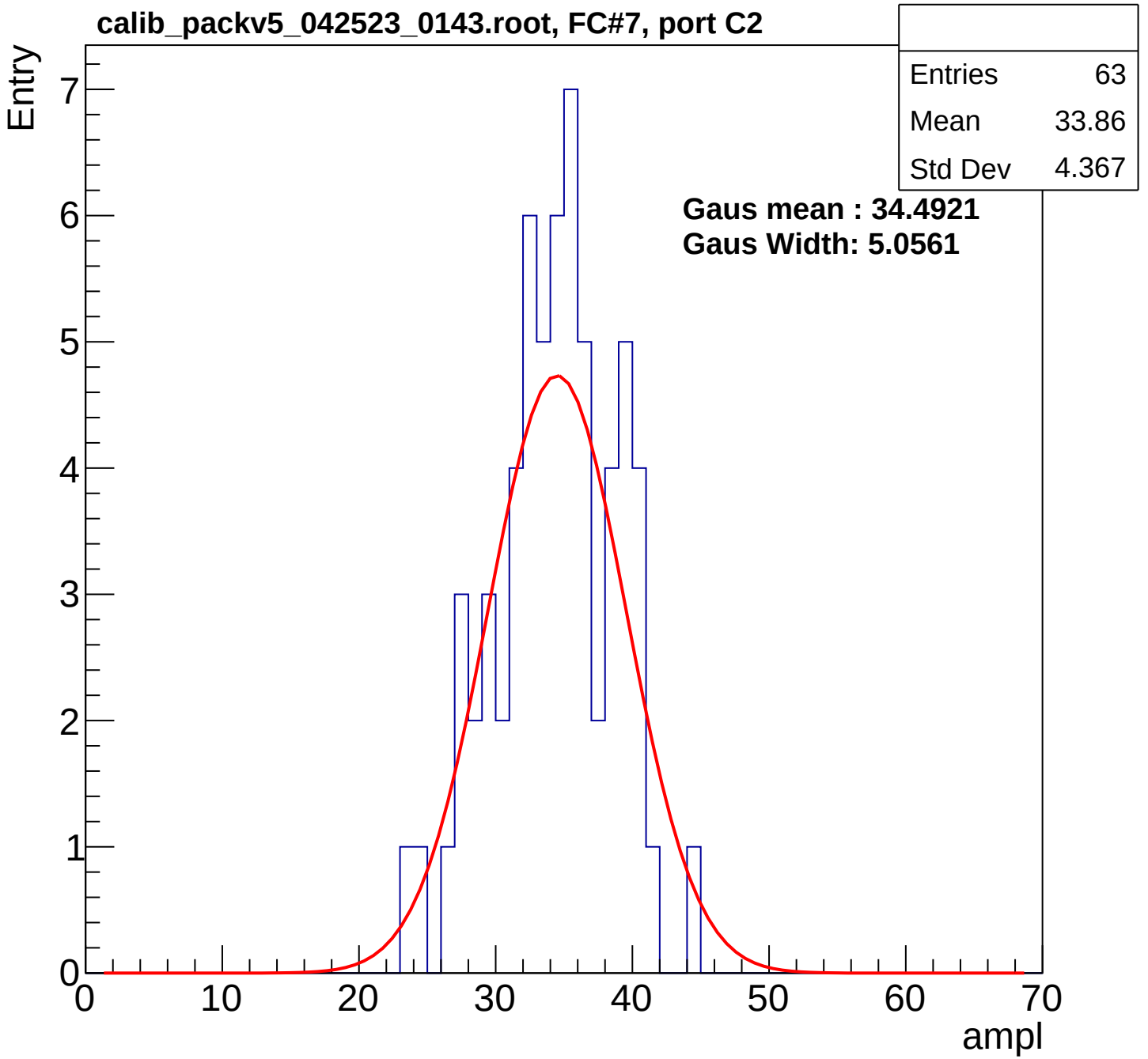
B1L103S, U6-ch120, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 33.86
Std Dev 4.367
Gaus mean : 34.4921
Gaus Width: 5.0561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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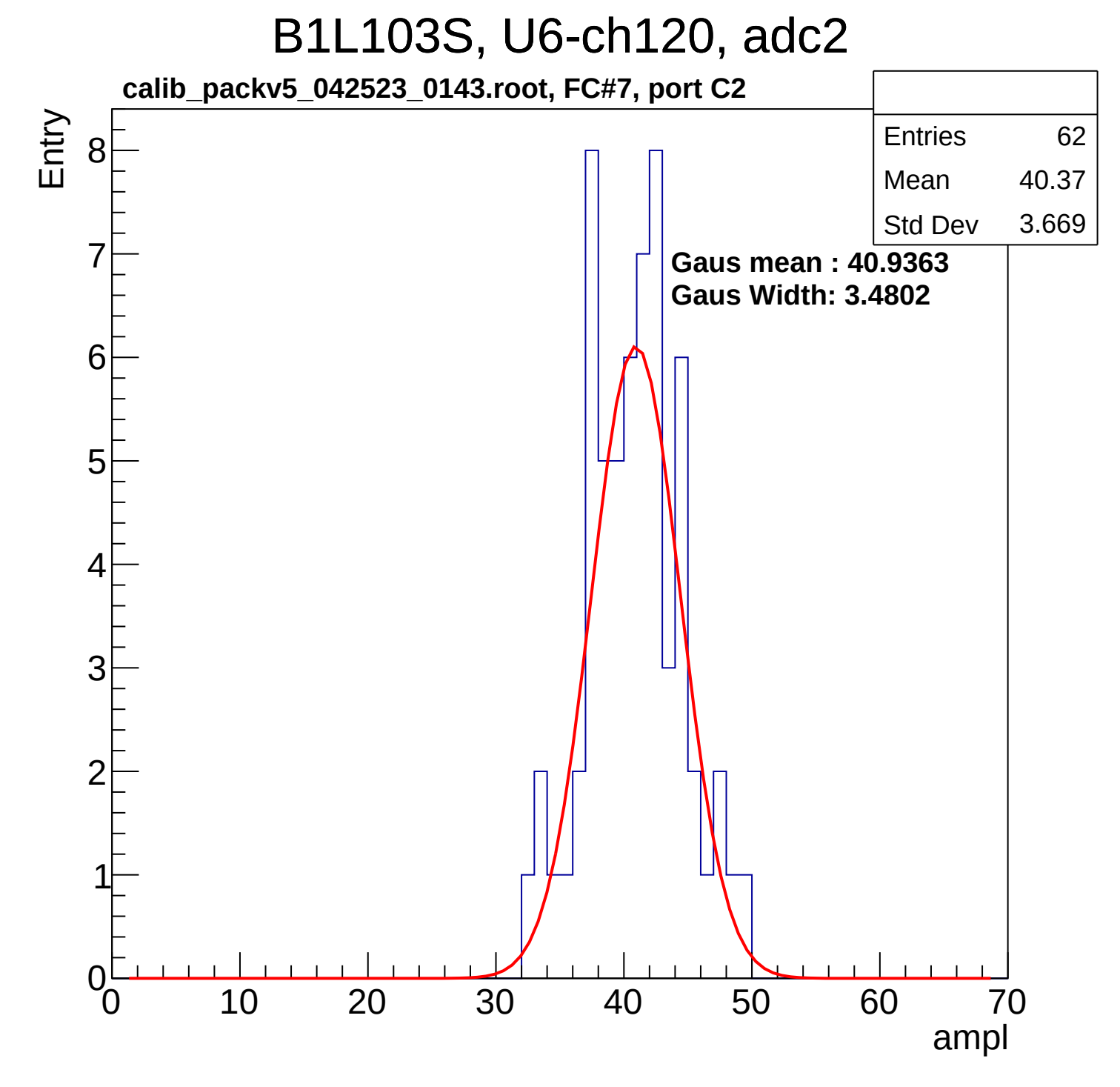
B1L103S, U6-ch120, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 40.37
Std Dev 3.669
Gaus mean : 40.9363
Gaus Width: 3.4802
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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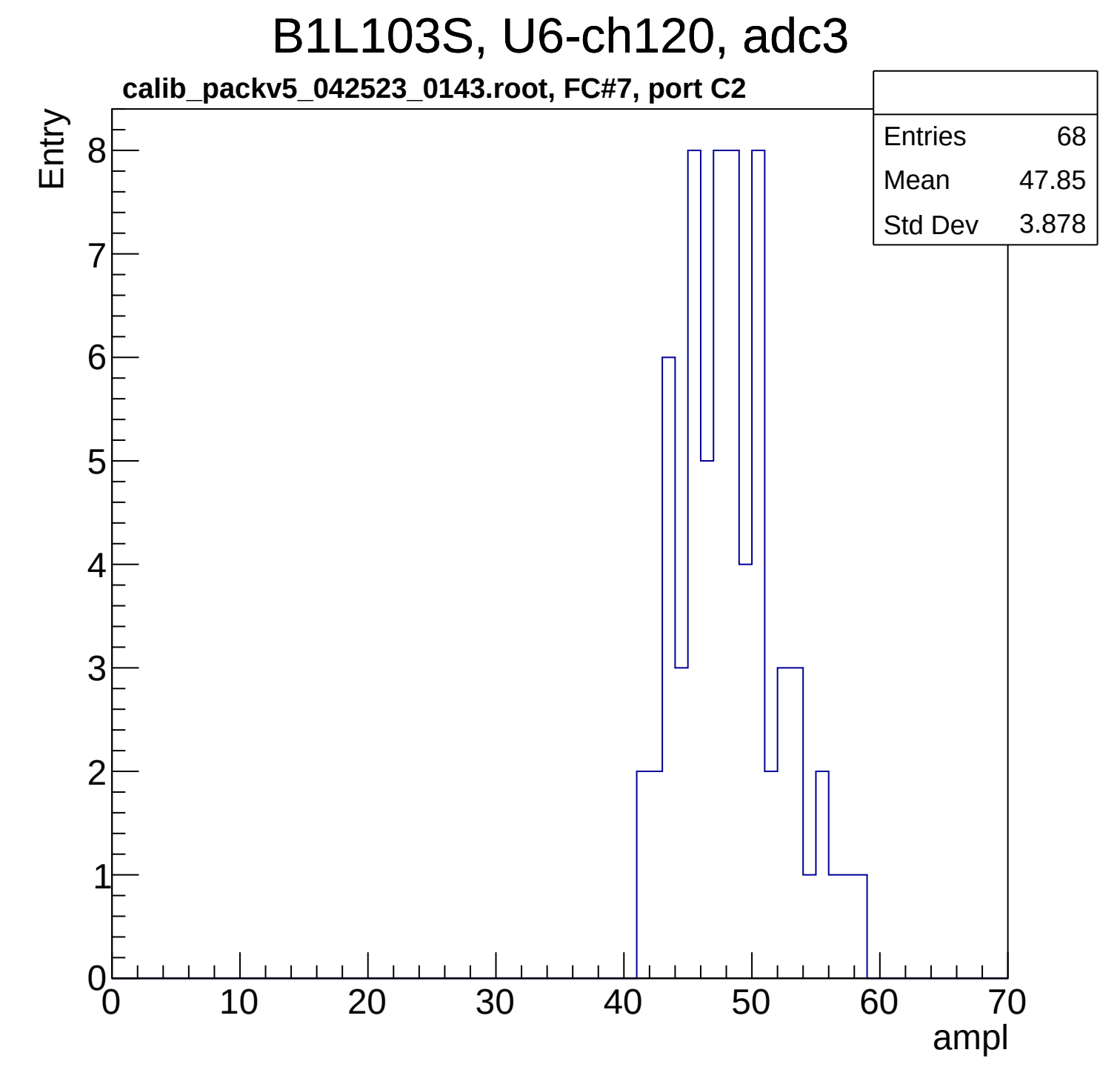
B1L103S, U6-ch120, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 47.85
Std Dev 3.878
Entry
ampl
0
1
2
3
4
5
6
7
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0
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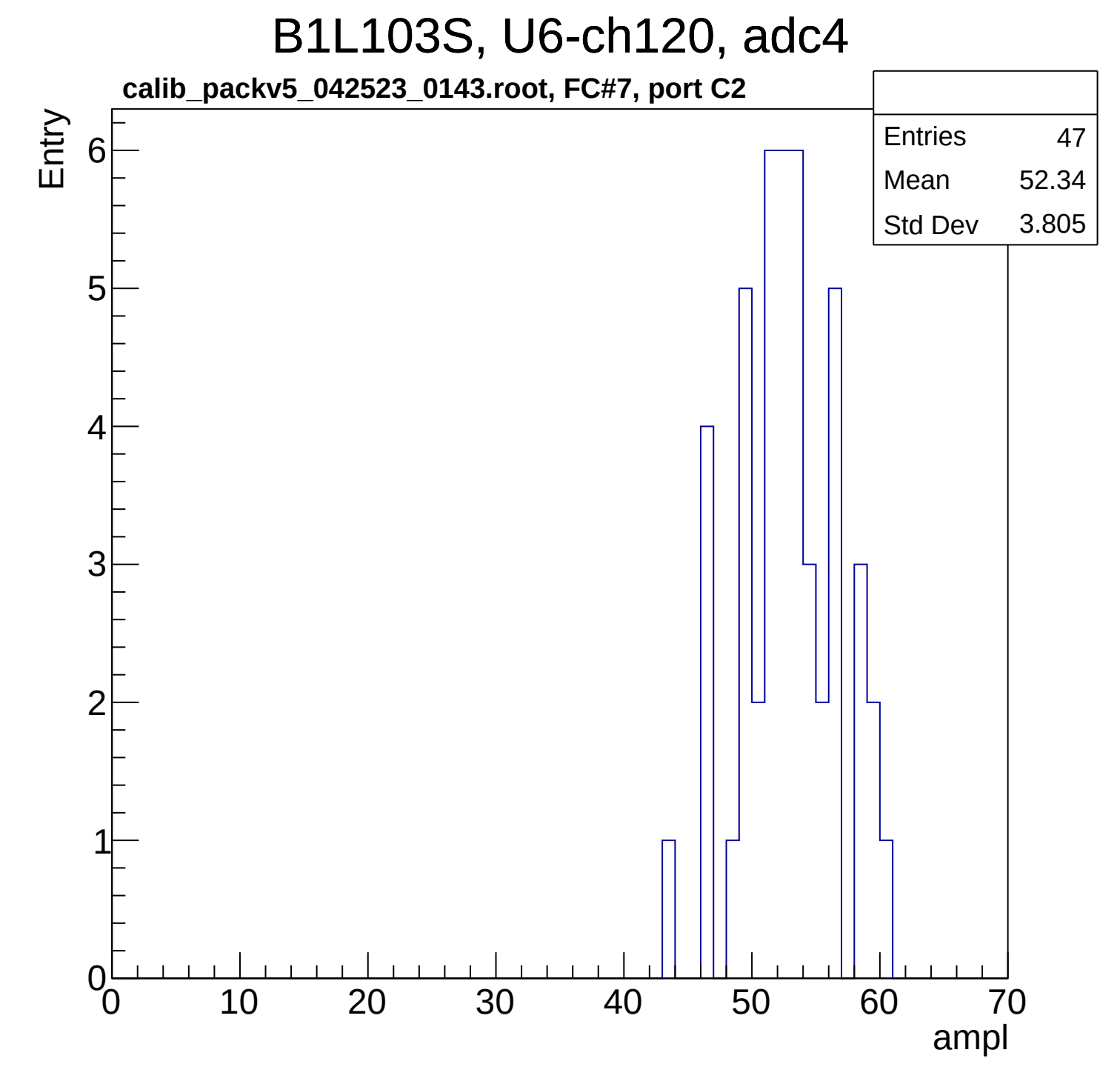
B1L103S, U6-ch120, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 52.34
Std Dev 3.805
Entry
ampl
0
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2
3
4
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0
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40
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60
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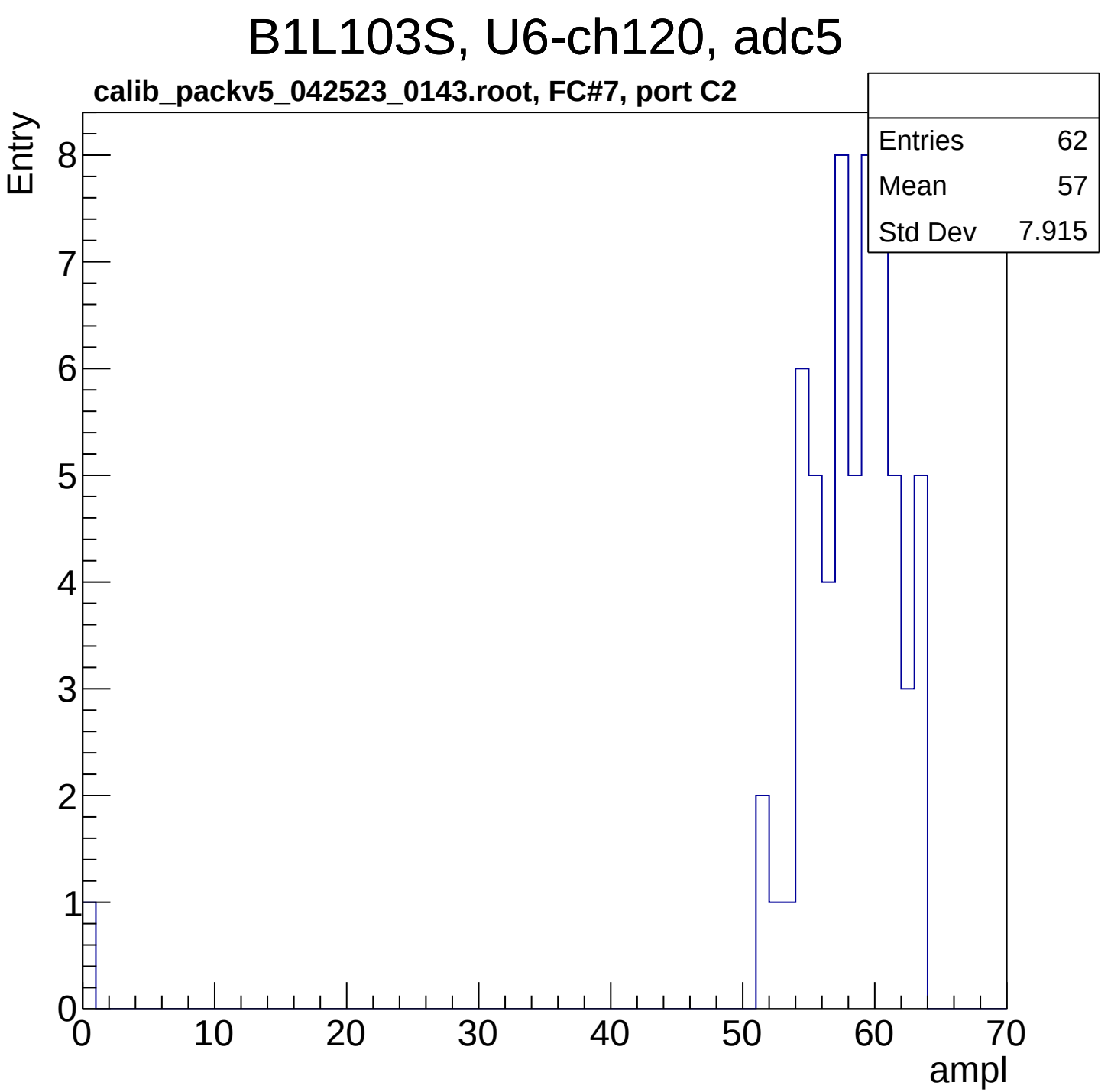
B1L103S, U6-ch120, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 57
Std Dev 7.915
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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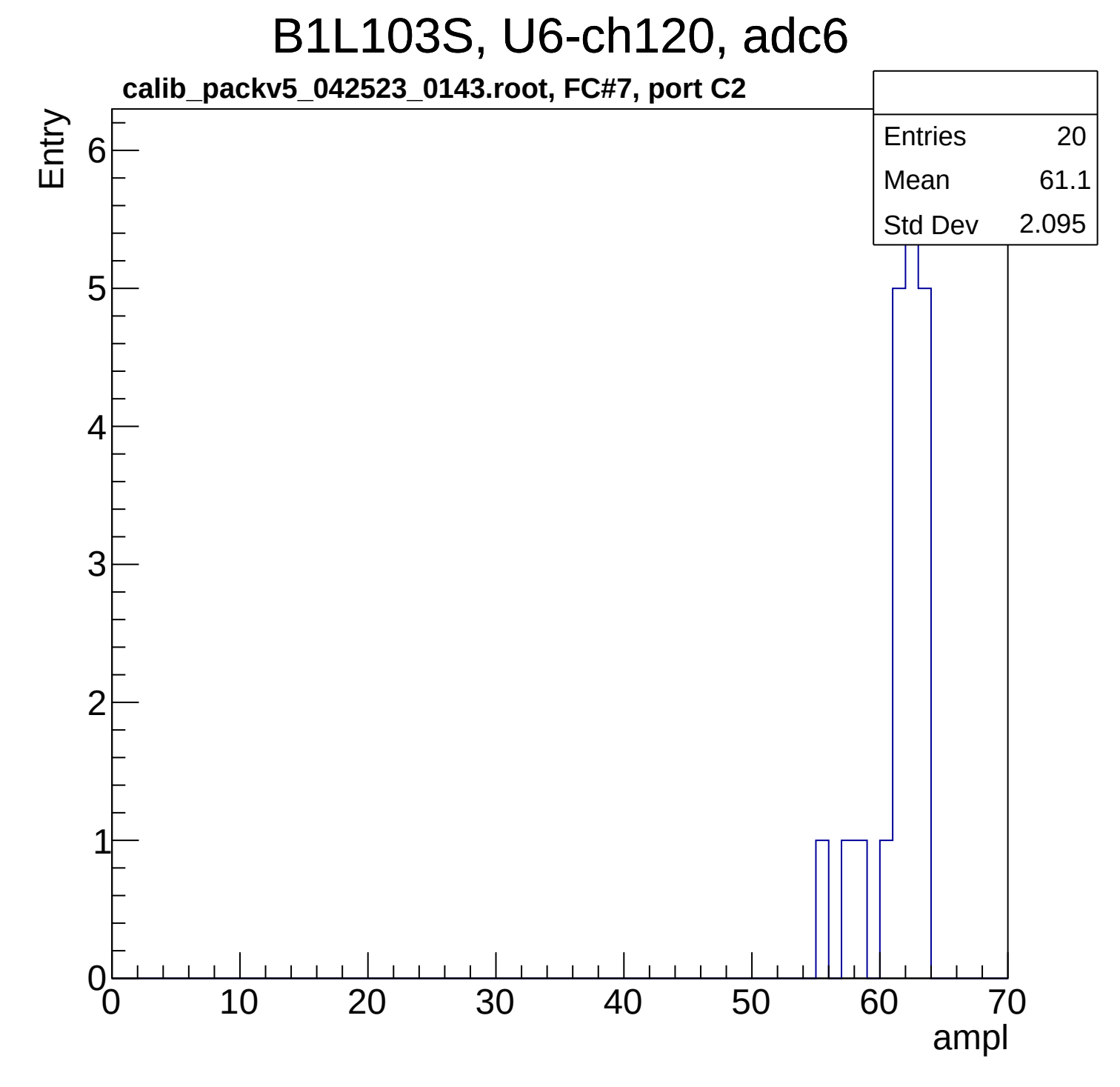
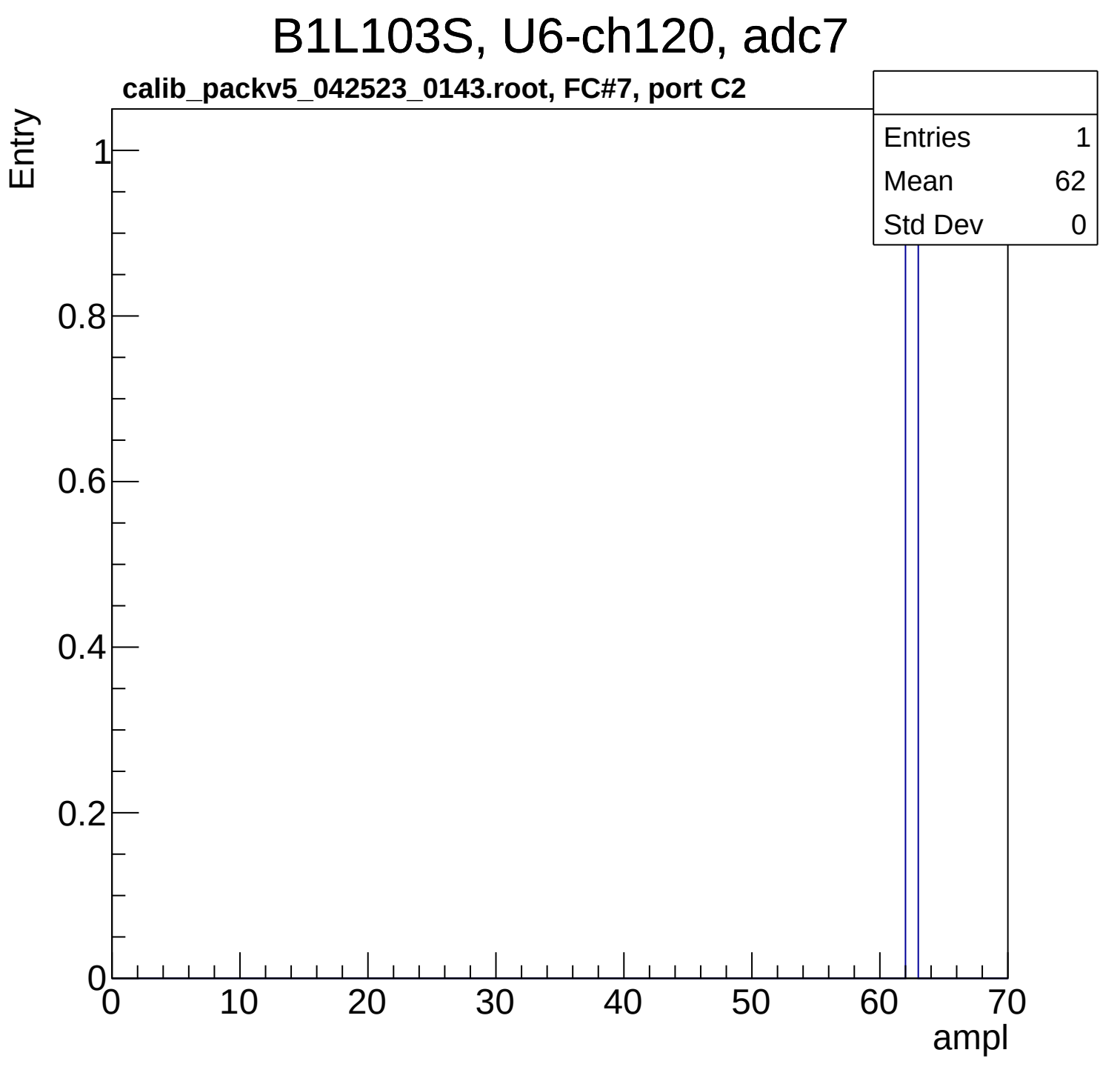
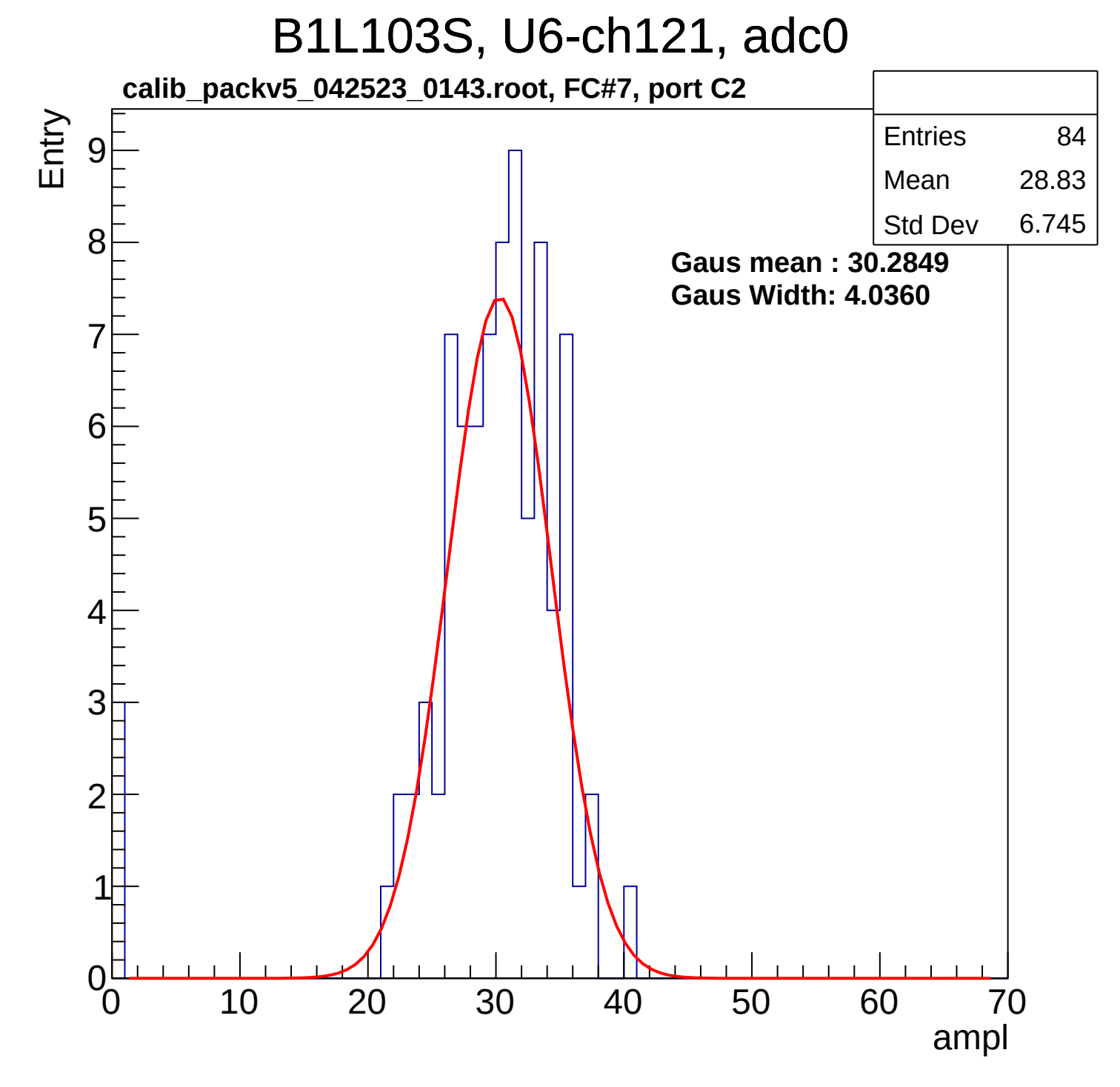
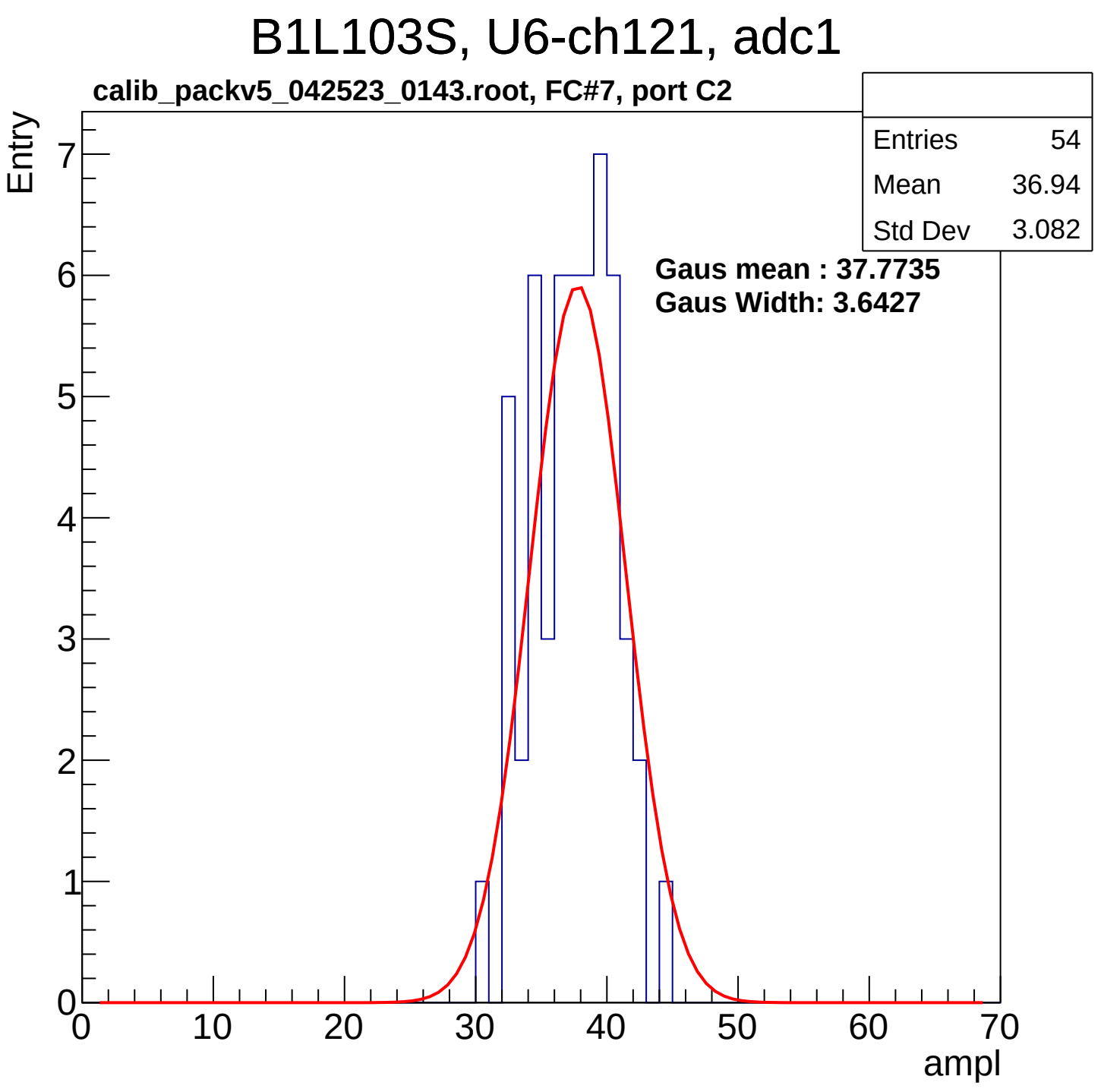
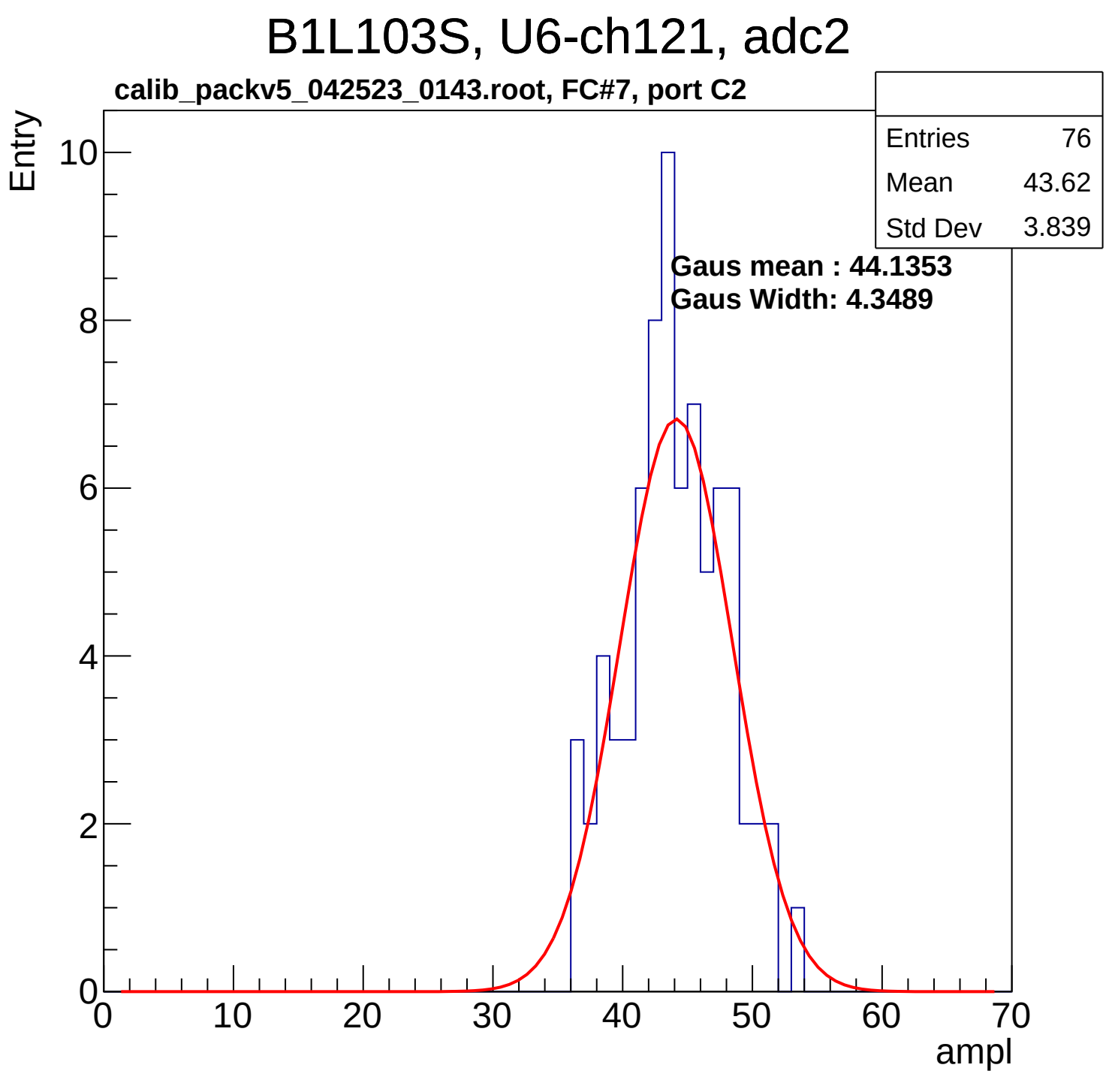
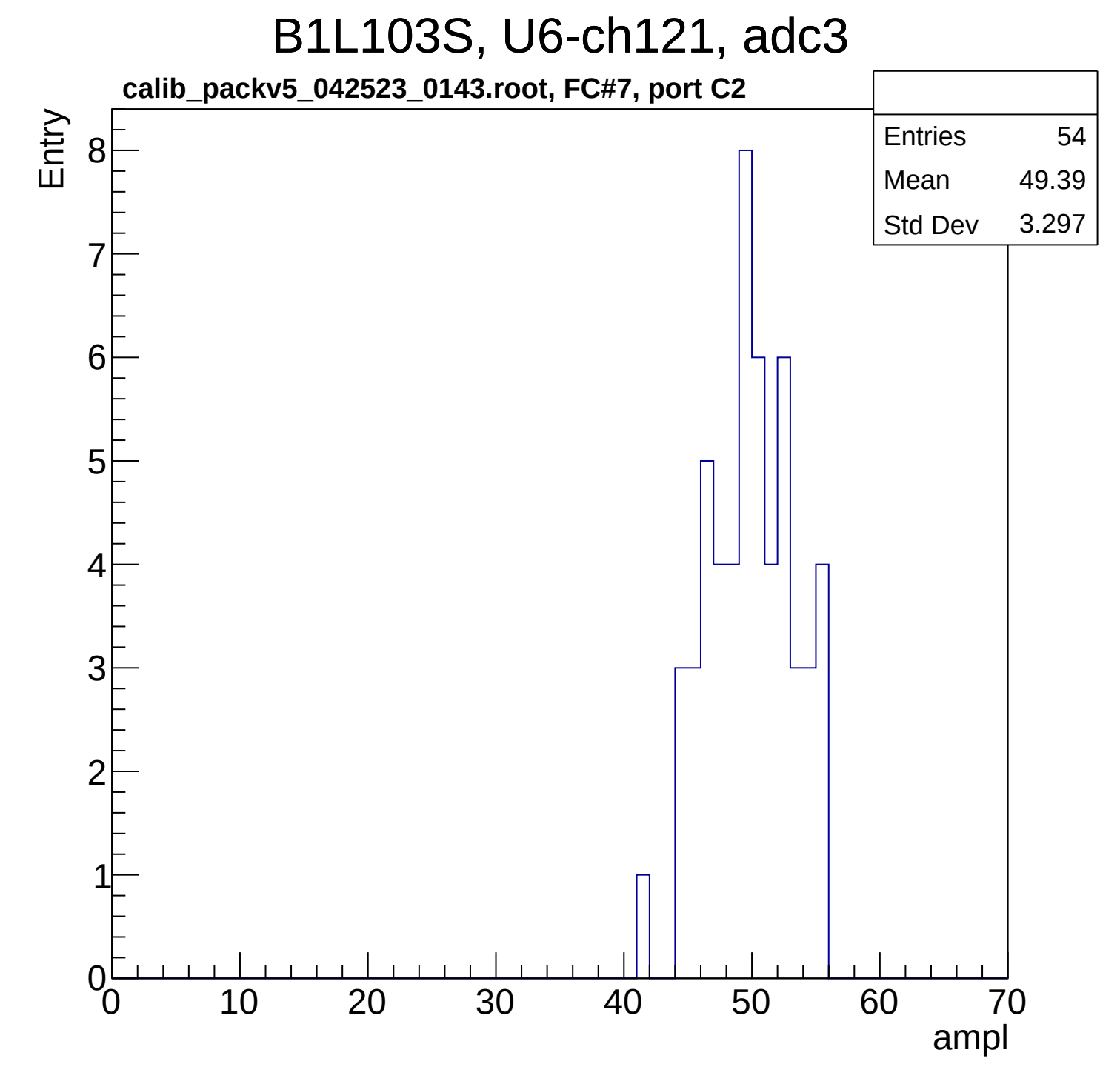

B1L103S, U6-ch120, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 61.1
Std Dev 2.095
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch120, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch121, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 28.83
Std Dev 6.745
Gaus mean : 30.2849
Gaus Width: 4.0360
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch121, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 36.94
Std Dev 3.082
Gaus mean : 37.7735
Gaus Width: 3.6427
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch121, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 43.62
Std Dev 3.839
Gaus mean : 44.1353
Gaus Width: 4.3489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch121, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.39
Std Dev 3.297
Entry
ampl
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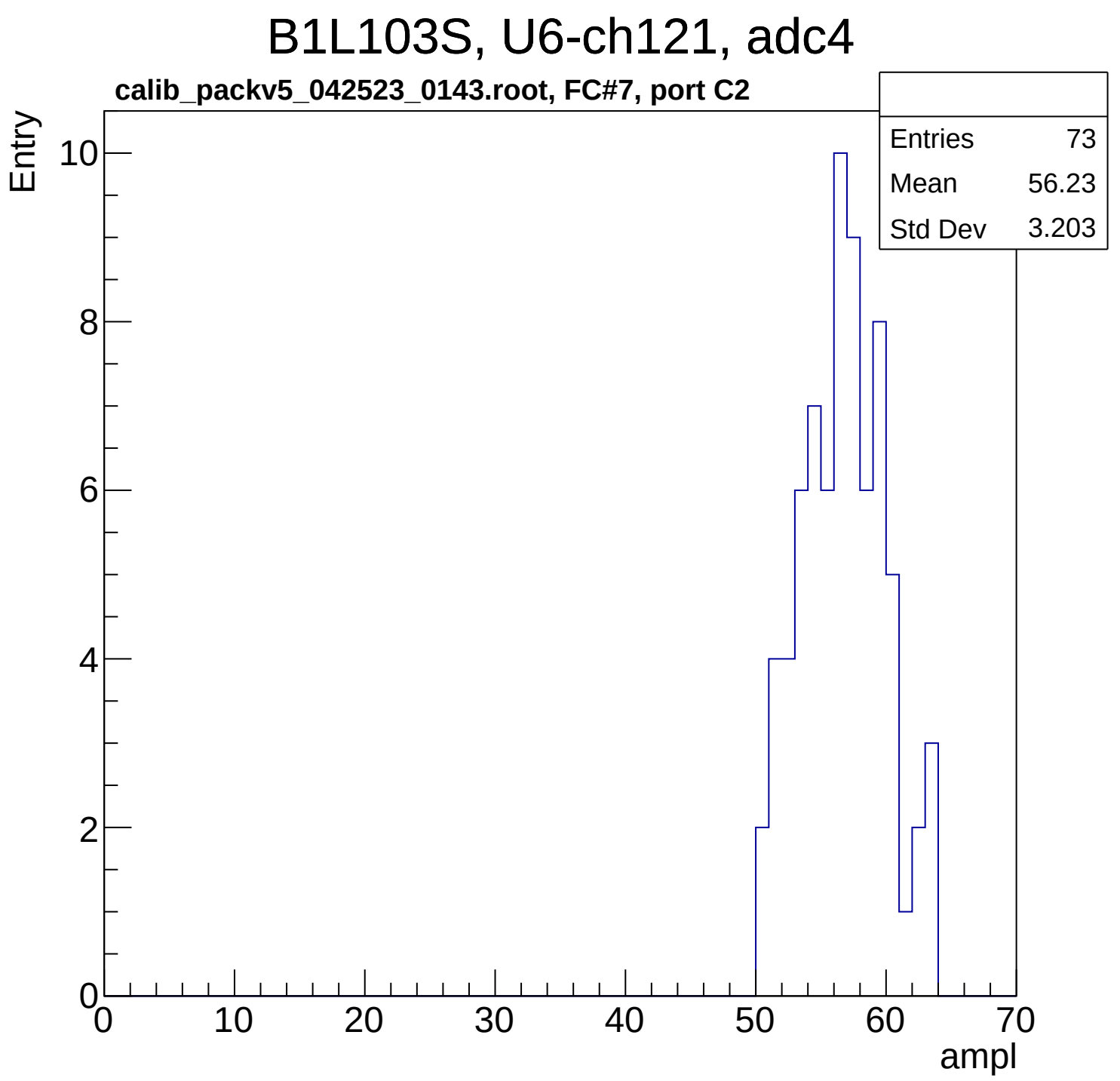
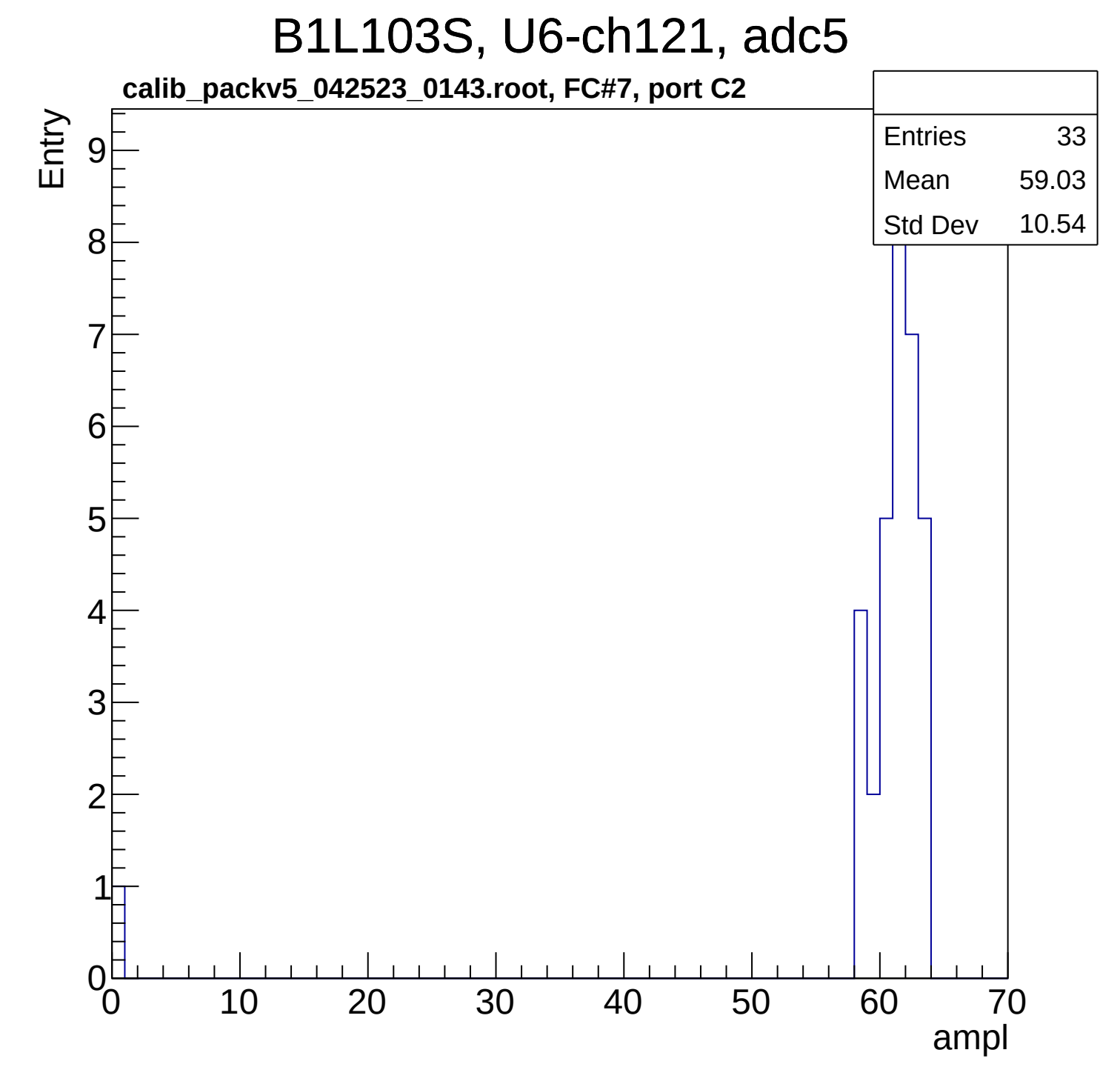
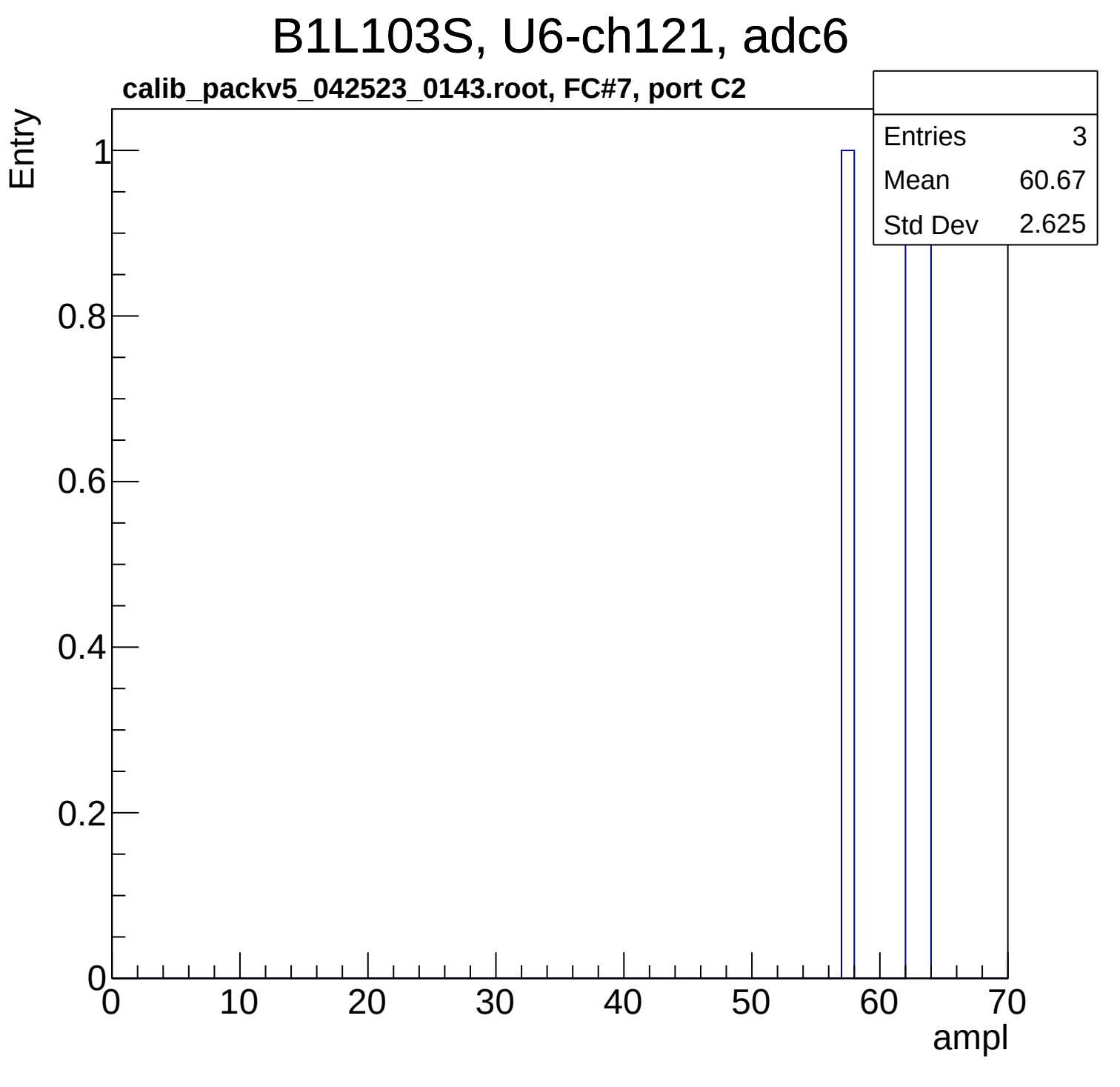
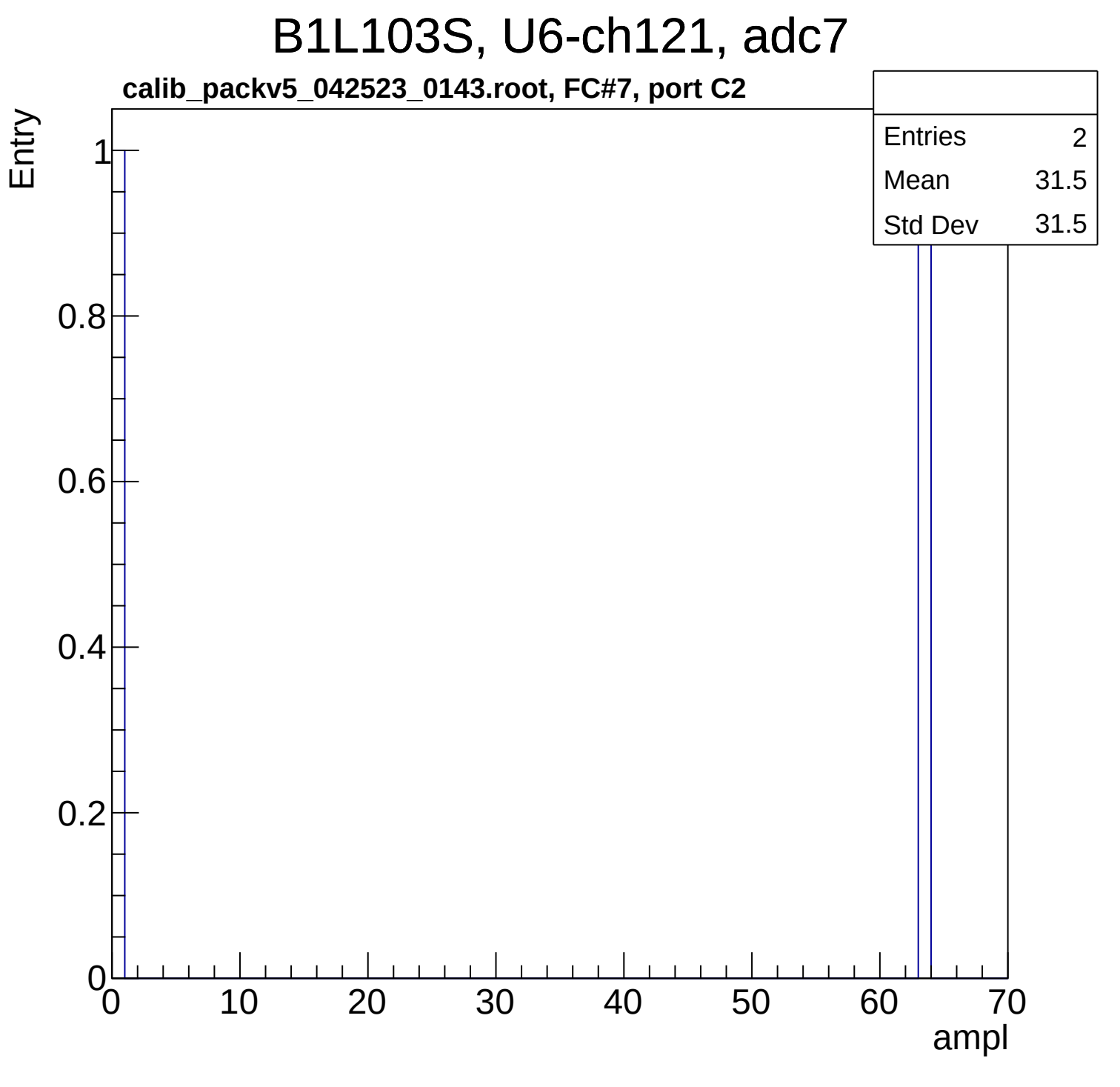
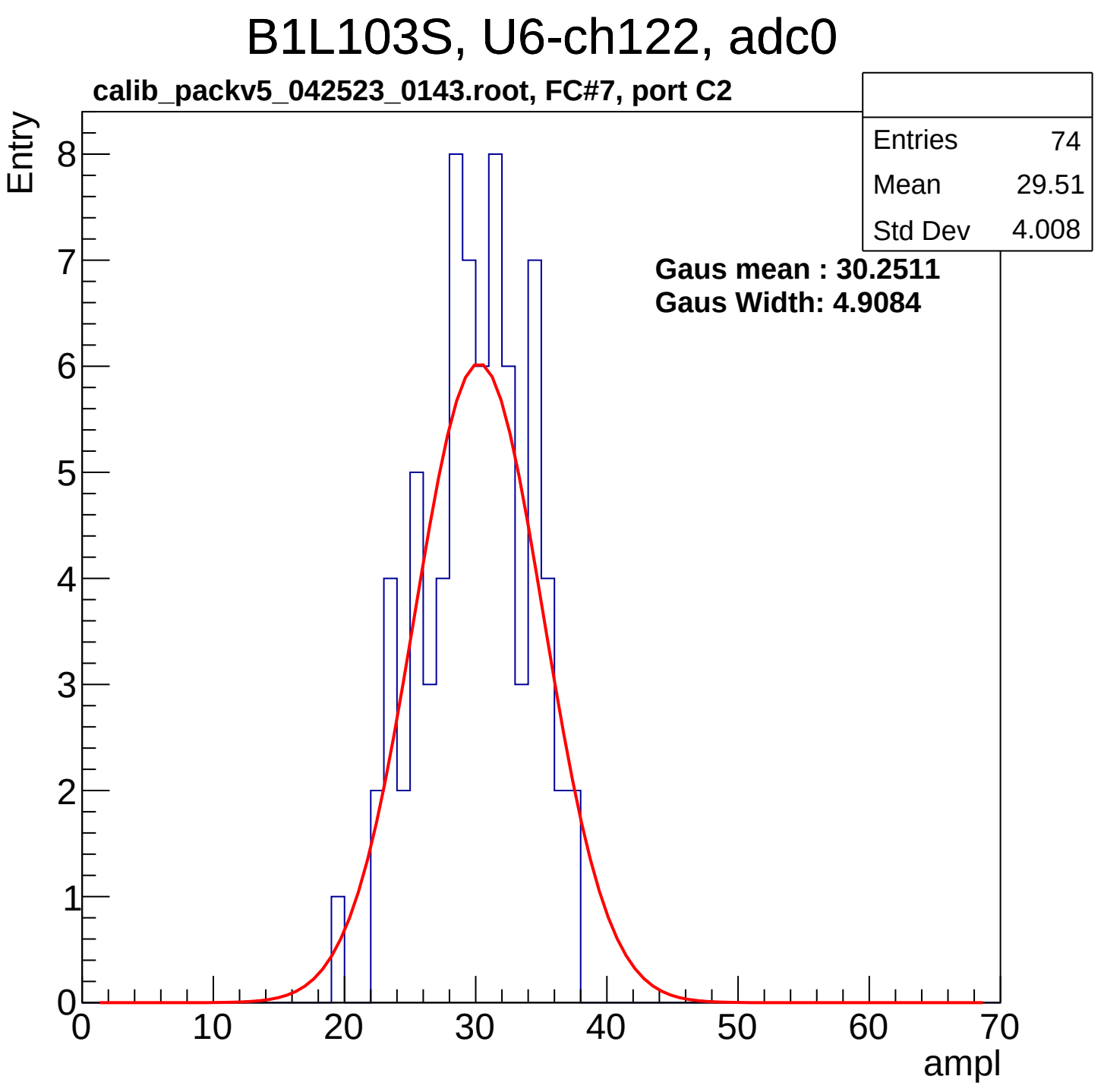
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

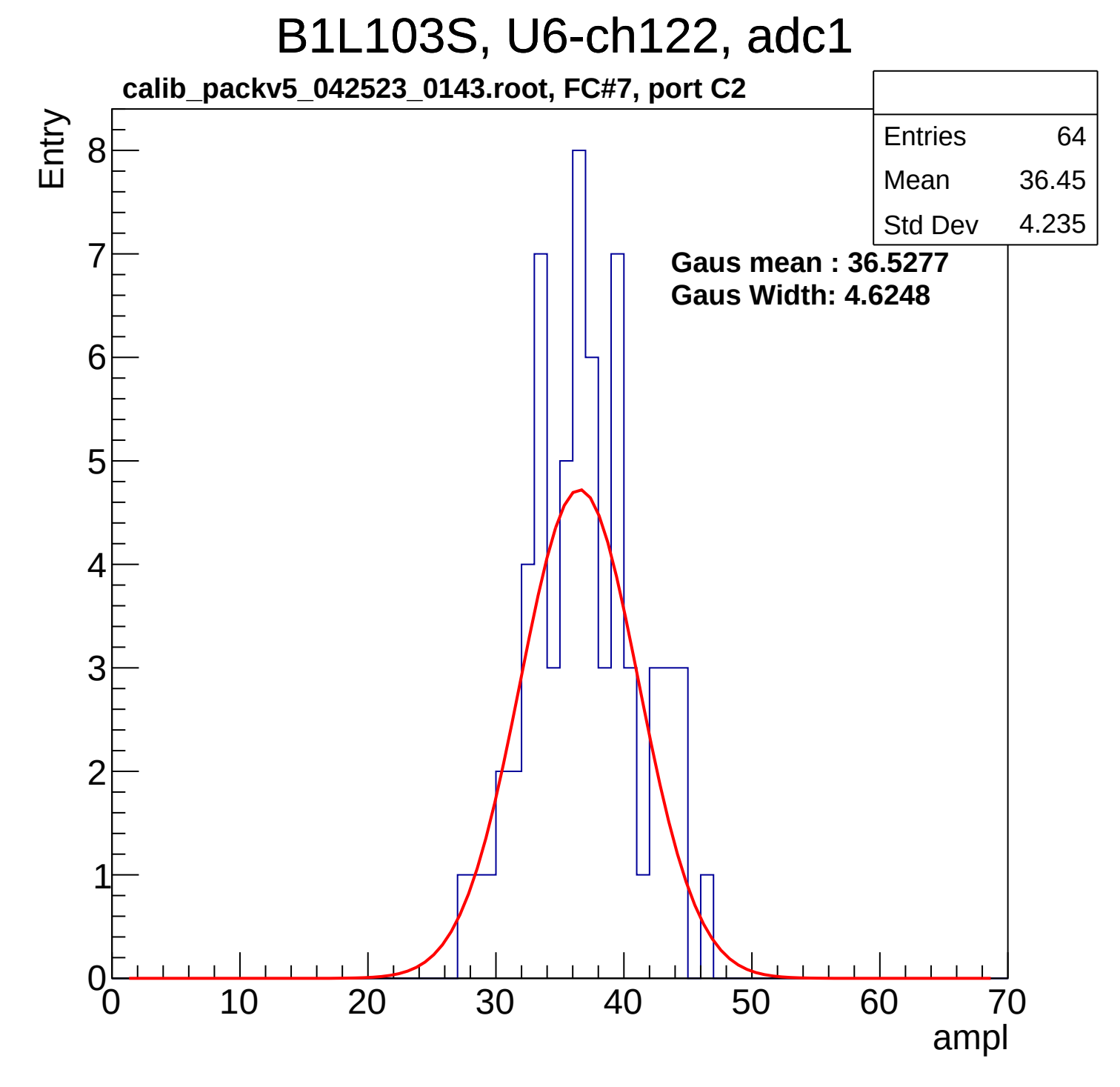

B1L103S, U6-ch121, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 56.23
Std Dev 3.203
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch121, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 59.03
Std Dev 10.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch121, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 60.67
Std Dev 2.625
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch121, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch122, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.51
Std Dev 4.008
Gaus mean : 30.2511
Gaus Width: 4.9084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch122, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 36.45
Std Dev 4.235
Gaus mean : 36.5277
Gaus Width: 4.6248
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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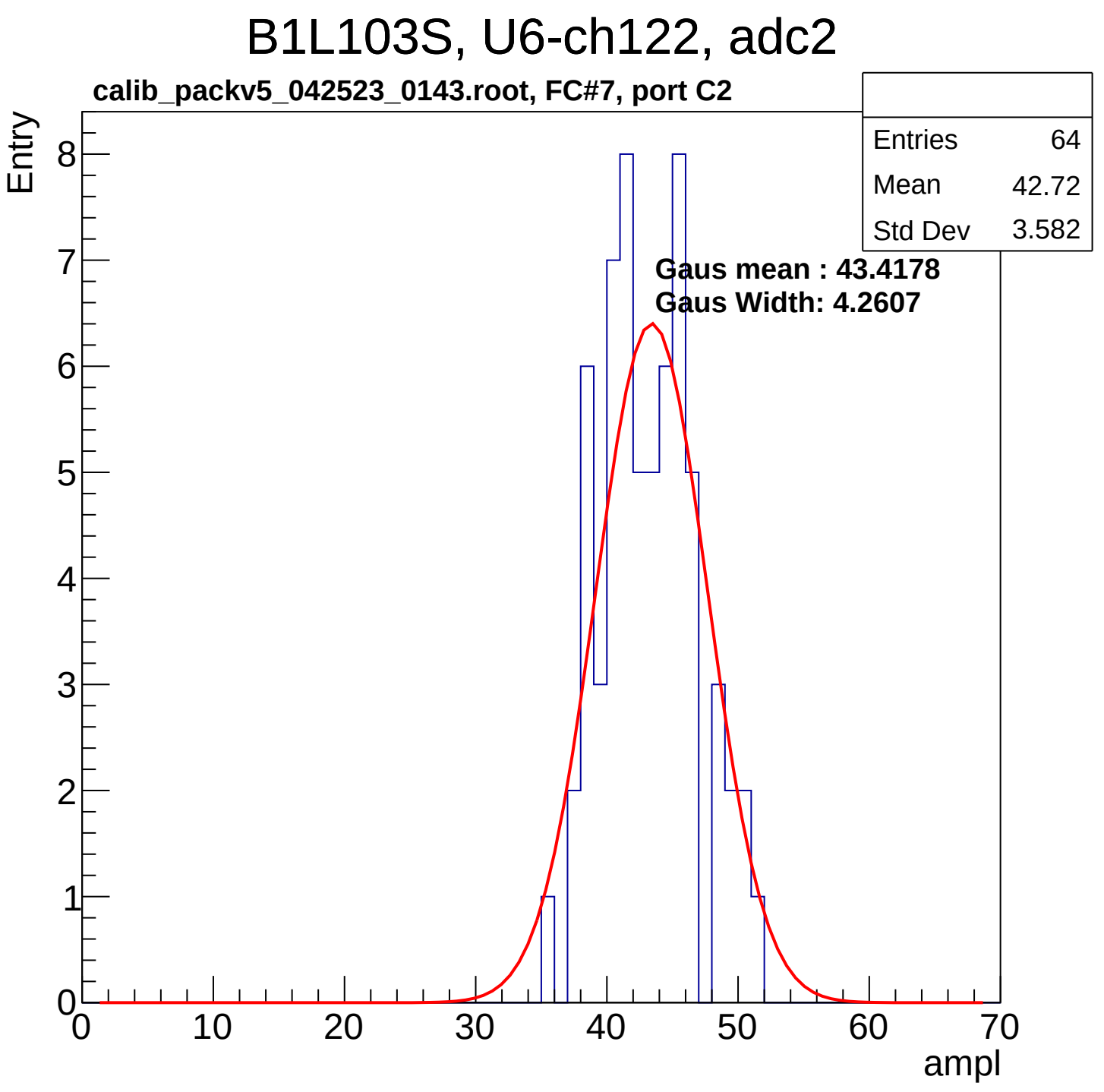
B1L103S, U6-ch122, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 42.72
Std Dev 3.582
Gaus mean : 43.4178
Gaus Width: 4.2607
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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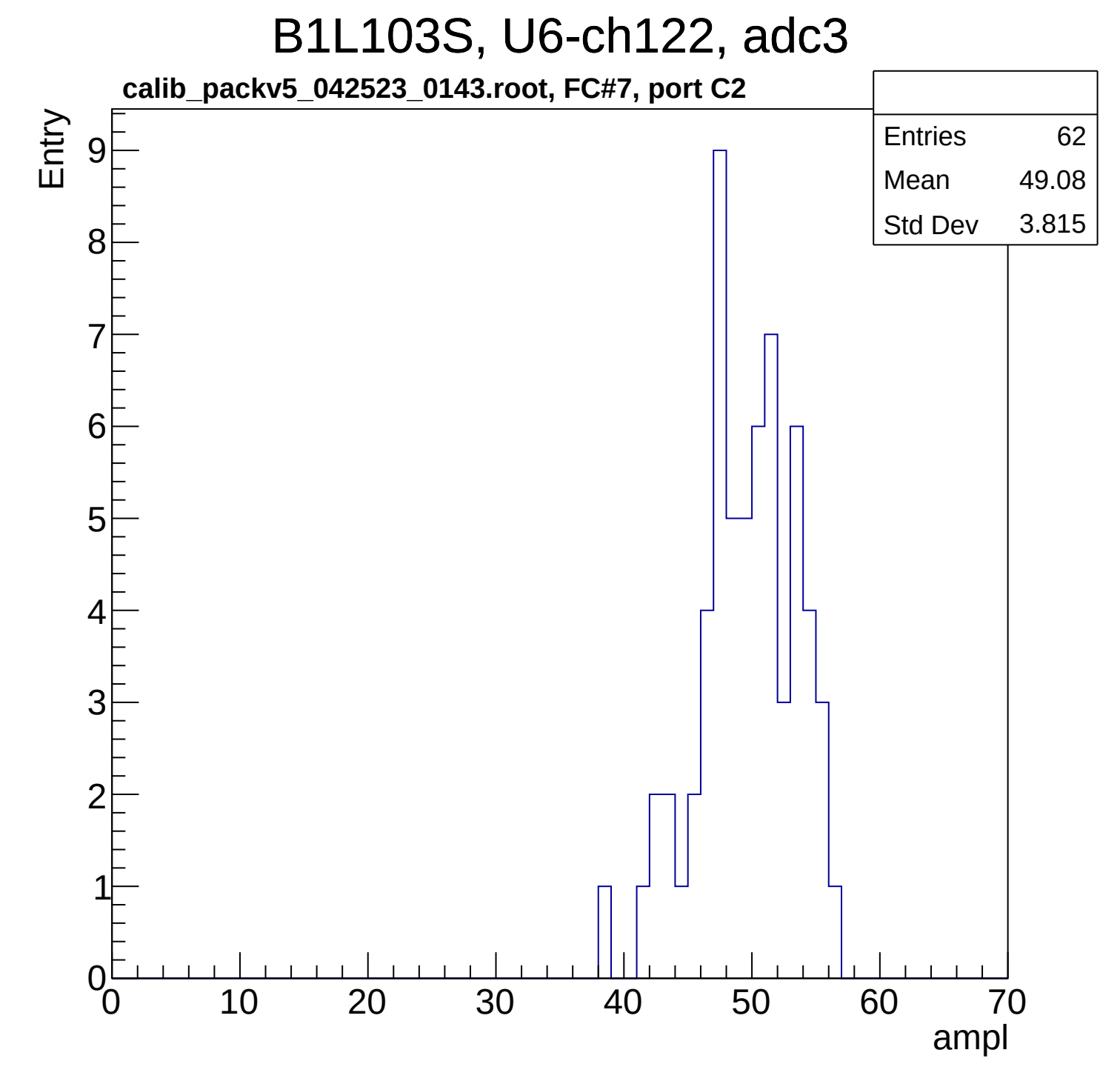
B1L103S, U6-ch122, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.08
Std Dev 3.815
Entry
ampl
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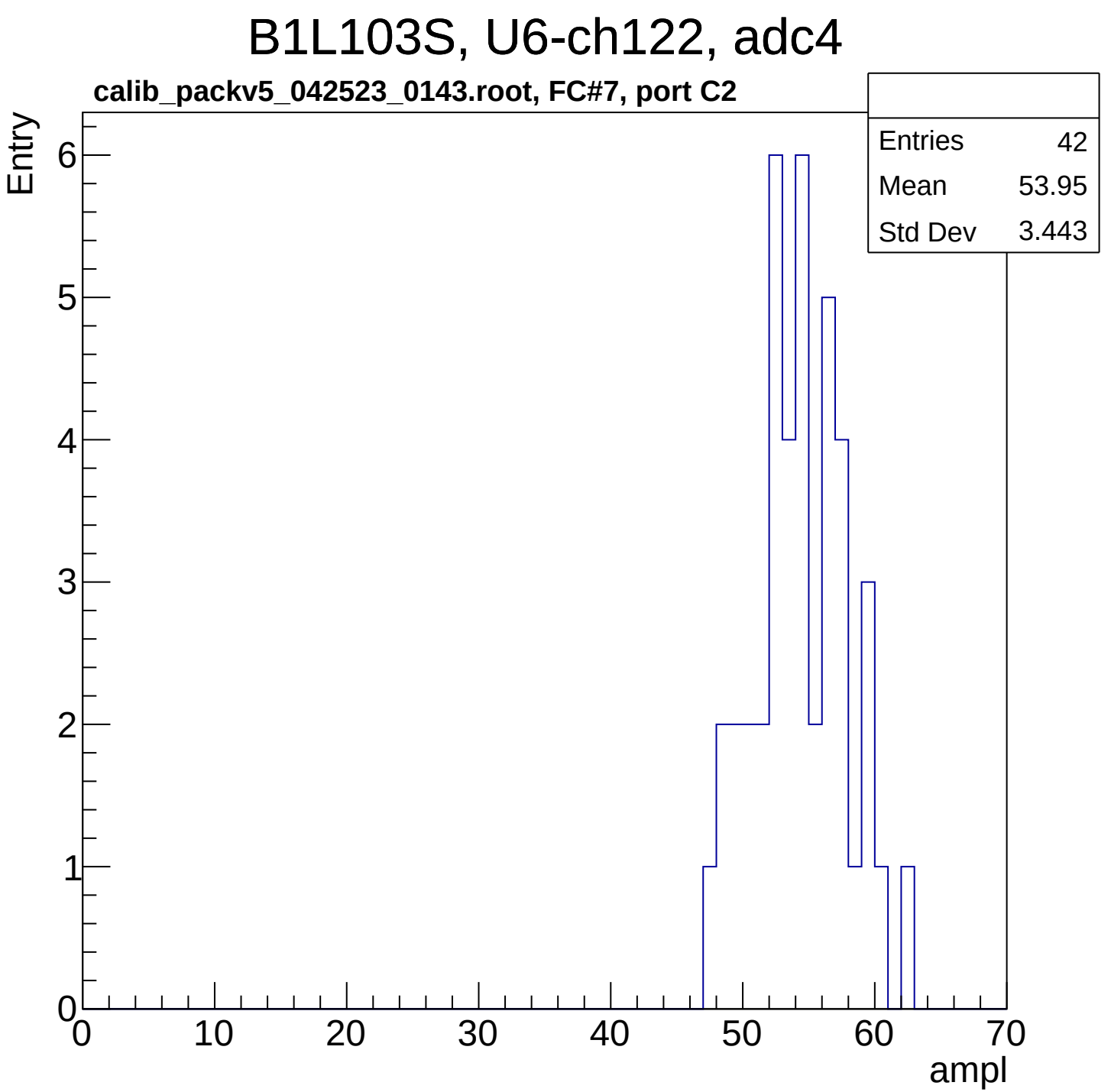
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch122, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 53.95
Std Dev 3.443
Entry
0
1
2
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4
5
6
0
10
20
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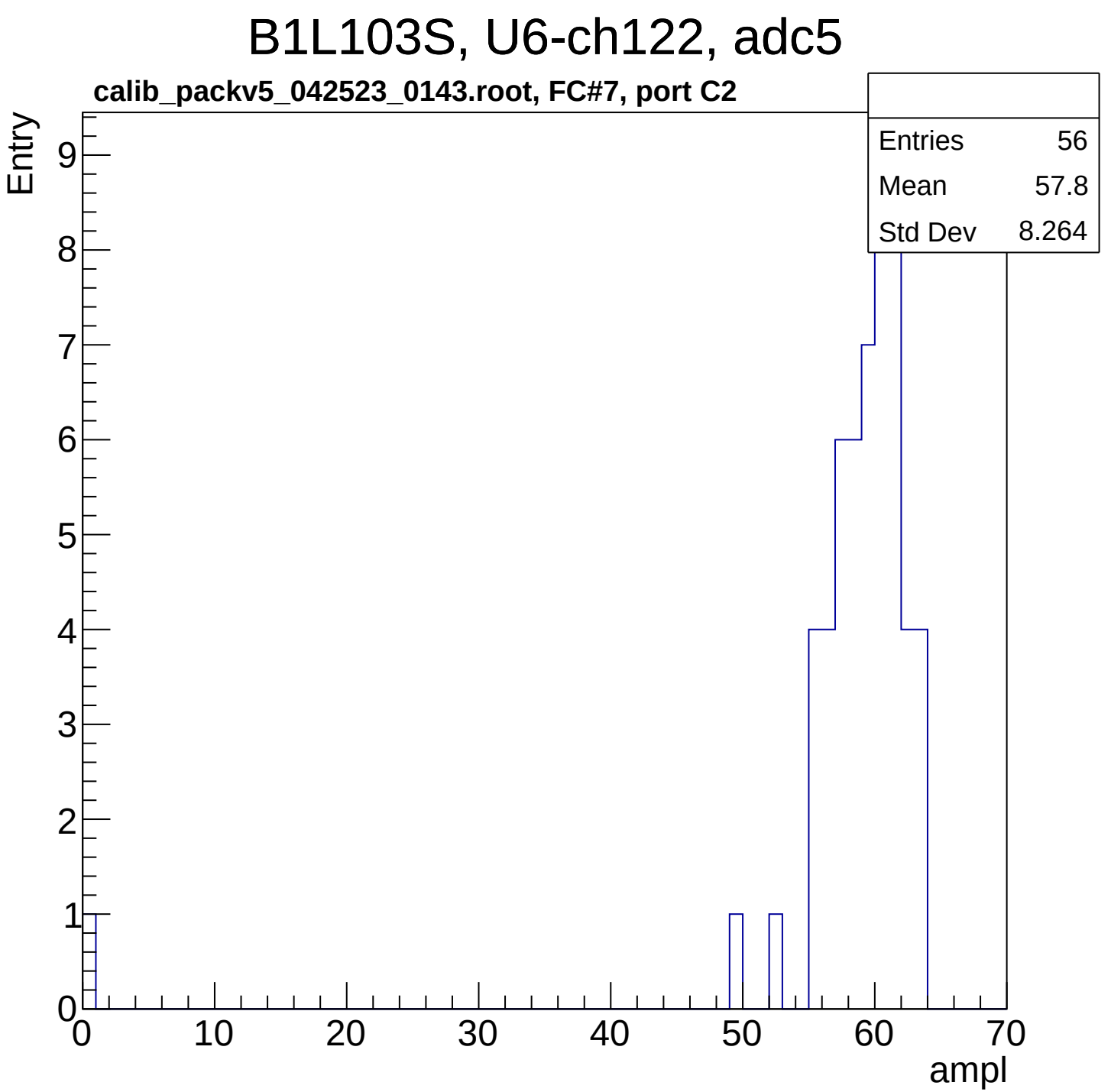
50
60
70
ampl


B1L103S, U6-ch122, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.8
Std Dev 8.264
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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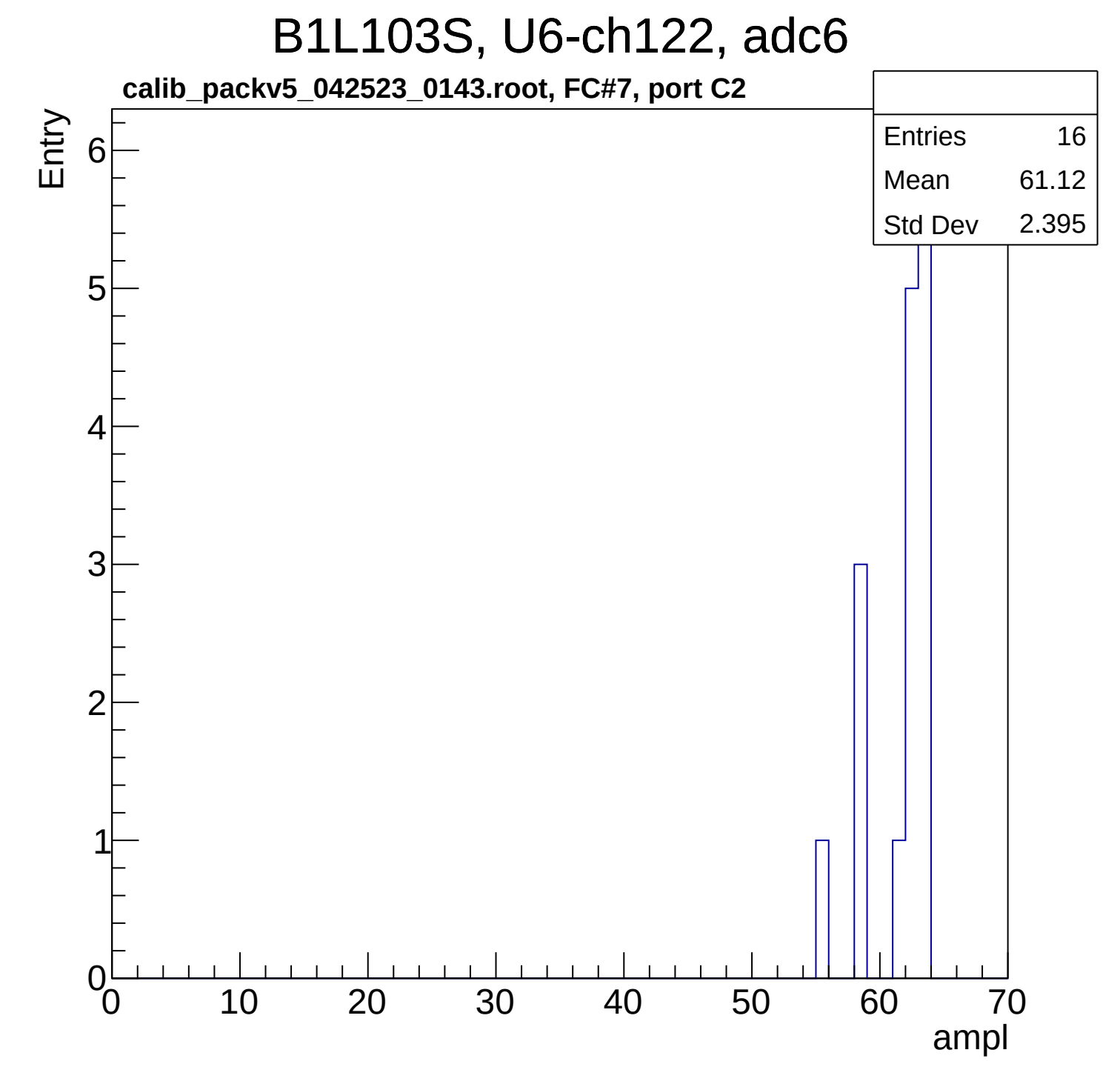
B1L103S, U6-ch122, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.12
Std Dev 2.395
Entry
0
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3
4
5
6
0
10
20
30
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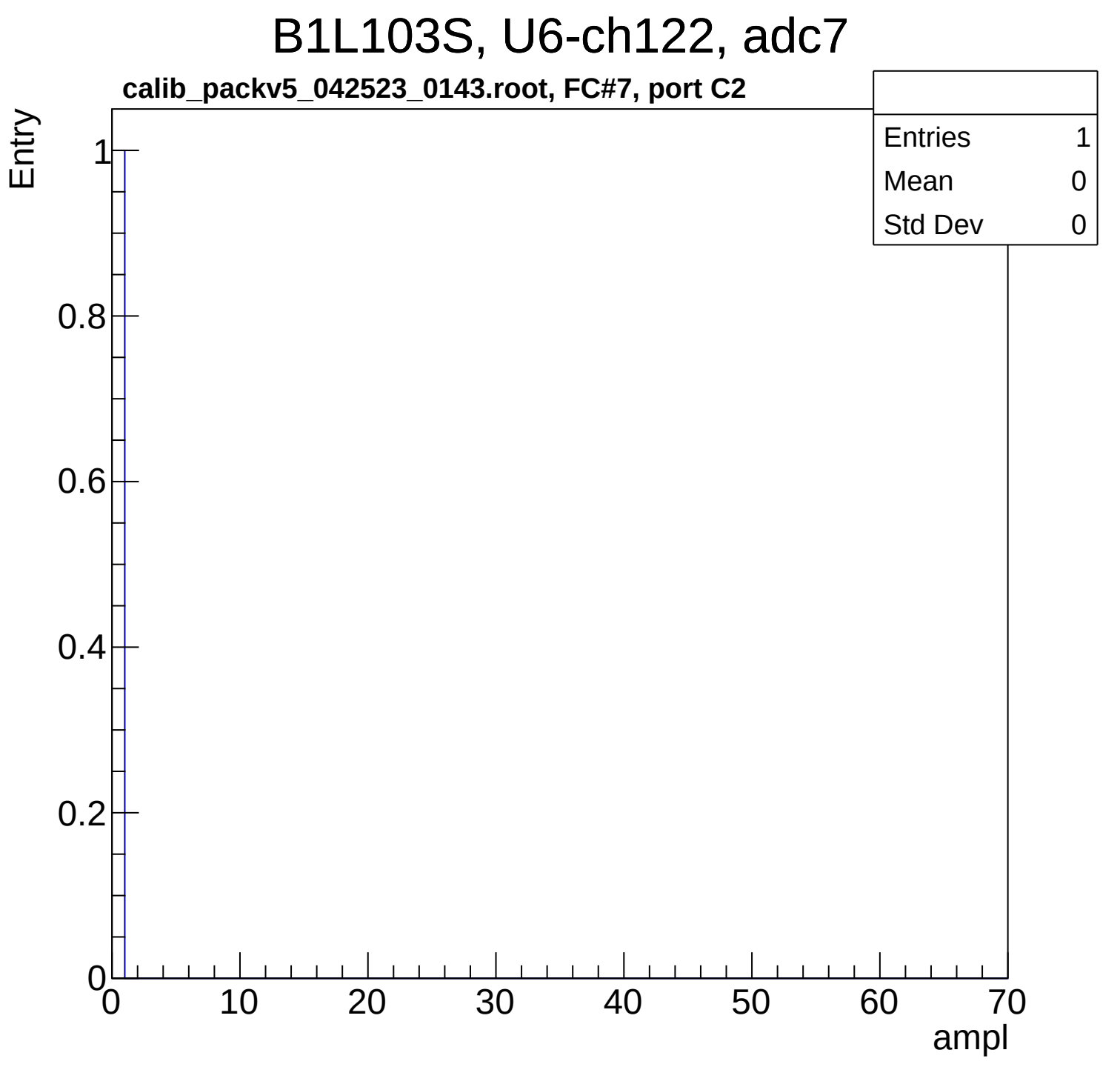
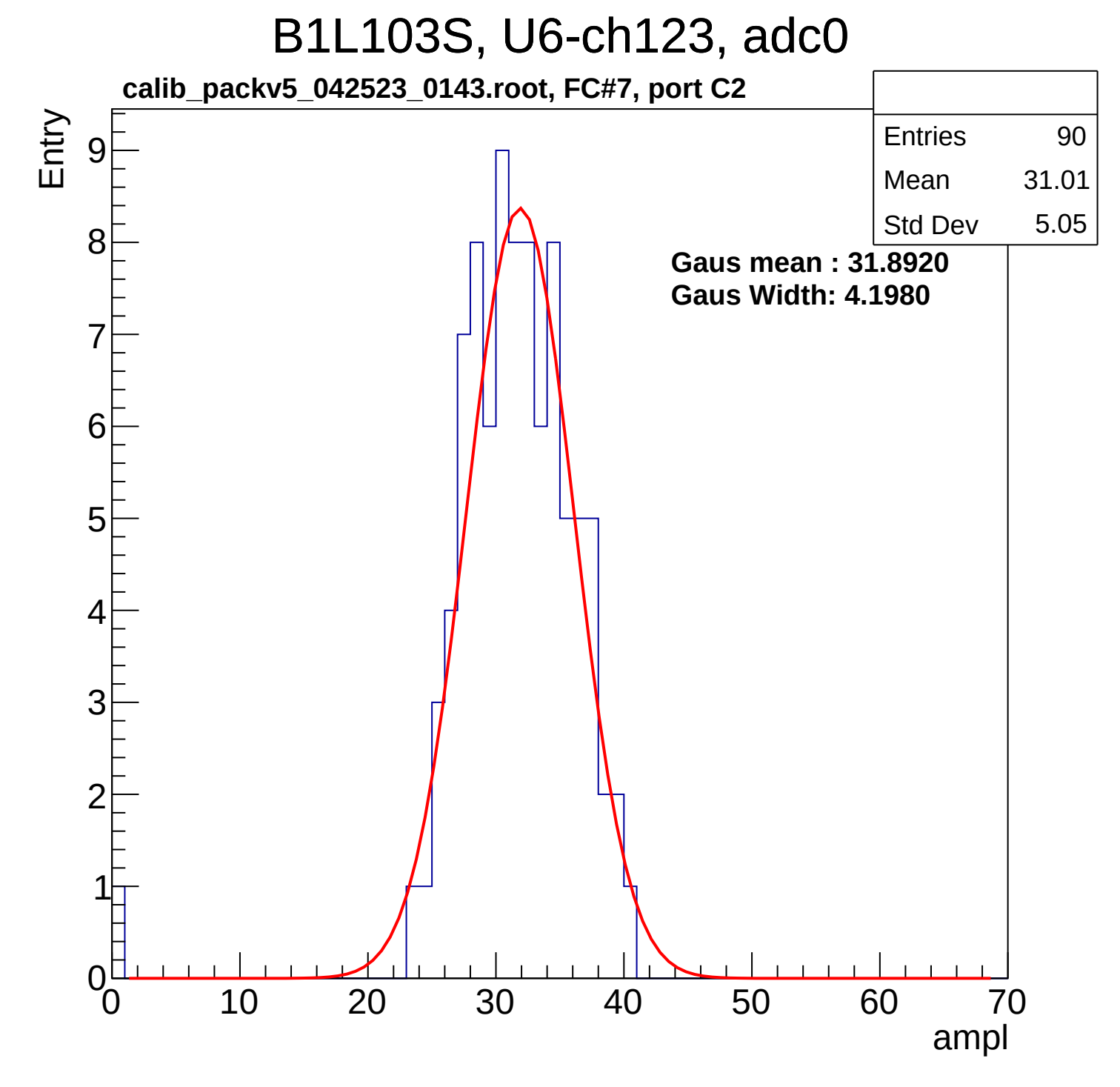
50
60
70
ampl


B1L103S, U6-ch122, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch123, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 31.01
Std Dev 5.05
Gaus mean : 31.8920
Gaus Width: 4.1980
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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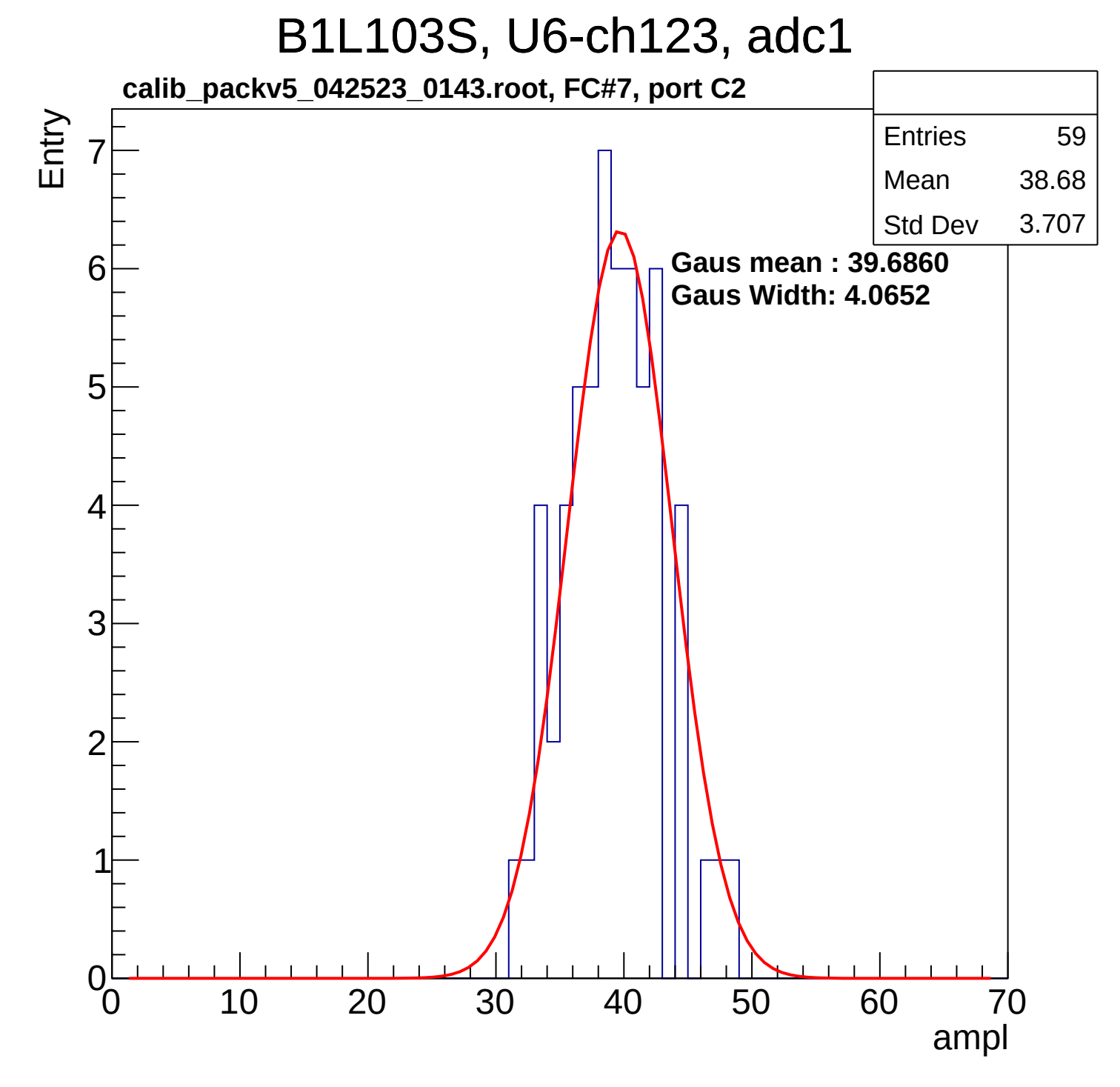
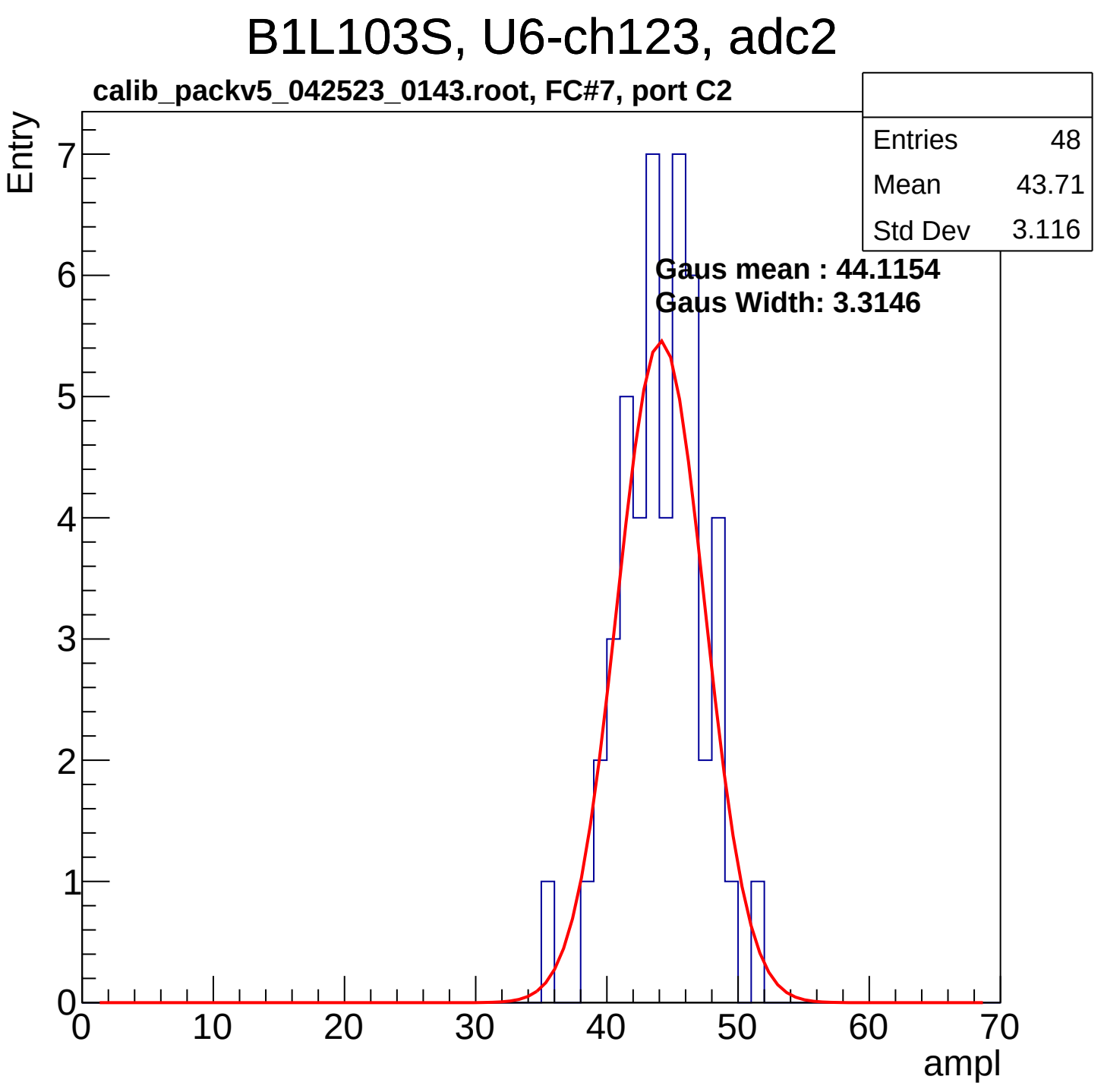
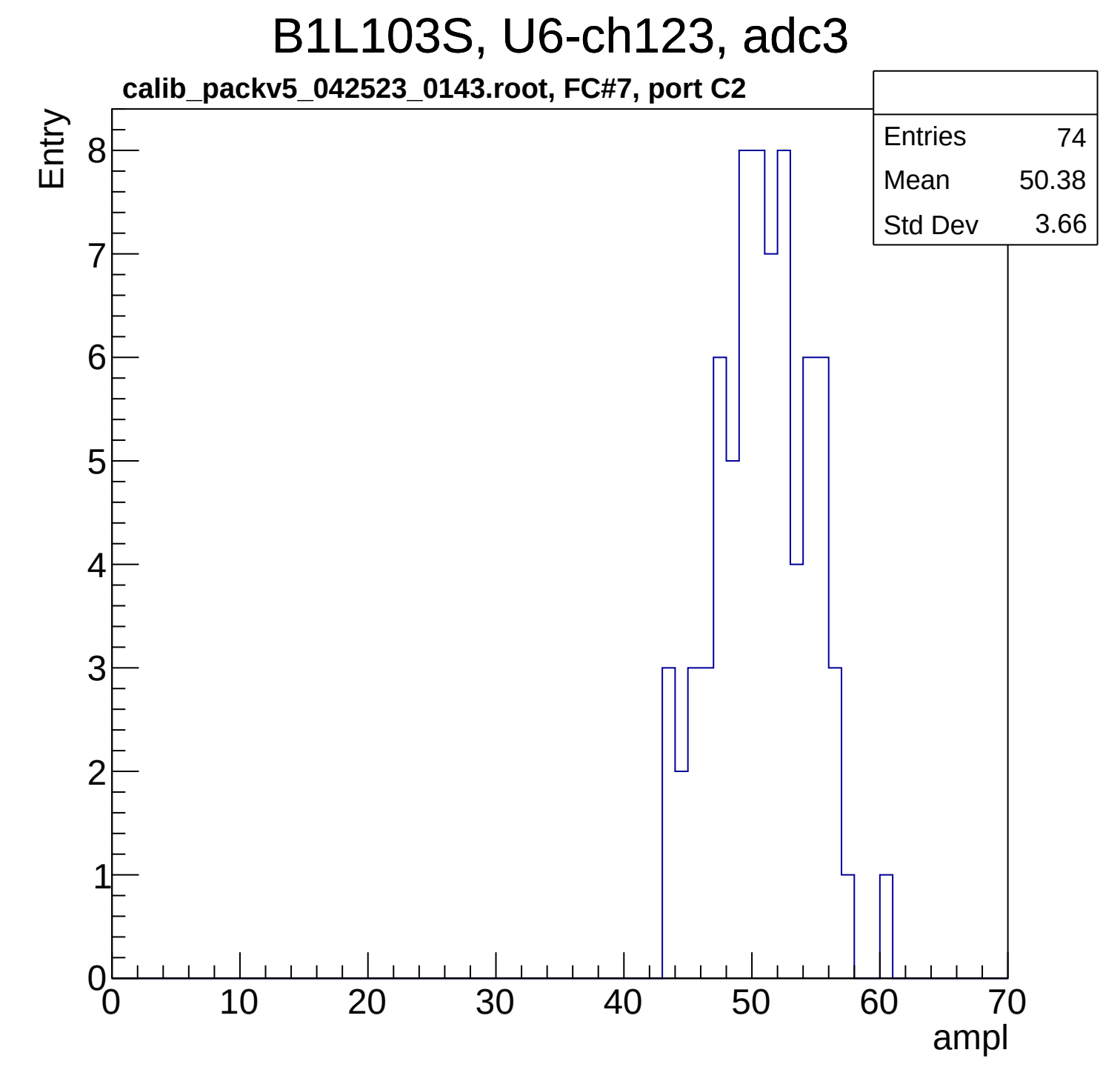

B1L103S, U6-ch123, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 38.68
Std Dev 3.707
Gaus mean : 39.6860
Gaus Width: 4.0652
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch123, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 43.71
Std Dev 3.116
Gaus mean : 44.1154
Gaus Width: 3.3146
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch123, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 50.38
Std Dev 3.66
Entry
ampl
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5
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0
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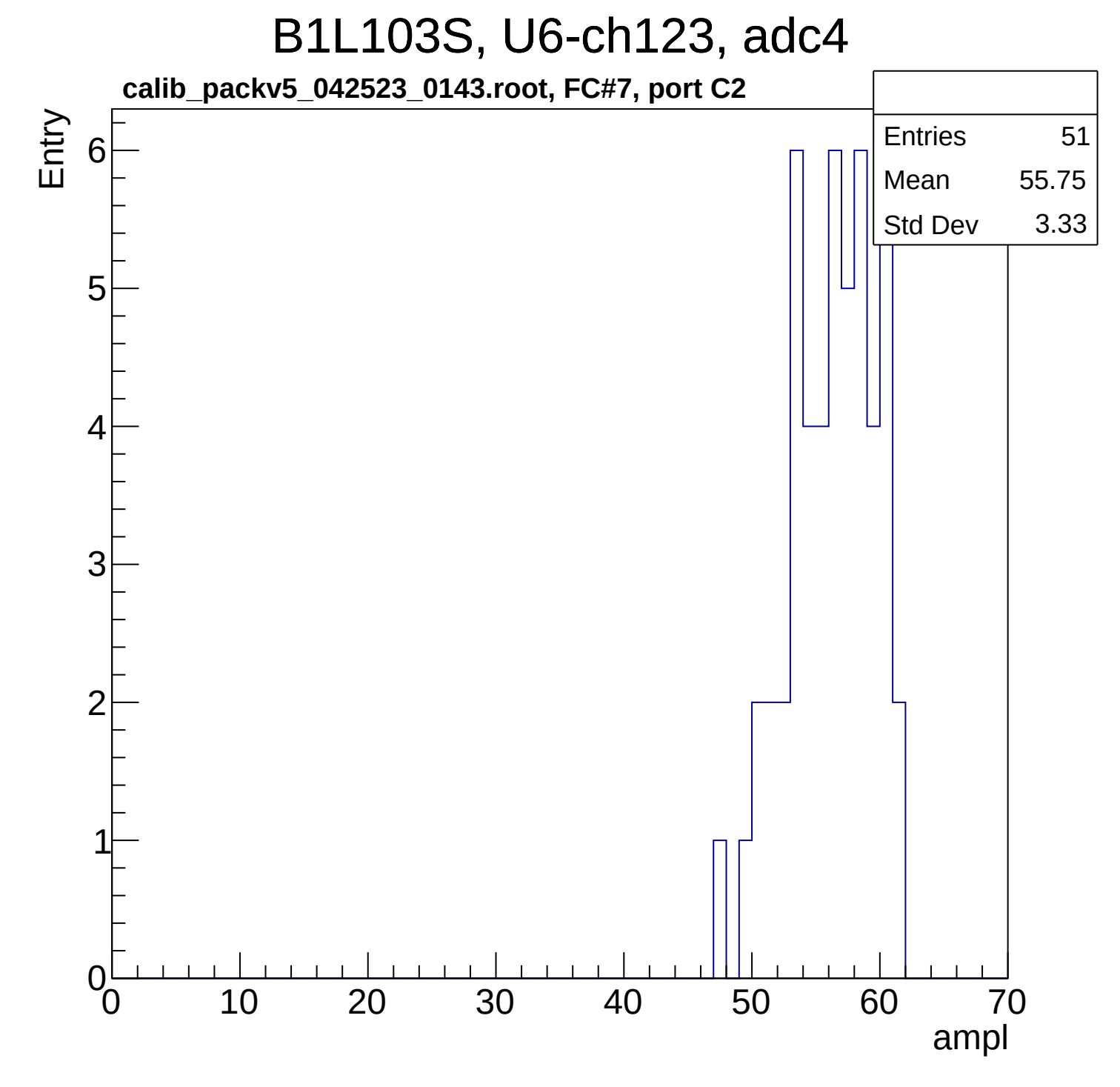
B1L103S, U6-ch123, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.75
Std Dev 3.33
Entry
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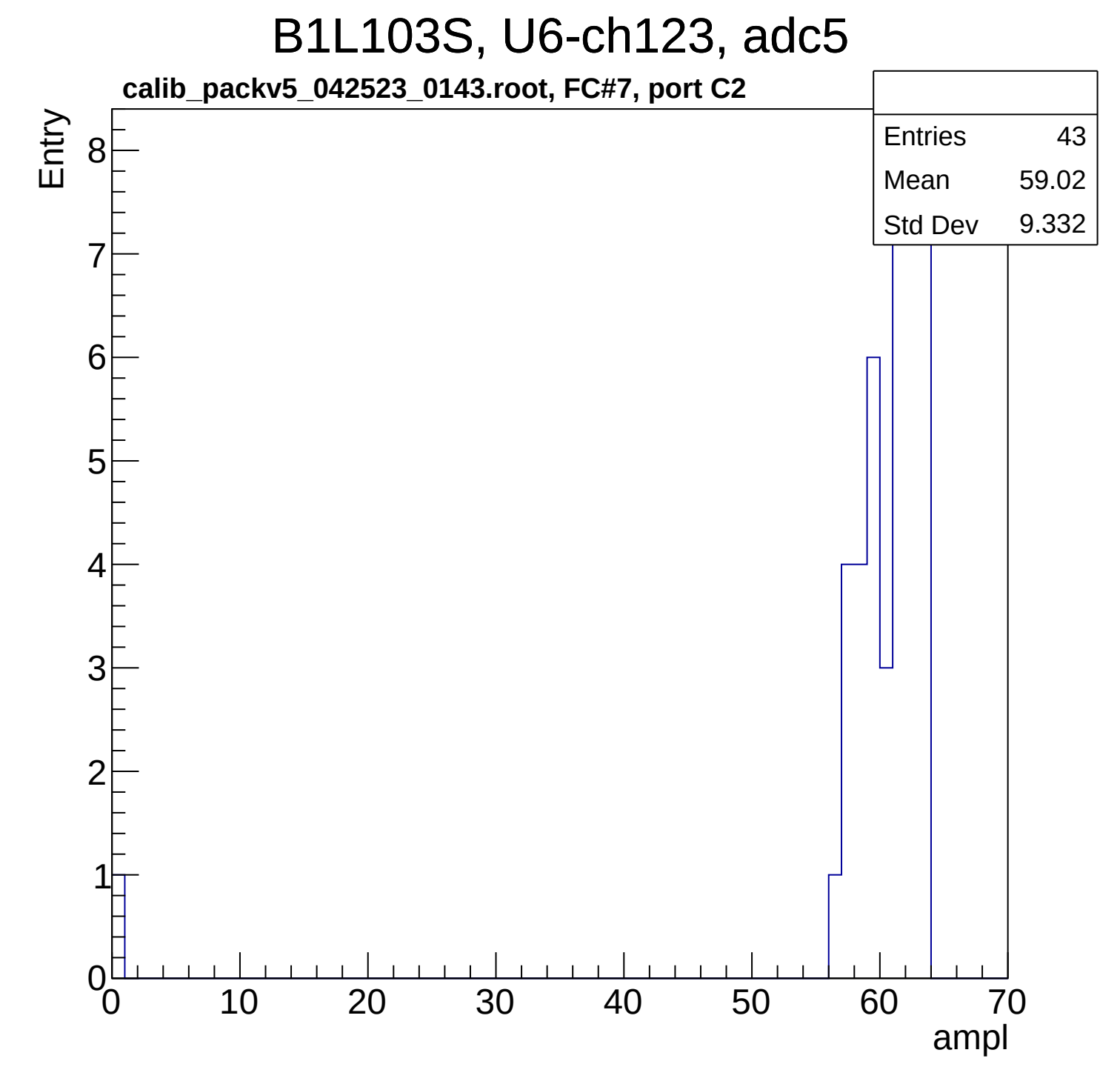
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch123, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.02
Std Dev 9.332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
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60
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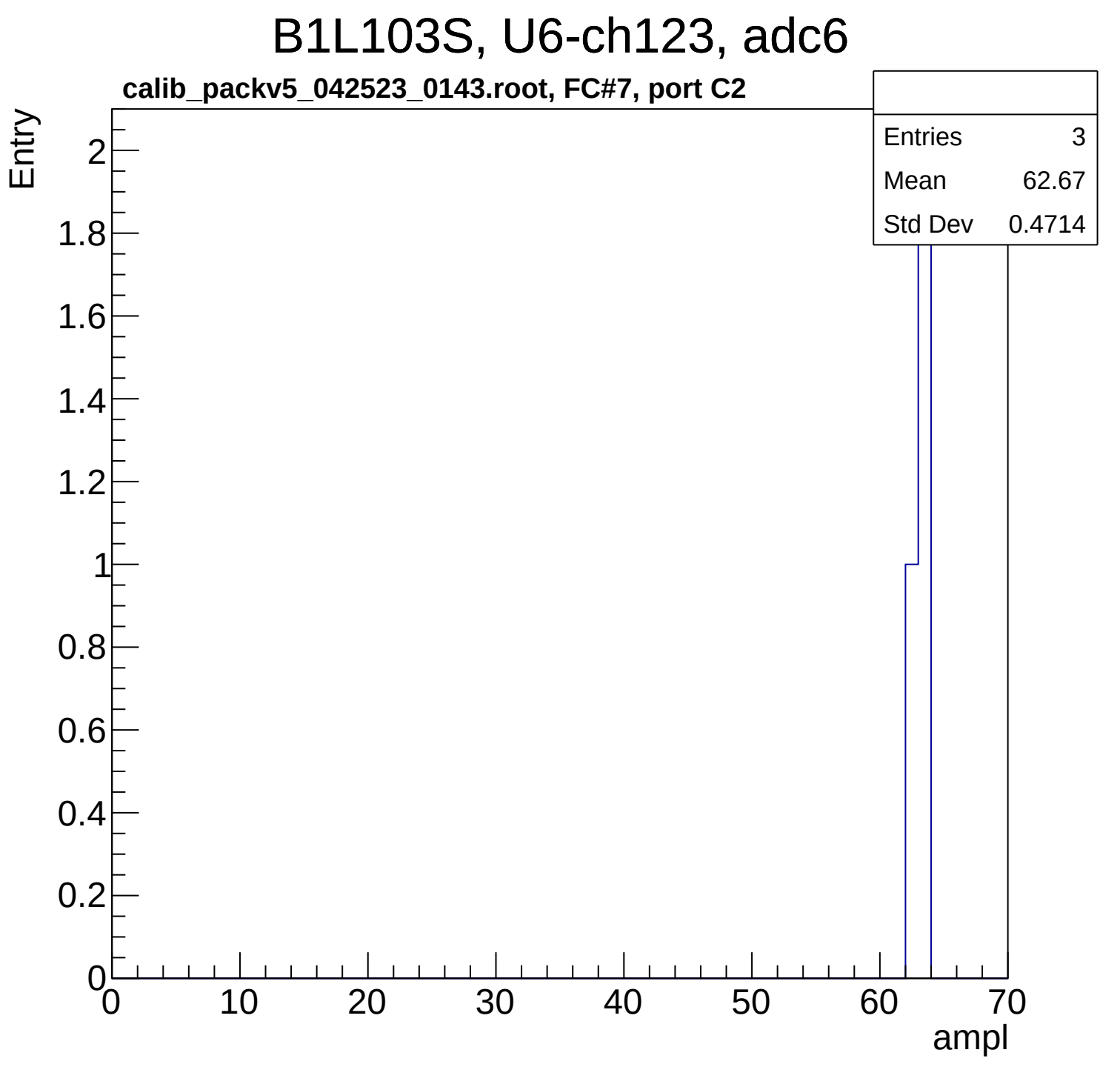
B1L103S, U6-ch123, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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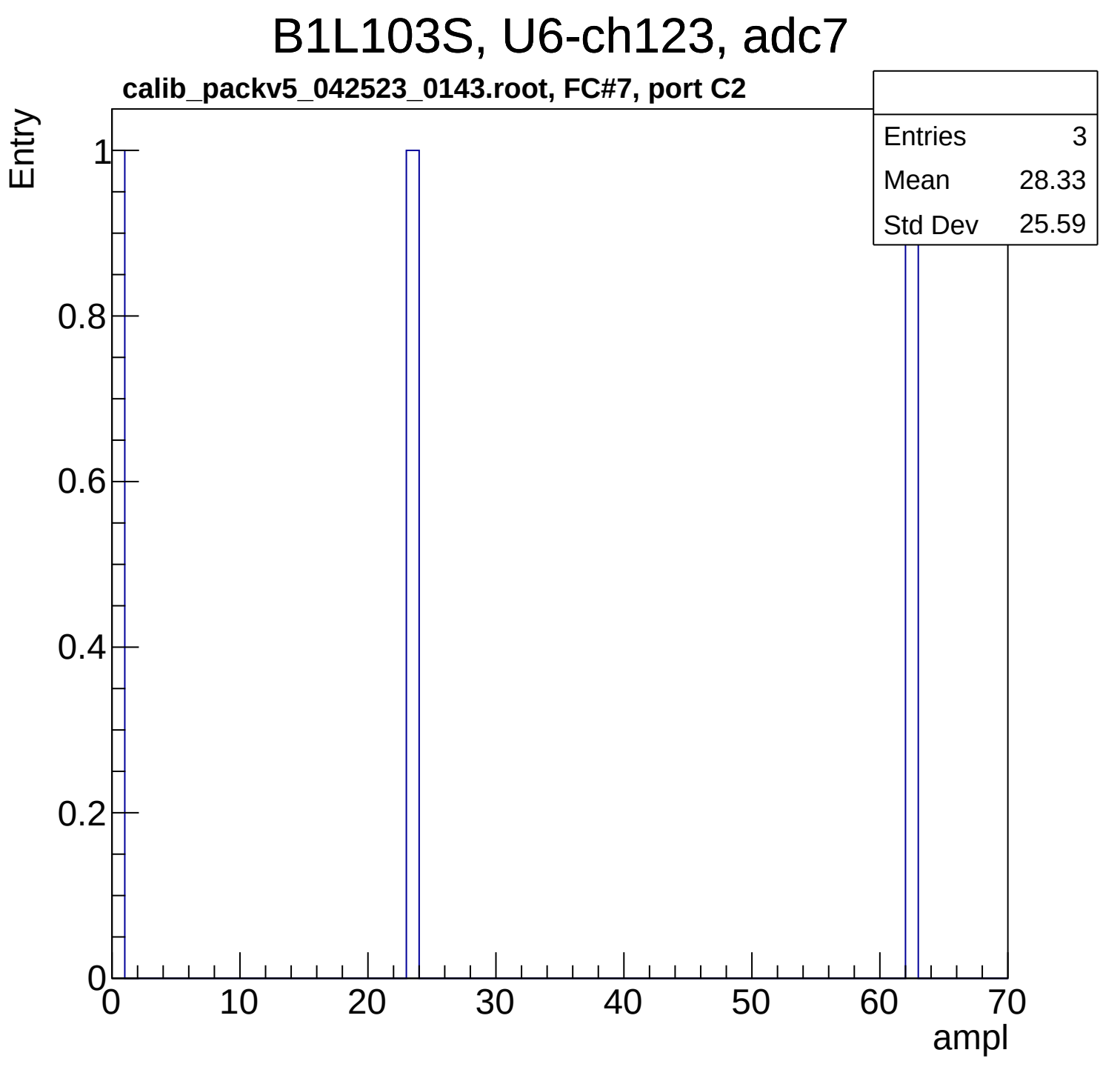
B1L103S, U6-ch123, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 28.33
Std Dev 25.59
Entry
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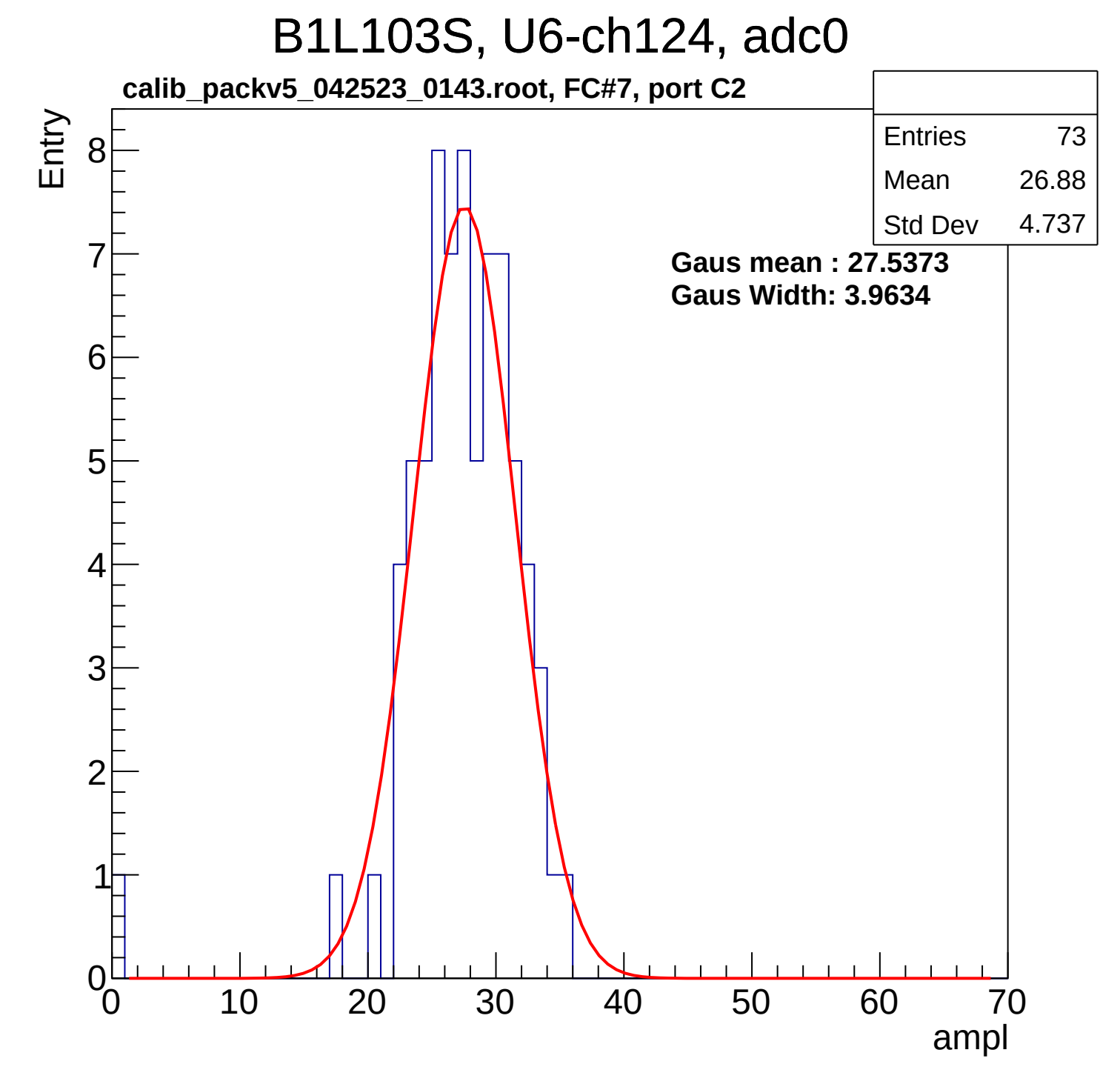
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch124, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 26.88
Std Dev 4.737
Gaus mean : 27.5373
Gaus Width: 3.9634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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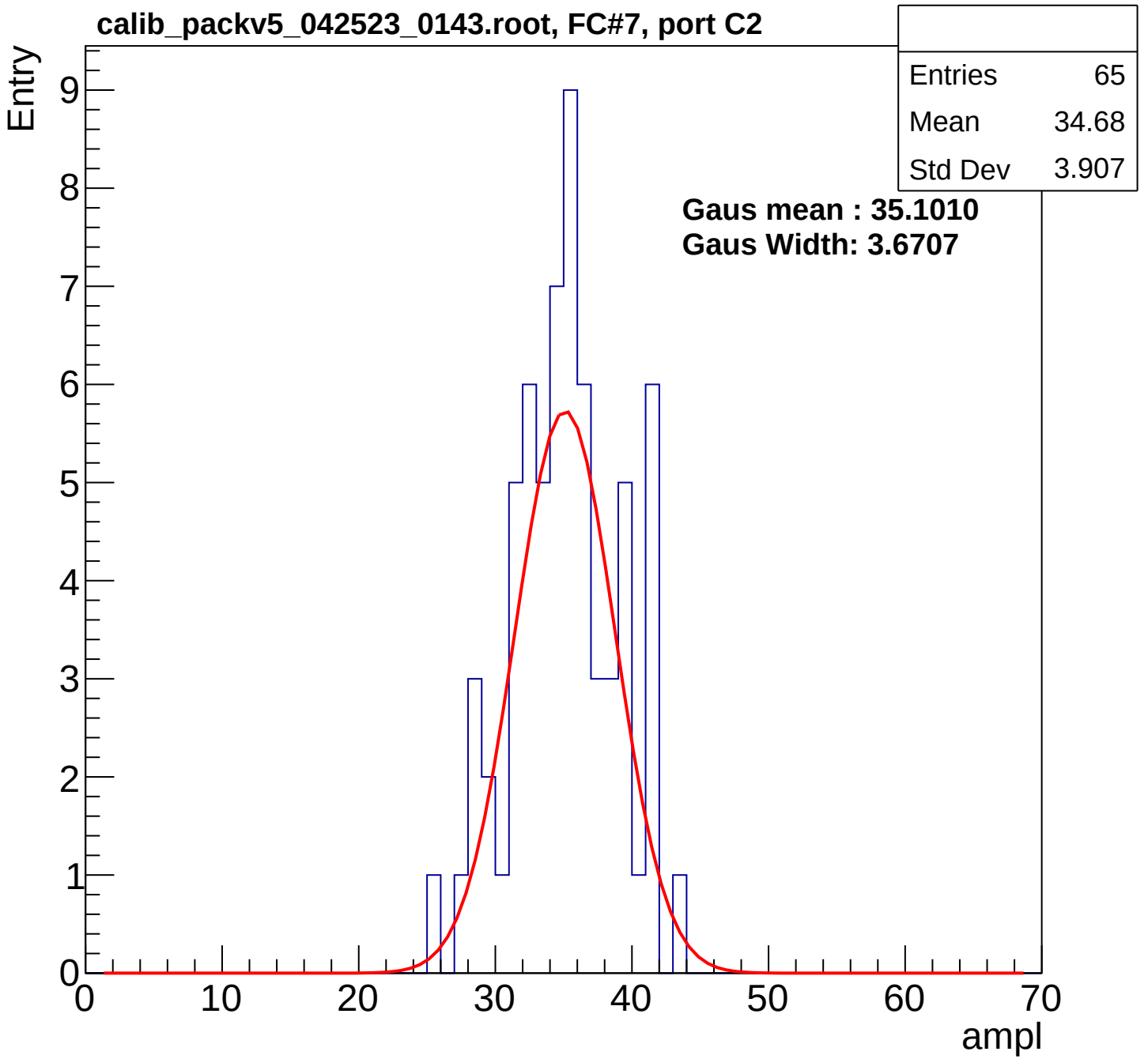
B1L103S, U6-ch124, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 34.68
Std Dev 3.907
Gaus mean : 35.1010
Gaus Width: 3.6707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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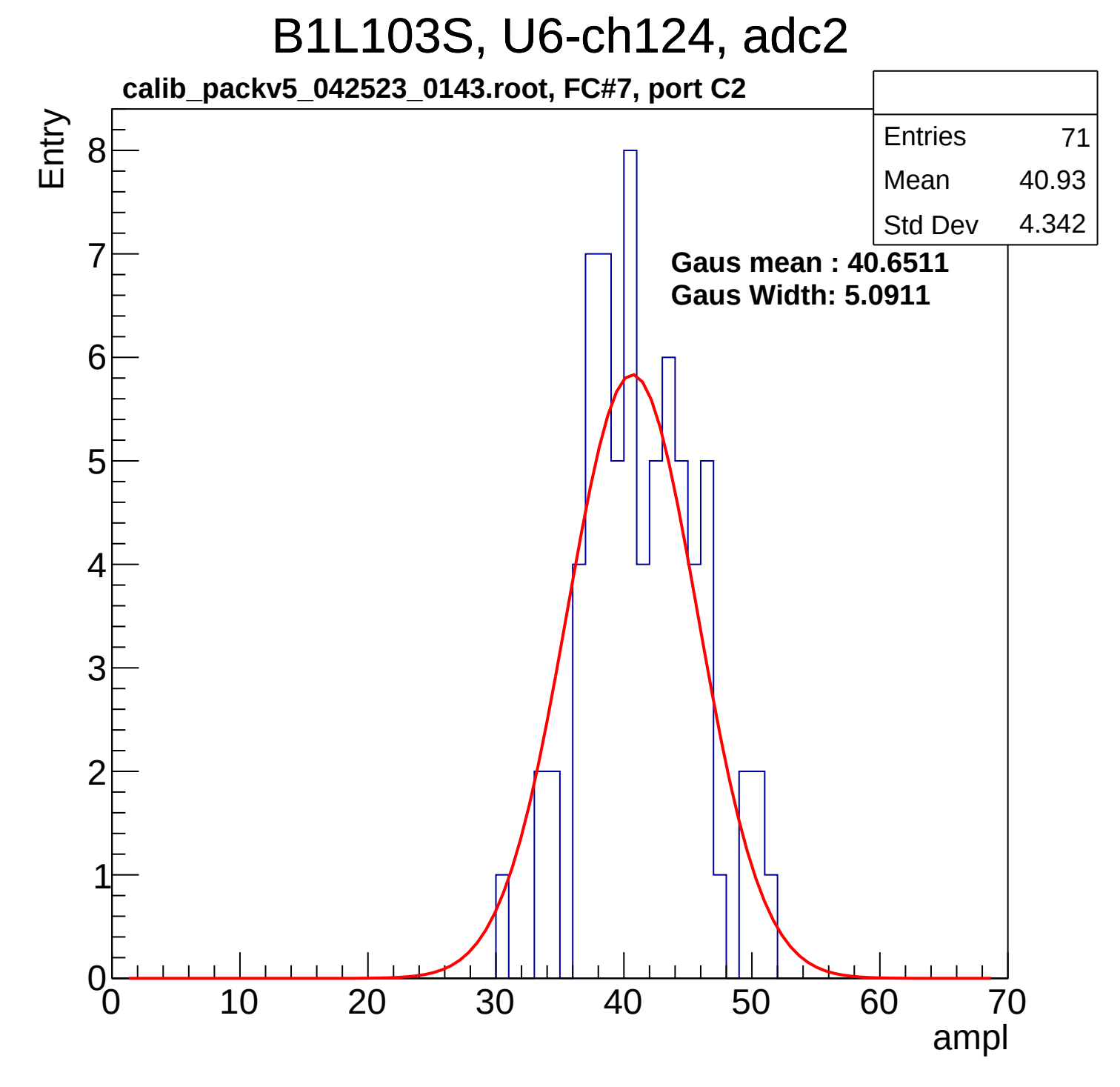
B1L103S, U6-ch124, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 40.93
Std Dev 4.342
Gaus mean : 40.6511
Gaus Width: 5.0911
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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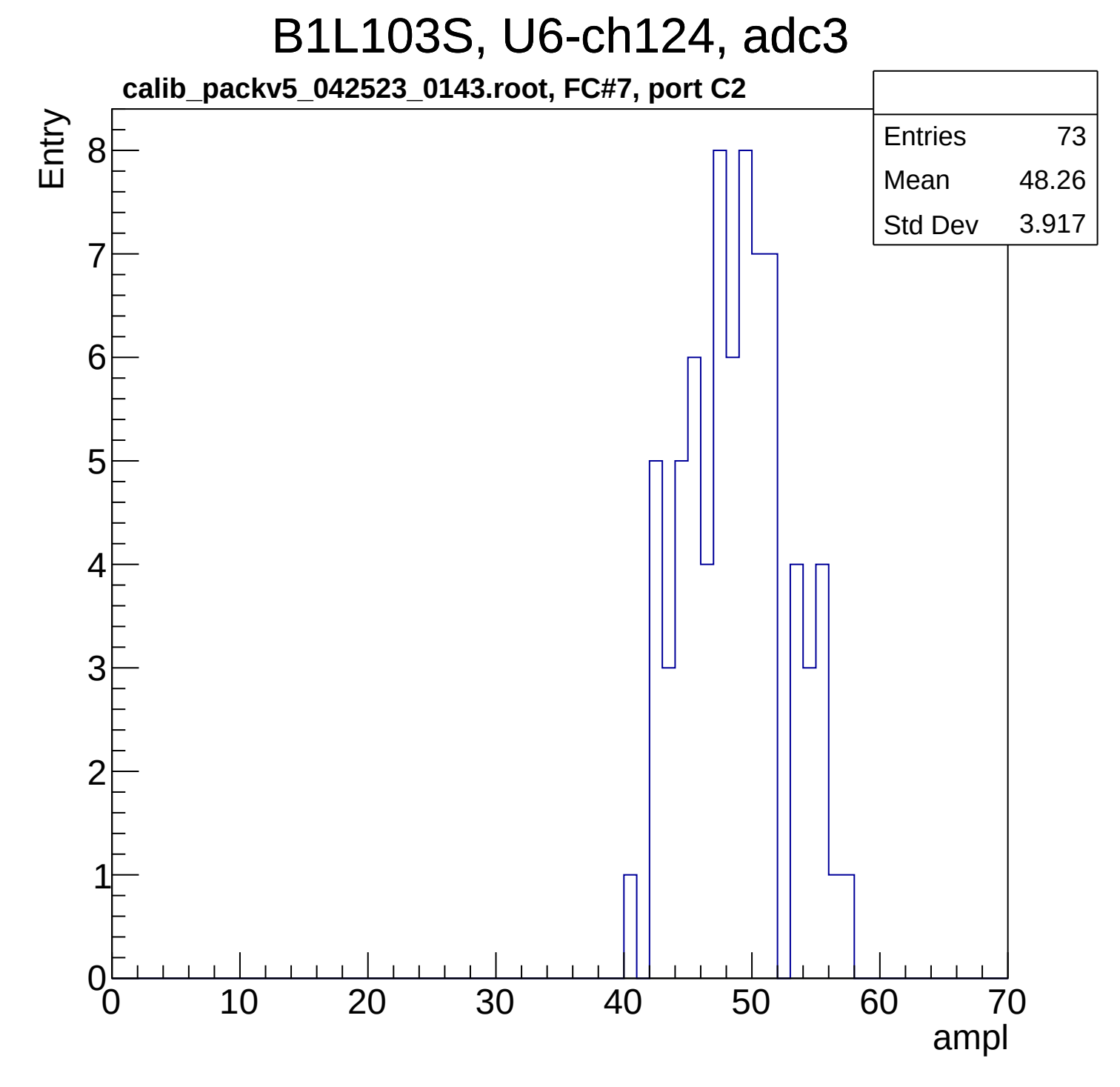
B1L103S, U6-ch124, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 48.26
Std Dev 3.917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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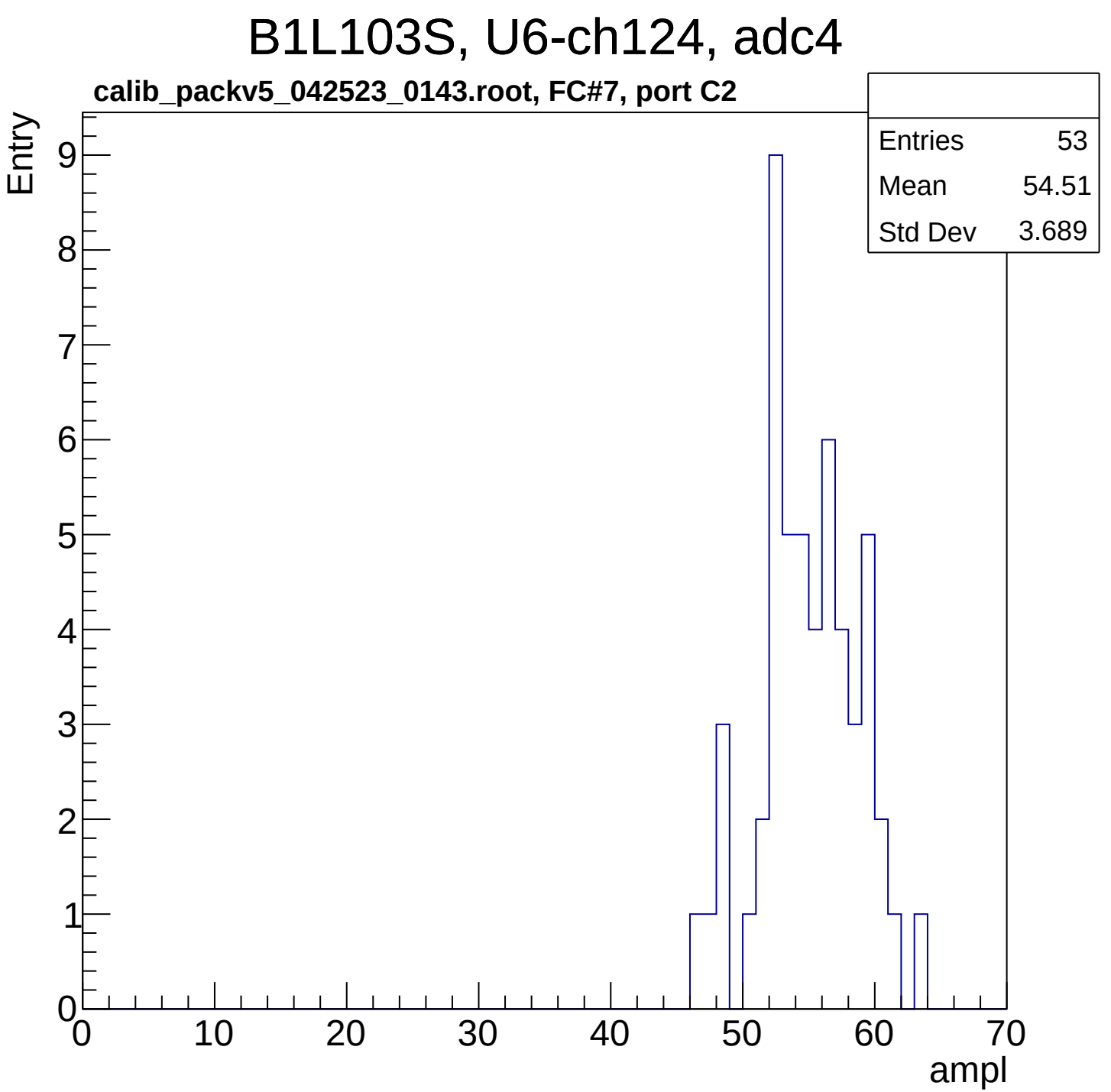
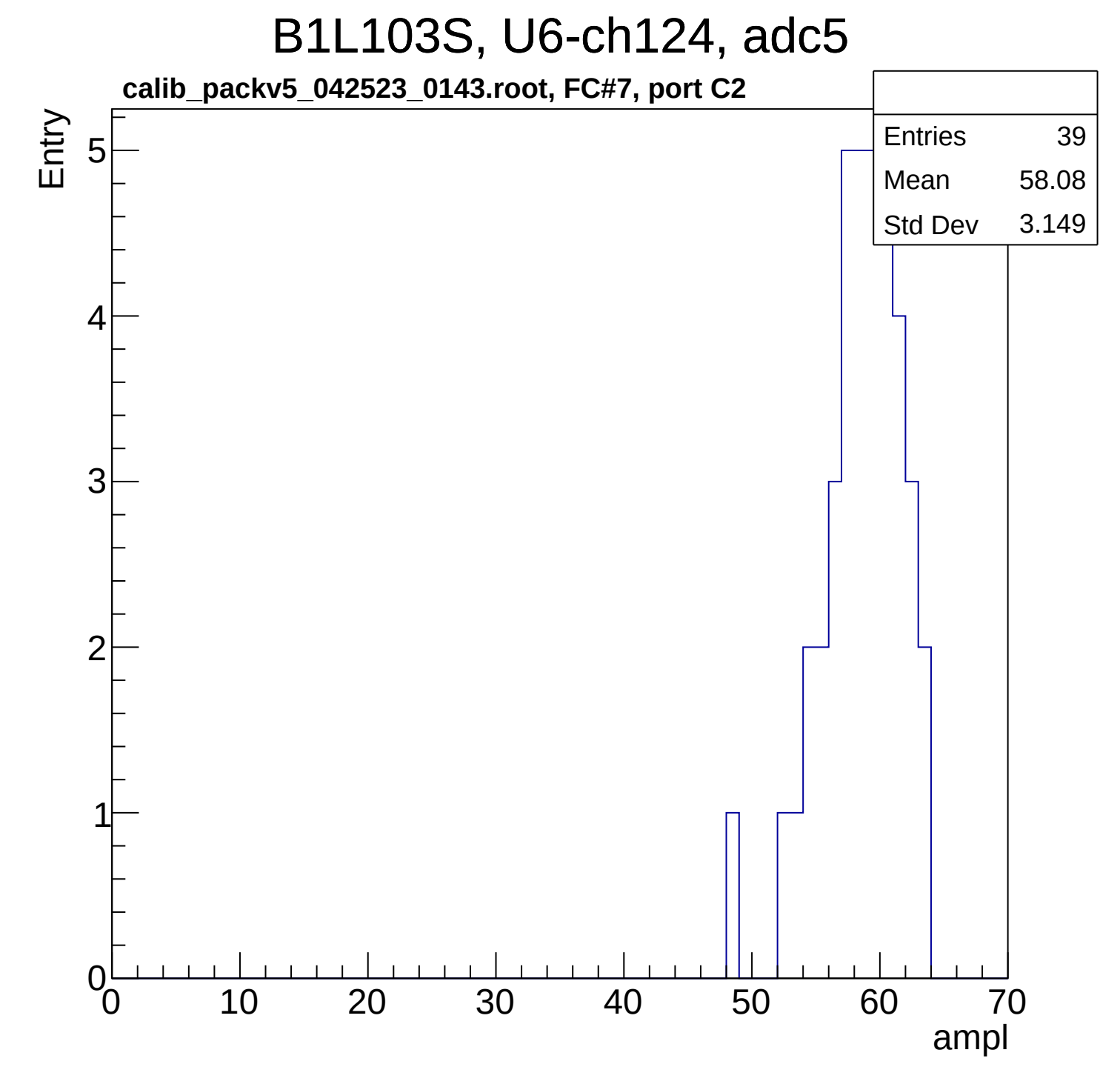
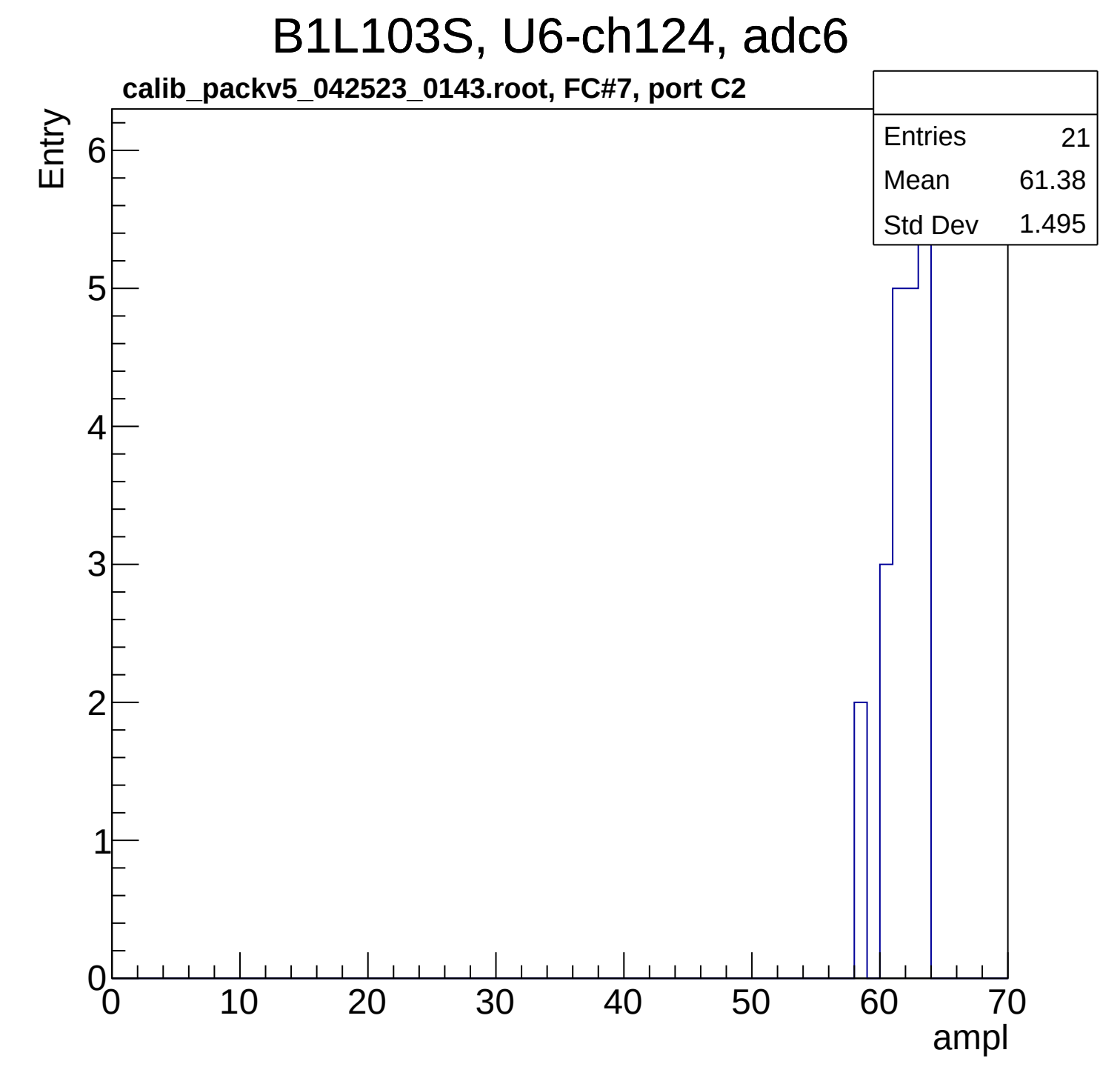
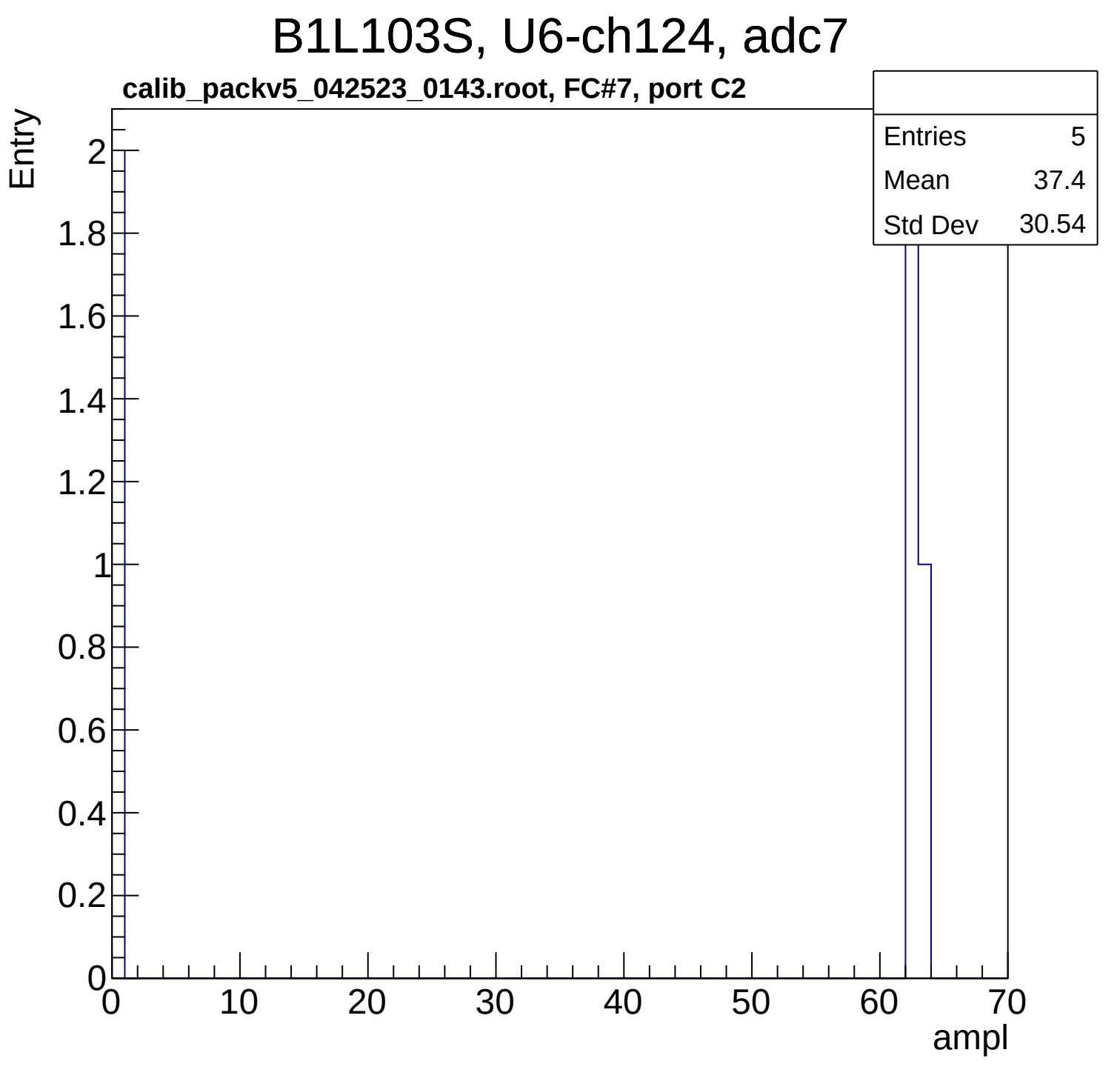

B1L103S, U6-ch124, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 54.51
Std Dev 3.689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch124, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.08
Std Dev 3.149
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch124, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 61.38
Std Dev 1.495
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch124, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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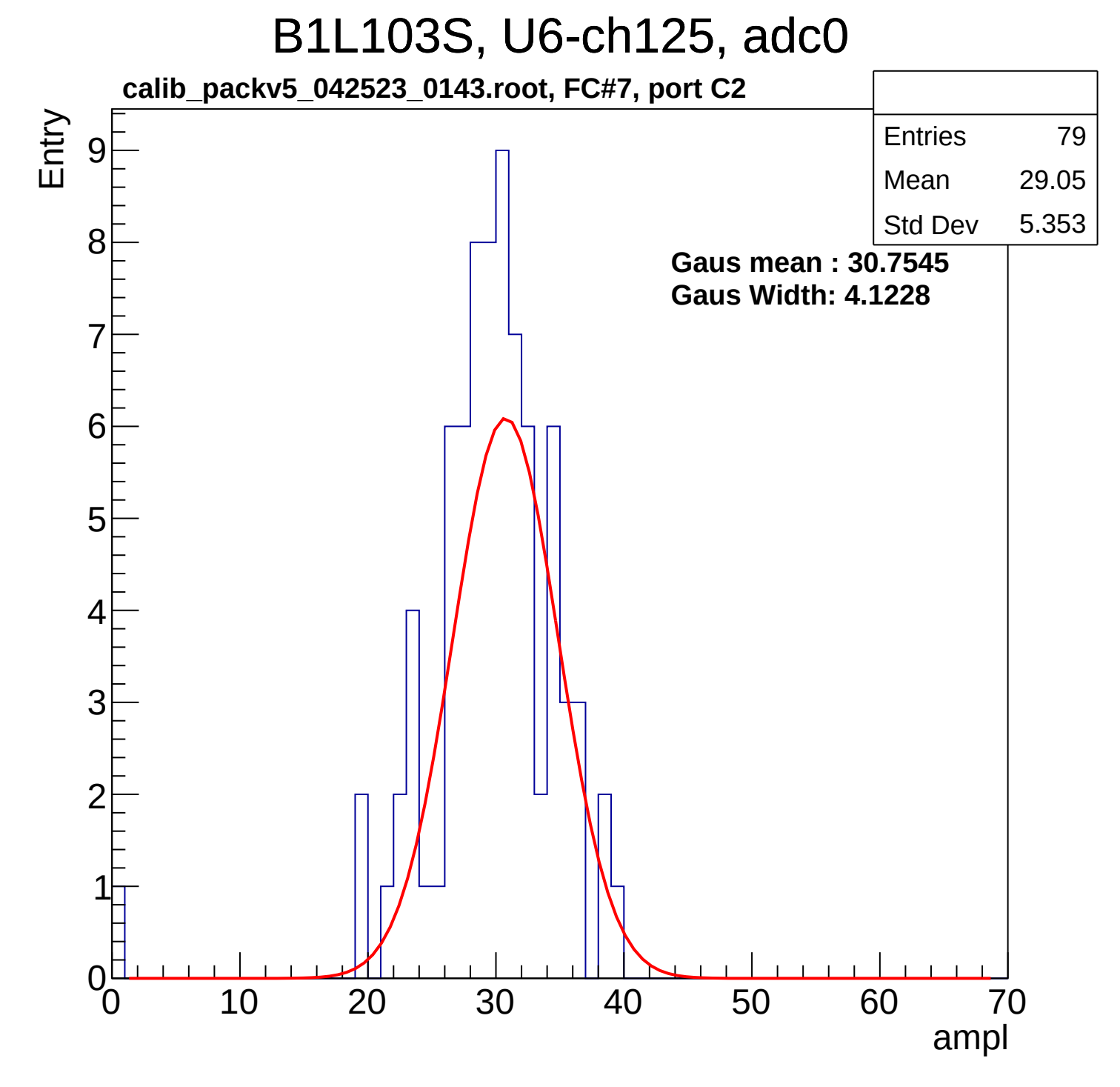
B1L103S, U6-ch125, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.05
Std Dev 5.353
Gaus mean : 30.7545
Gaus Width: 4.1228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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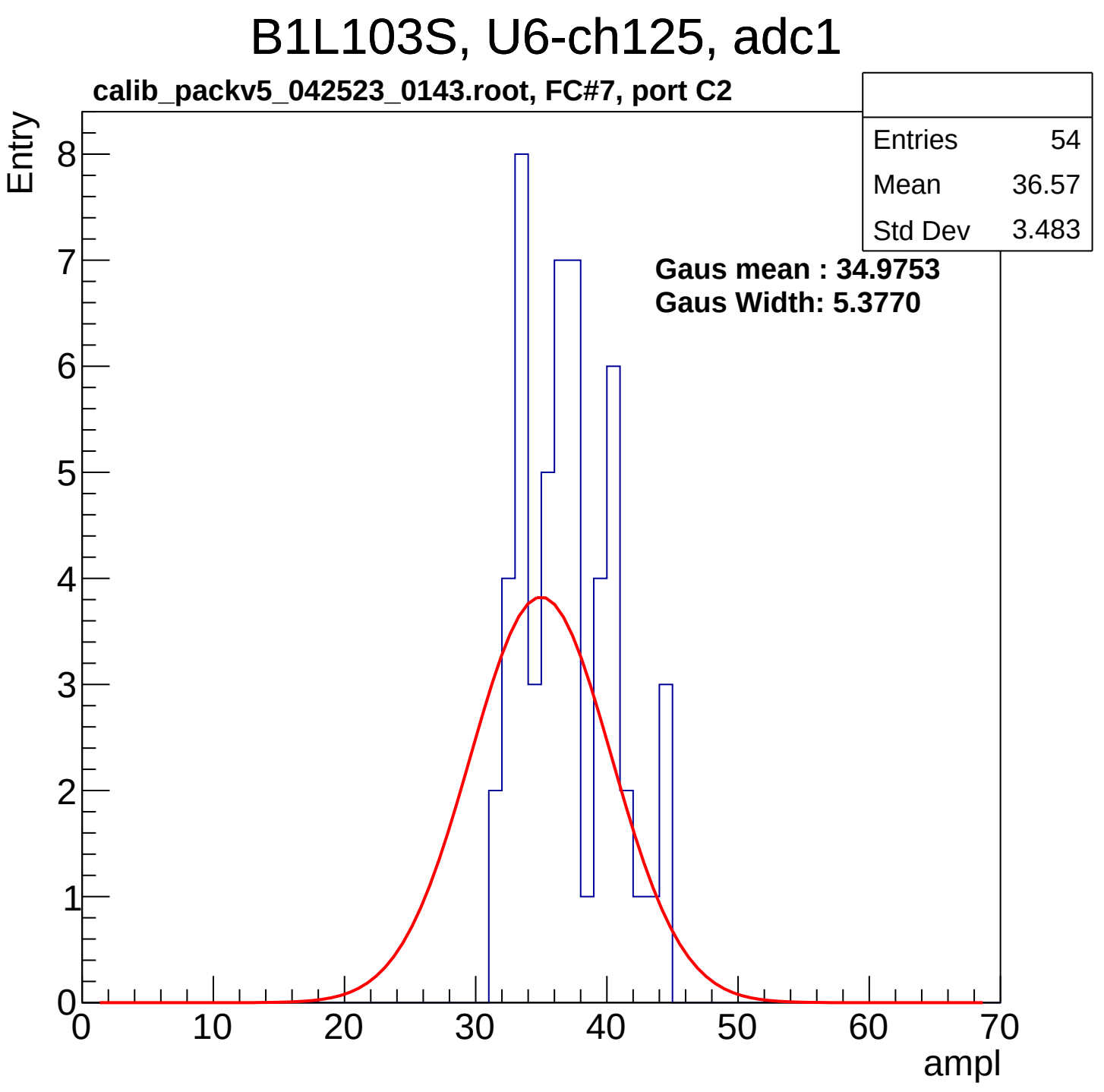
B1L103S, U6-ch125, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 36.57
Std Dev 3.483
Gaus mean : 34.9753
Gaus Width: 5.3770
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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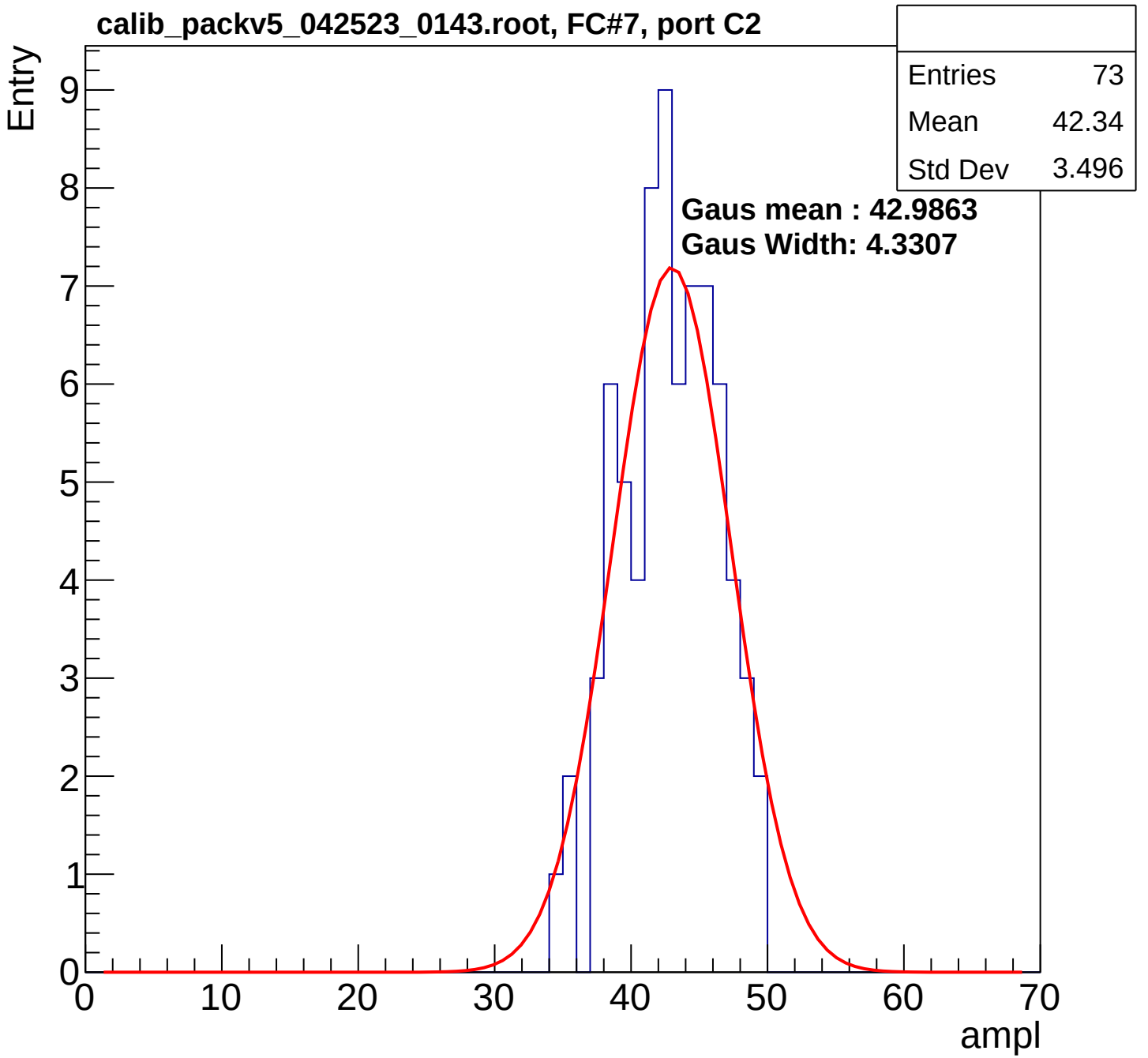
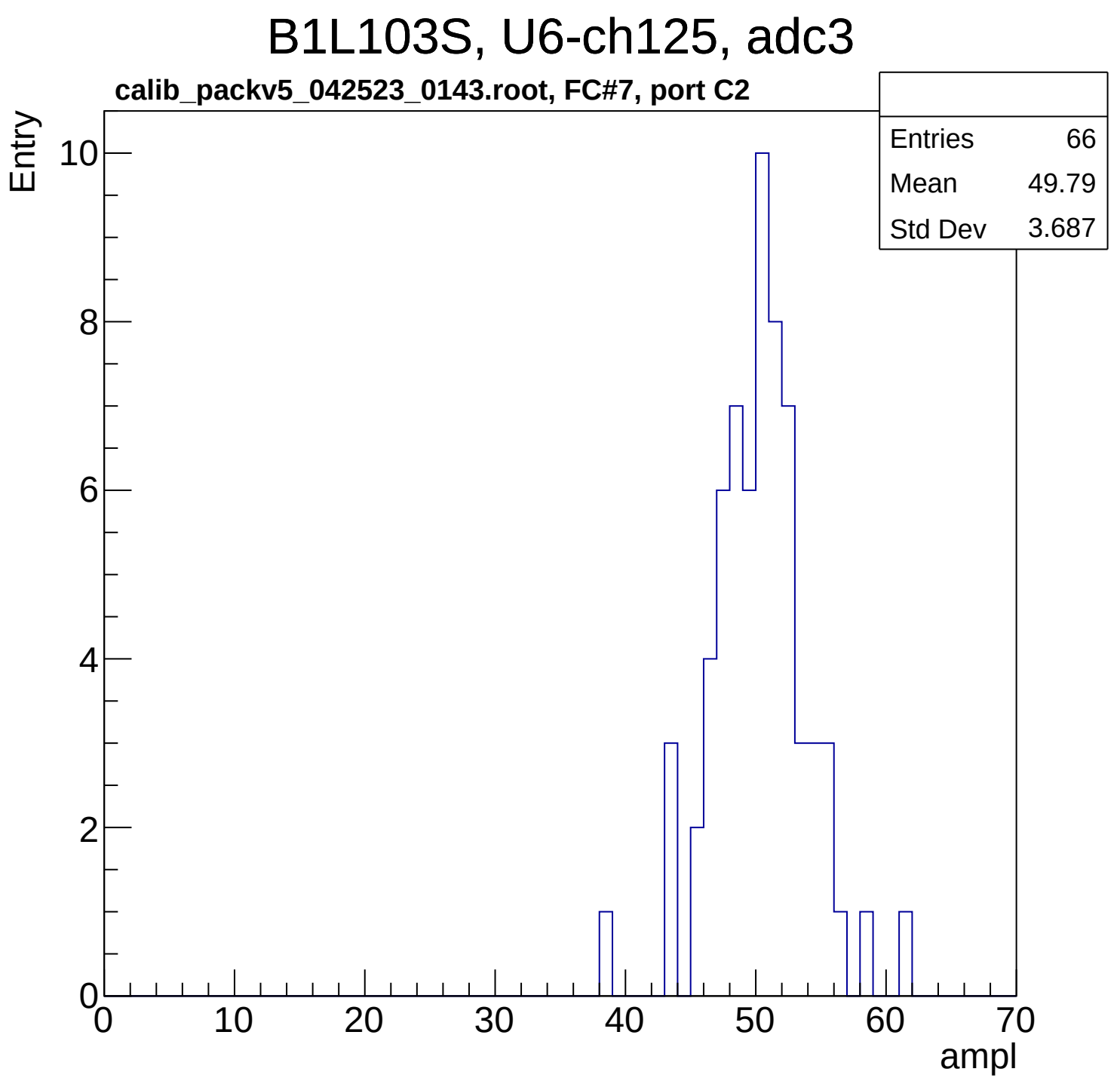
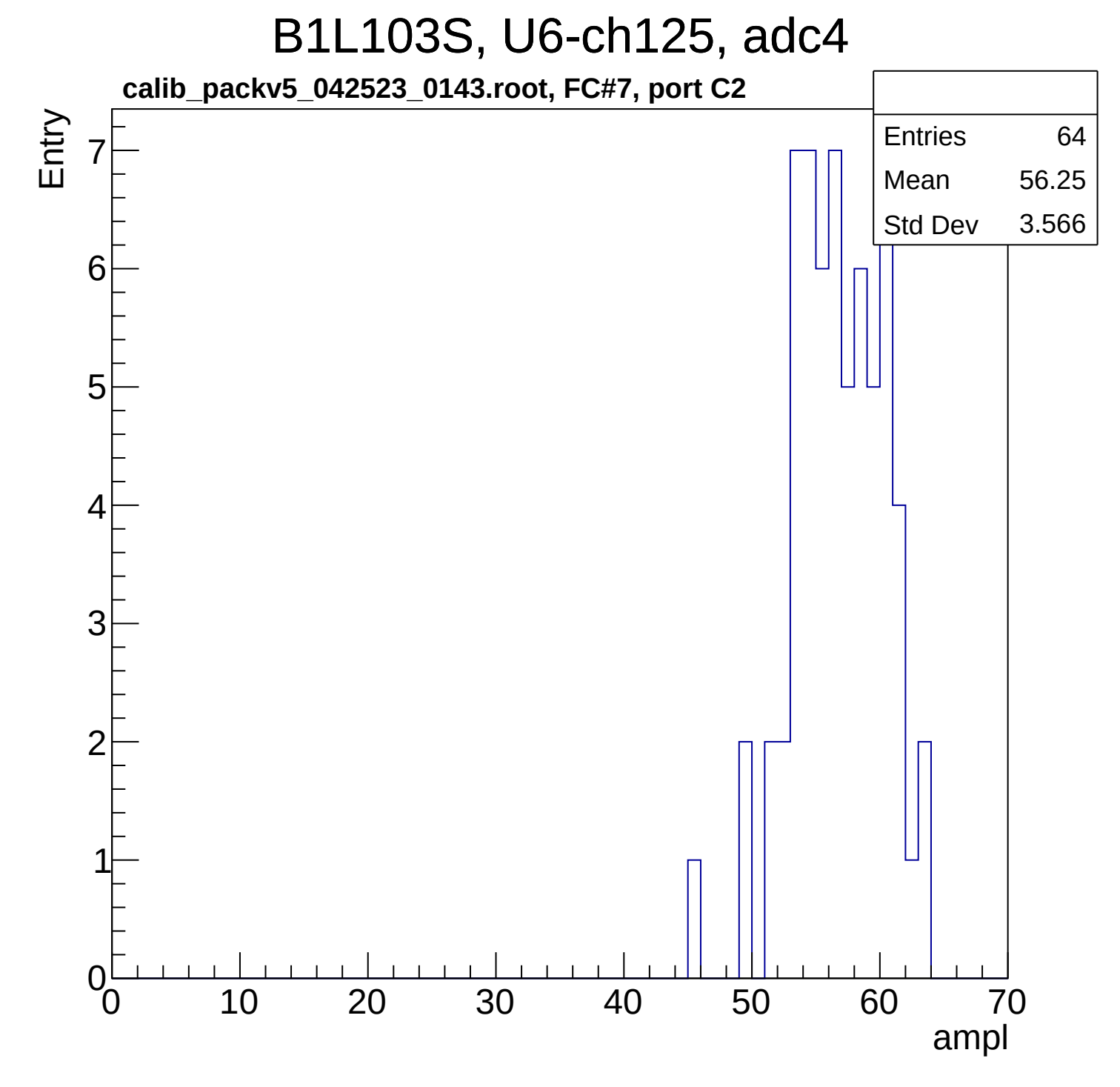

B1L103S, U6-ch125, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 42.34
Std Dev 3.496
Gaus mean : 42.9863
Gaus Width: 4.3307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U6-ch125, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 49.79
Std Dev 3.687
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U6-ch125, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 56.25
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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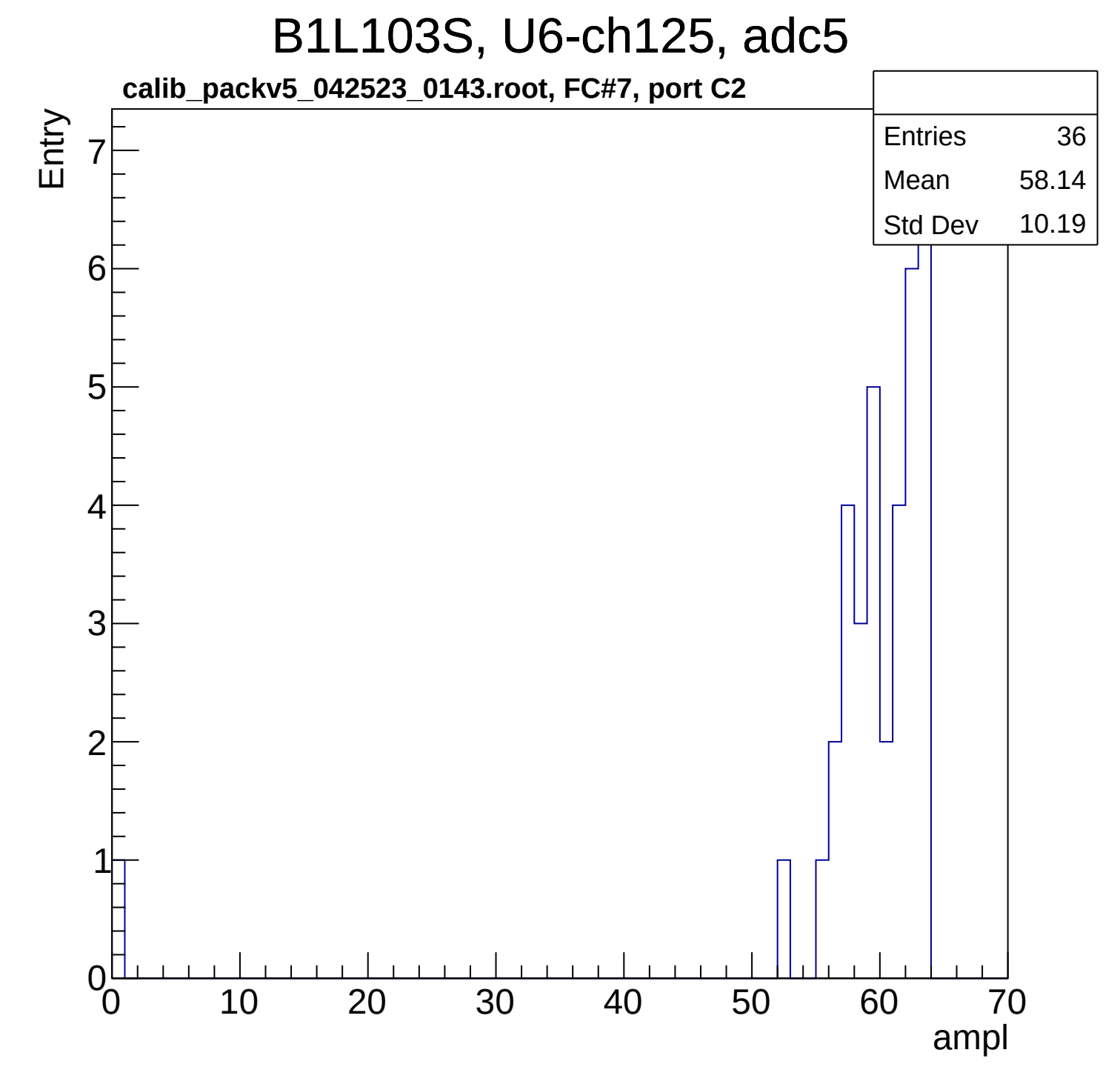
B1L103S, U6-ch125, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.14
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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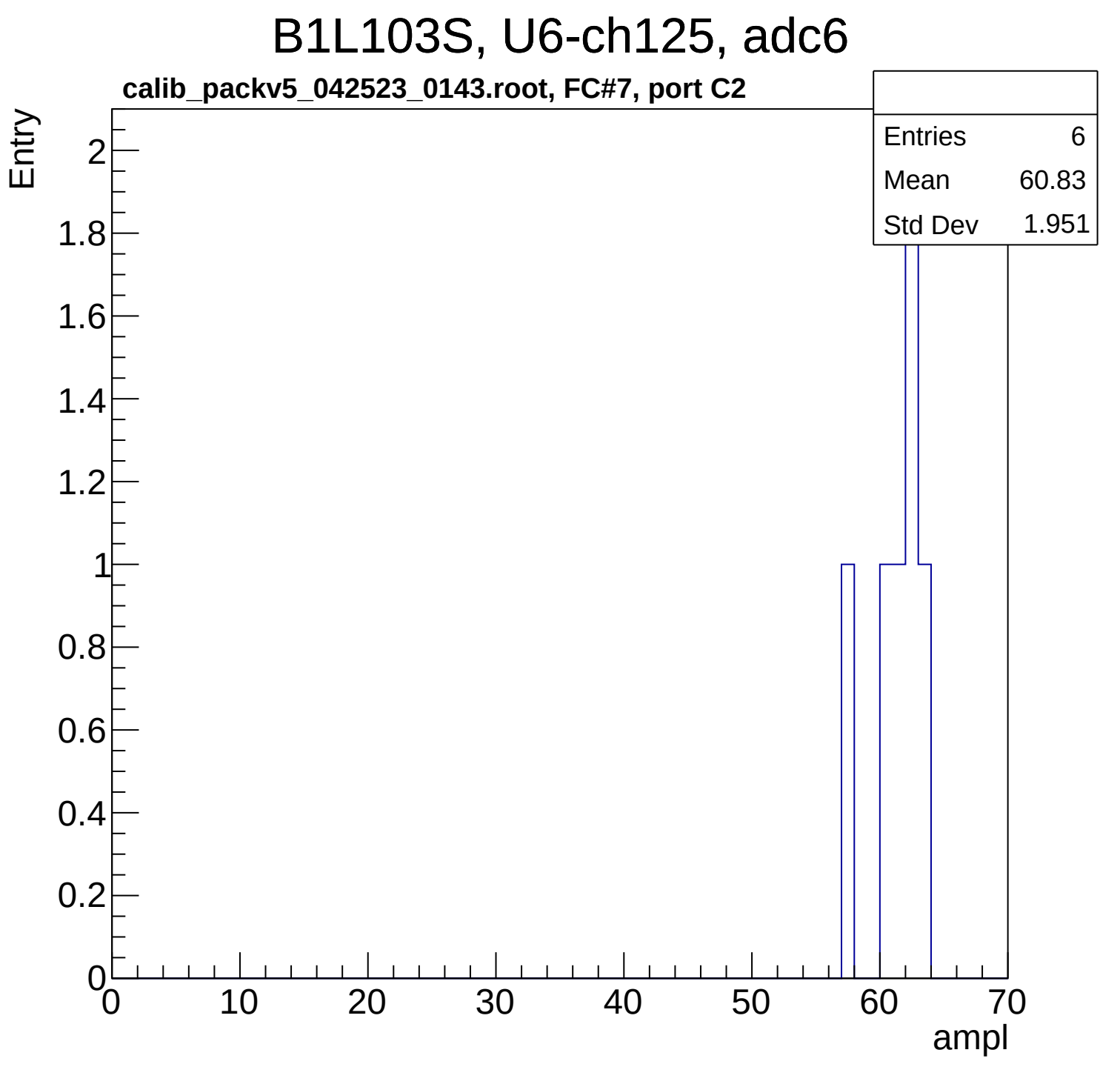
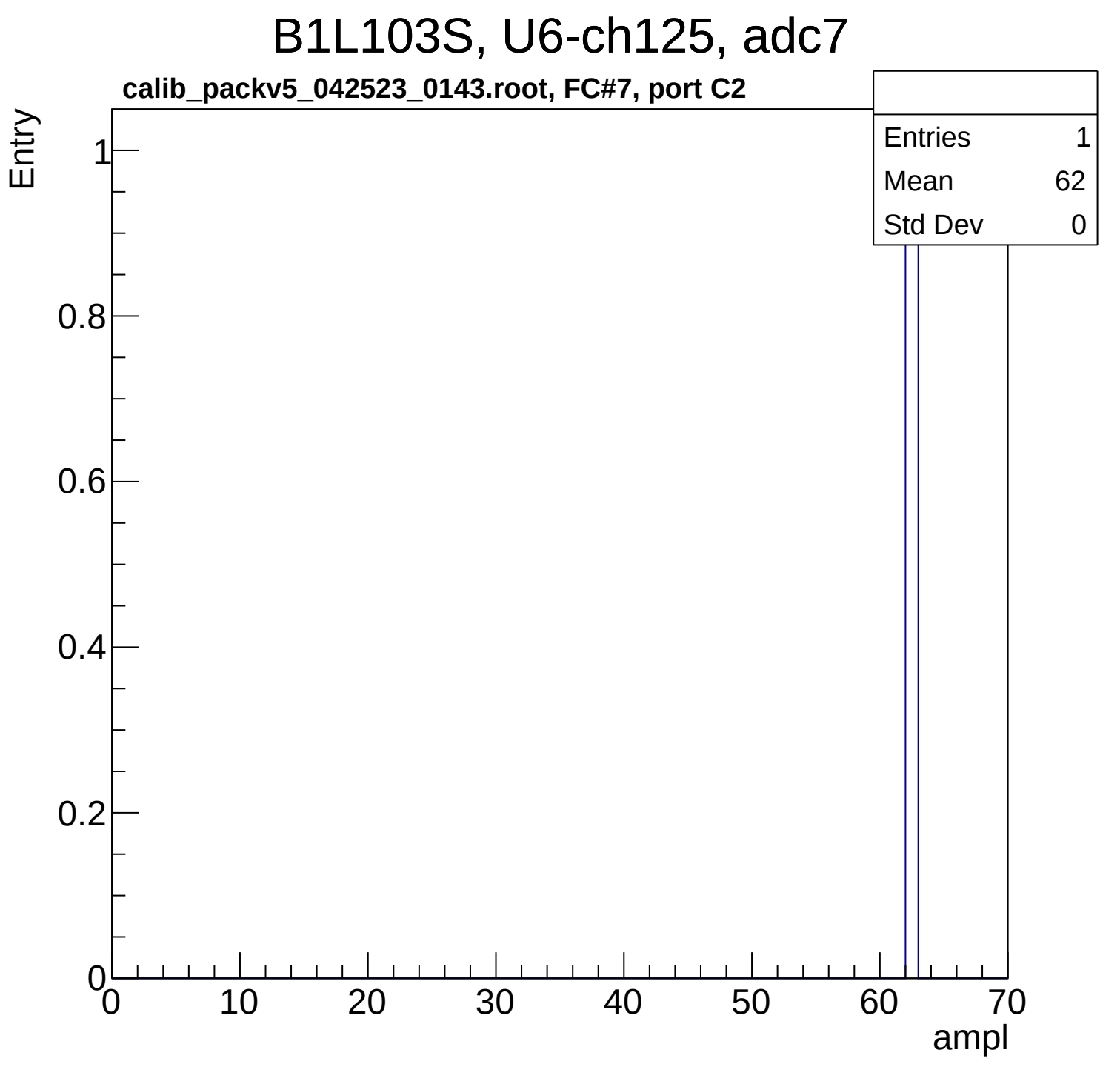

B1L103S, U6-ch125, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 60.83
Std Dev 1.951
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch125, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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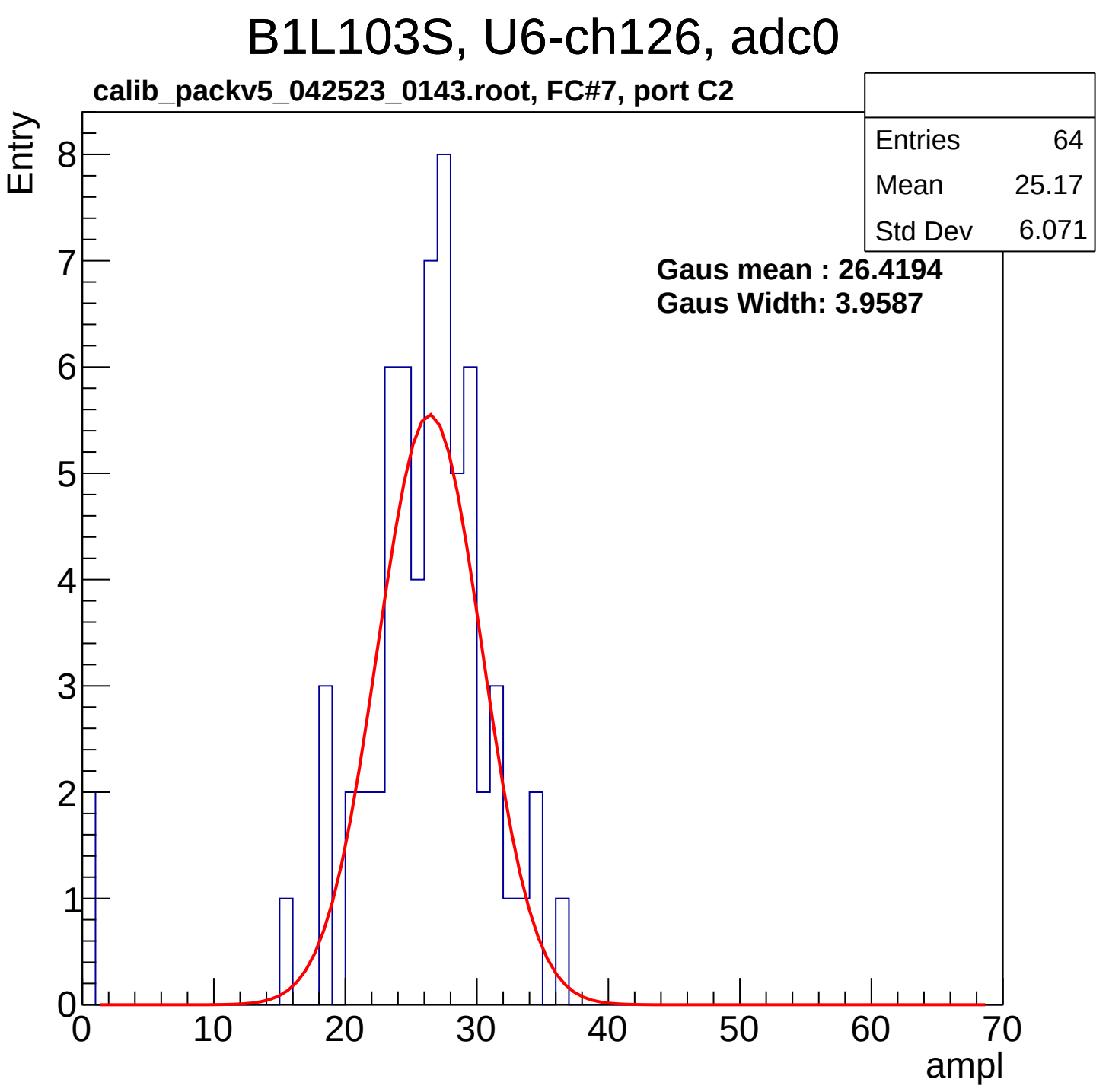
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch126, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 25.17
Std Dev 6.071
Gaus mean : 26.4194
Gaus Width: 3.9587
Entry
ampl
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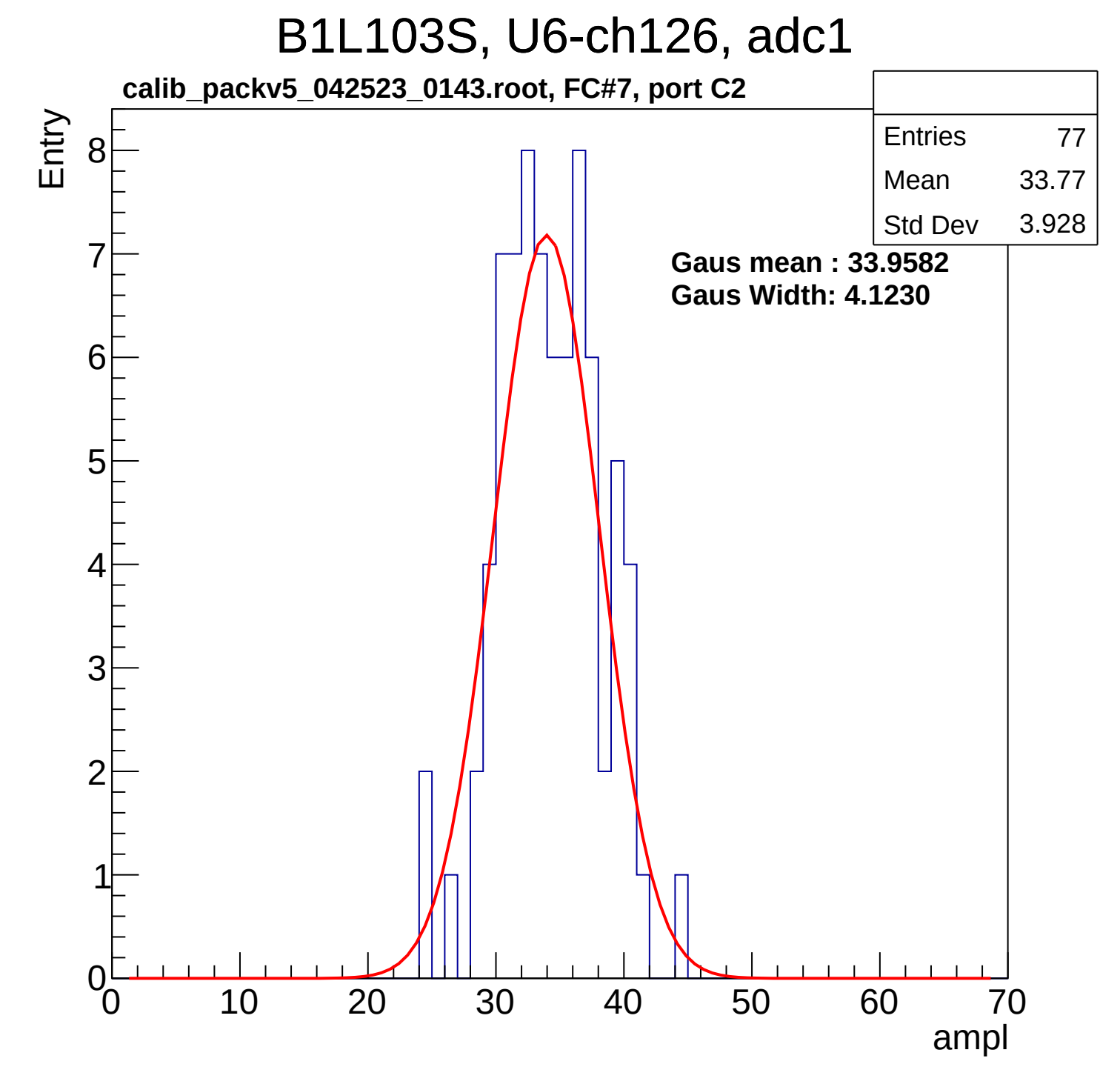
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U6-ch126, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 33.77
Std Dev 3.928
Gaus mean : 33.9582
Gaus Width: 4.1230
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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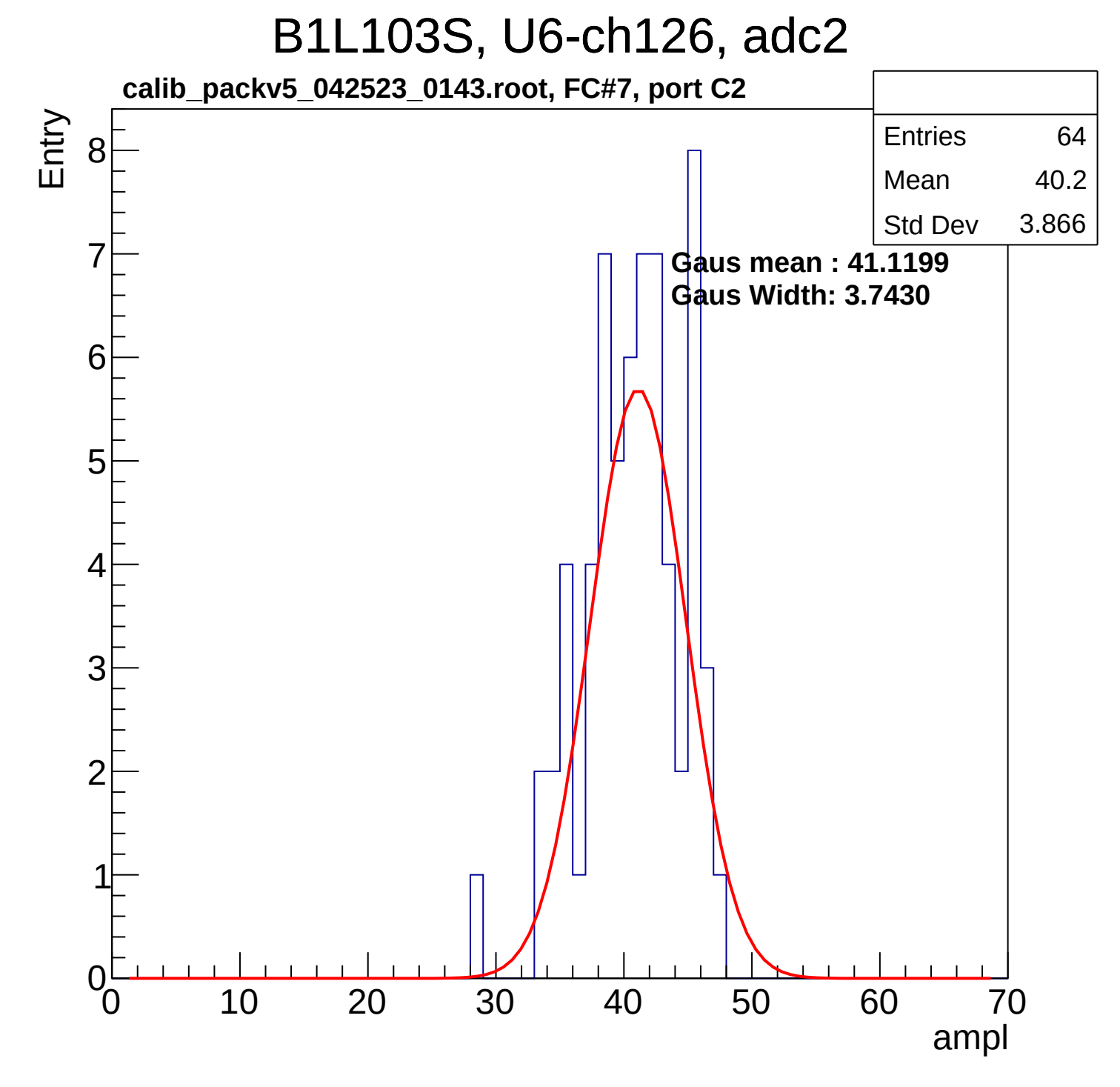
B1L103S, U6-ch126, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.2
Std Dev 3.866
Gaus mean : 41.1199
Gaus Width: 3.7430
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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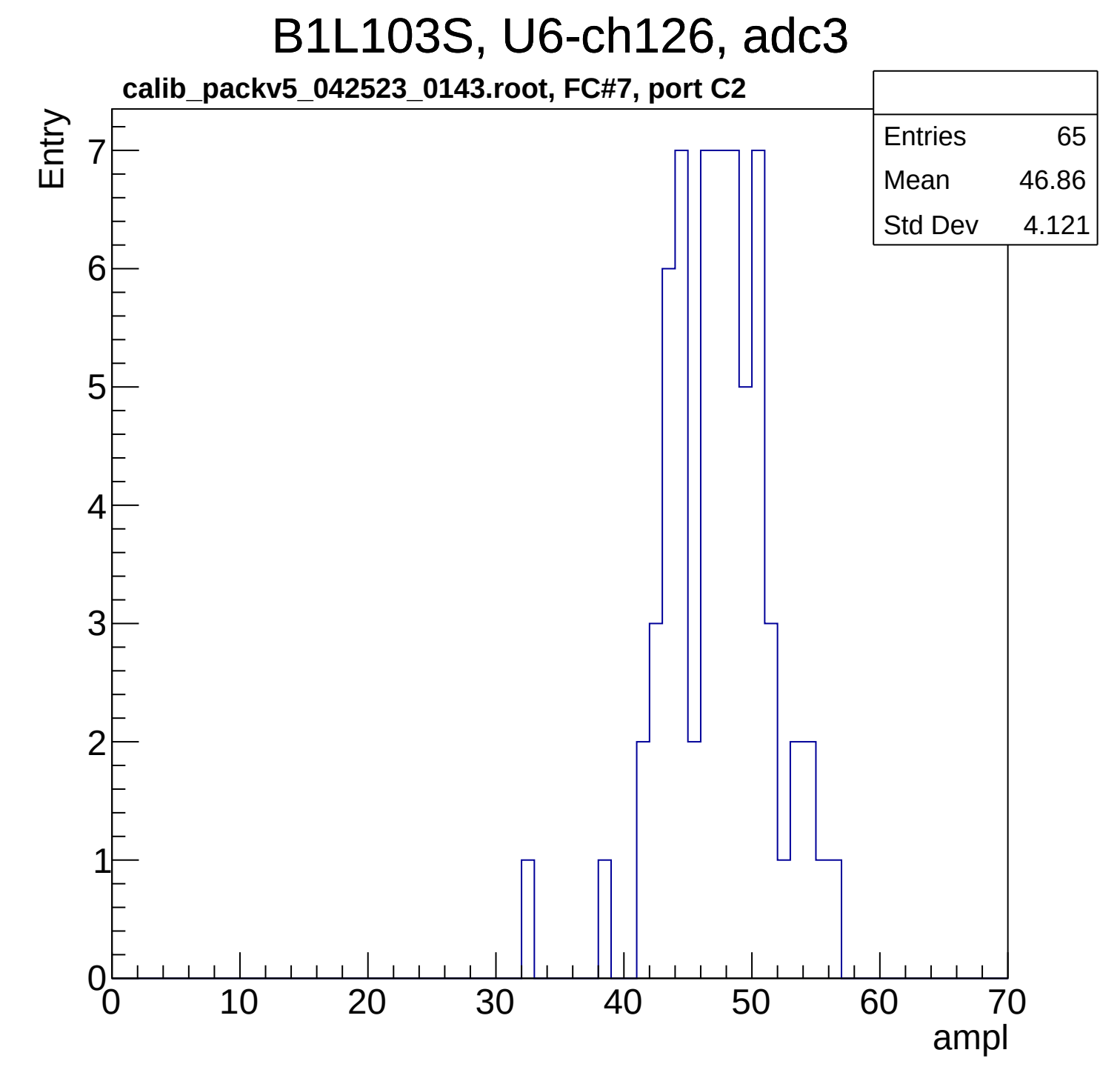
B1L103S, U6-ch126, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 46.86
Std Dev 4.121
Entry
ampl
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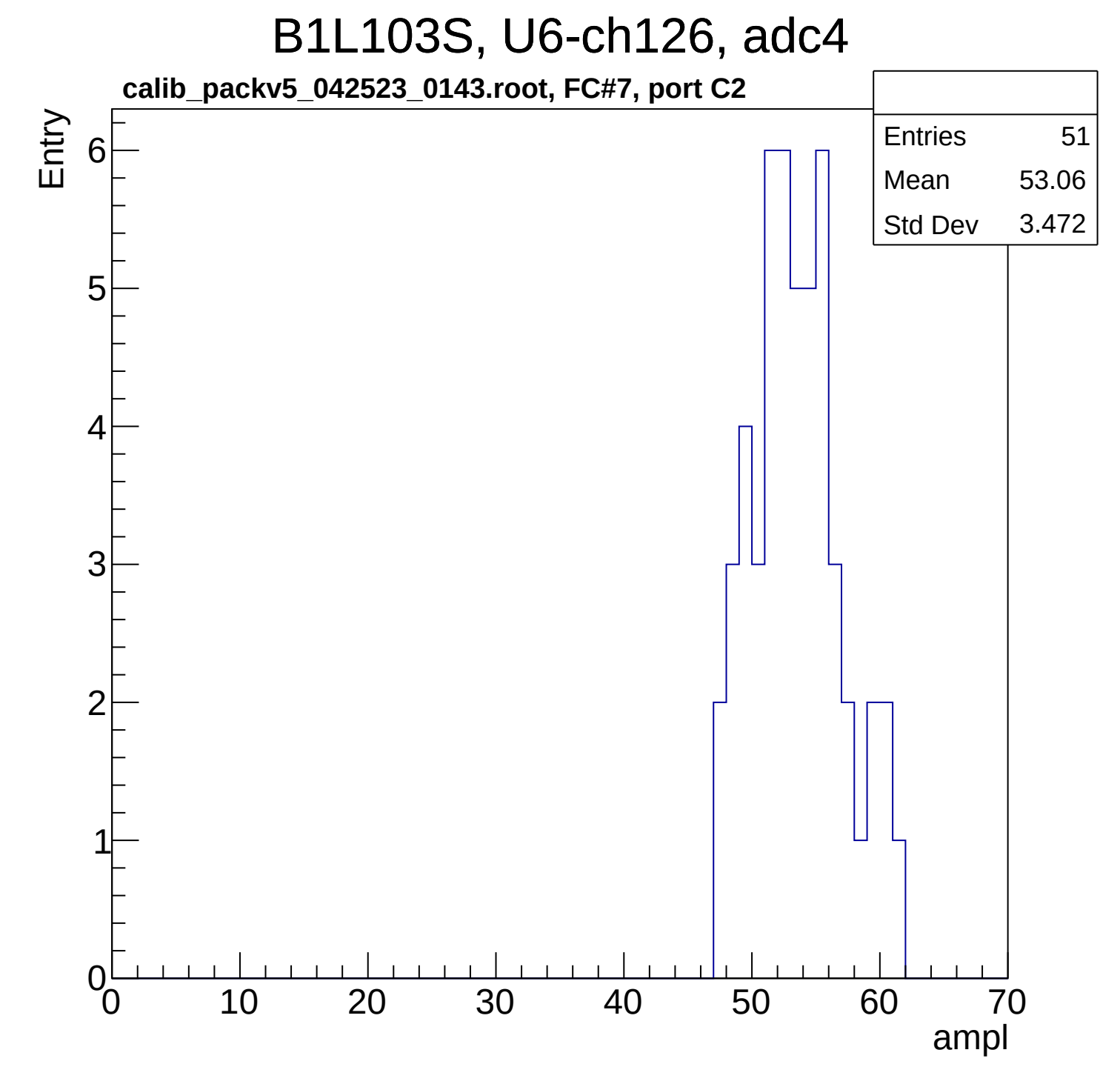
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch126, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 53.06
Std Dev 3.472
Entry
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3
4
5
6
0
10
20
30
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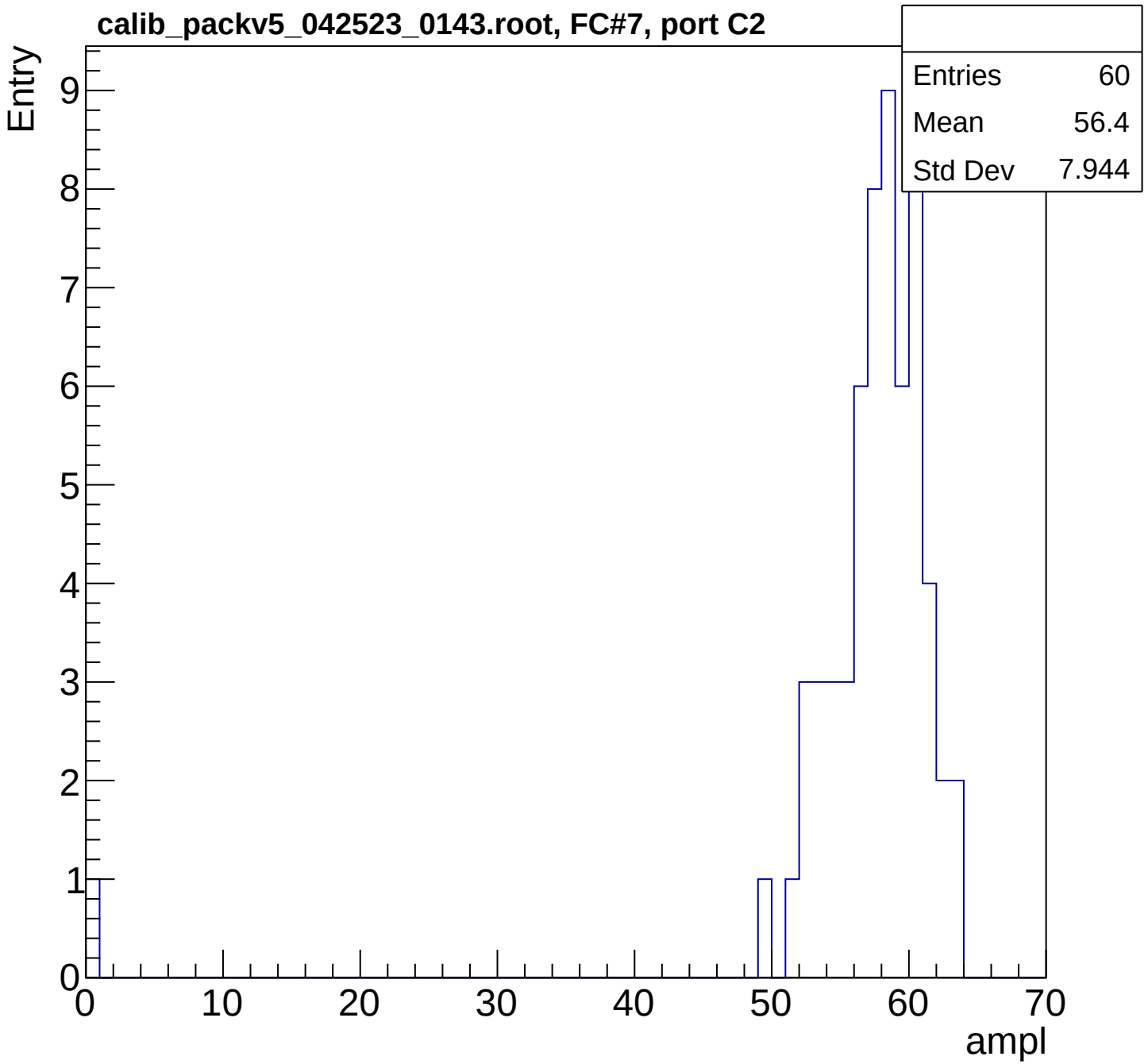
50
60
70
ampl


B1L103S, U6-ch126, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.4
Std Dev 7.944
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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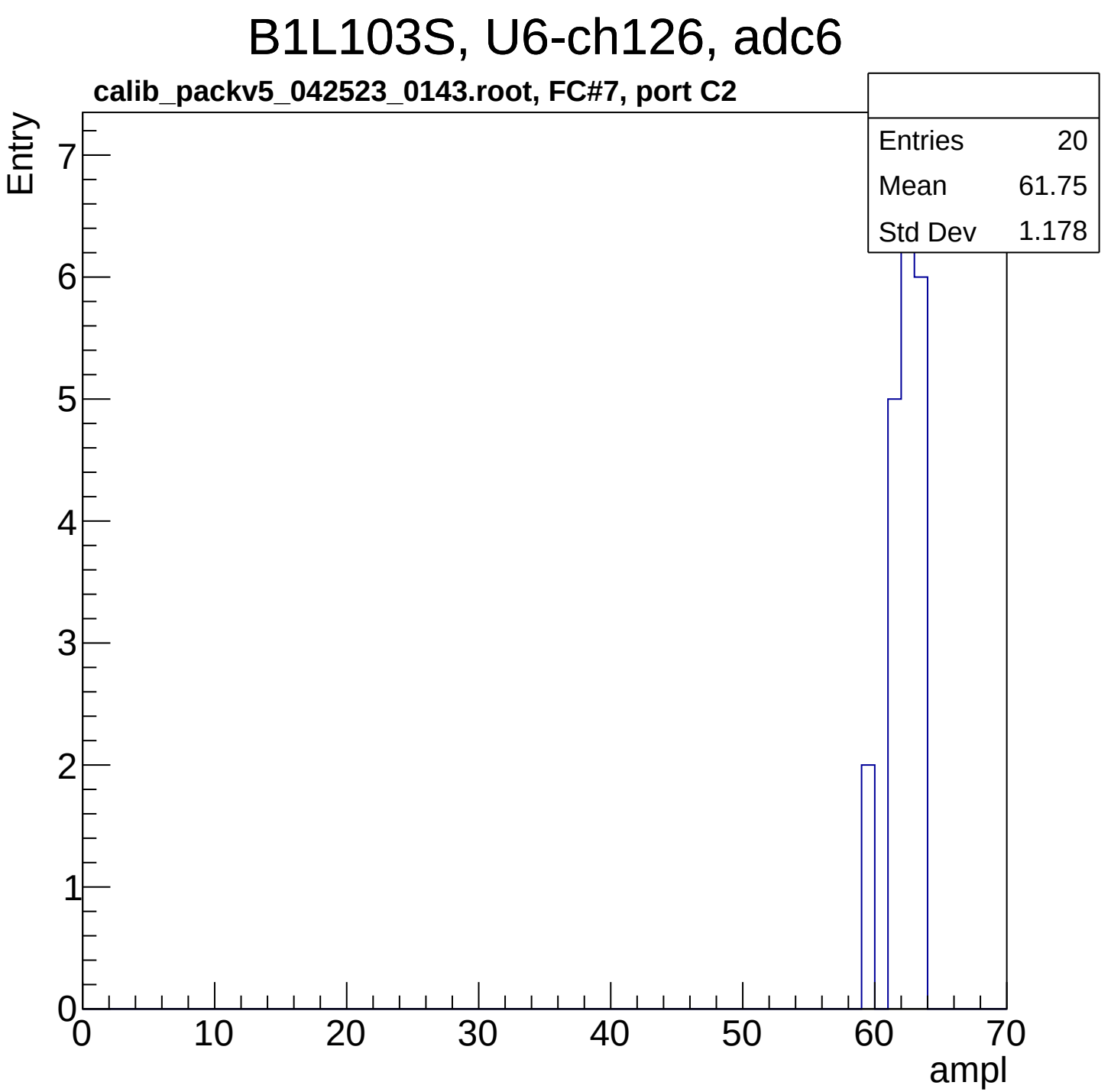
60
70


B1L103S, U6-ch126, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 61.75
Std Dev 1.178
Entry
ampl
0
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2
3
4
5
6
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0
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40
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60
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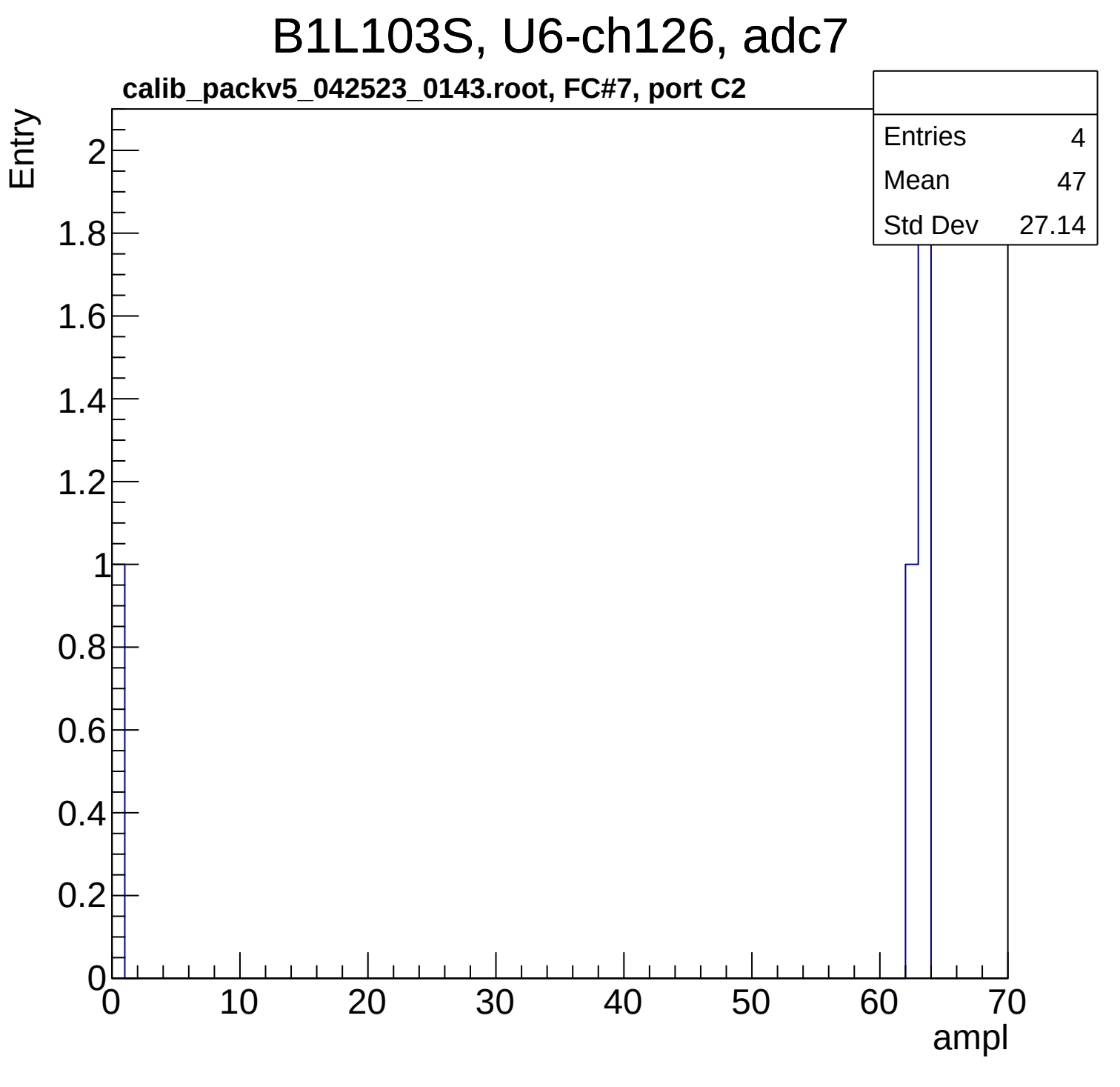
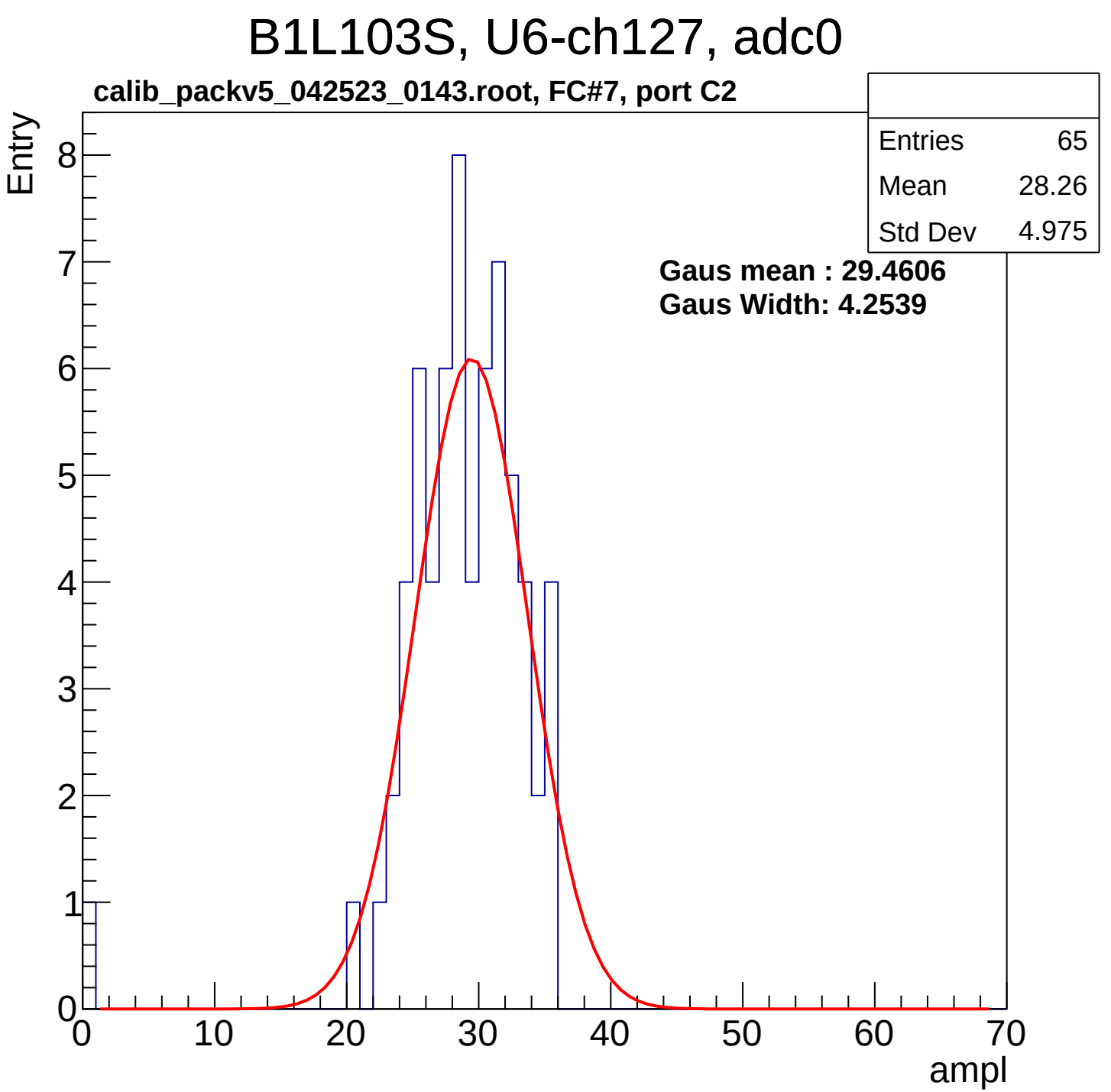

B1L103S, U6-ch126, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U6-ch127, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 28.26
Std Dev 4.975
Gaus mean : 29.4606
Gaus Width: 4.2539
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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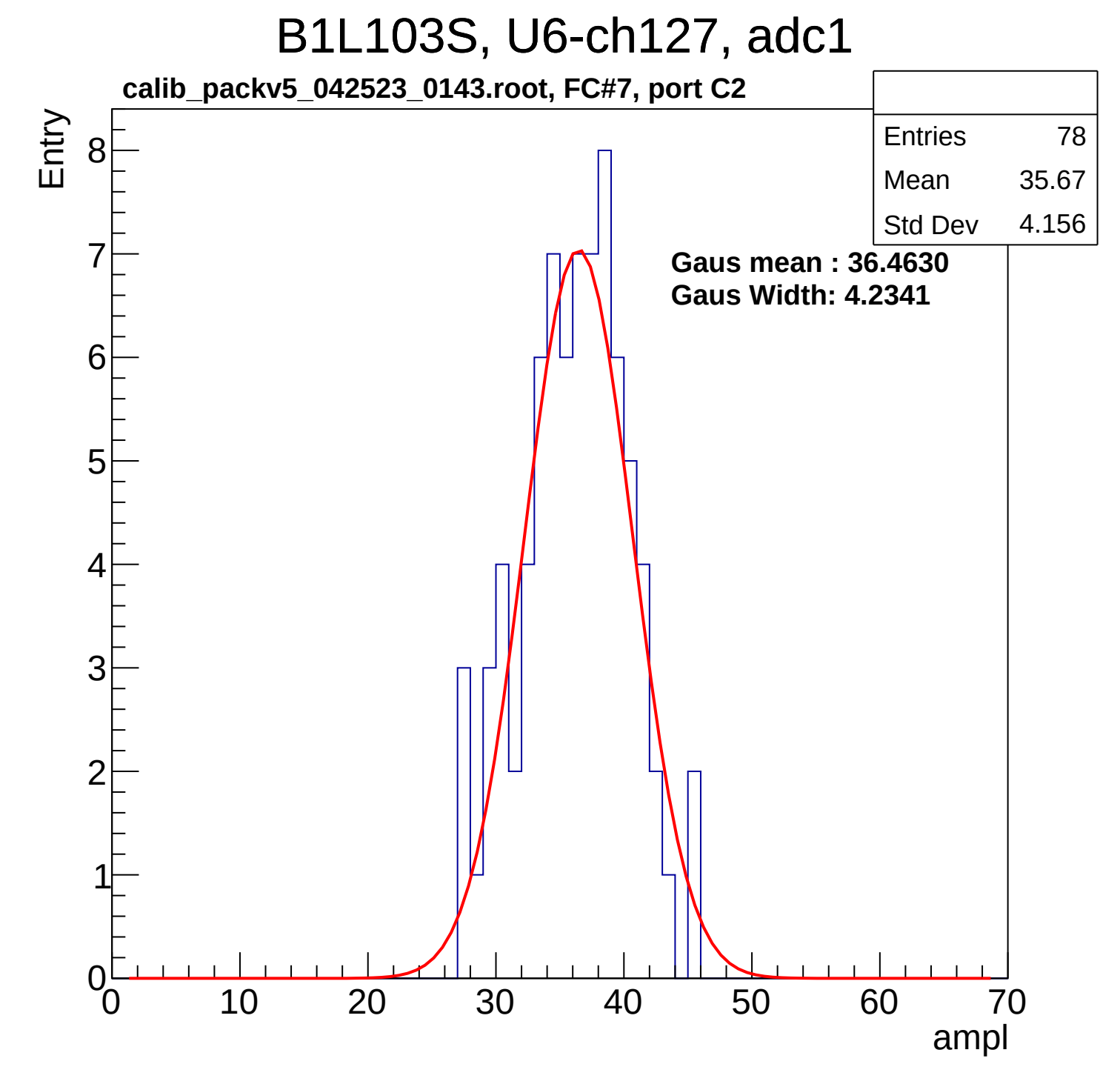
60
70


B1L103S, U6-ch127, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 35.67
Std Dev 4.156
Gaus mean : 36.4630
Gaus Width: 4.2341
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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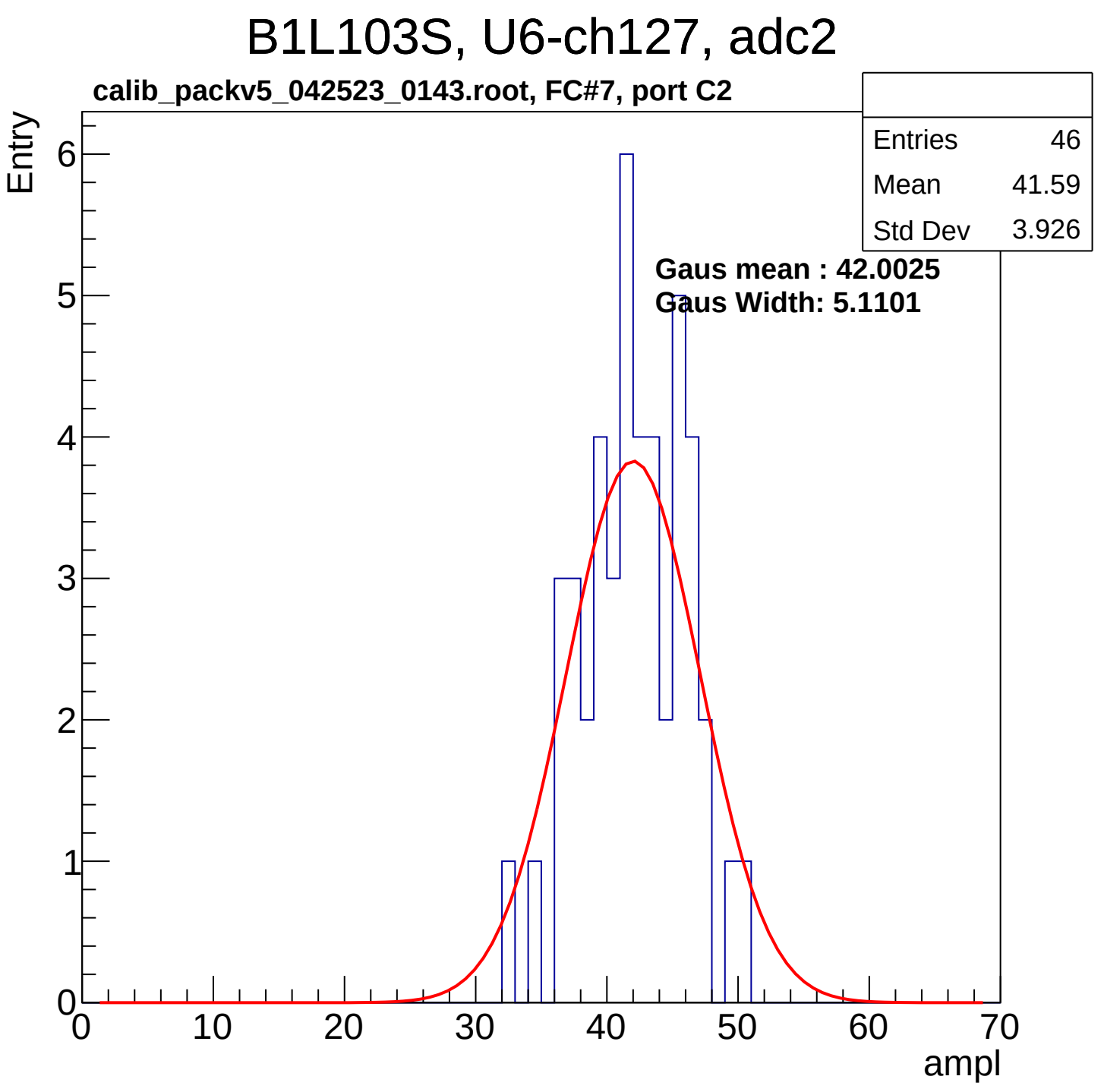
B1L103S, U6-ch127, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 41.59
Std Dev 3.926
Gaus mean : 42.0025
Gaus Width: 5.1101
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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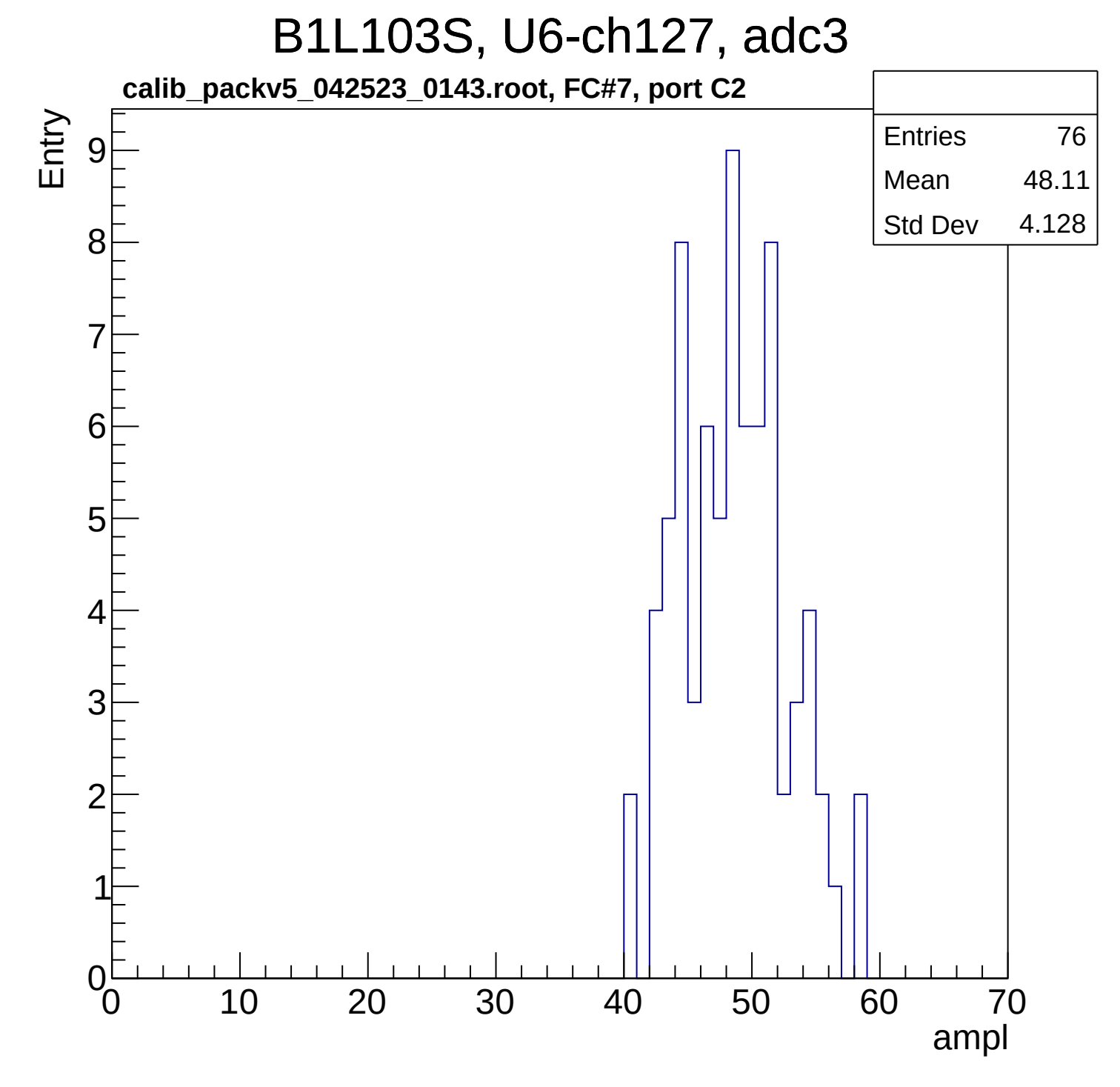
B1L103S, U6-ch127, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 48.11
Std Dev 4.128
Entry
ampl
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3
4
5
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7
8
9
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10
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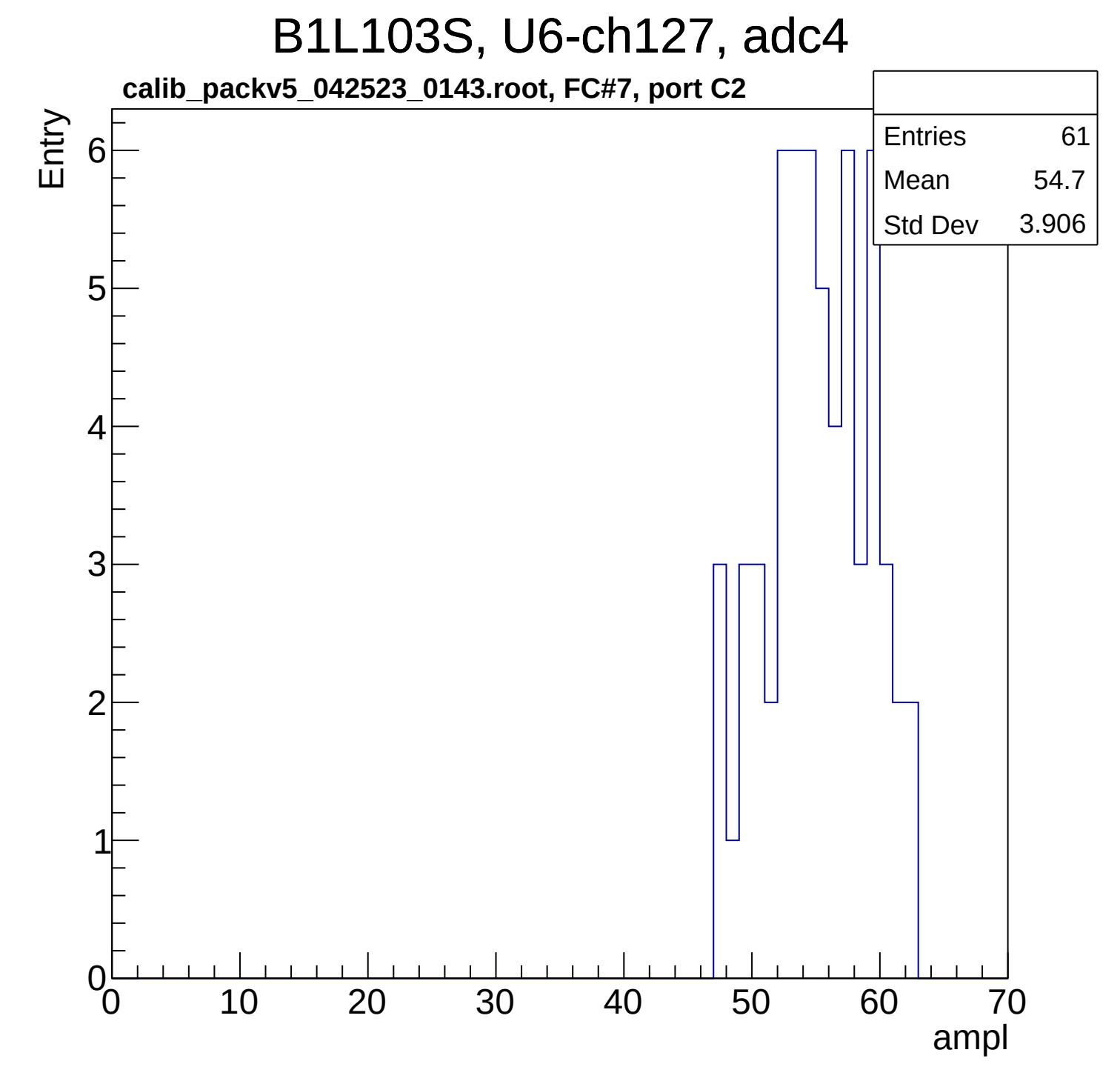
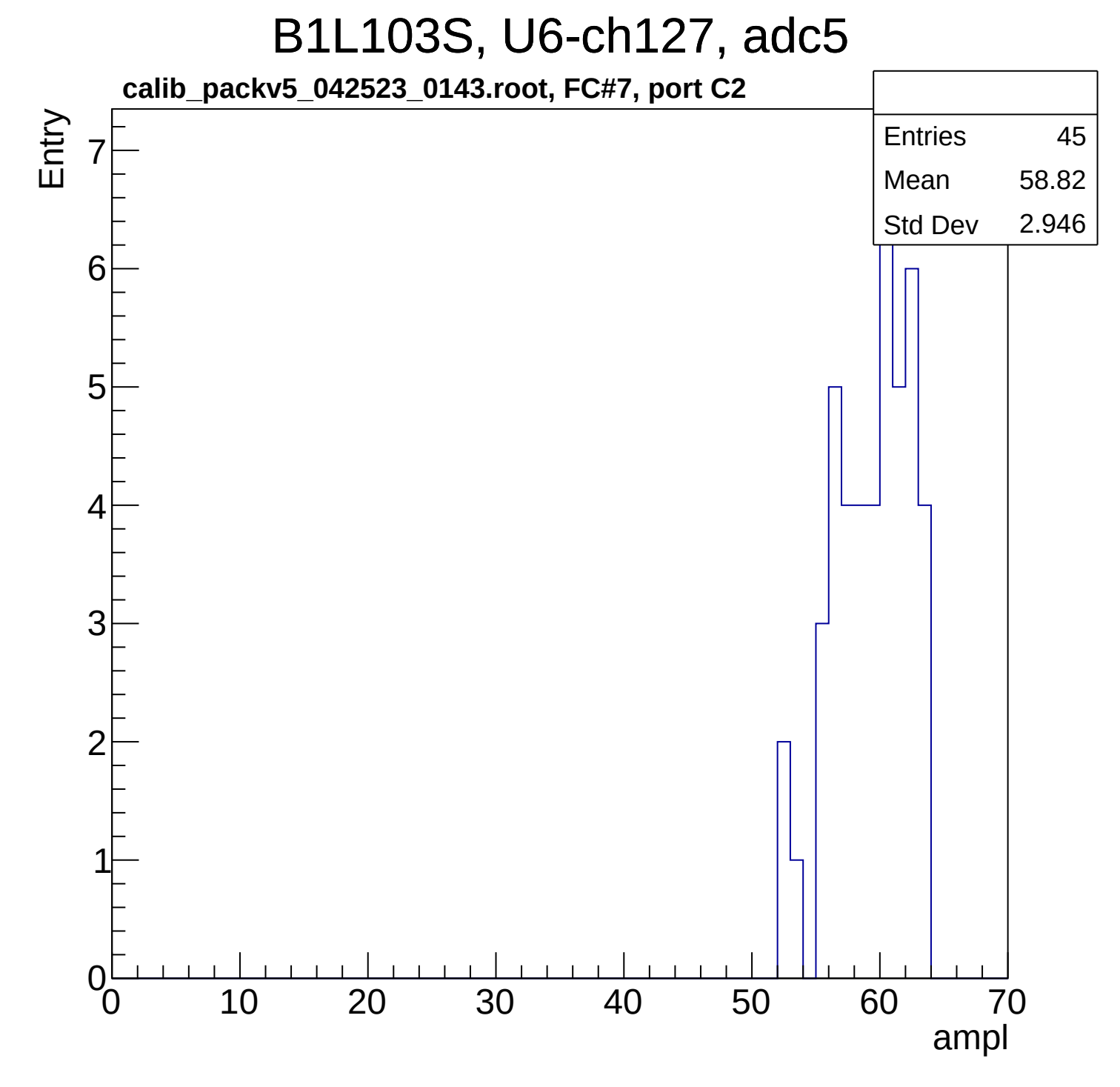
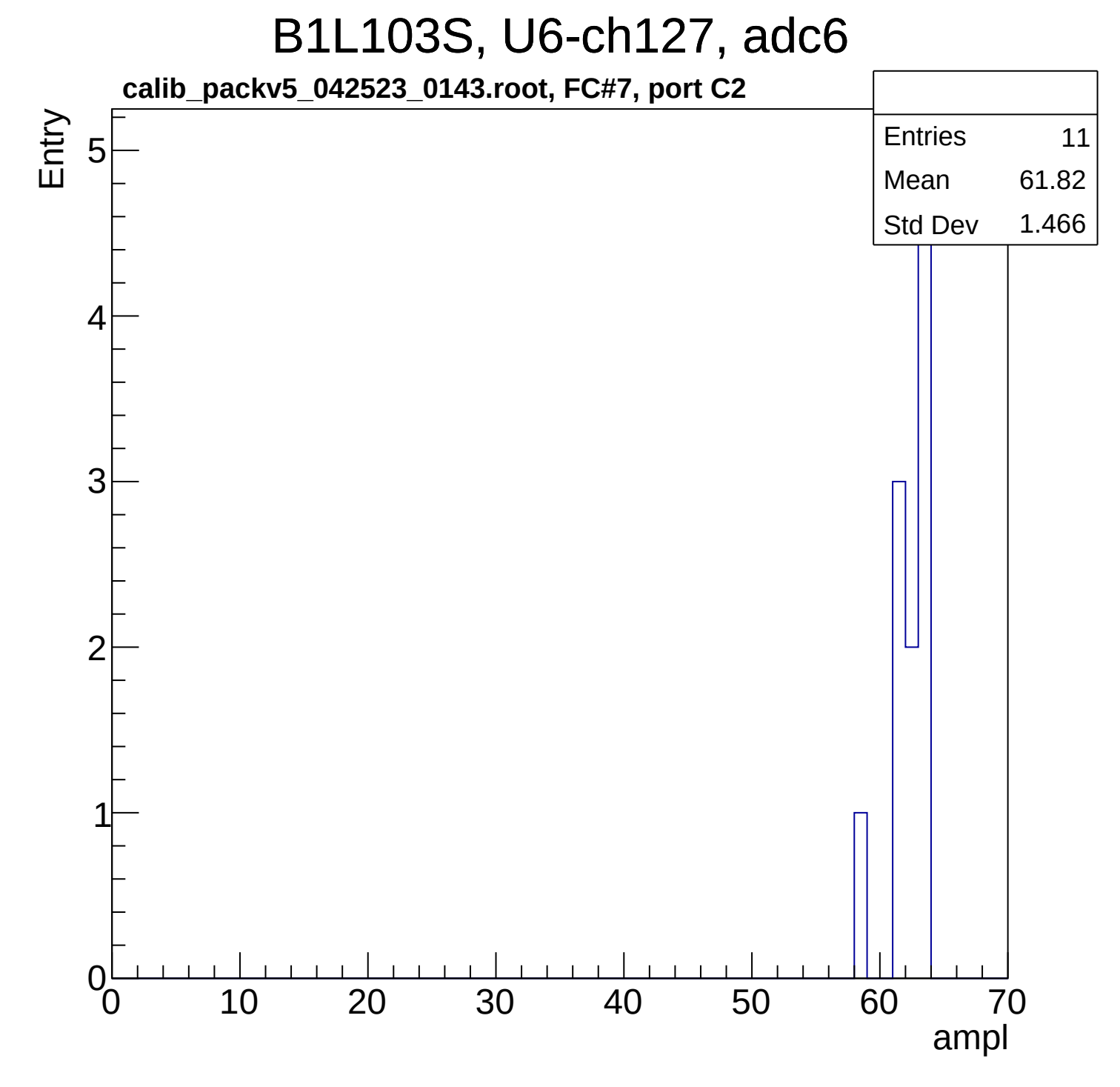
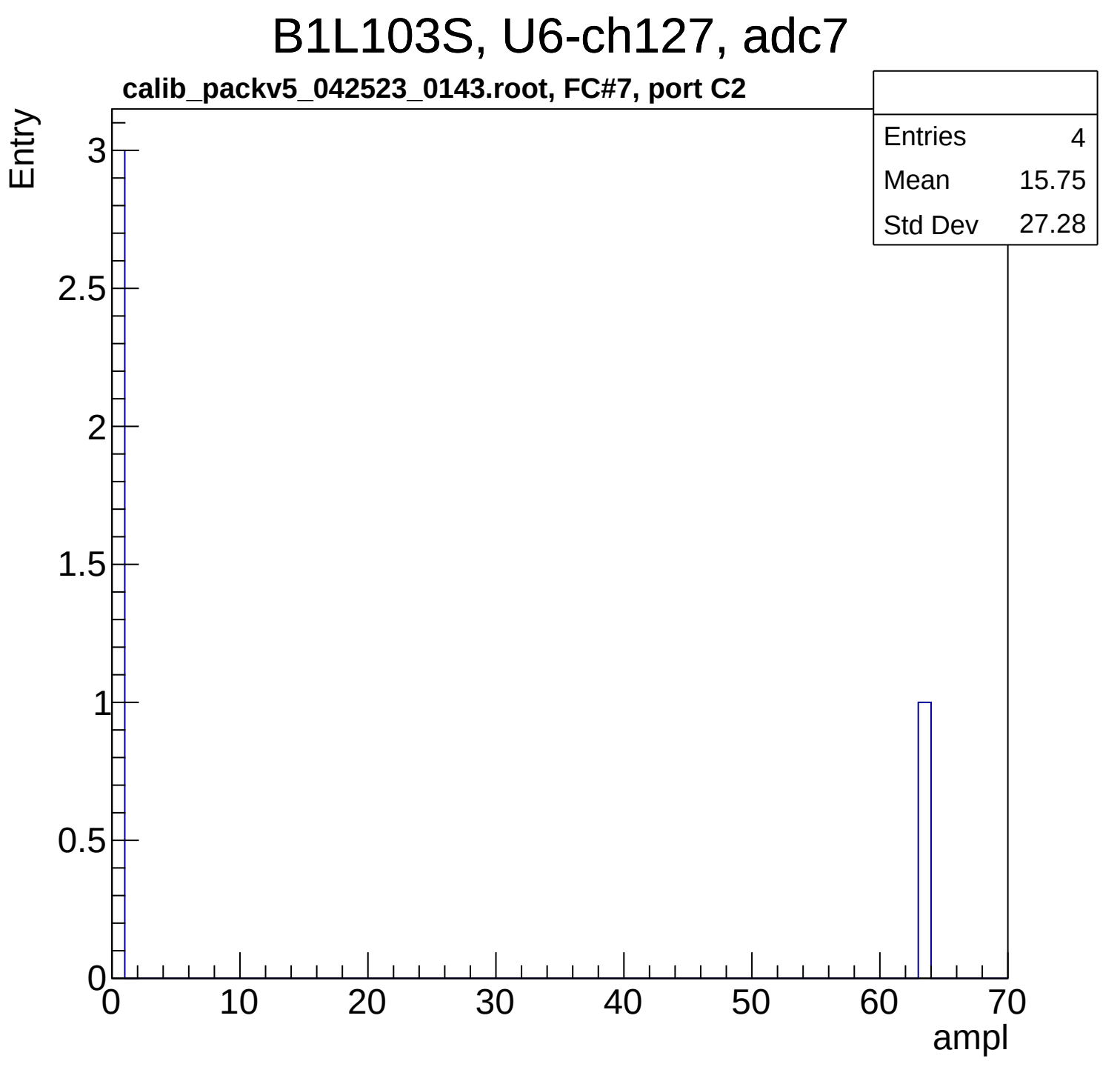
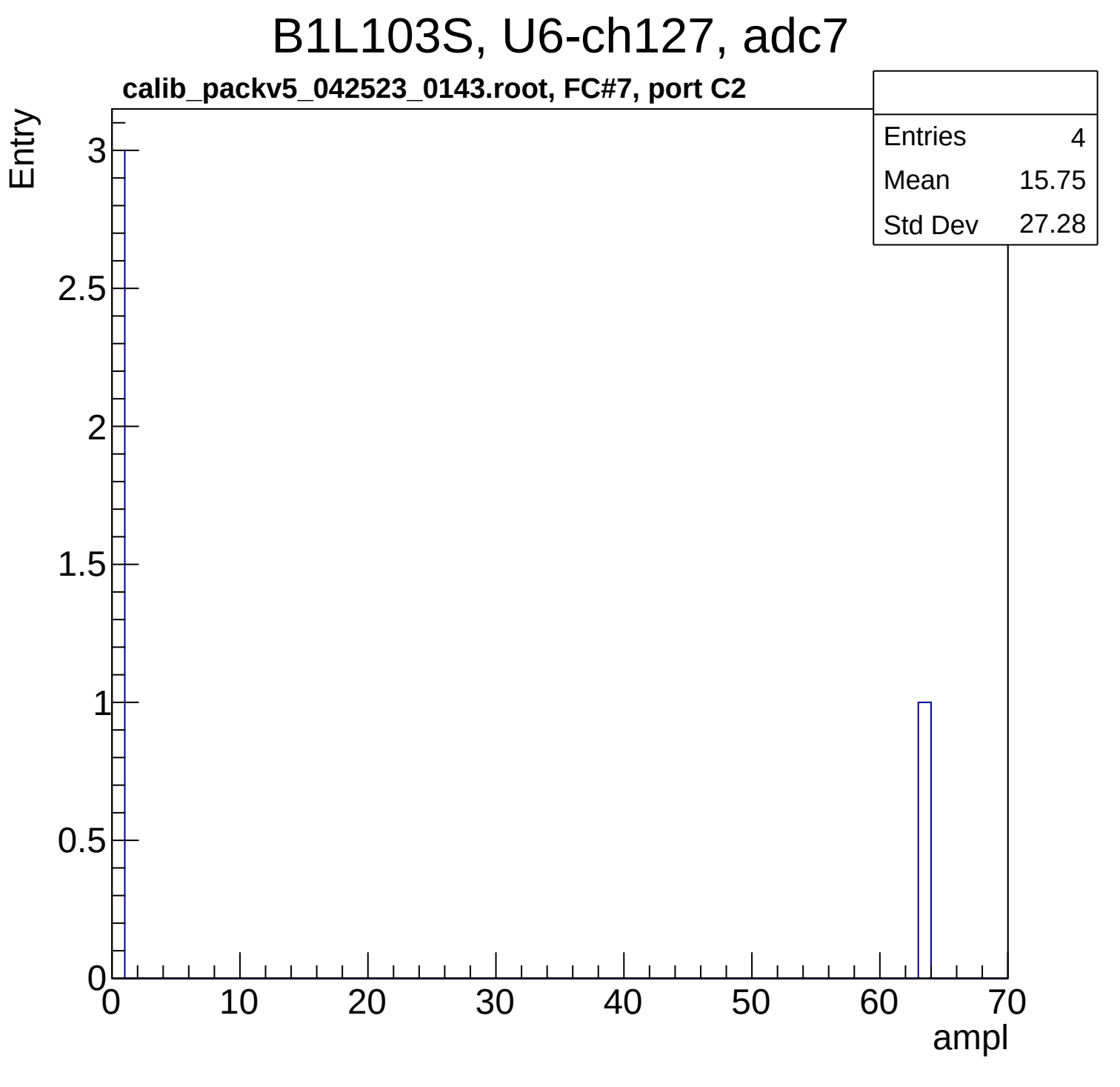

B1L103S, U6-ch127, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 54.7
Std Dev 3.906
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U6-ch127, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.82
Std Dev 2.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U6-ch127, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61.82
Std Dev 1.466
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U6-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl