

B1L100S, U9-ch0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.45
Std Dev	12.42

Turn on : 25.2429

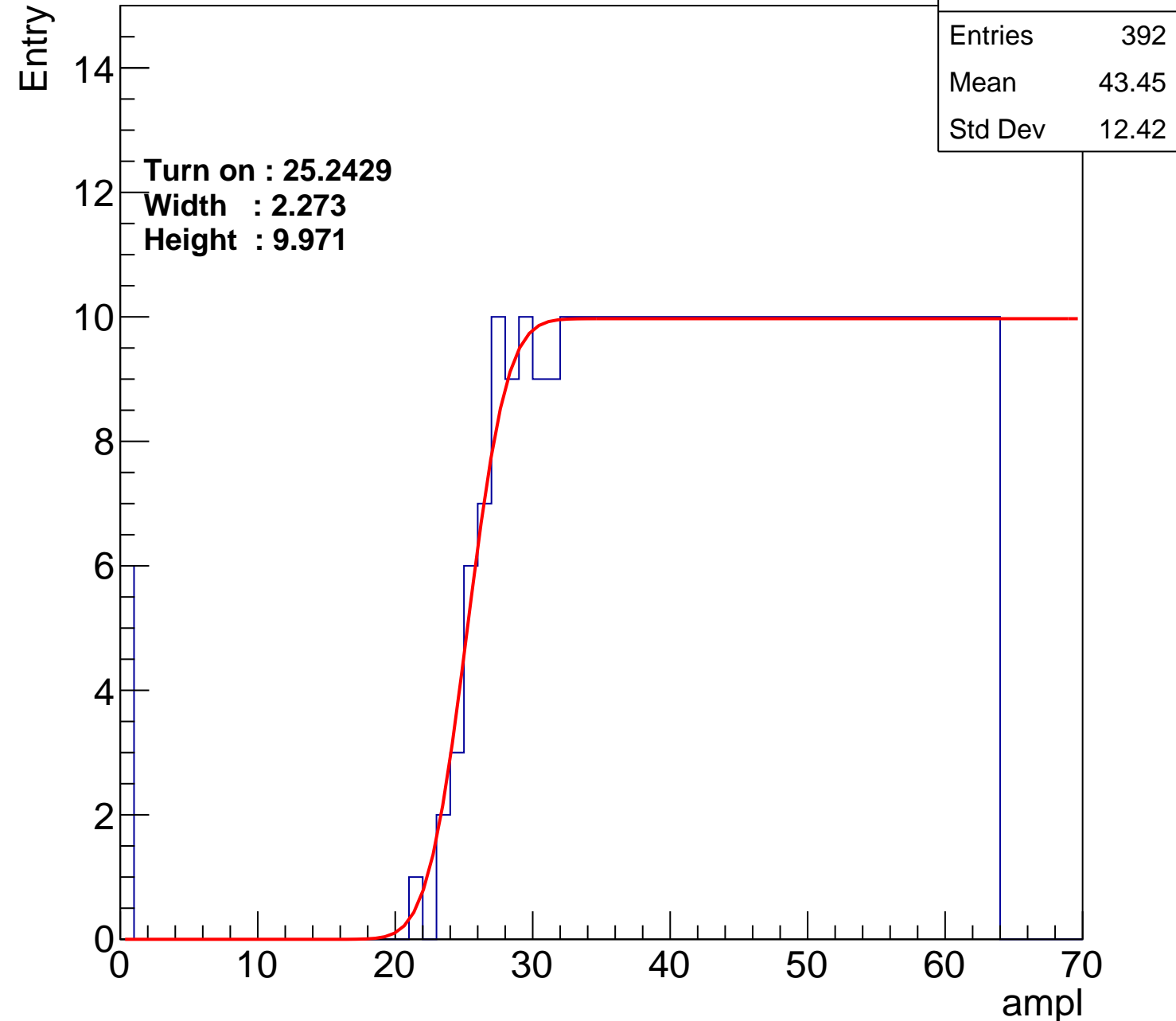
Width : 2.273

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch1

calib_packv5_042523_0143.root, FC#4, port A2

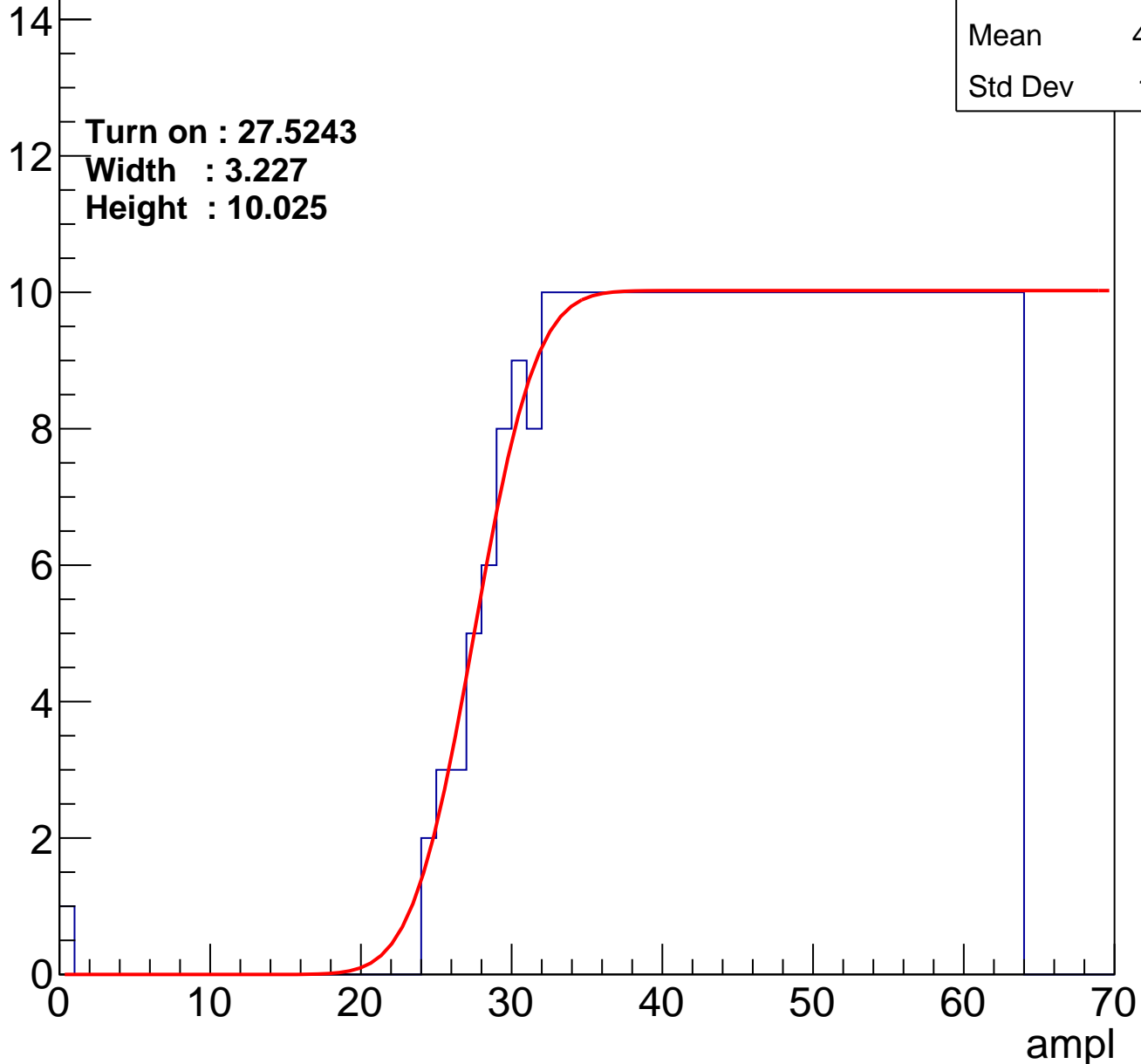
Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.5243

Width : 3.227

Height : 10.025

Entry



B1L100S, U9-ch2

calib_packv5_042523_0143.root, FC#4, port A2

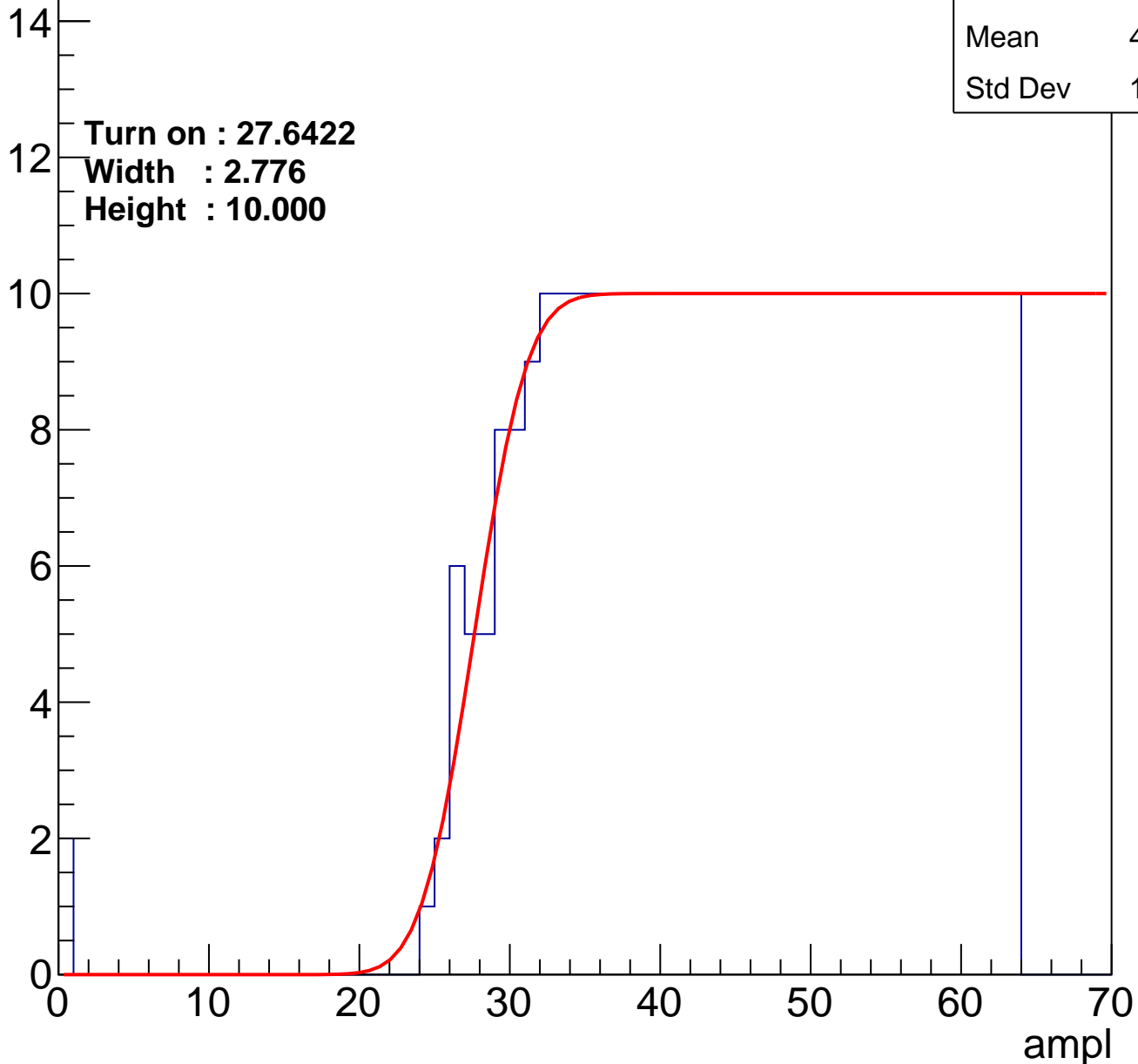
Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.6422

Width : 2.776

Height : 10.000

Entry



B1L100S, U9-ch3

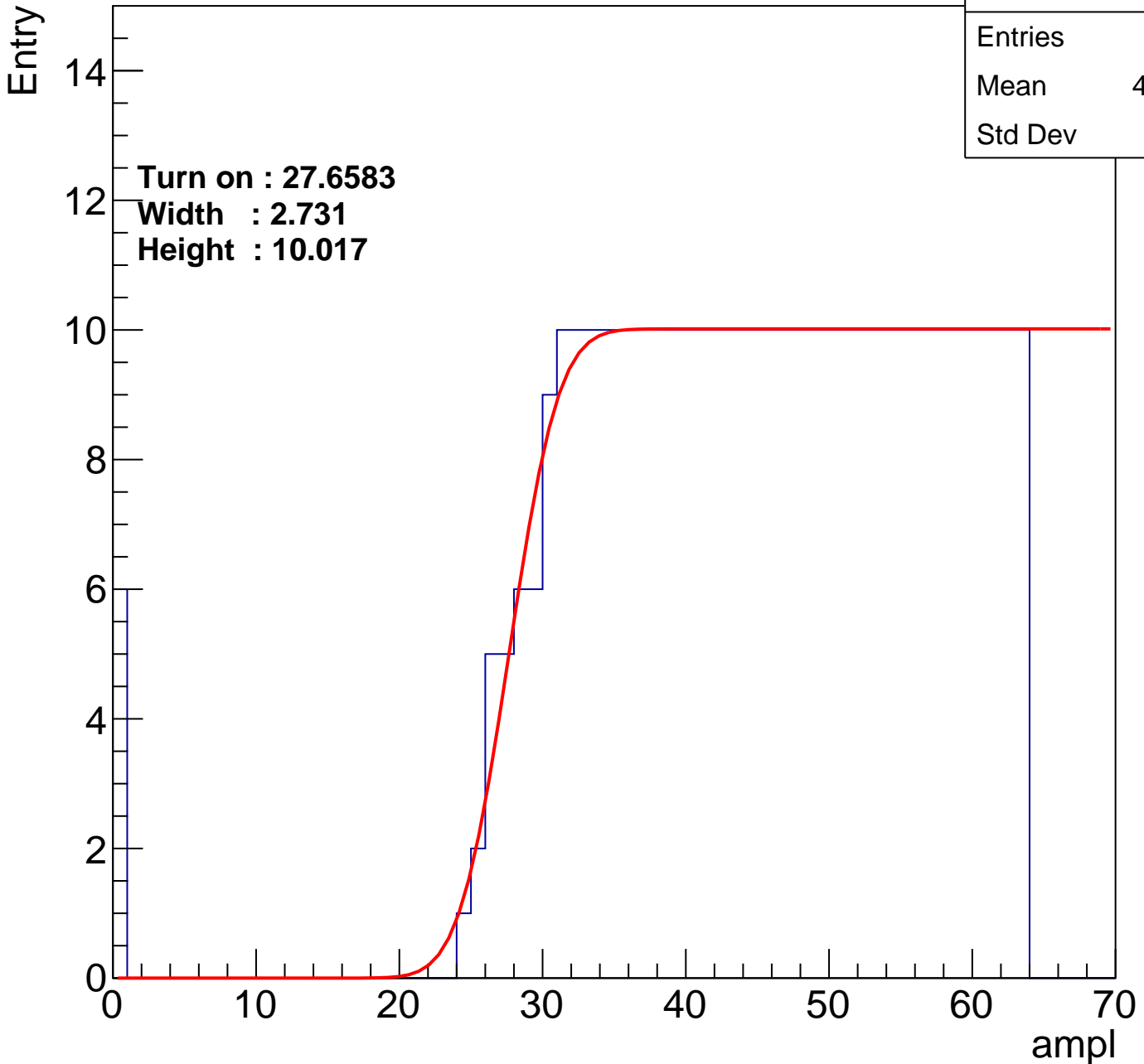
calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.49
Std Dev	12

Turn on : 27.6583

Width : 2.731

Height : 10.017



B1L100S, U9-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 27.3990

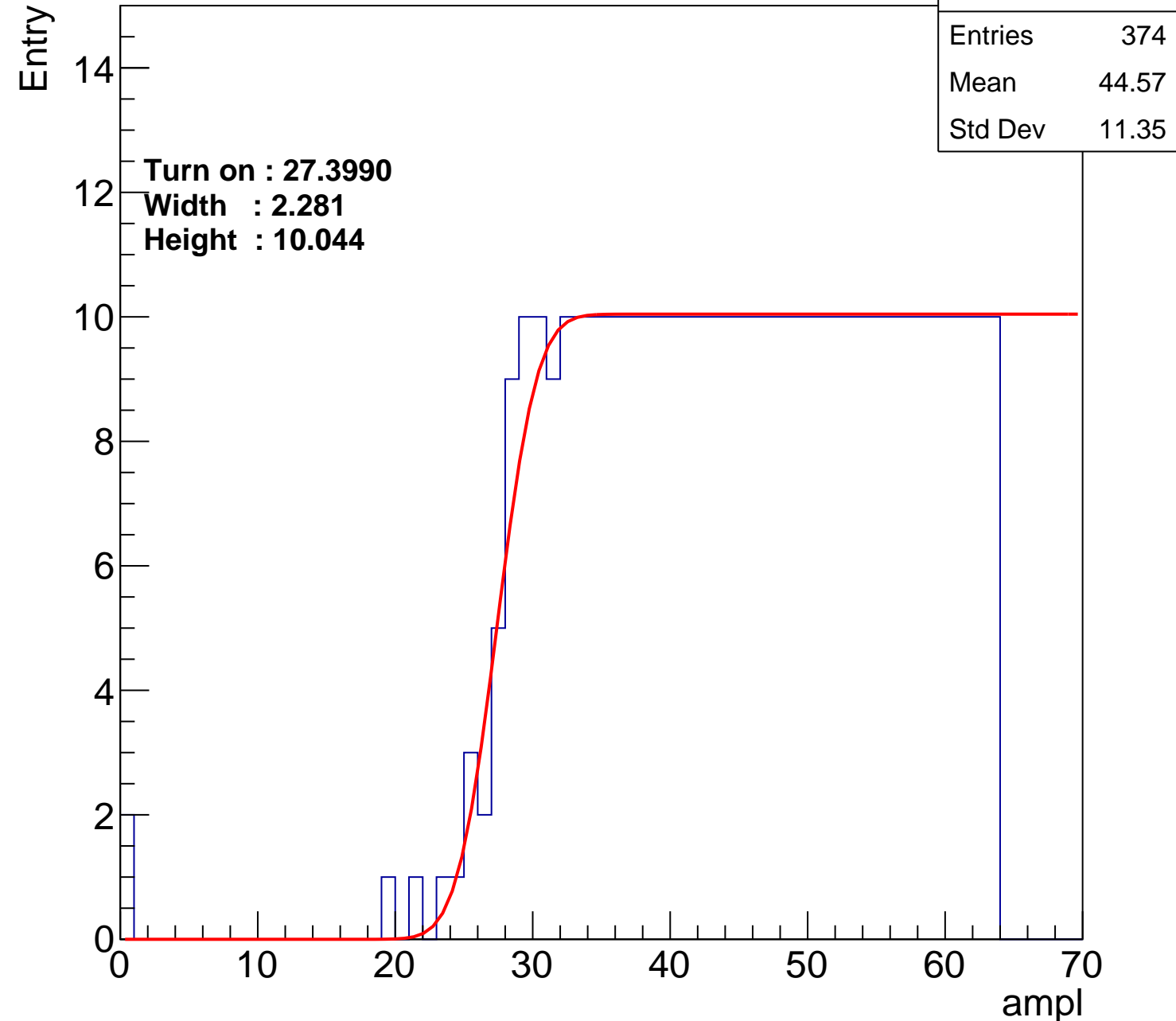
Width : 2.281

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch5

calib_packv5_042523_0143.root, FC#4, port A2

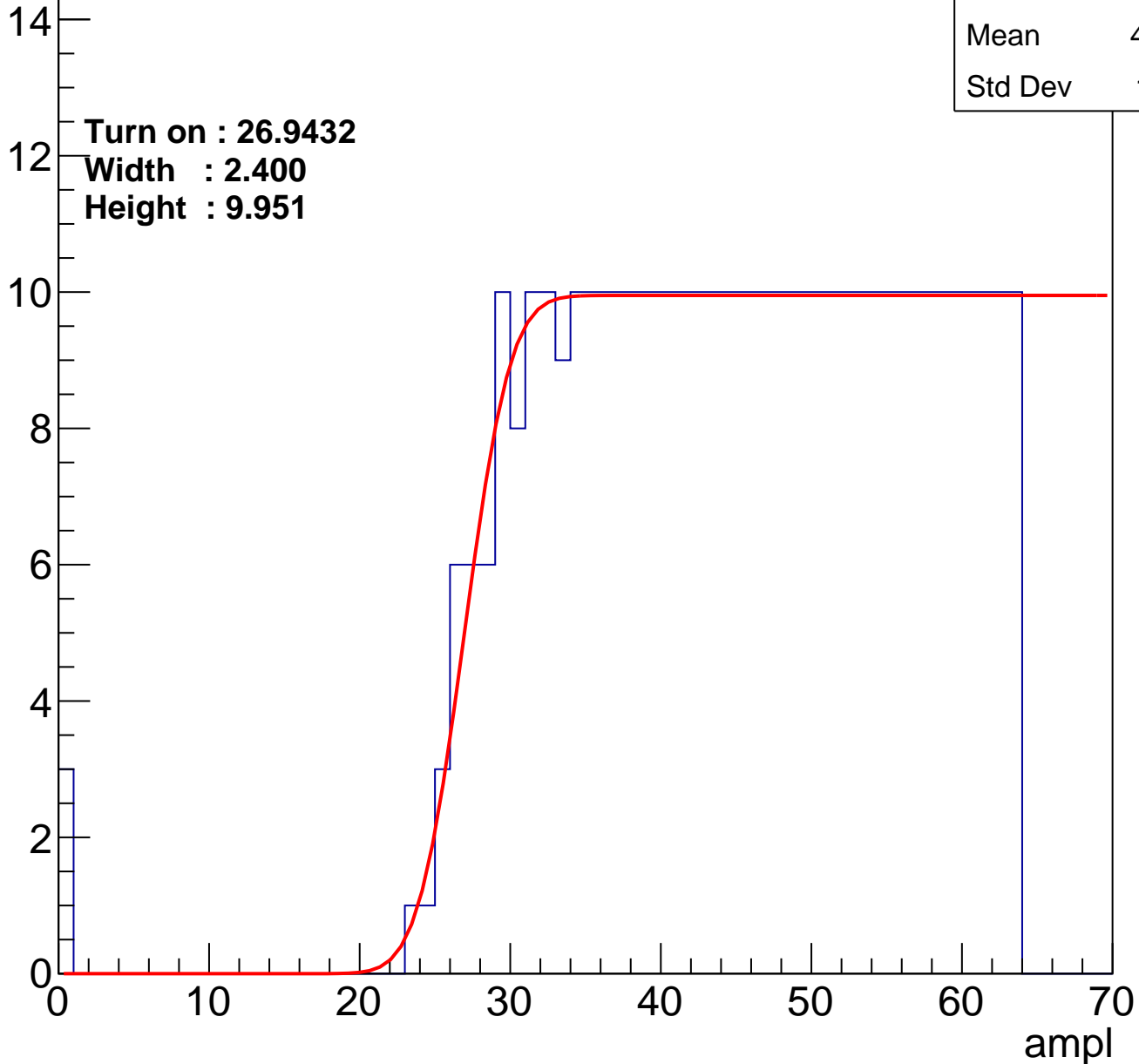
Entries	373
Mean	44.54
Std Dev	11.51

Turn on : 26.9432

Width : 2.400

Height : 9.951

Entry



B1L100S, U9-ch6

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	44.87
Std Dev	11.44

Turn on : 28.7611

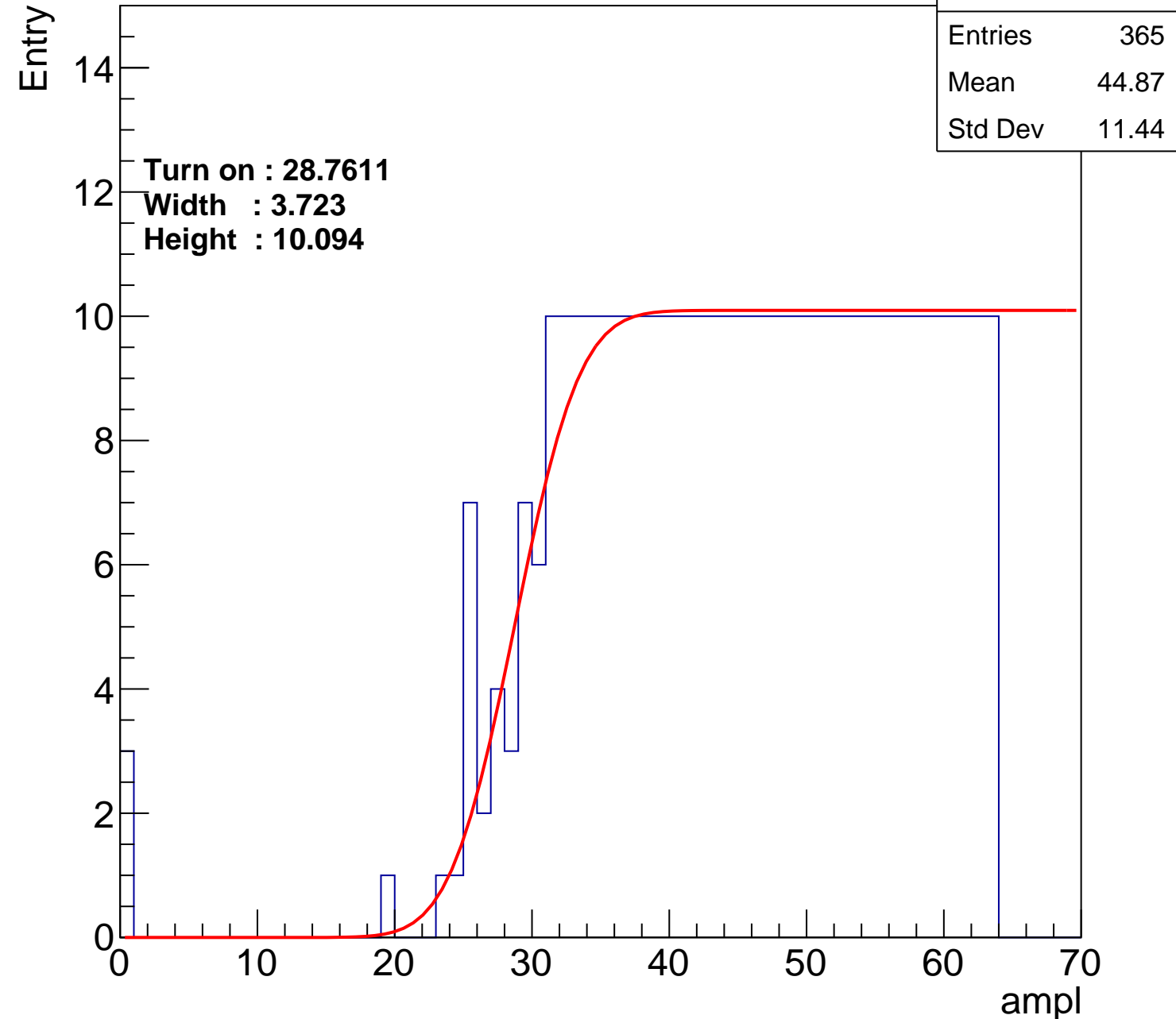
Width : 3.723

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch7

calib_packv5_042523_0143.root, FC#4, port A2

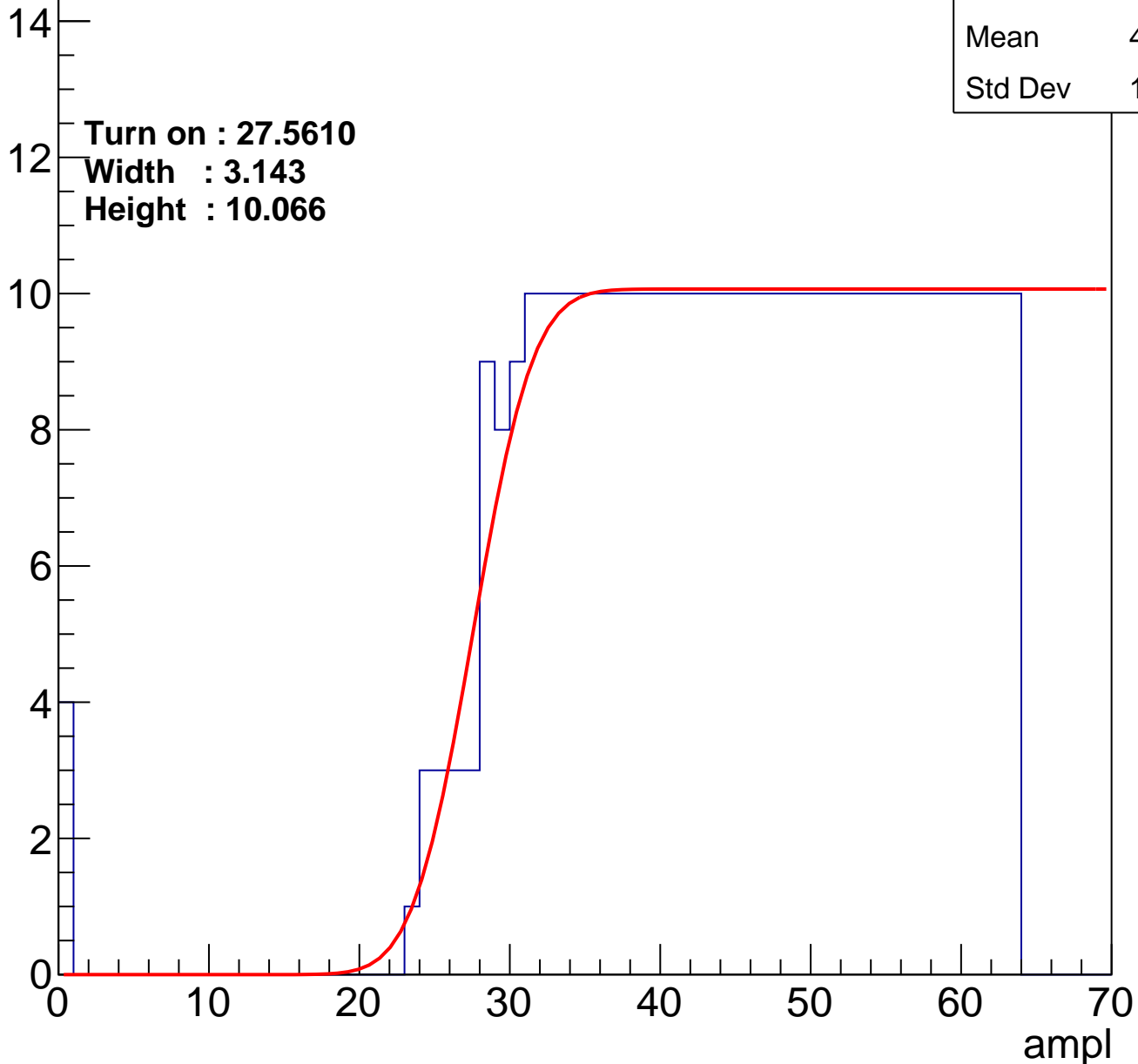
Entries	373
Mean	44.49
Std Dev	11.69

Turn on : 27.5610

Width : 3.143

Height : 10.066

Entry



B1L100S, U9-ch8

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.7
Std Dev	11.79

Turn on : 24.8278

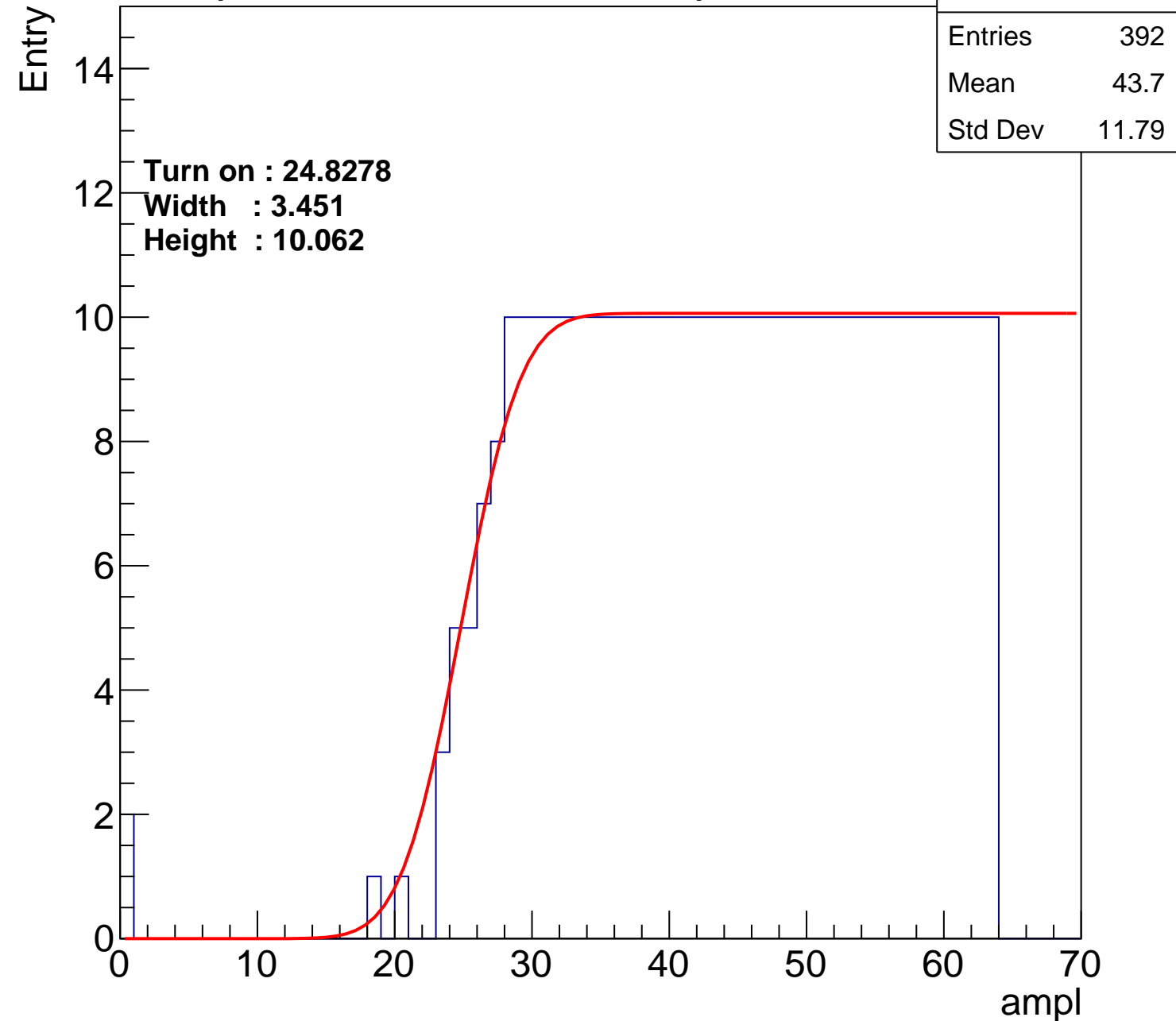
Width : 3.451

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch9

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.94
Std Dev	10.97

Turn on : 27.3635

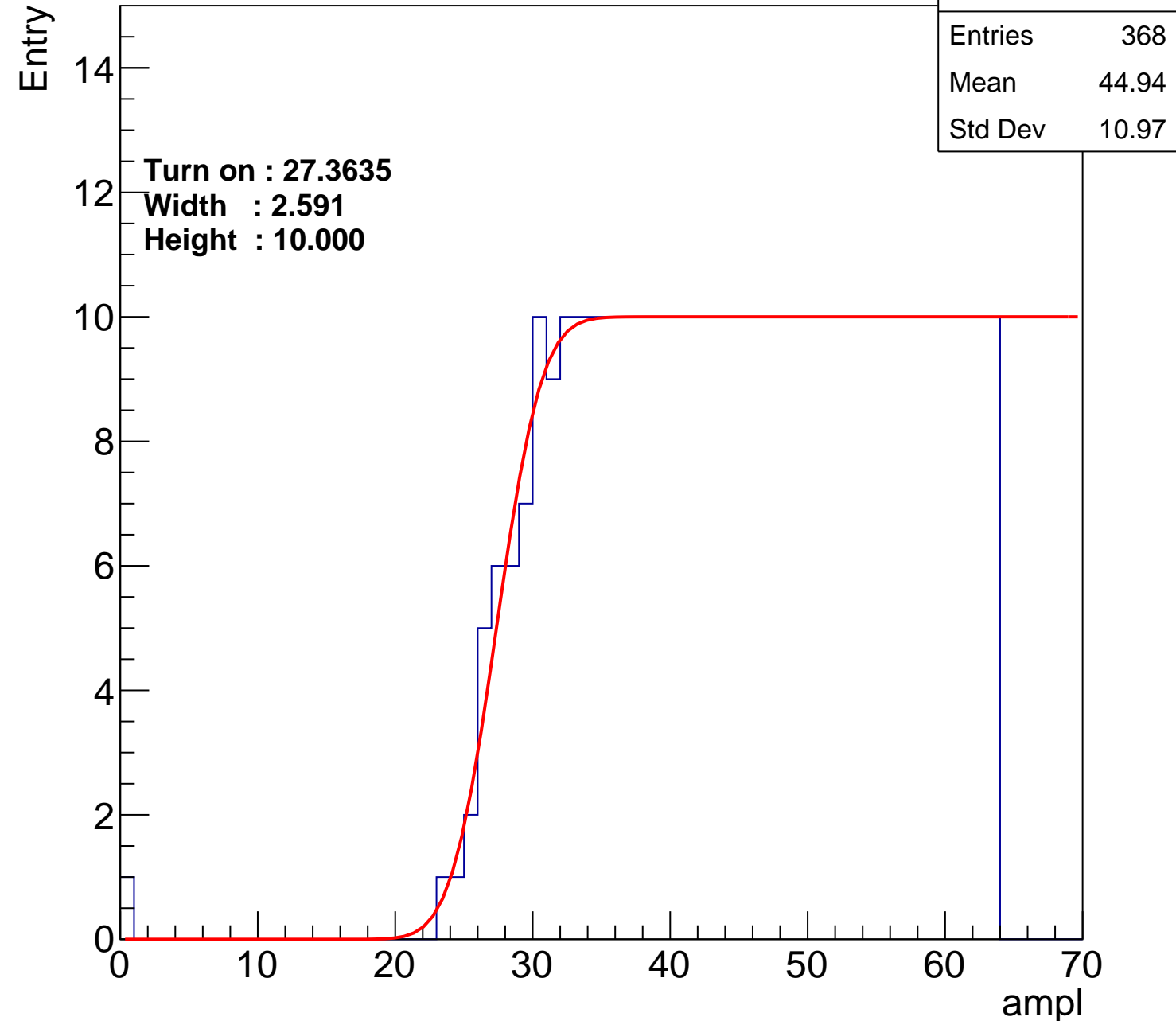
Width : 2.591

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch10

calib_packv5_042523_0143.root, FC#4, port A2

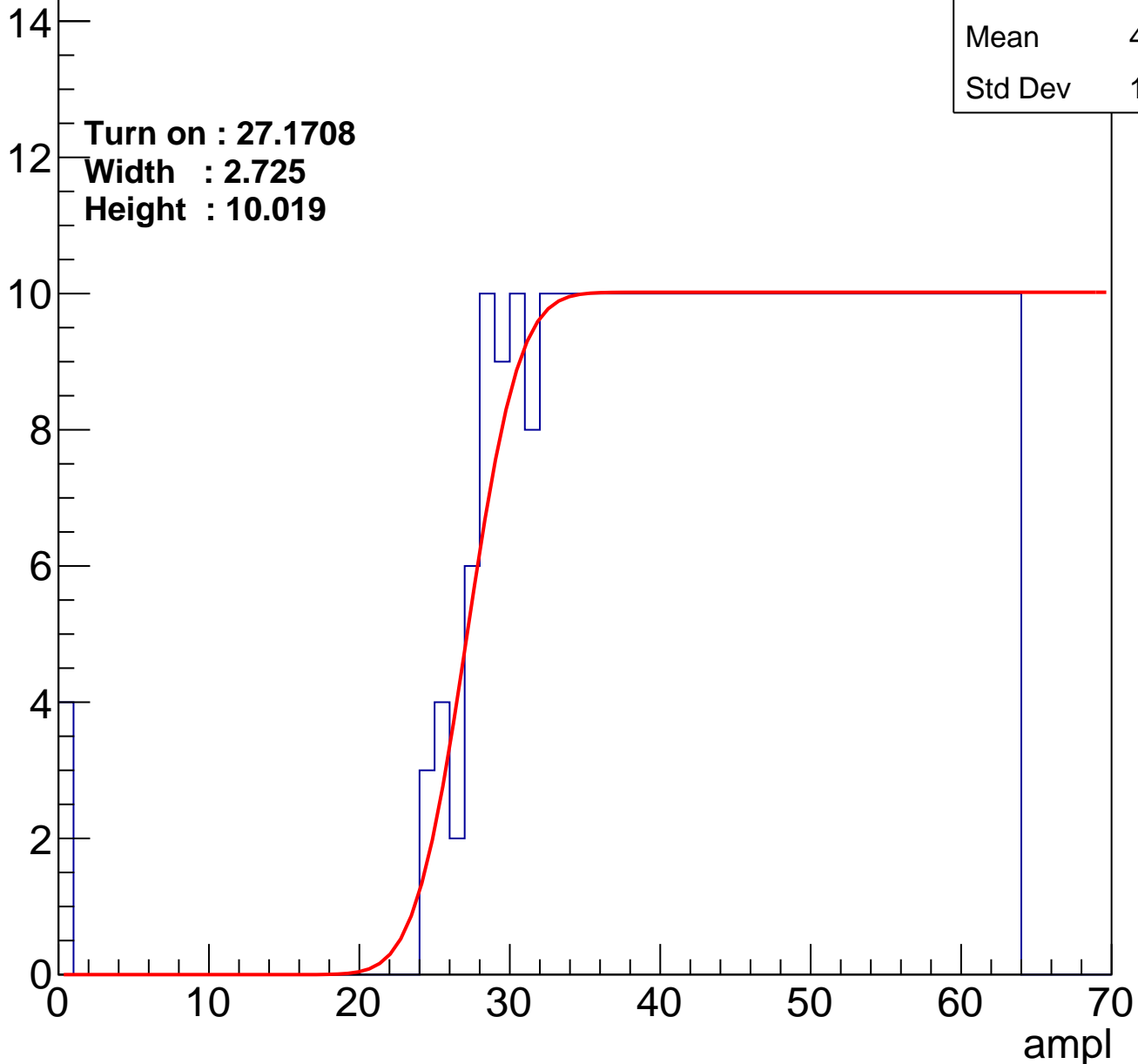
Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 27.1708

Width : 2.725

Height : 10.019

Entry



B1L100S, U9-ch11

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.56
Std Dev	11.63

Turn on : 26.7543

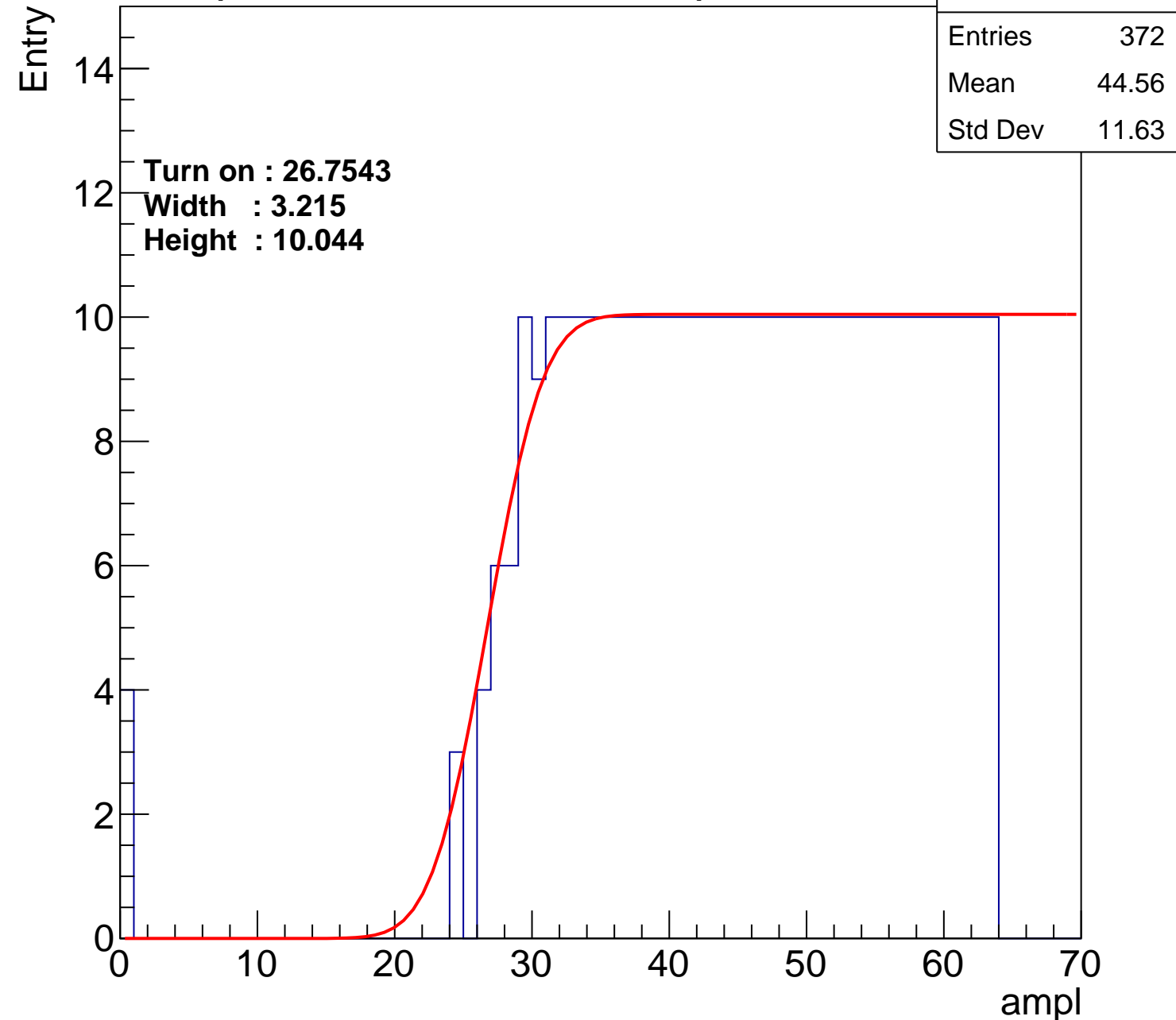
Width : 3.215

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch12

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.14
Std Dev	11.22

Turn on : 27.8959

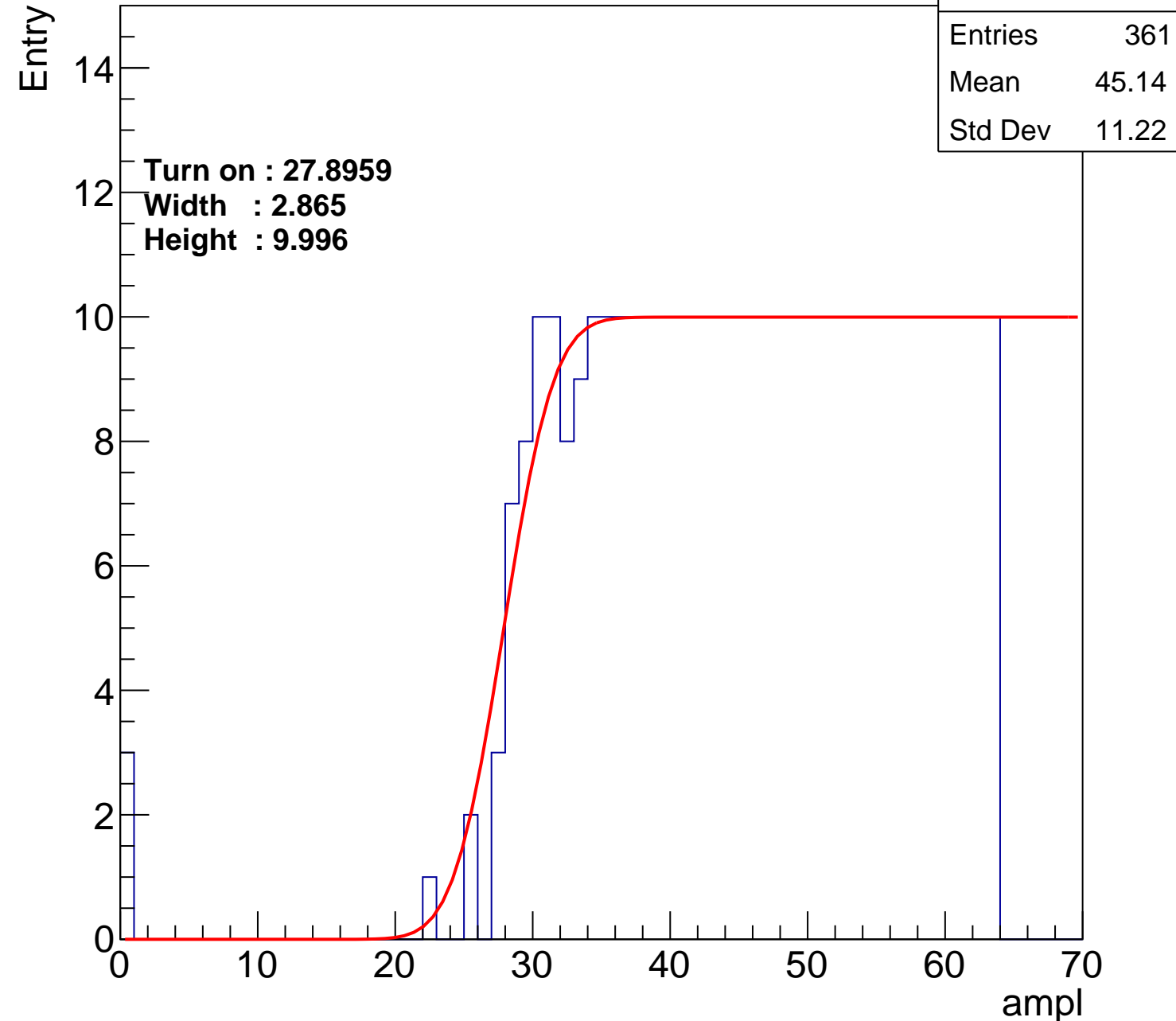
Width : 2.865

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch13

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.21
Std Dev	11.86

Turn on : 26.8260

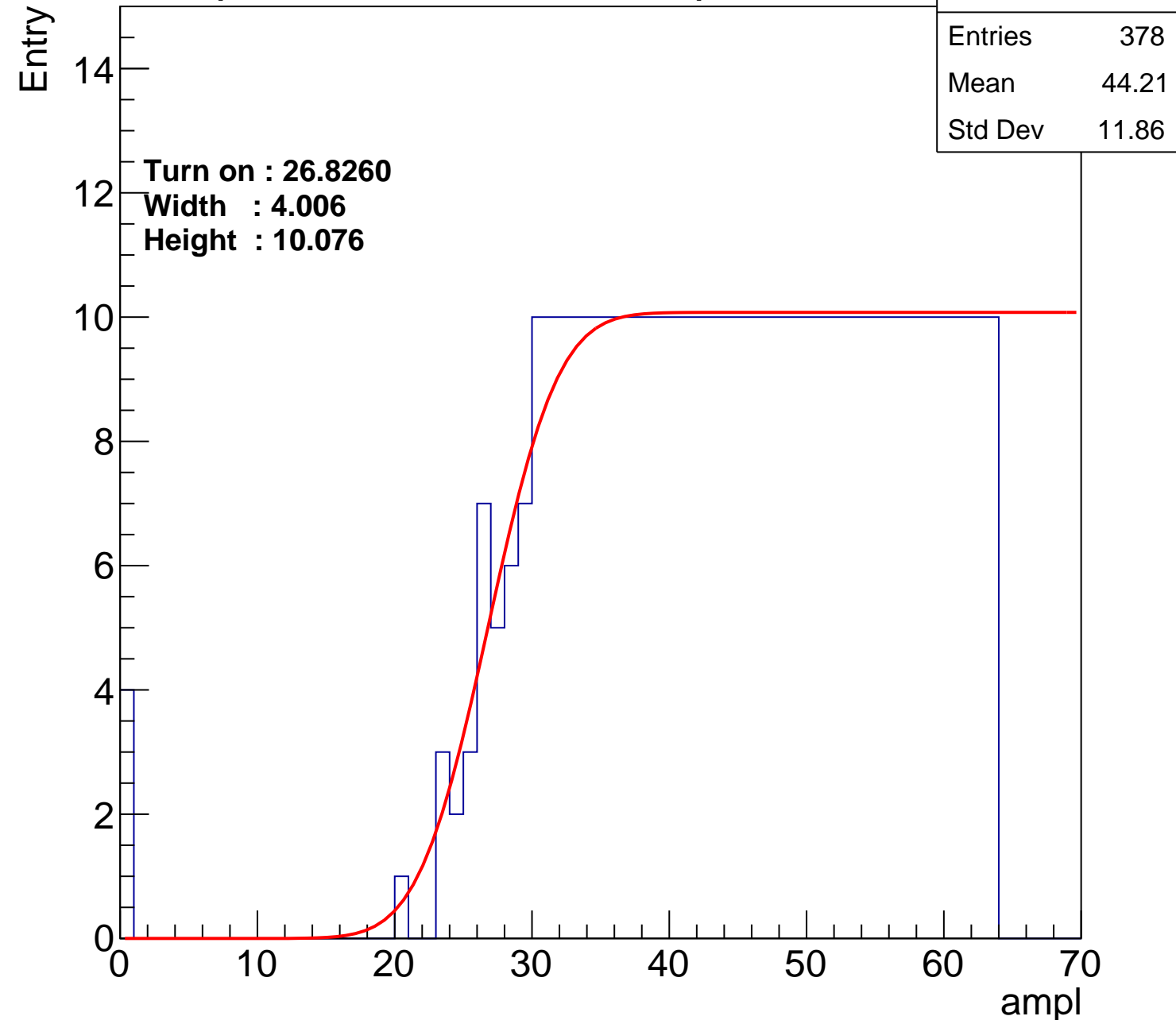
Width : 4.006

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch14

calib_packv5_042523_0143.root, FC#4, port A2

Entries	352
Mean	45.34
Std Dev	11.66

Turn on : 29.9137

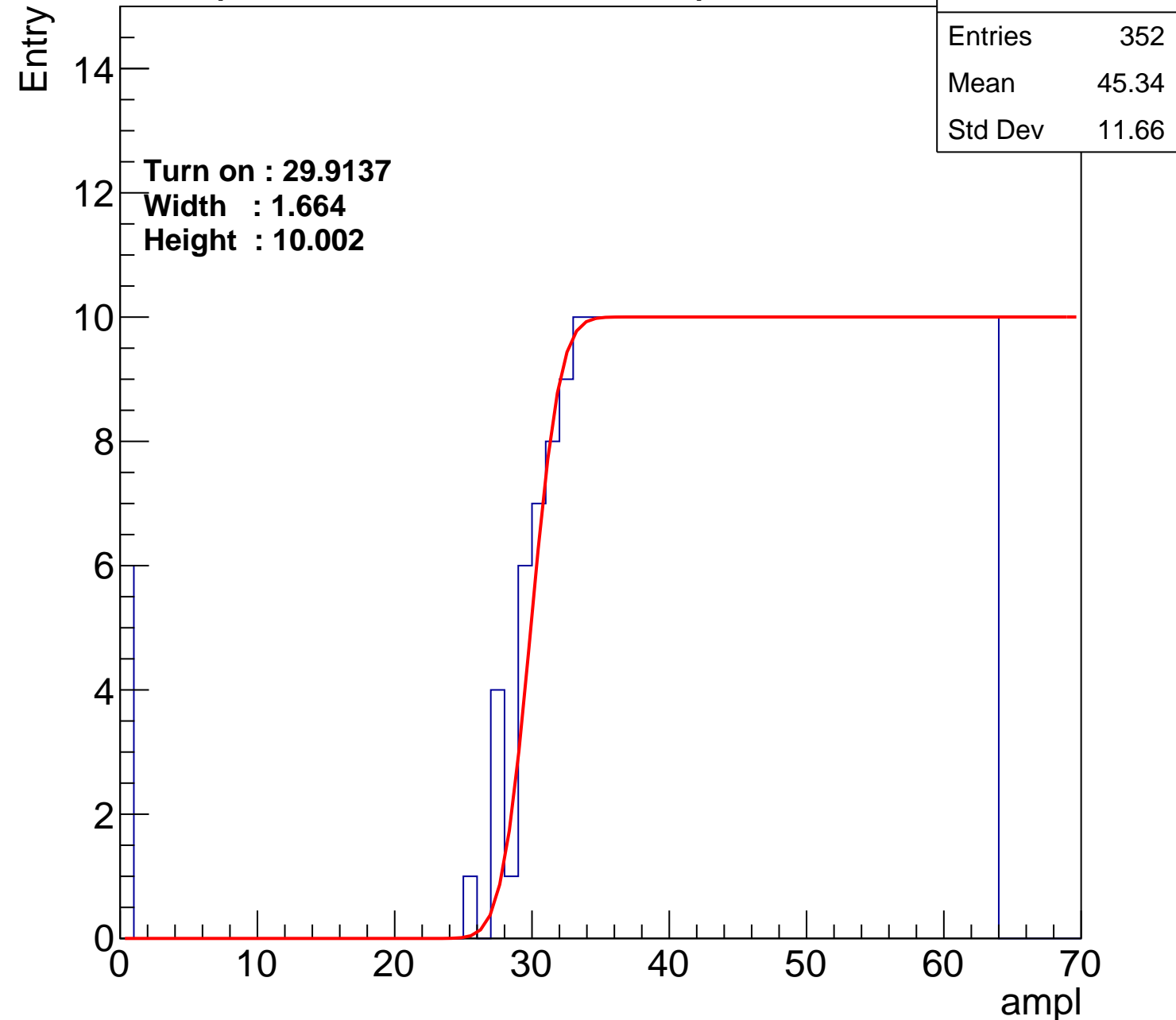
Width : 1.664

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch15

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 26.2741

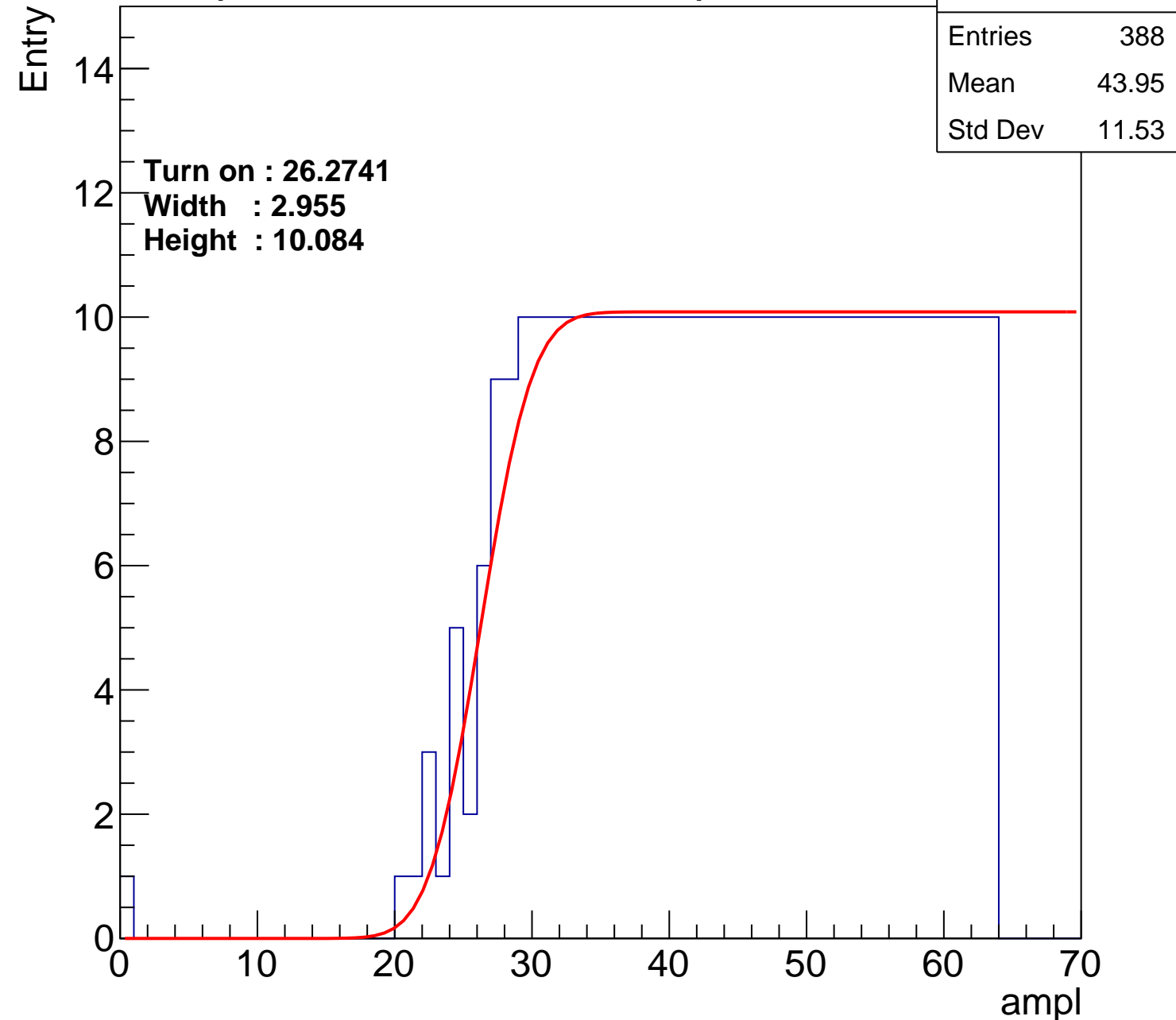
Width : 2.955

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch16

calib_packv5_042523_0143.root, FC#4, port A2

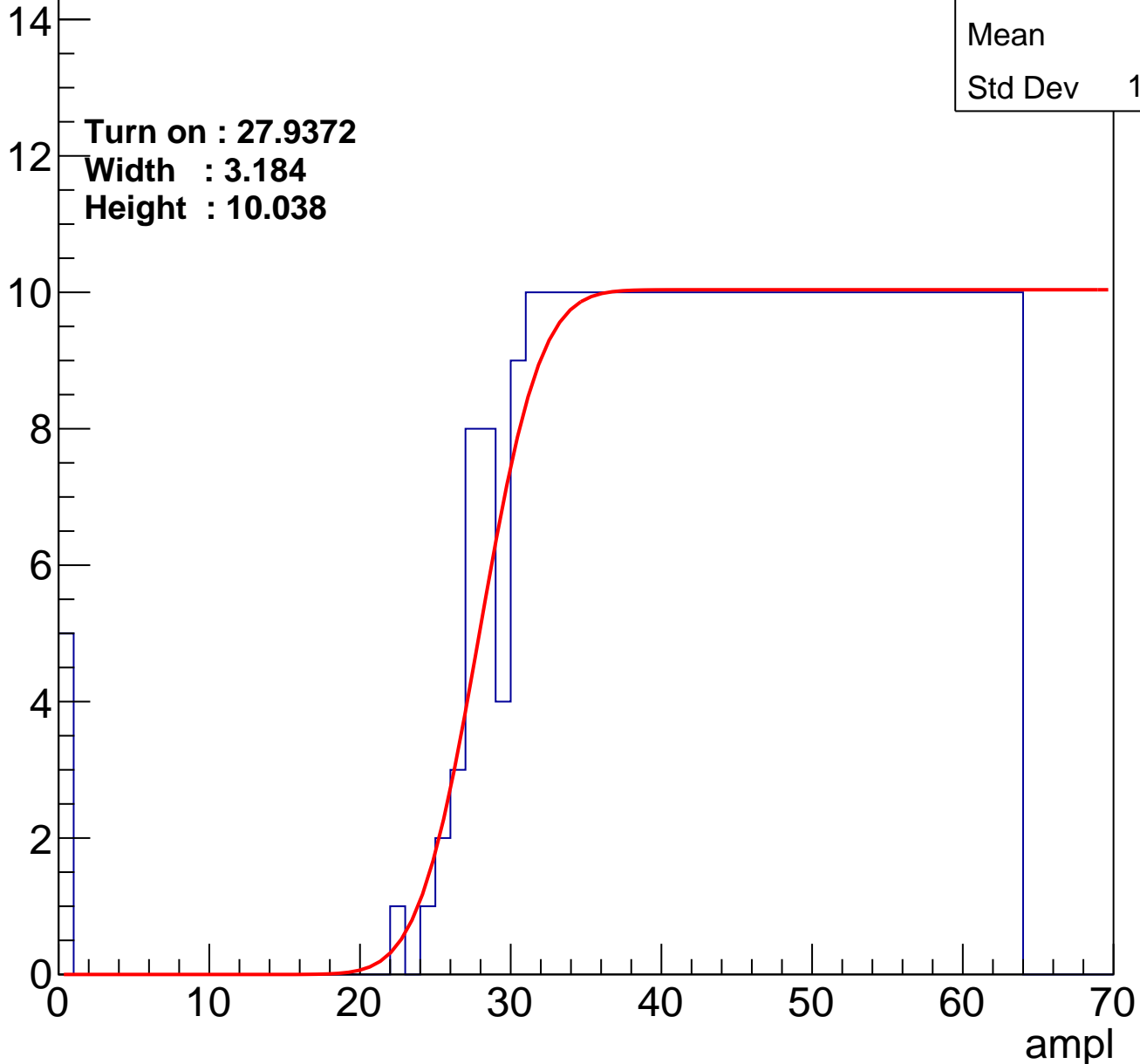
Entries	371
Mean	44.5
Std Dev	11.85

Turn on : 27.9372

Width : 3.184

Height : 10.038

Entry



B1L100S, U9-ch17

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.36
Std Dev	11.28

Turn on : 28.7863

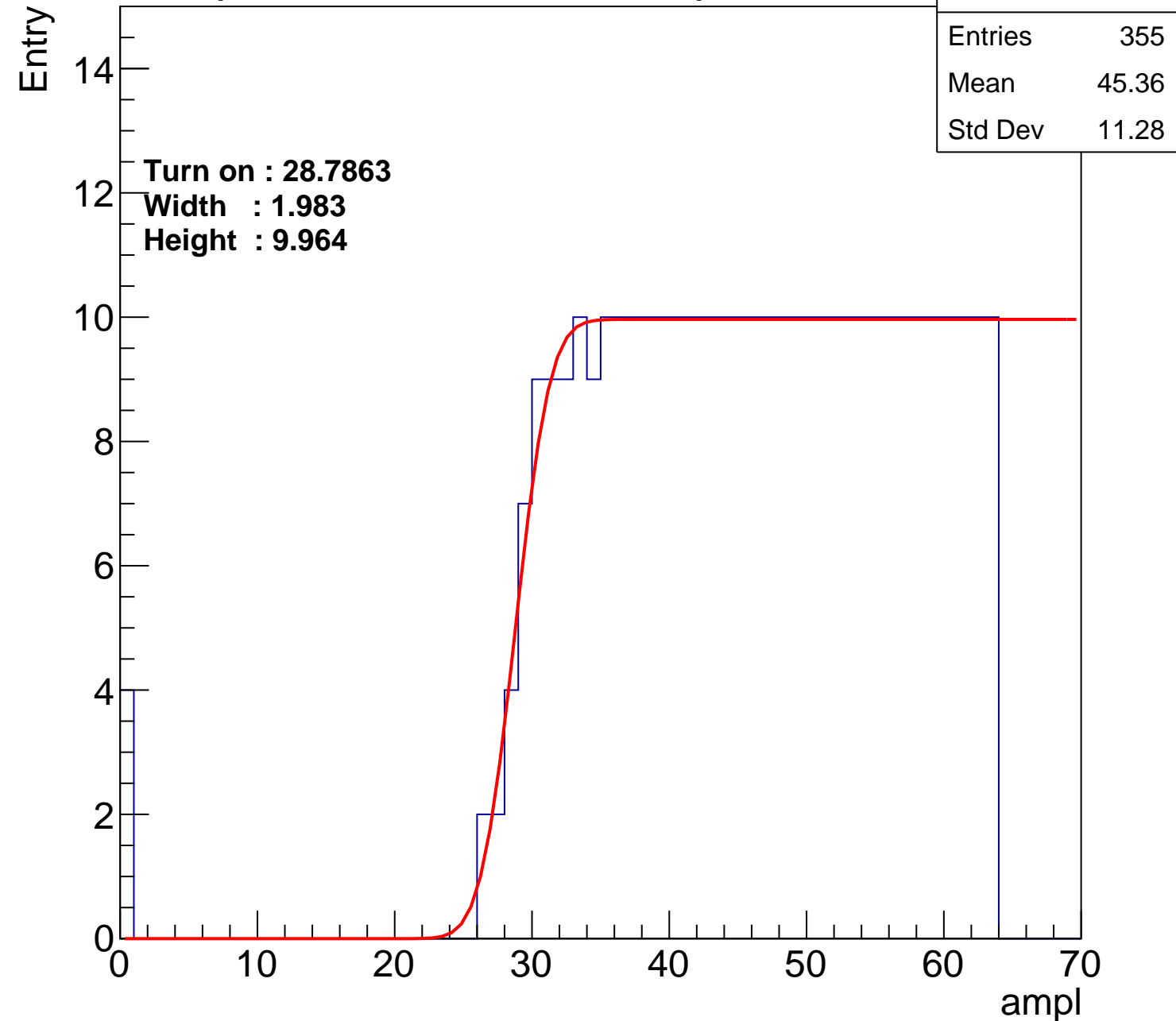
Width : 1.983

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch18

calib_packv5_042523_0143.root, FC#4, port A2

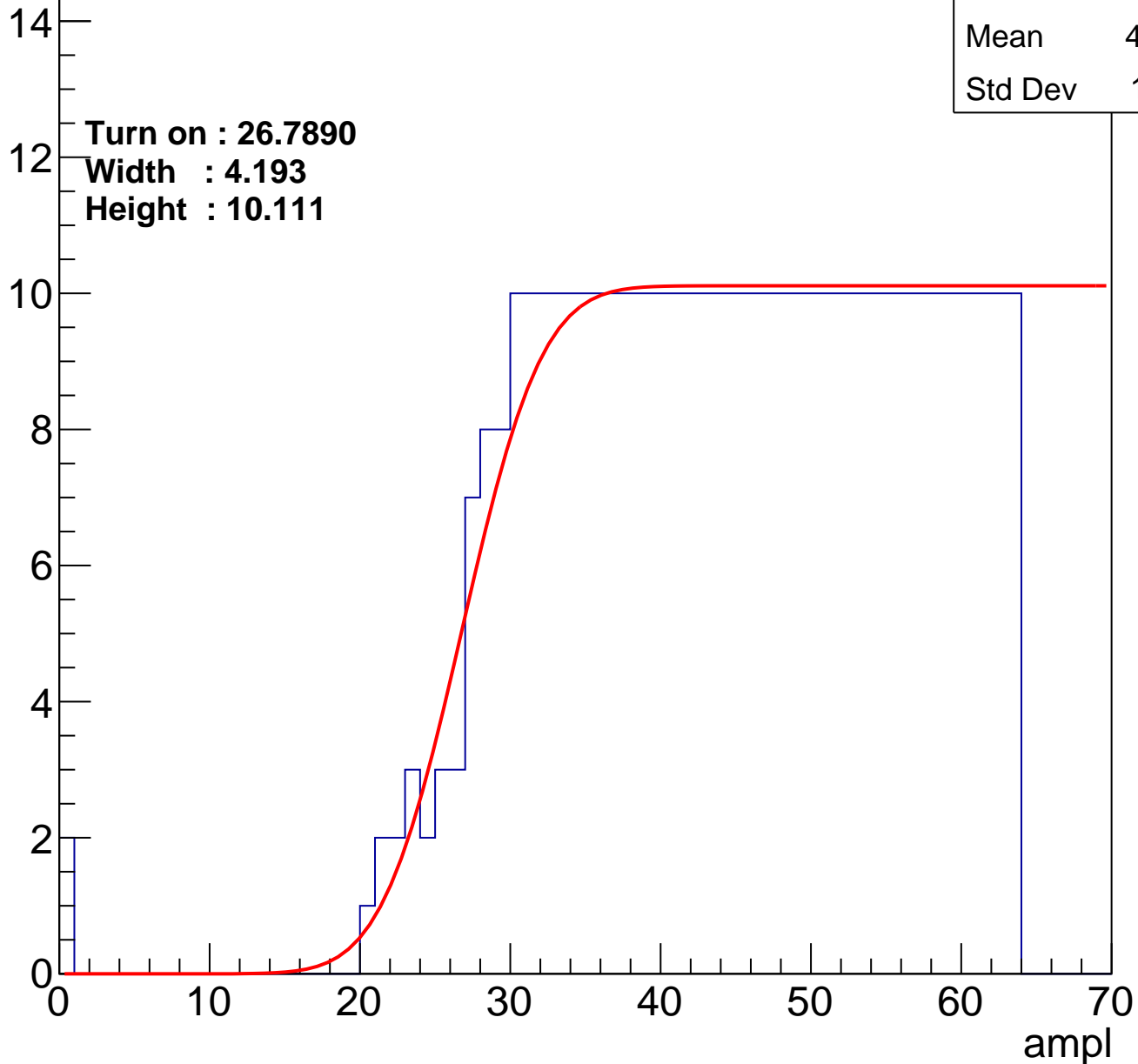
Entries	381
Mean	44.18
Std Dev	11.61

Turn on : 26.7890

Width : 4.193

Height : 10.111

Entry



B1L100S, U9-ch19

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.21
Std Dev	11.08

Turn on : 28.9118

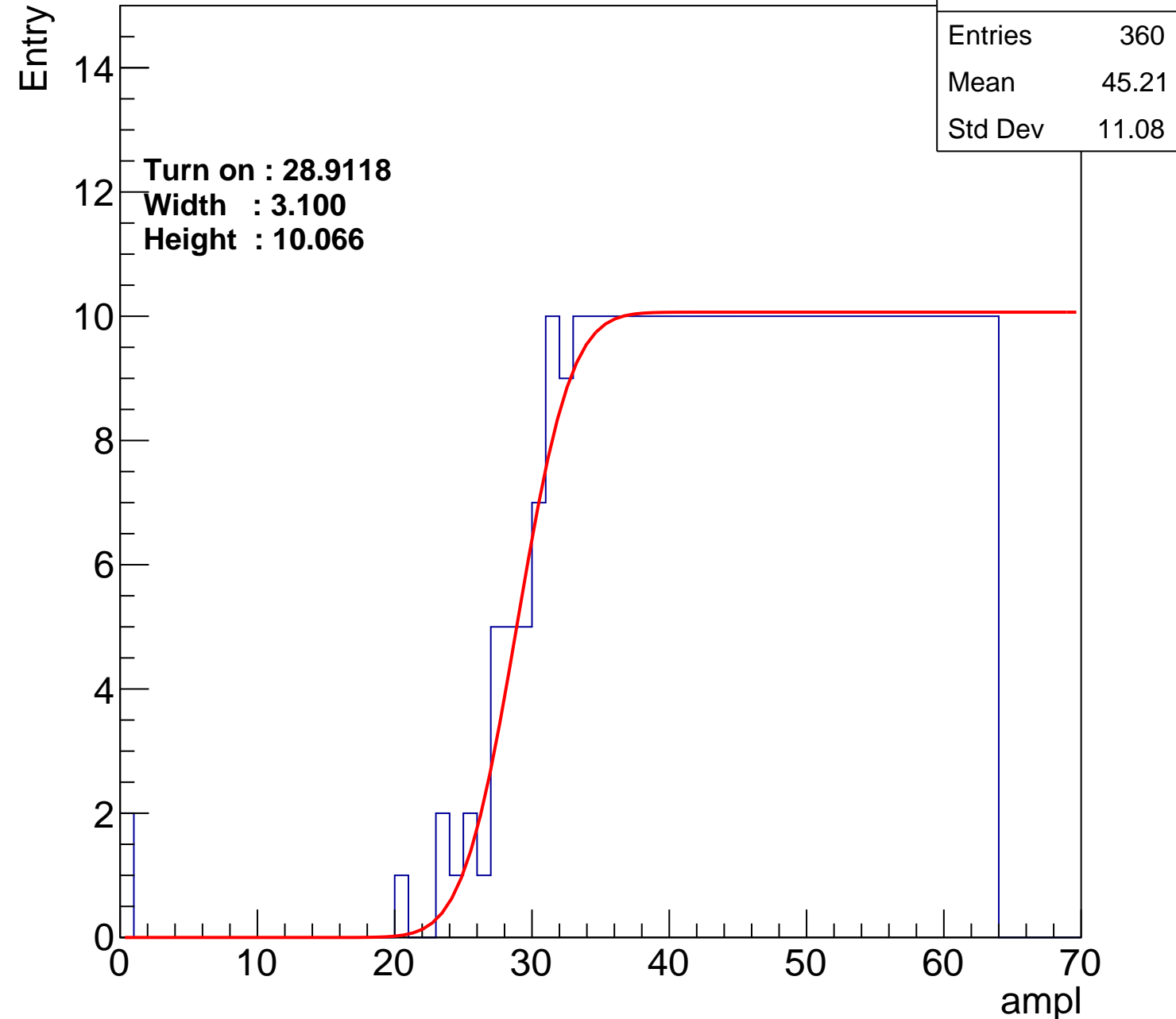
Width : 3.100

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 25.9534

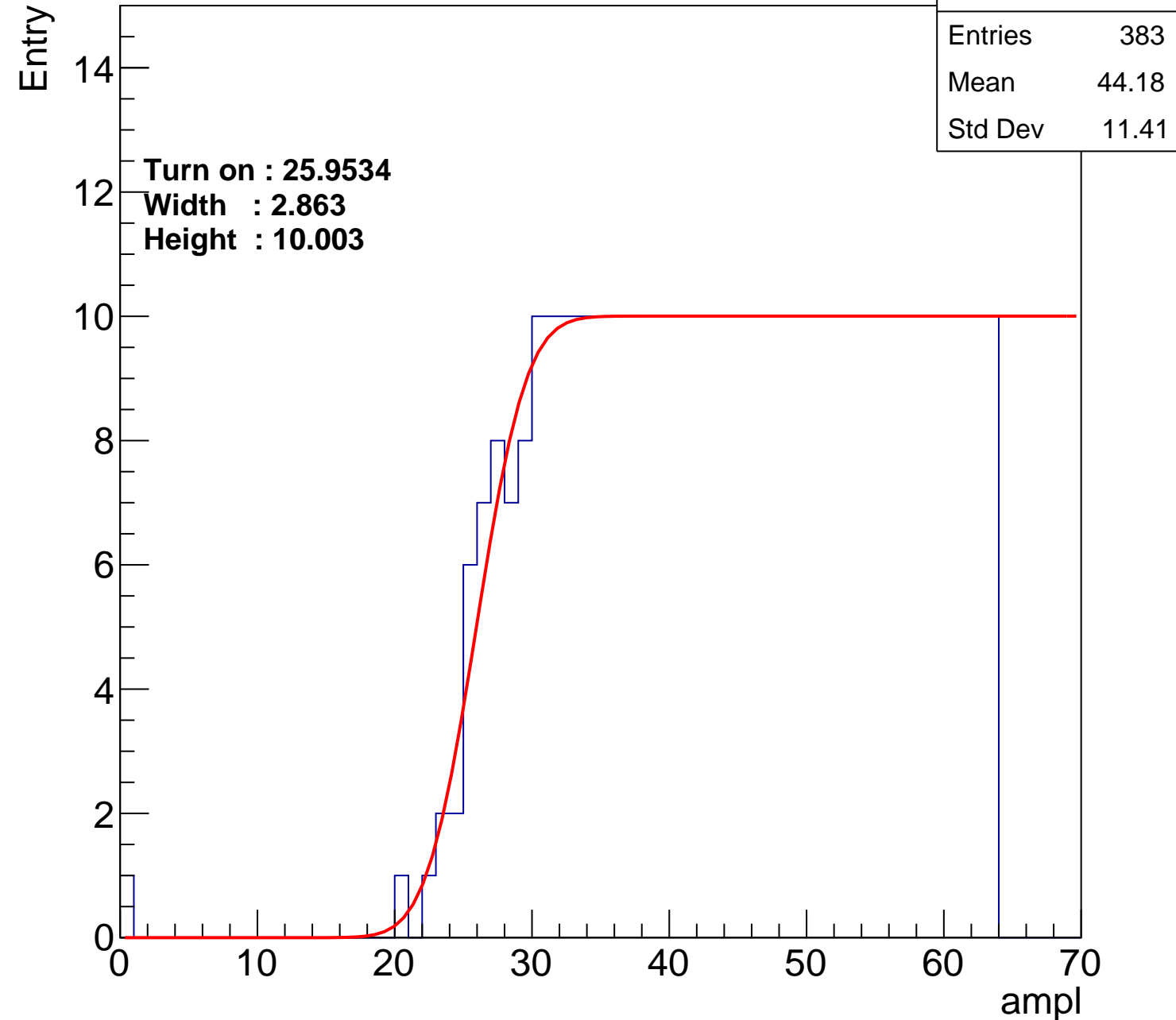
Width : 2.863

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch21

calib_packv5_042523_0143.root, FC#4, port A2

Entries	351
Mean	45.72
Std Dev	10.74

Turn on : 28.9615

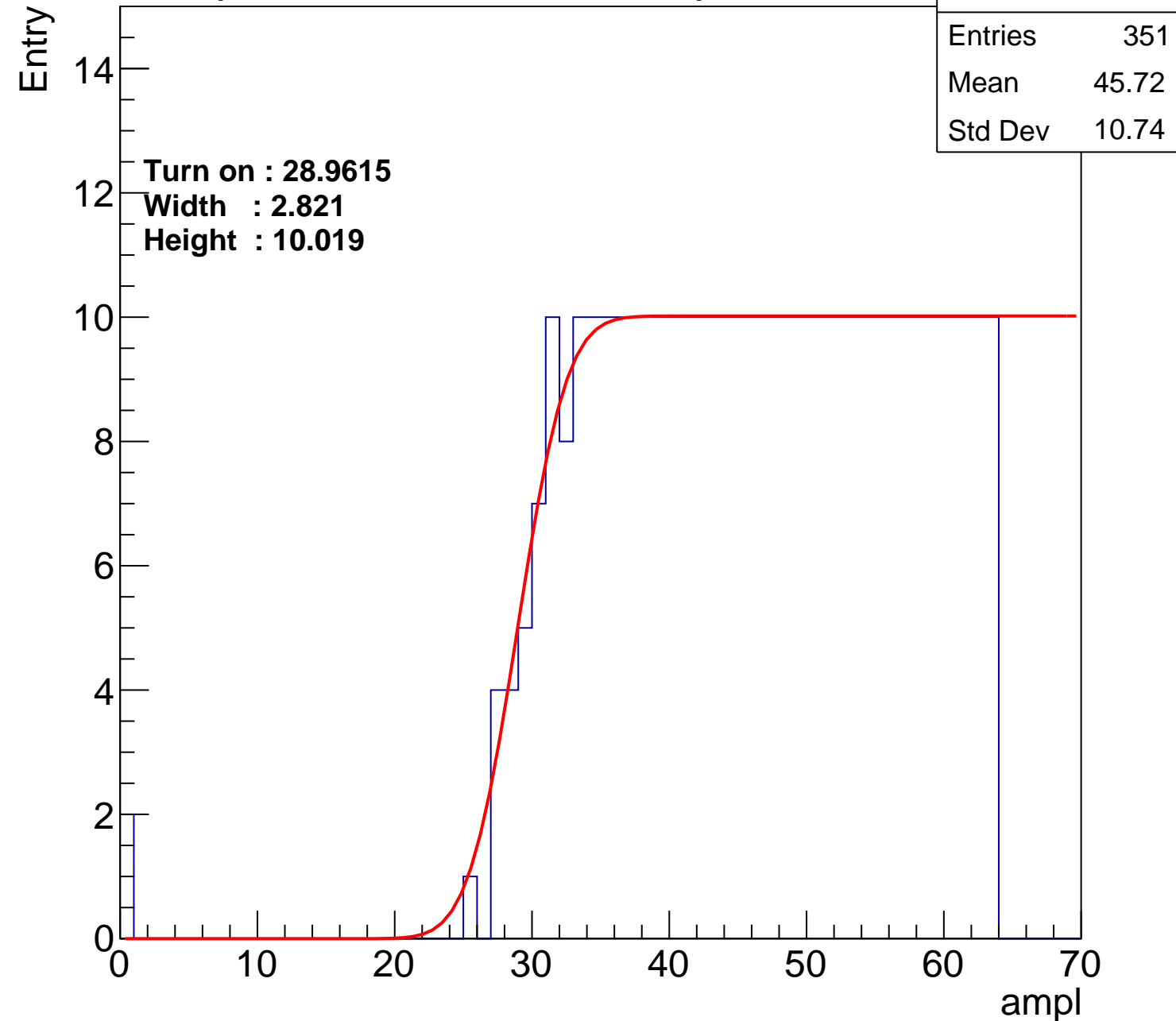
Width : 2.821

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch22

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 27.1484

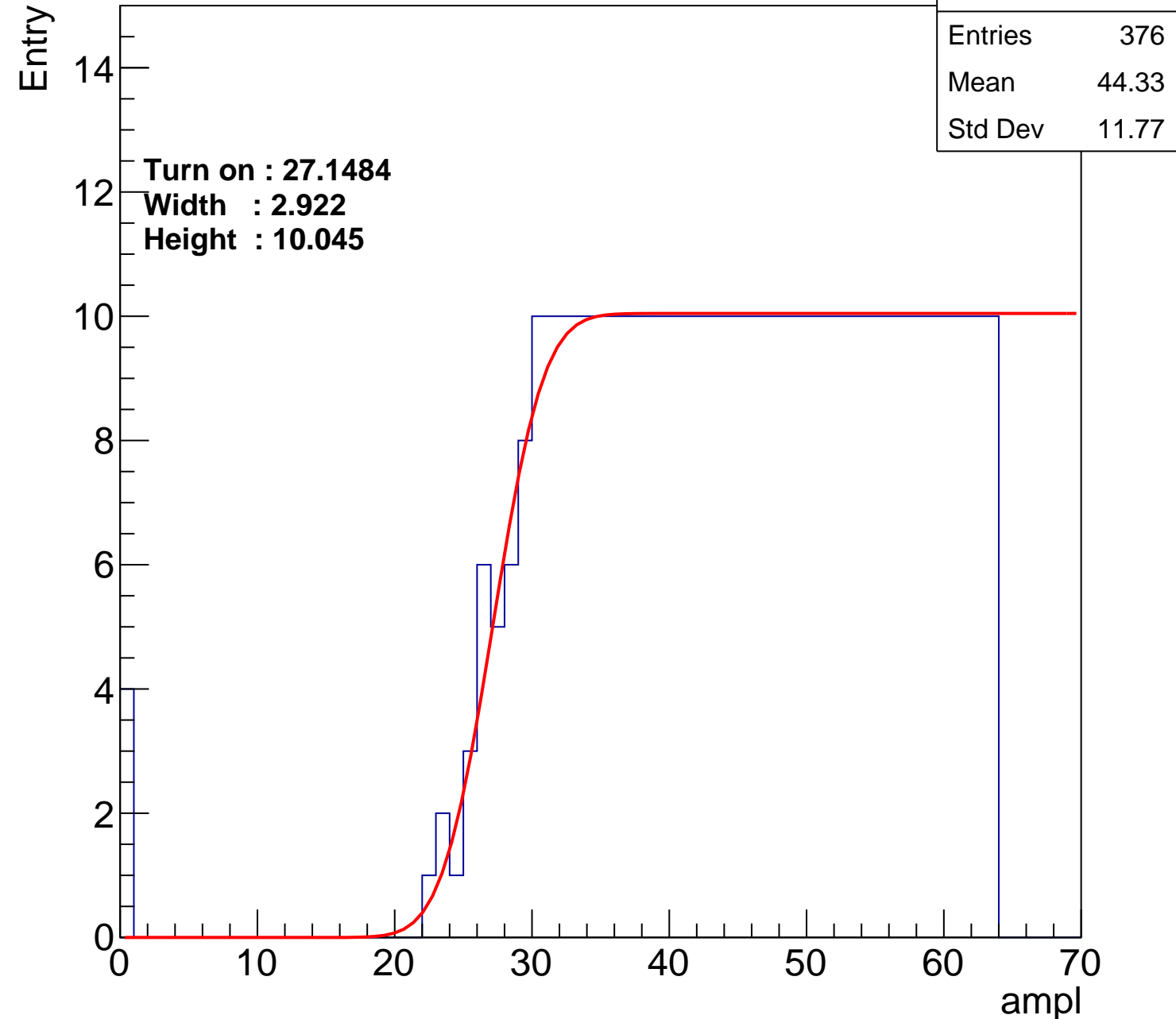
Width : 2.922

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.28
Std Dev	11.8

Turn on : 26.5342

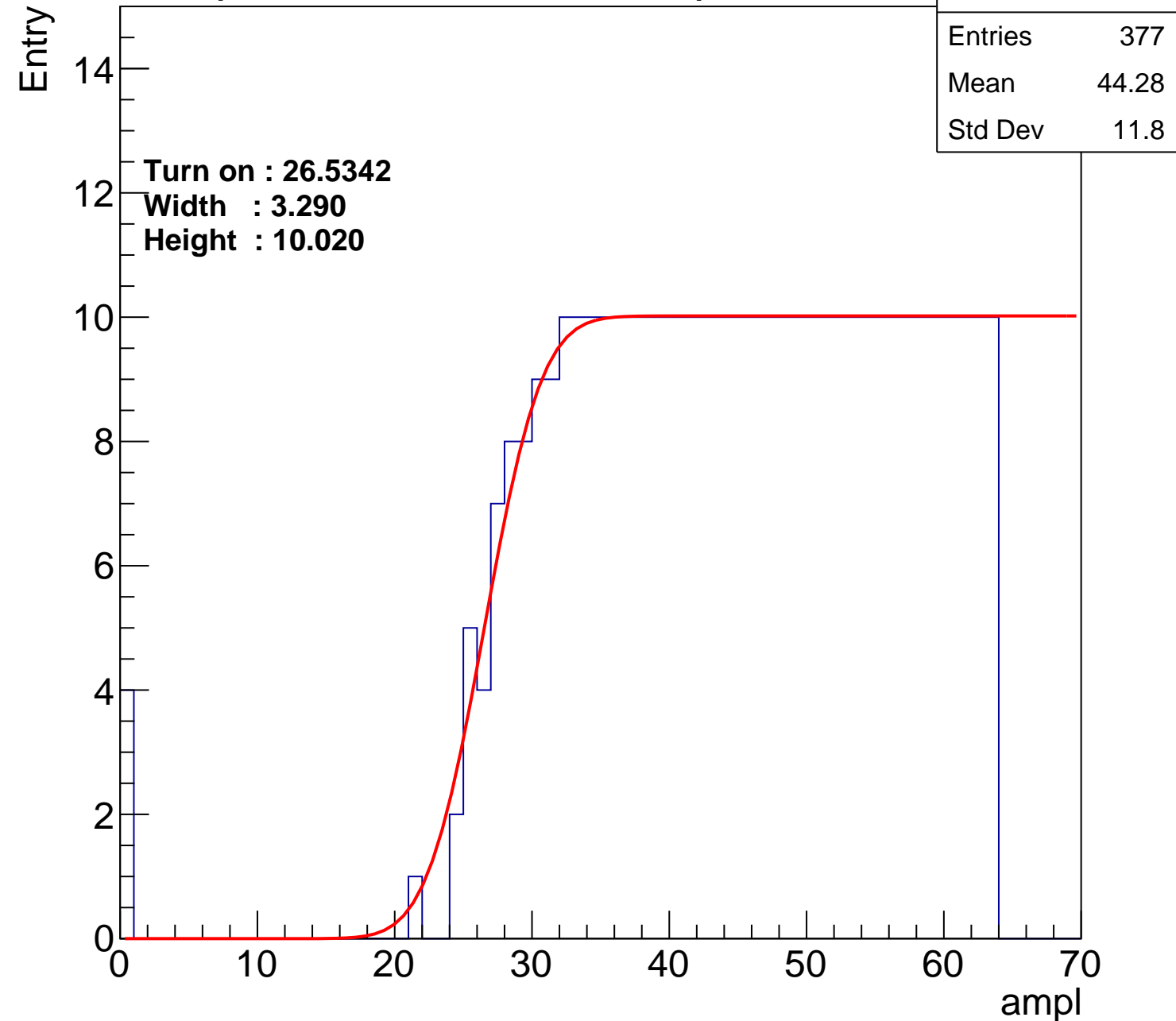
Width : 3.290

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch24

calib_packv5_042523_0143.root, FC#4, port A2

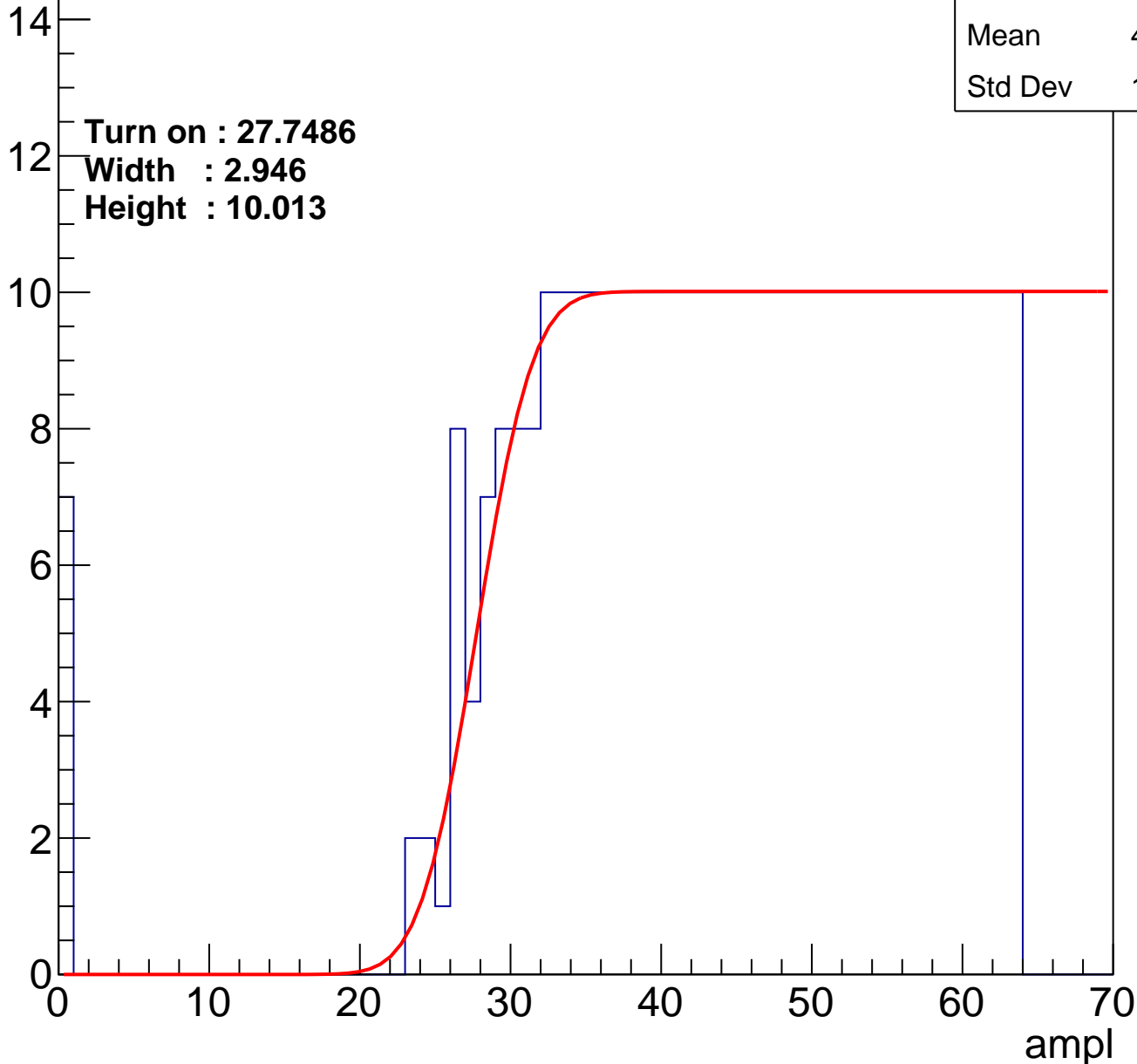
Entries	375
Mean	44.14
Std Dev	12.33

Turn on : 27.7486

Width : 2.946

Height : 10.013

Entry



B1L100S, U9-ch25

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.3328

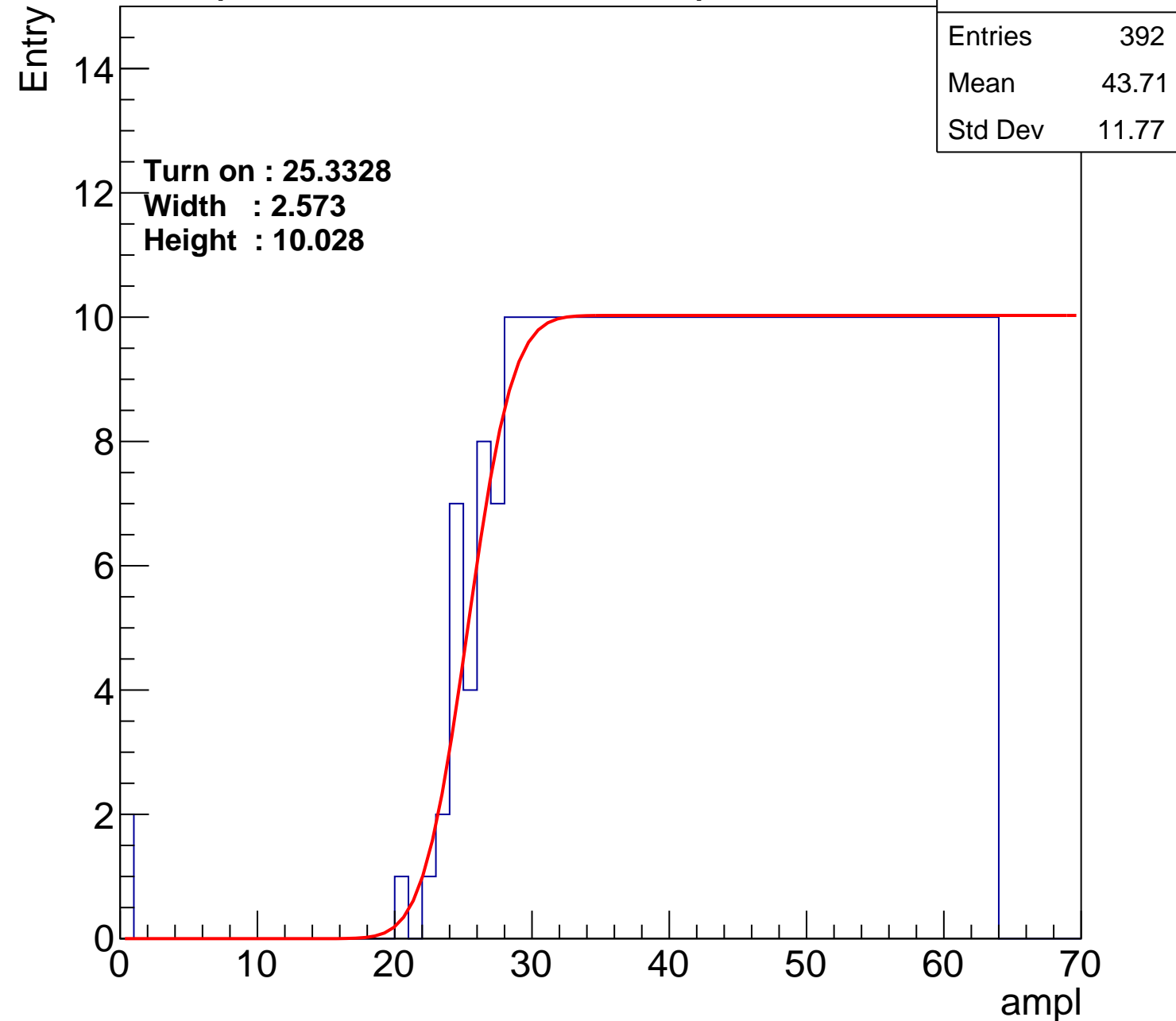
Width : 2.573

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.26
Std Dev	11.69

Turn on : 26.8255

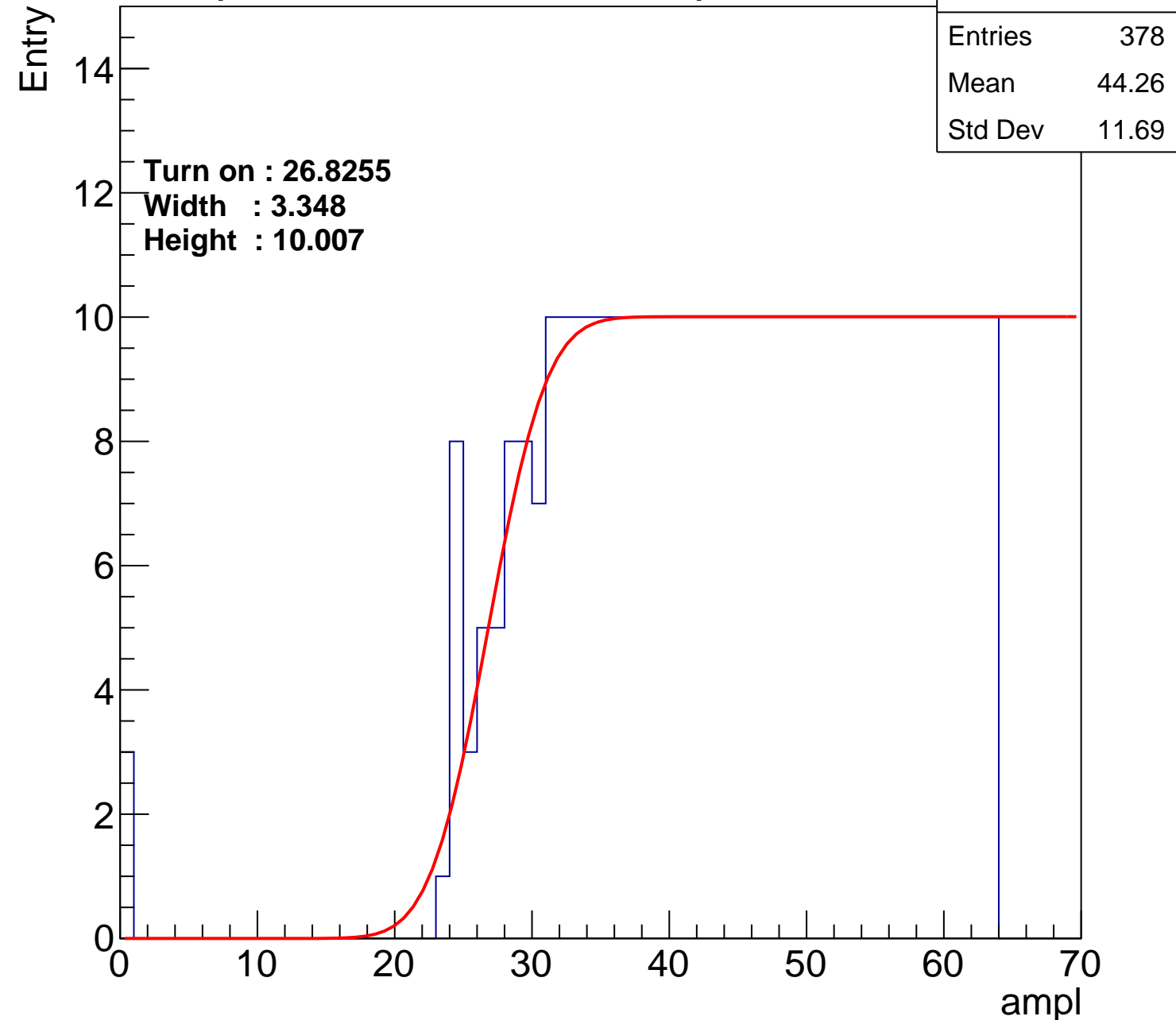
Width : 3.348

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	44.03
Std Dev	11.44

Turn on : 25.6131

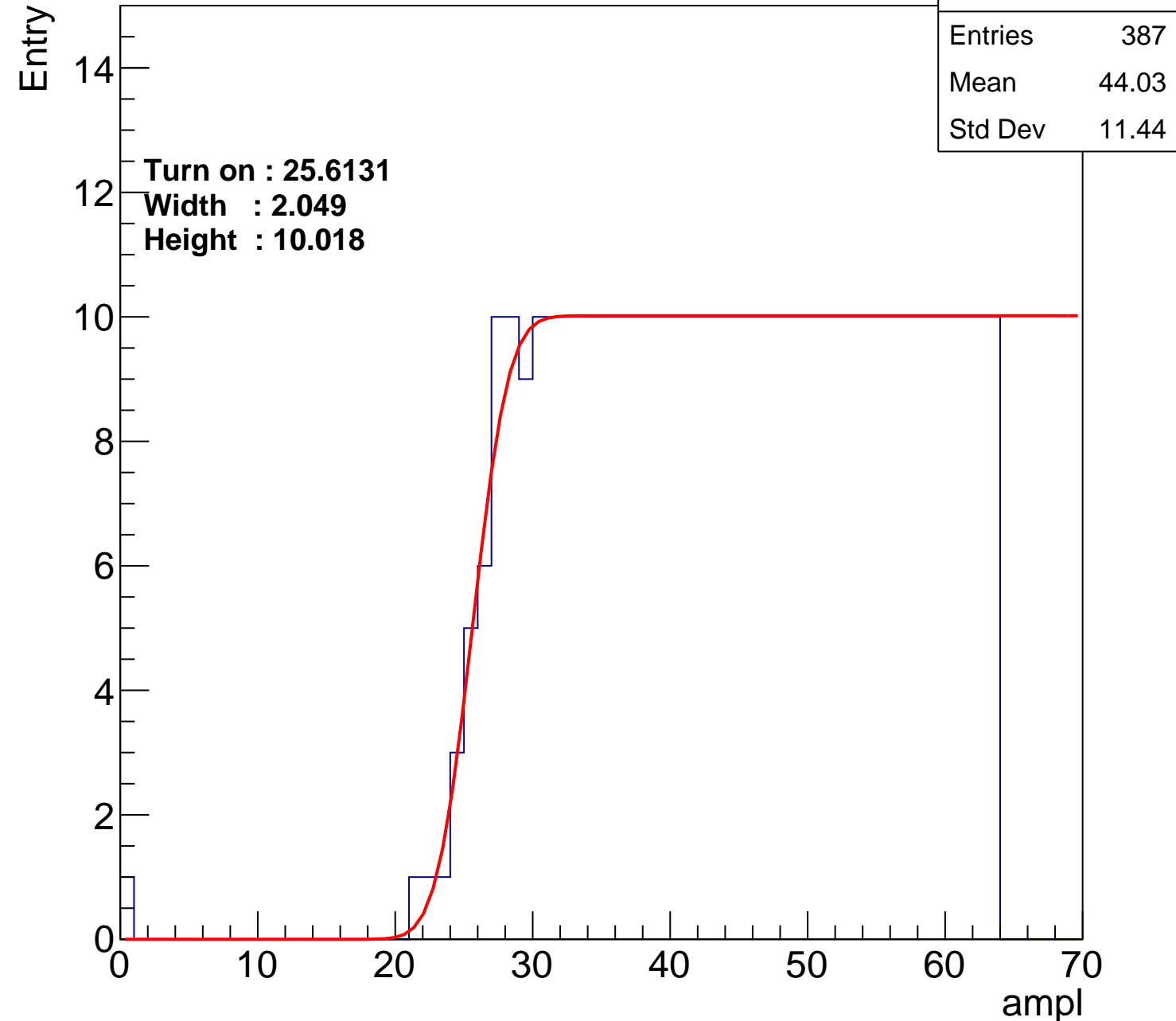
Width : 2.049

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch28

calib_packv5_042523_0143.root, FC#4, port A2

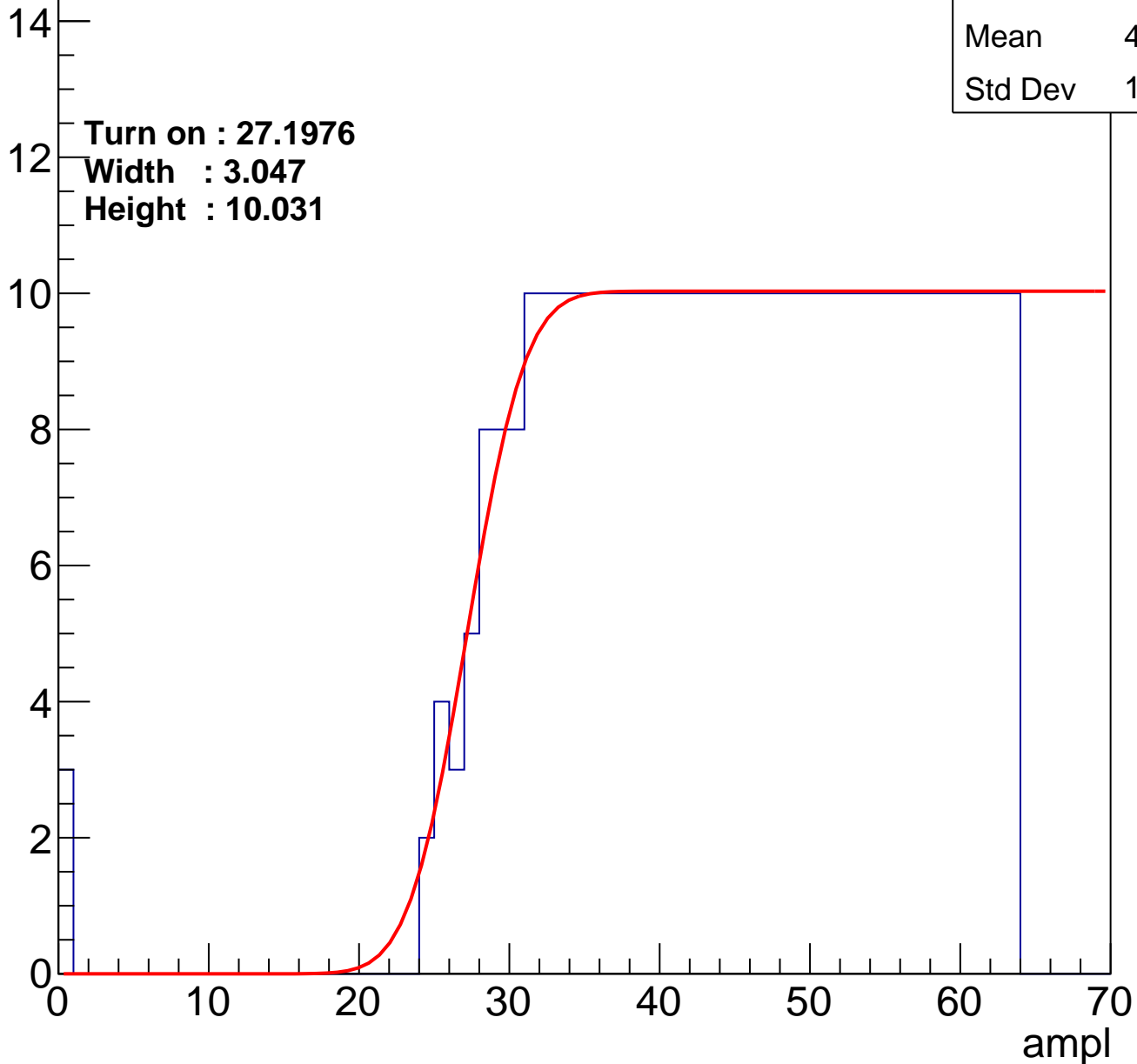
Entries	371
Mean	44.65
Std Dev	11.45

Turn on : 27.1976

Width : 3.047

Height : 10.031

Entry



B1L100S, U9-ch29

calib_packv5_042523_0143.root, FC#4, port A2

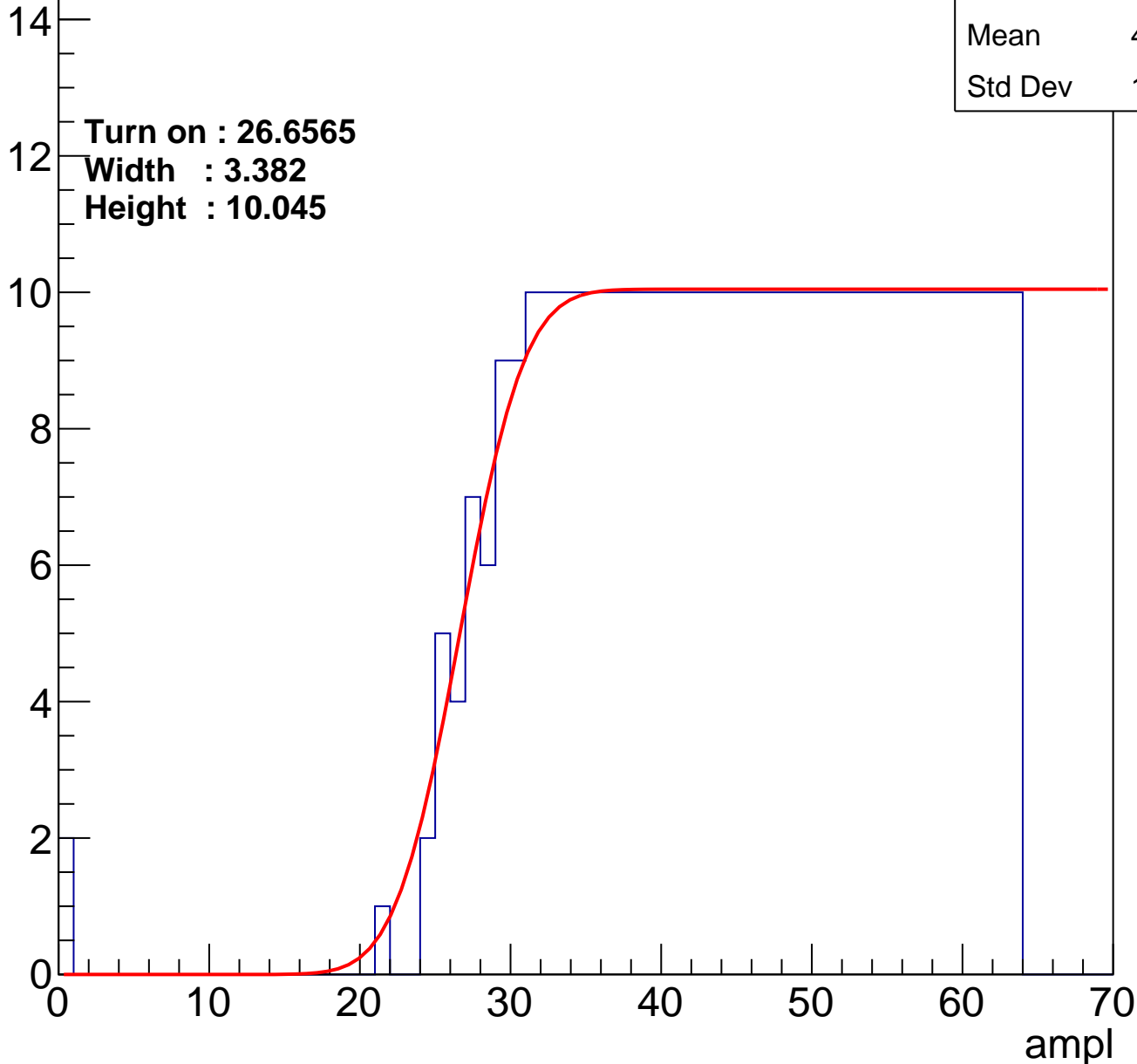
Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.6565

Width : 3.382

Height : 10.045

Entry



B1L100S, U9-ch30

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.27
Std Dev	11.8

Turn on : 26.8506

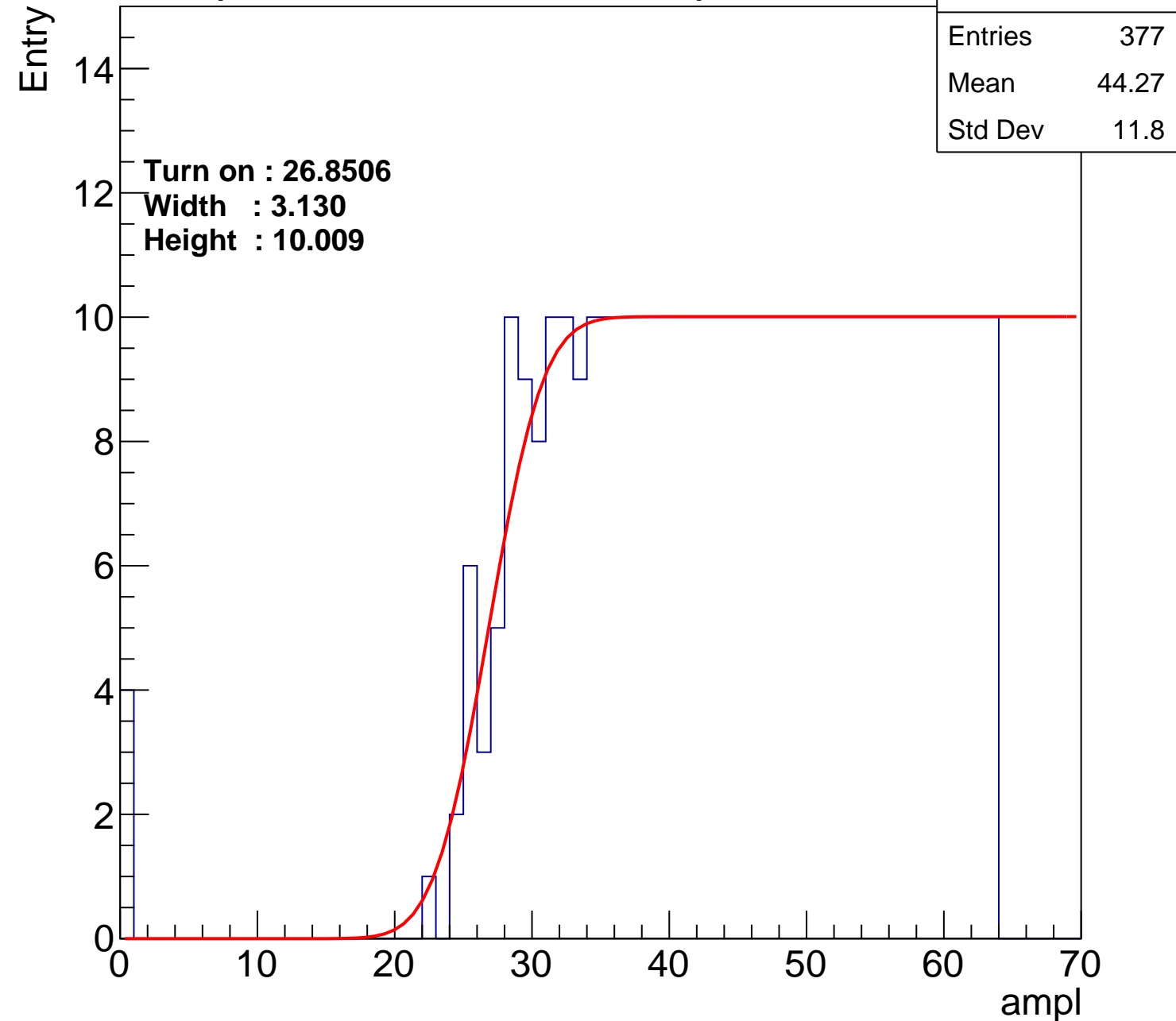
Width : 3.130

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries 389

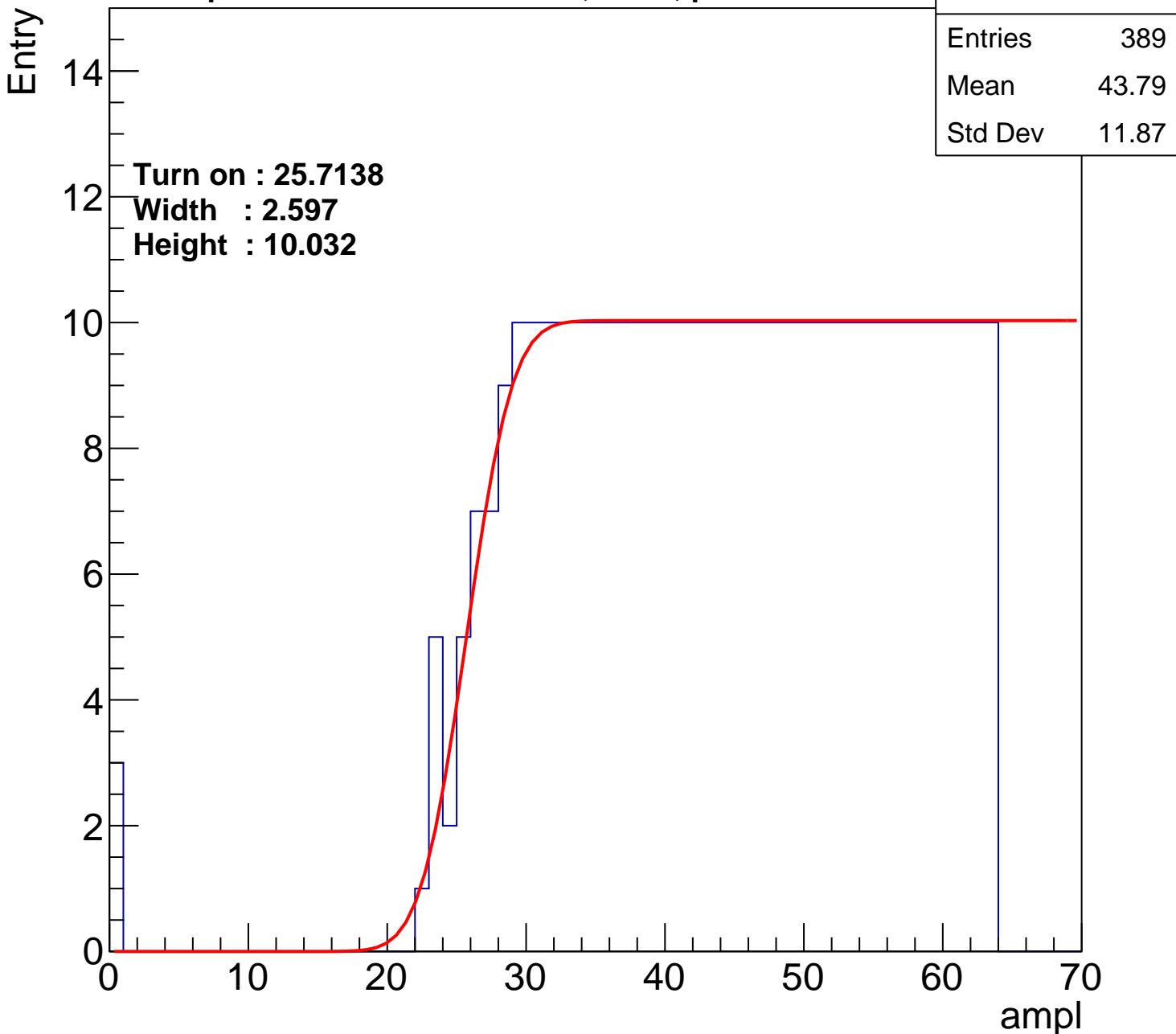
Mean 43.79

Std Dev 11.87

Turn on : 25.7138

Width : 2.597

Height : 10.032



B1L100S, U9-ch32

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 26.9143

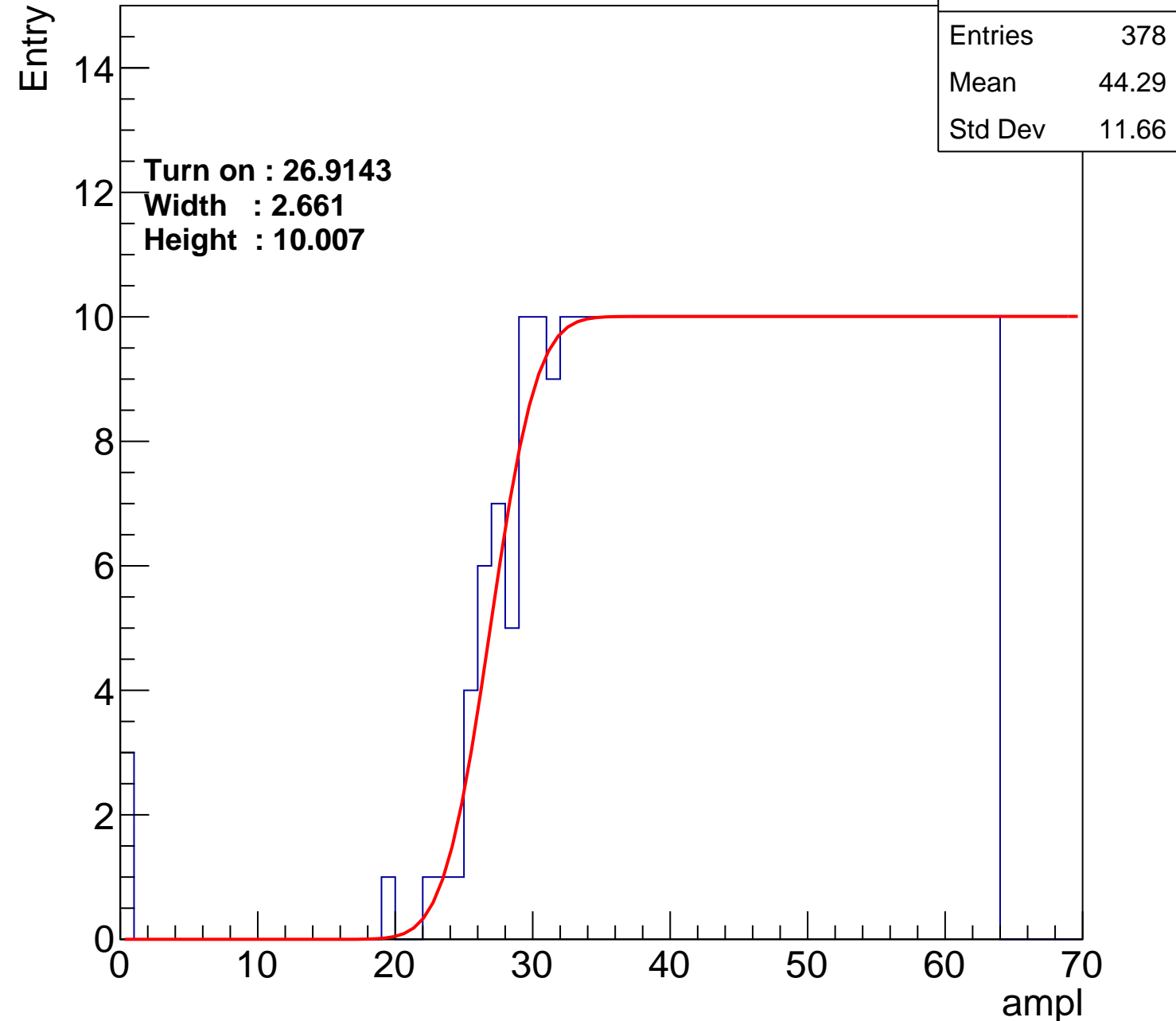
Width : 2.661

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.9
Std Dev	11

Turn on : 27.5538

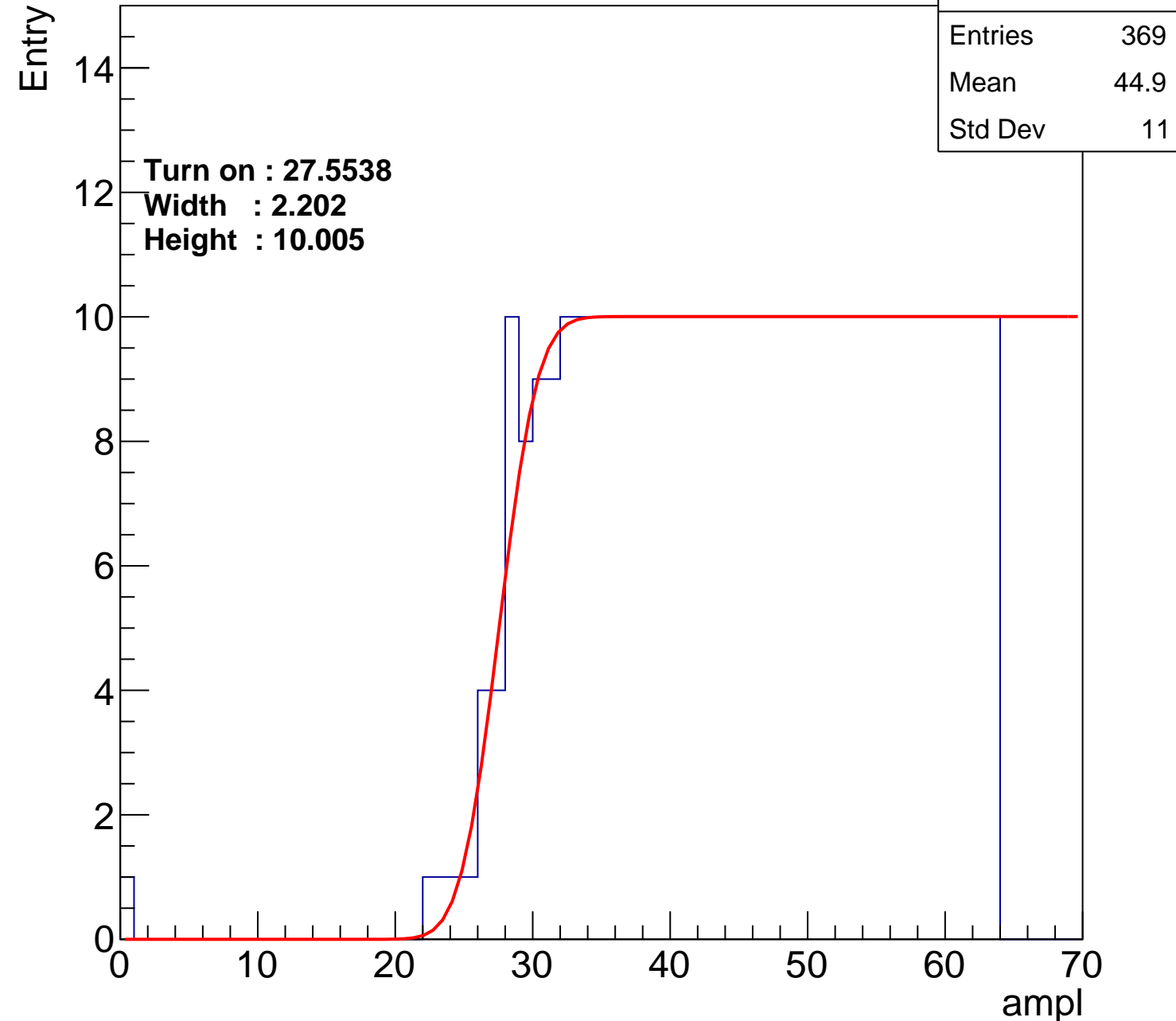
Width : 2.202

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch34

calib_packv5_042523_0143.root, FC#4, port A2

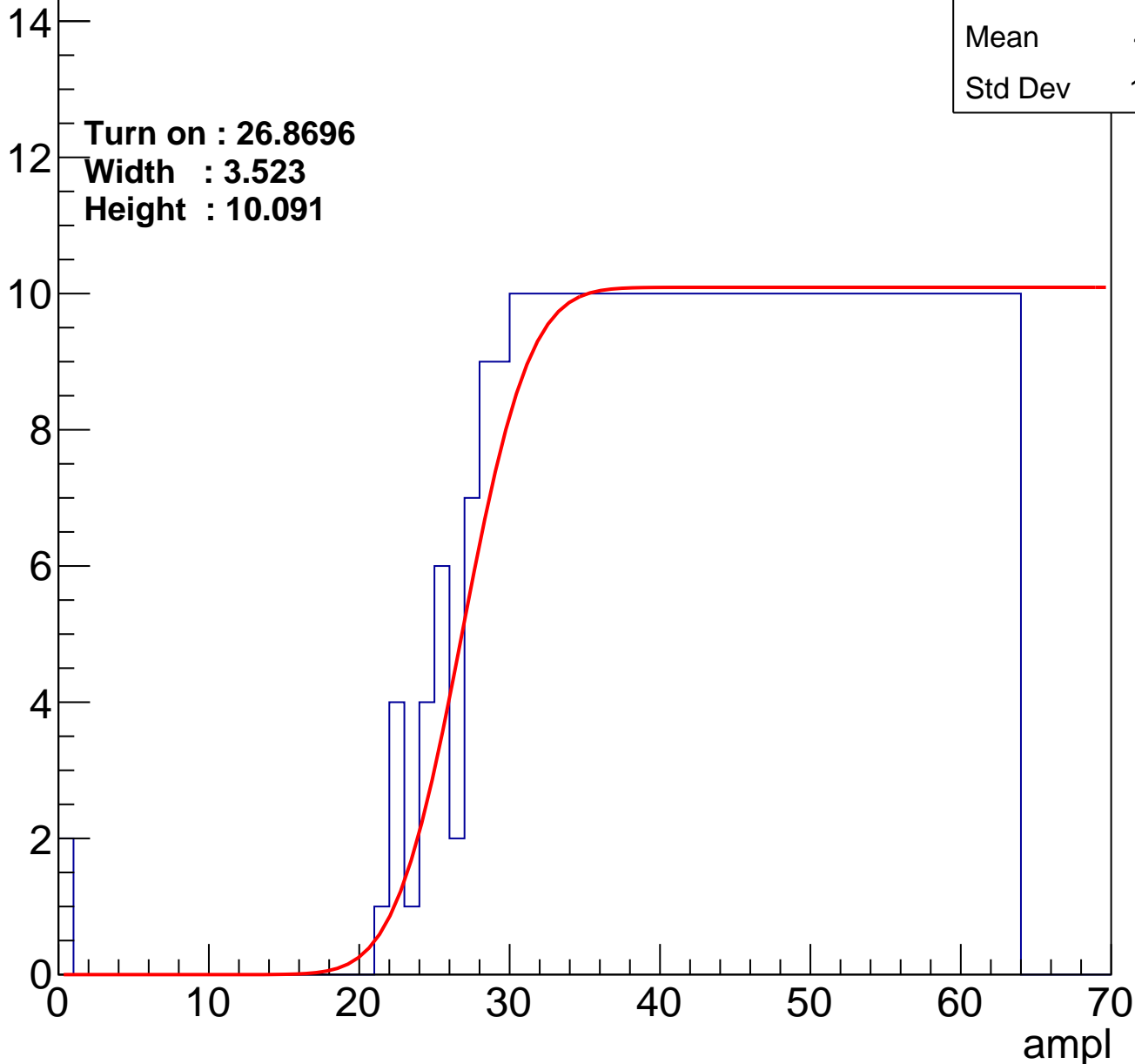
Entries	385
Mean	44.01
Std Dev	11.67

Turn on : 26.8696

Width : 3.523

Height : 10.091

Entry



B1L100S, U9-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	347
Mean	45.82
Std Dev	10.9

Turn on : 29.8861

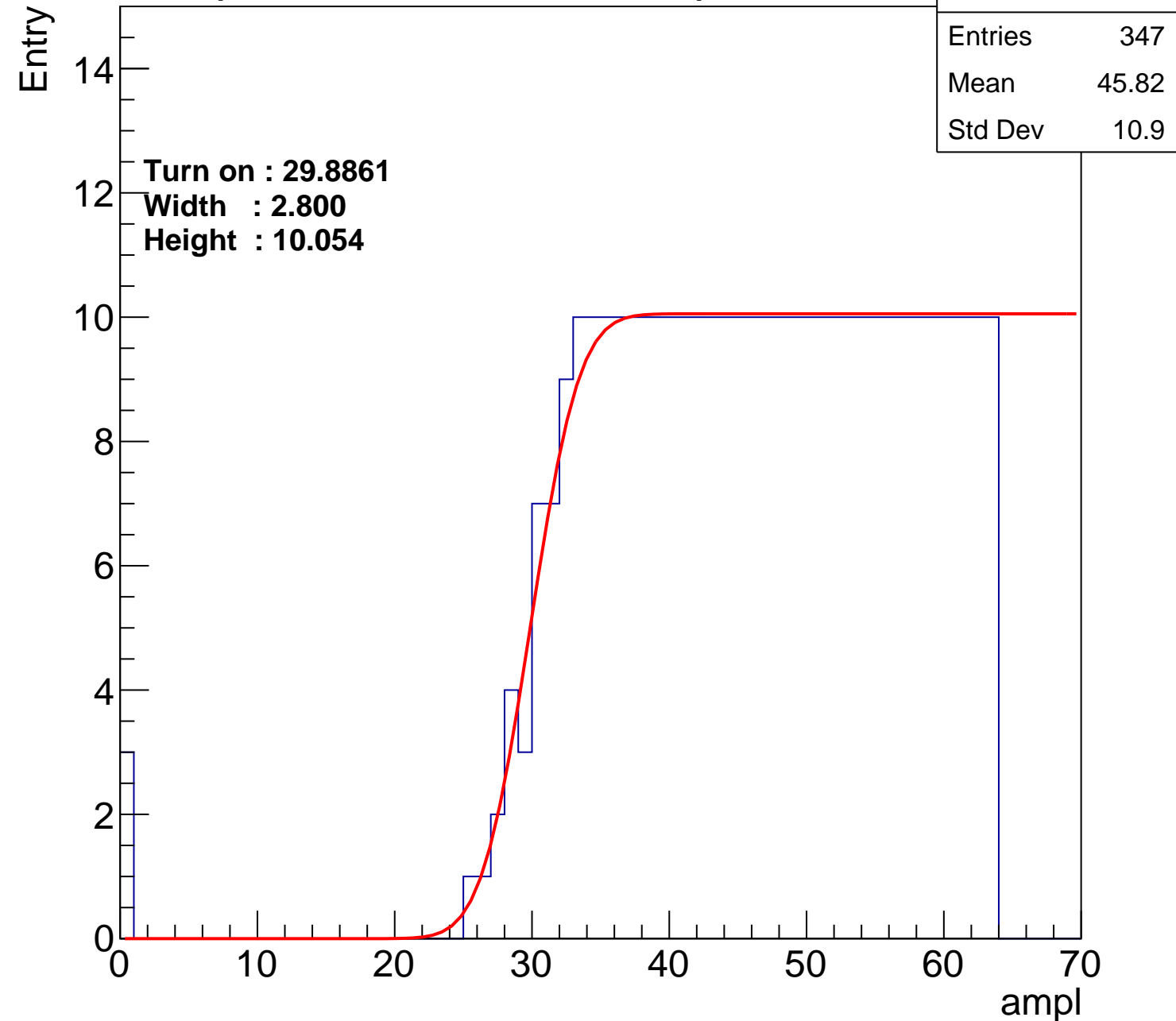
Width : 2.800

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch36

calib_packv5_042523_0143.root, FC#4, port A2

Entries	406
Mean	43.05
Std Dev	12.02

Turn on : 23.5691

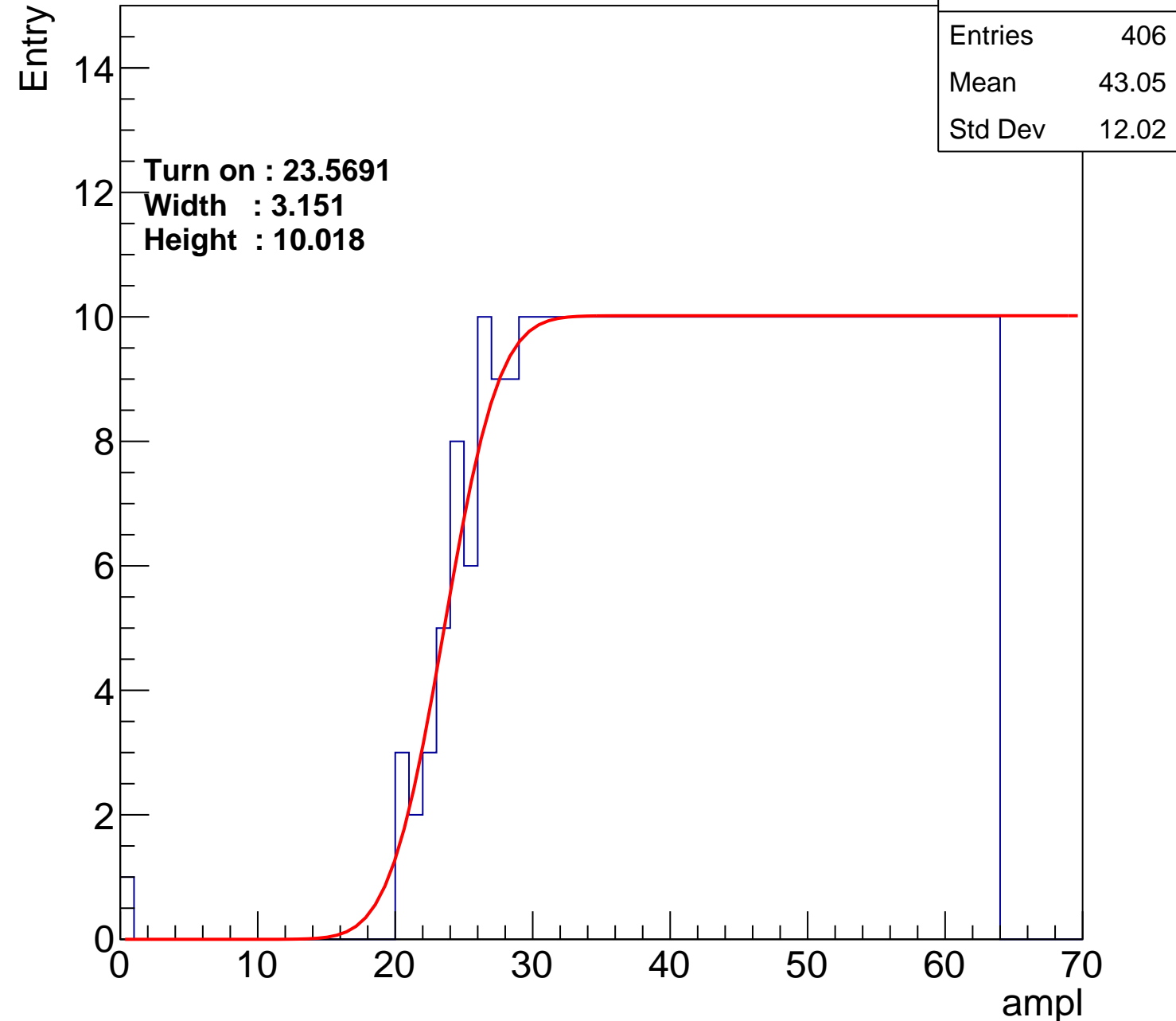
Width : 3.151

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch37

calib_packv5_042523_0143.root, FC#4, port A2

Entries	330
Mean	46.75
Std Dev	10.22

Turn on : 30.9746

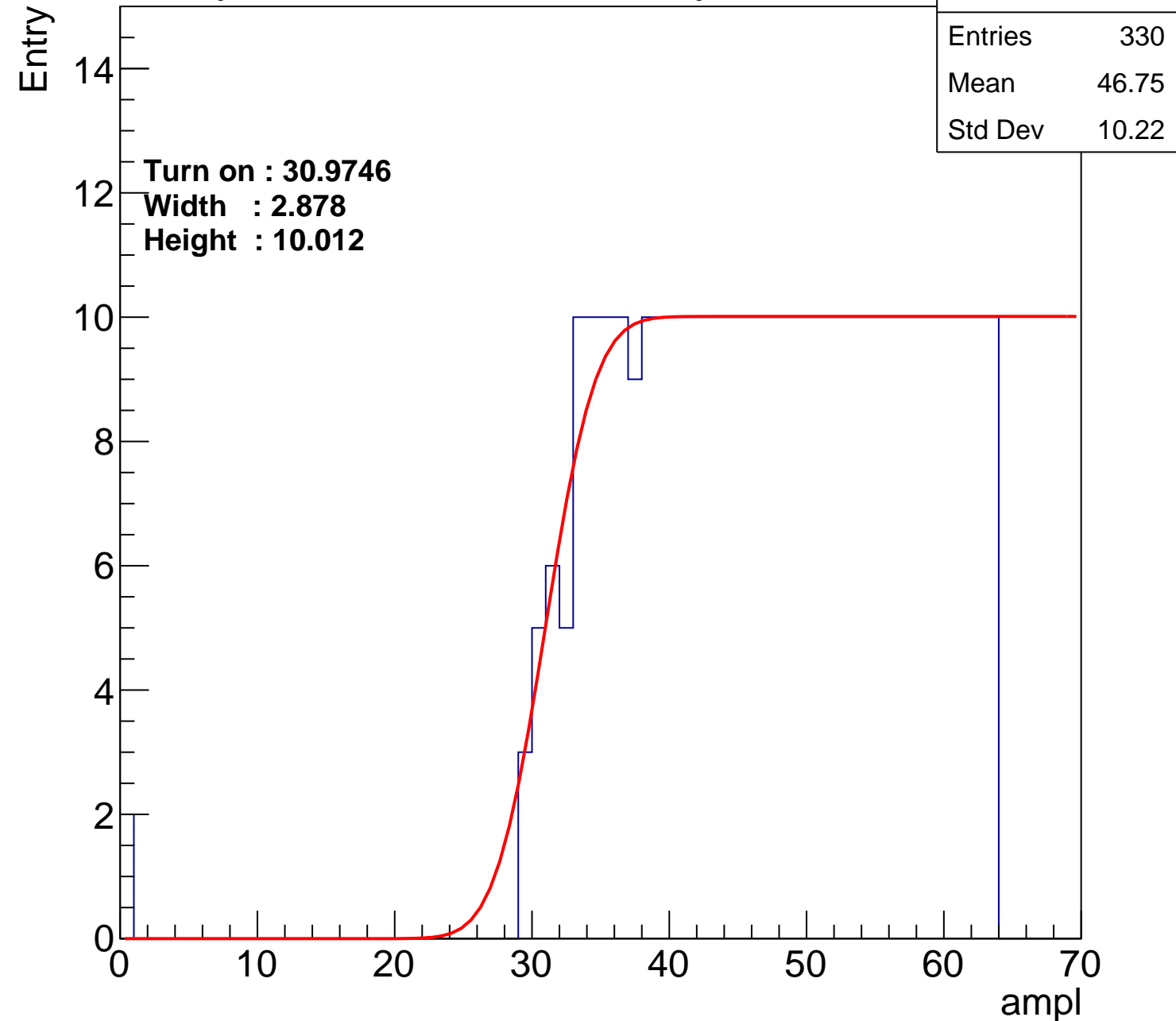
Width : 2.878

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.17
Std Dev	11.23

Turn on : 28.4929

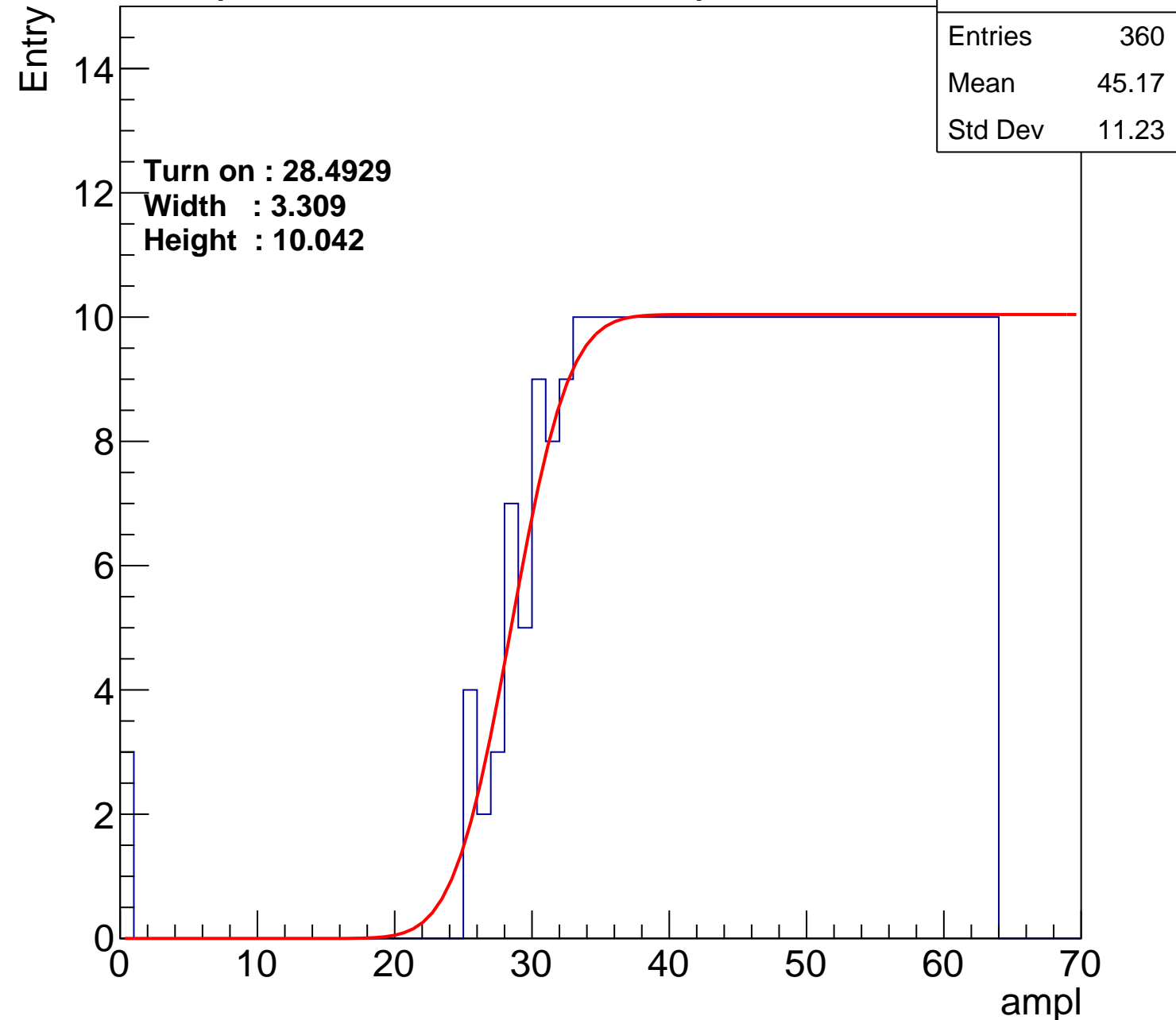
Width : 3.309

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.9518

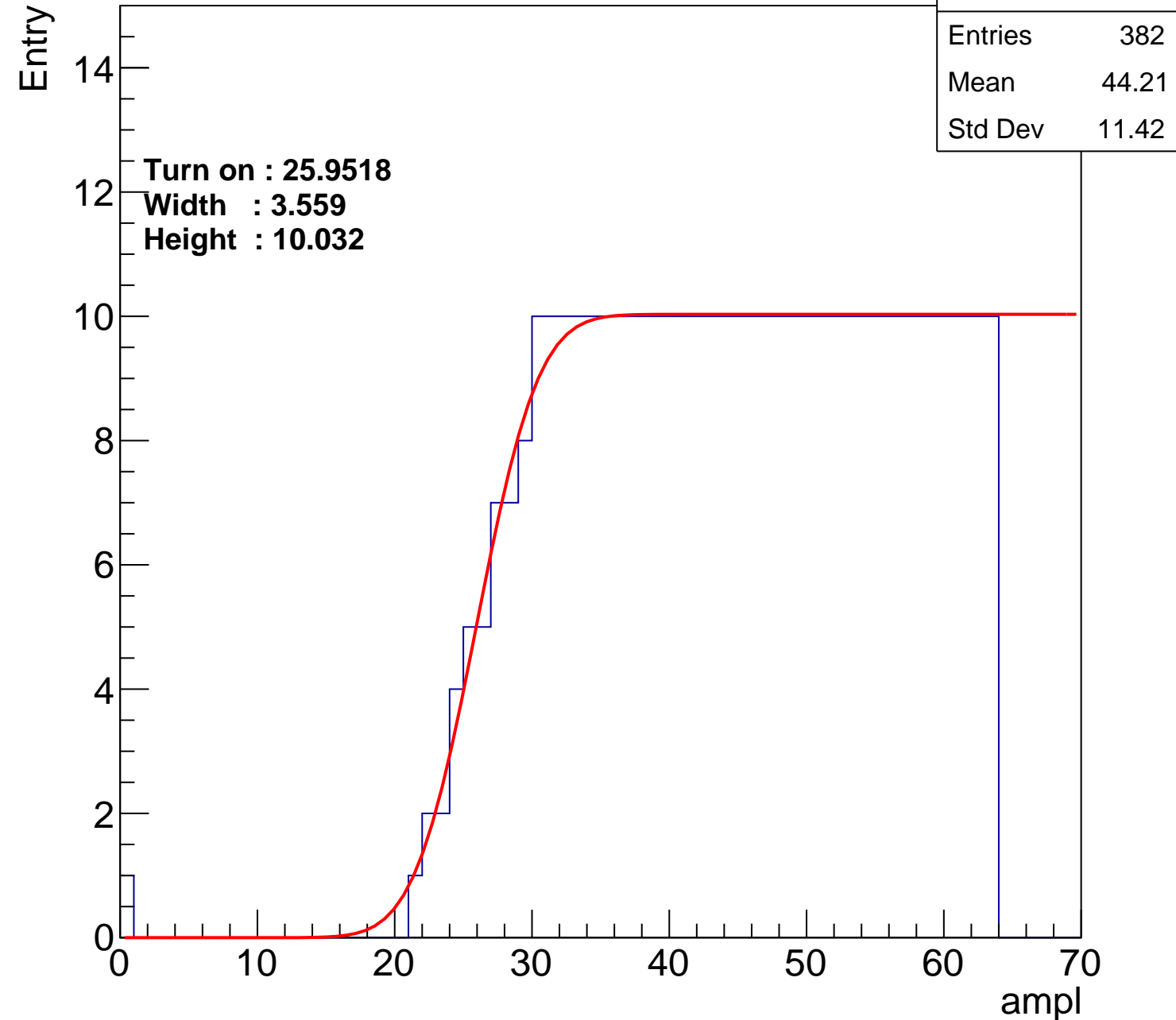
Width : 3.559

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch40

calib_packv5_042523_0143.root, FC#4, port A2

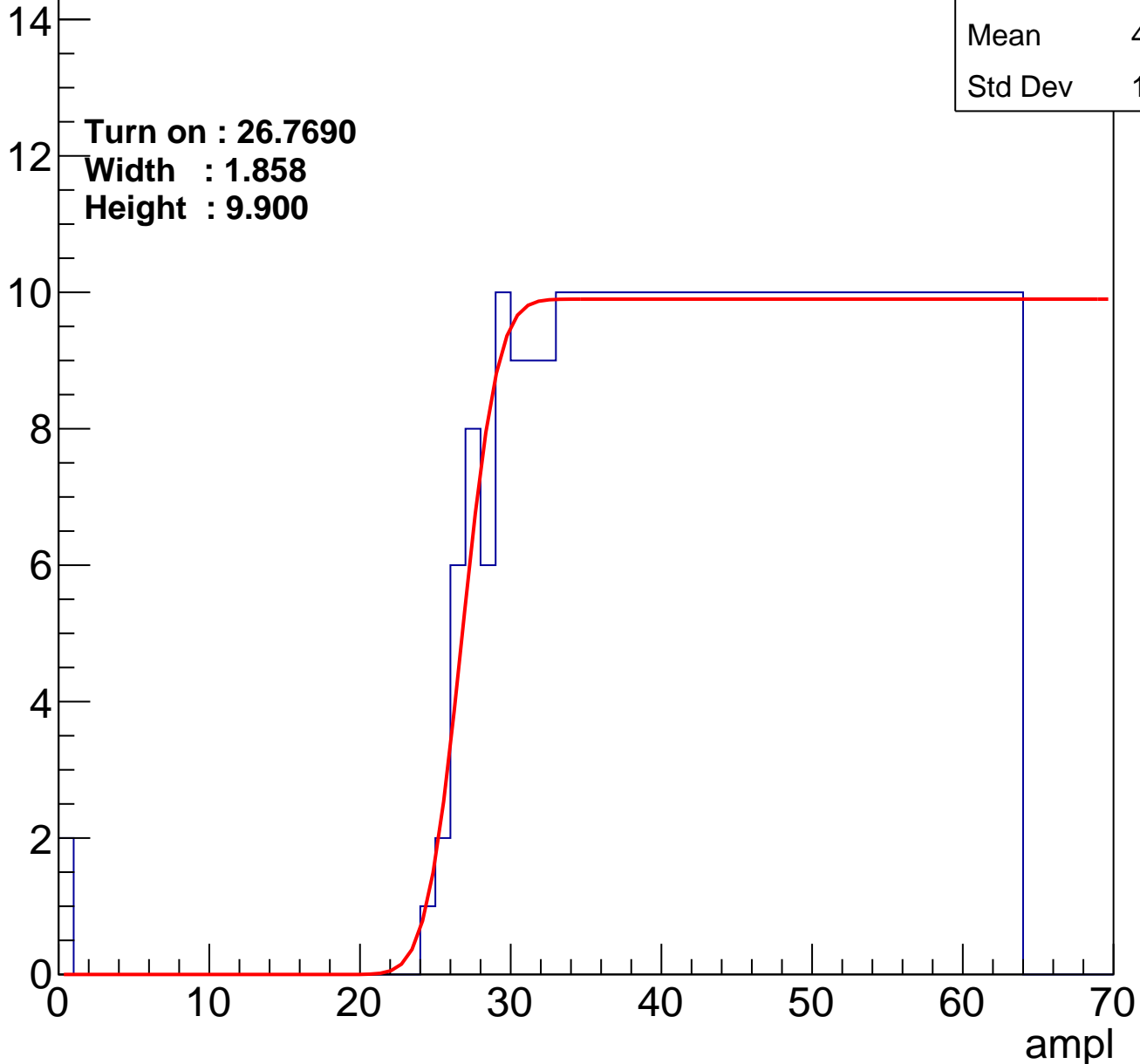
Entries	372
Mean	44.68
Std Dev	11.26

Turn on : 26.7690

Width : 1.858

Height : 9.900

Entry



B1L100S, U9-ch41

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.87
Std Dev	11.24

Turn on : 27.5608

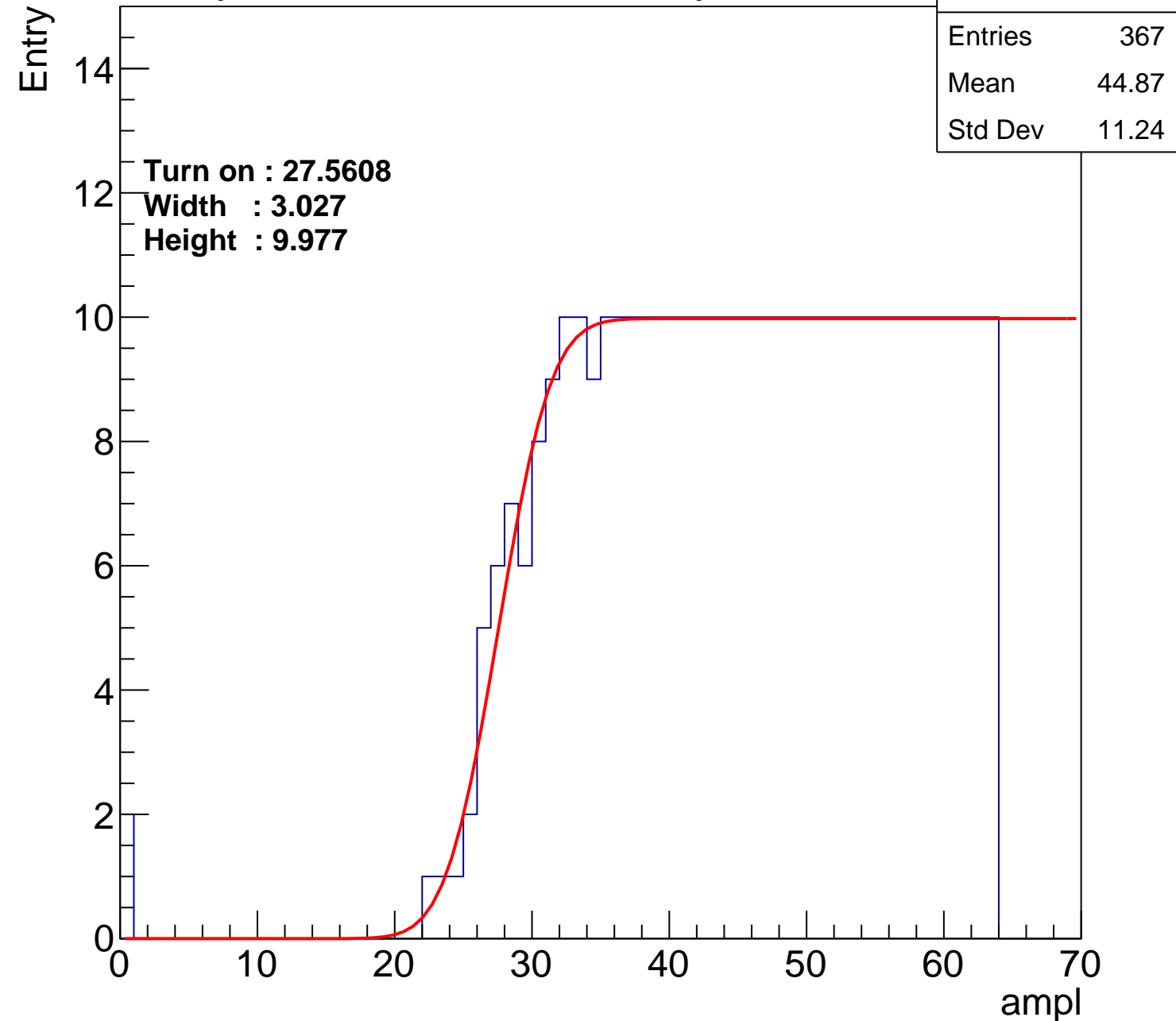
Width : 3.027

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch42

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 28.2343

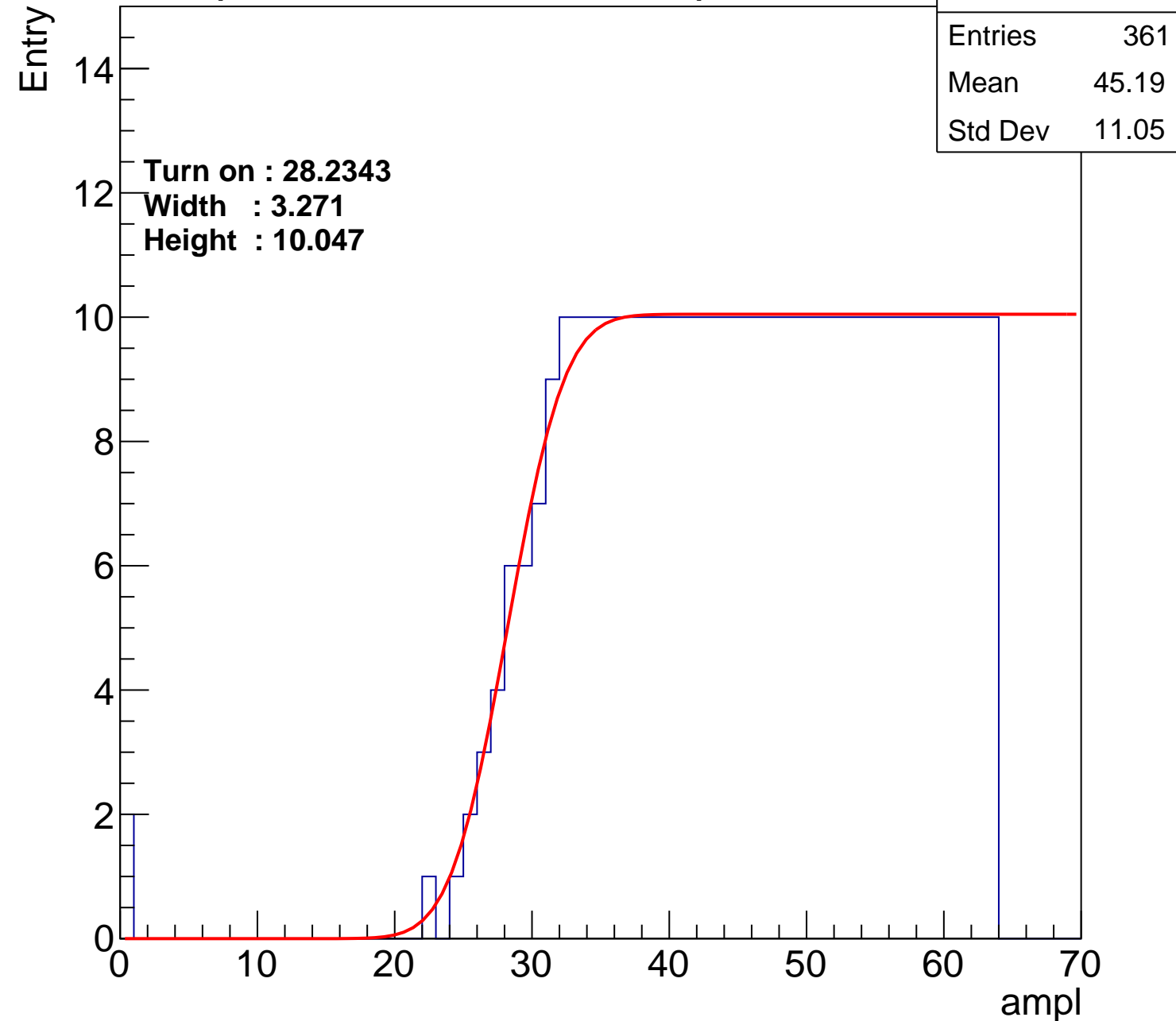
Width : 3.271

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.2413

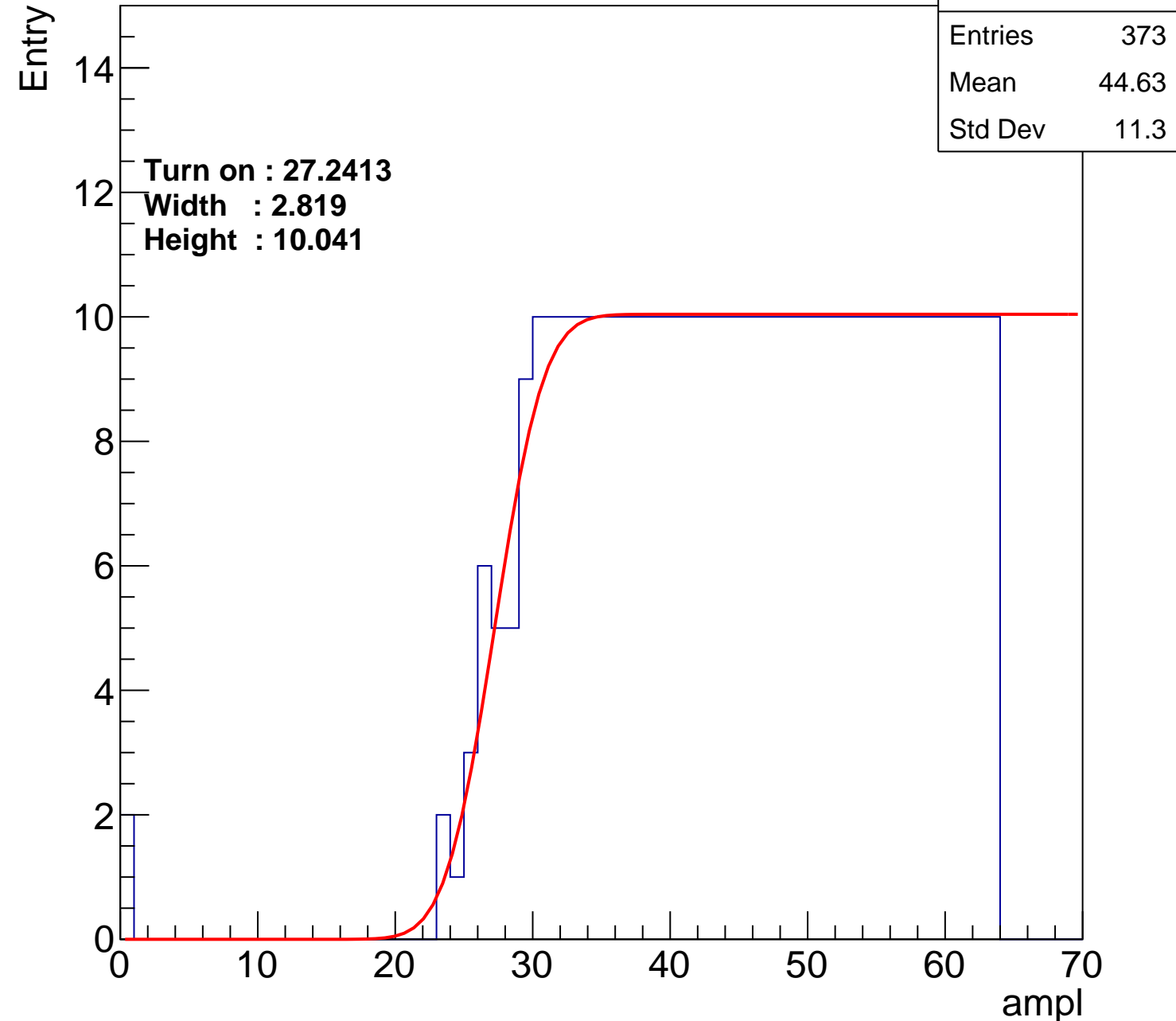
Width : 2.819

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch44

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.2480

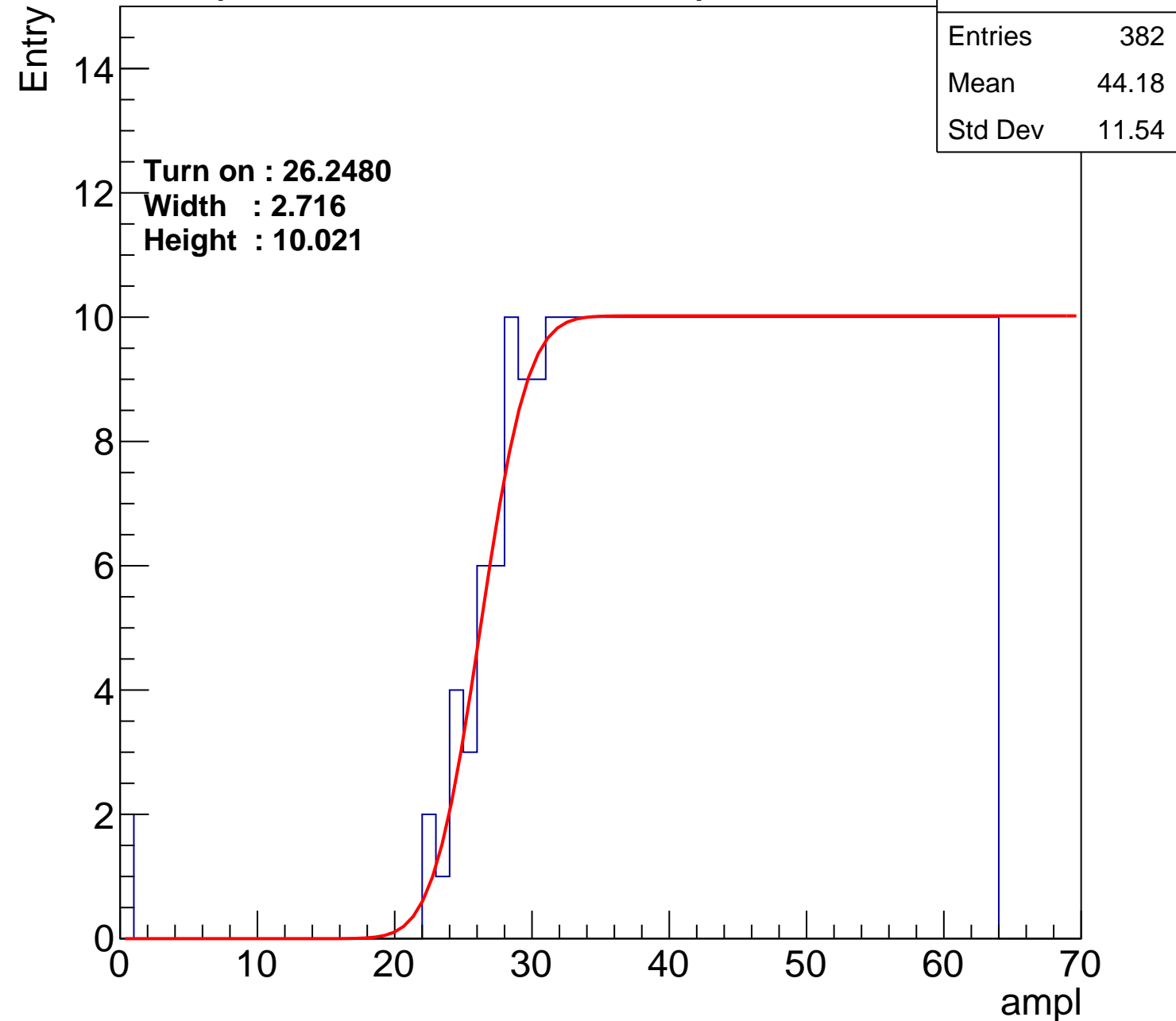
Width : 2.716

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch45

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.23
Std Dev	11.53

Turn on : 29.0609

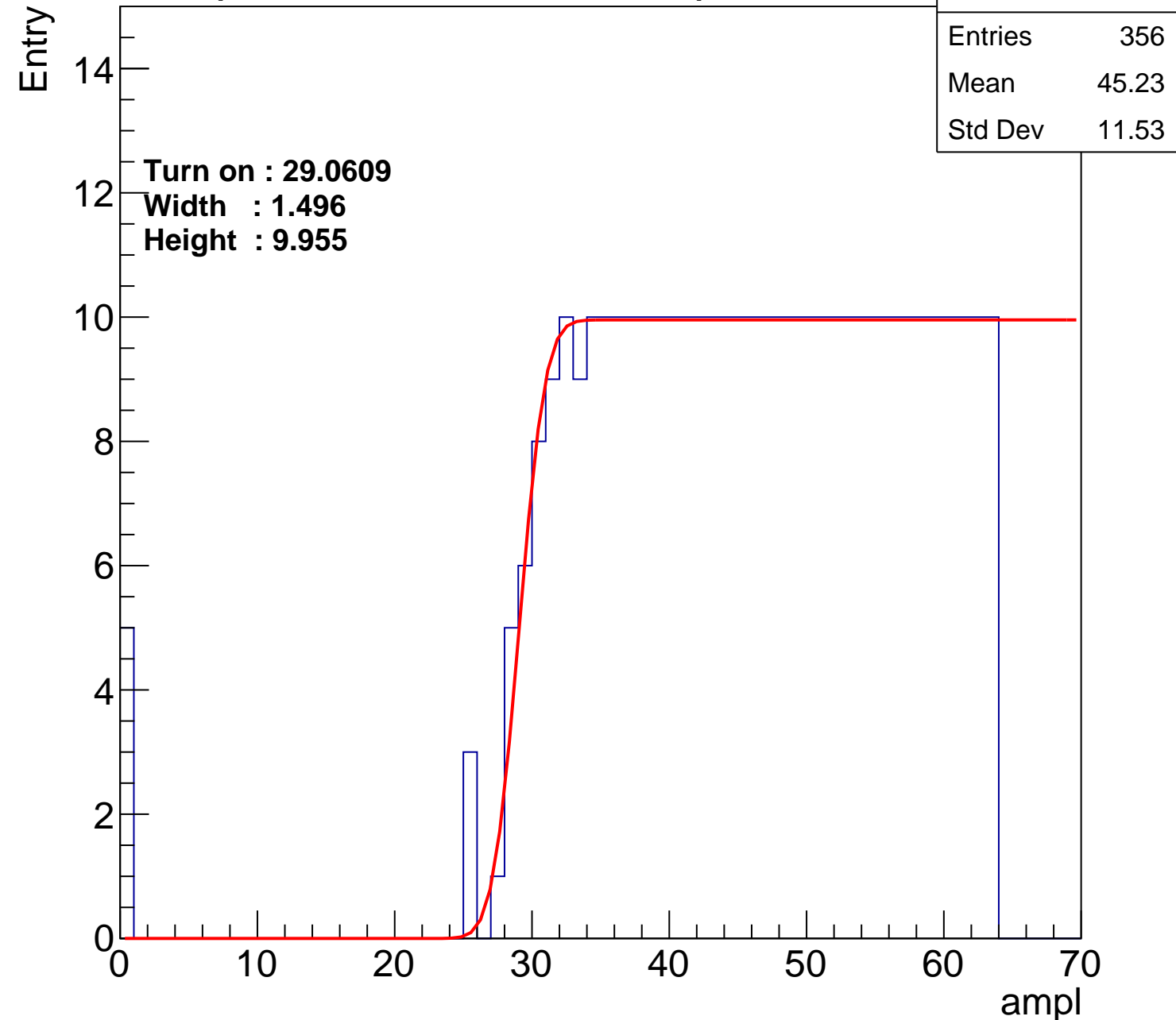
Width : 1.496

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch46

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 27.0256

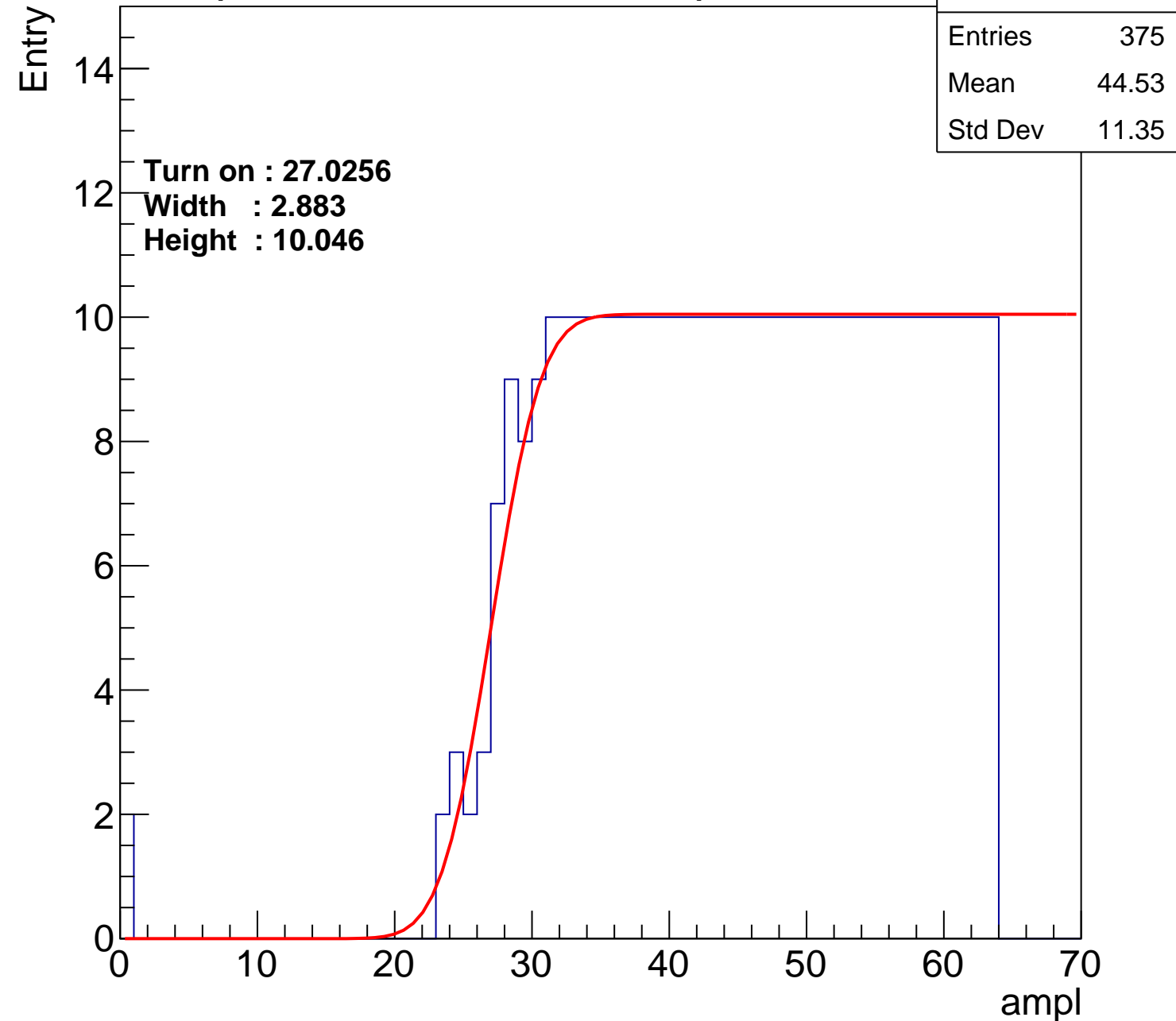
Width : 2.883

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch47

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.9742

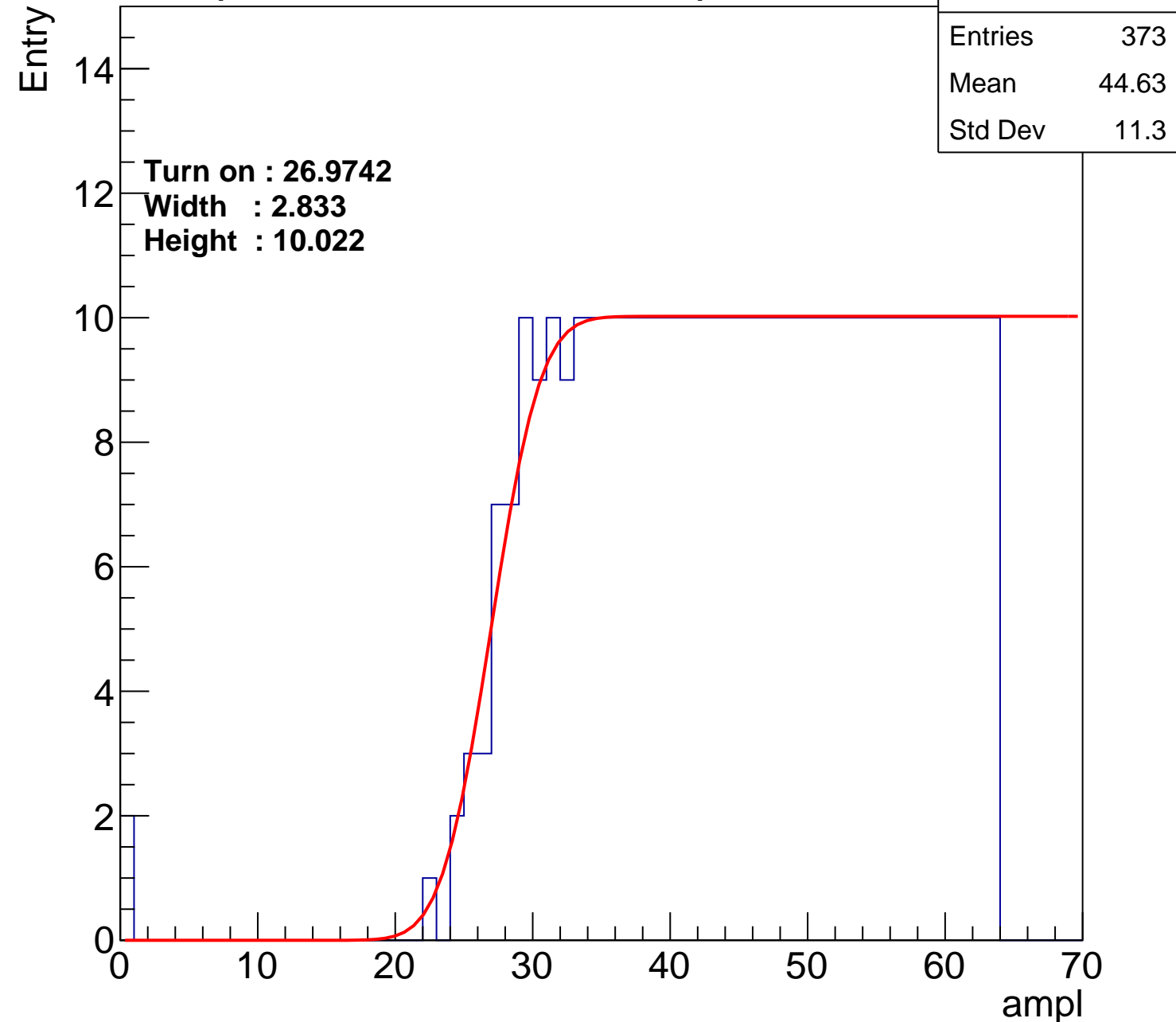
Width : 2.833

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch48

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.5
Std Dev	11.54

Turn on : 27.3746

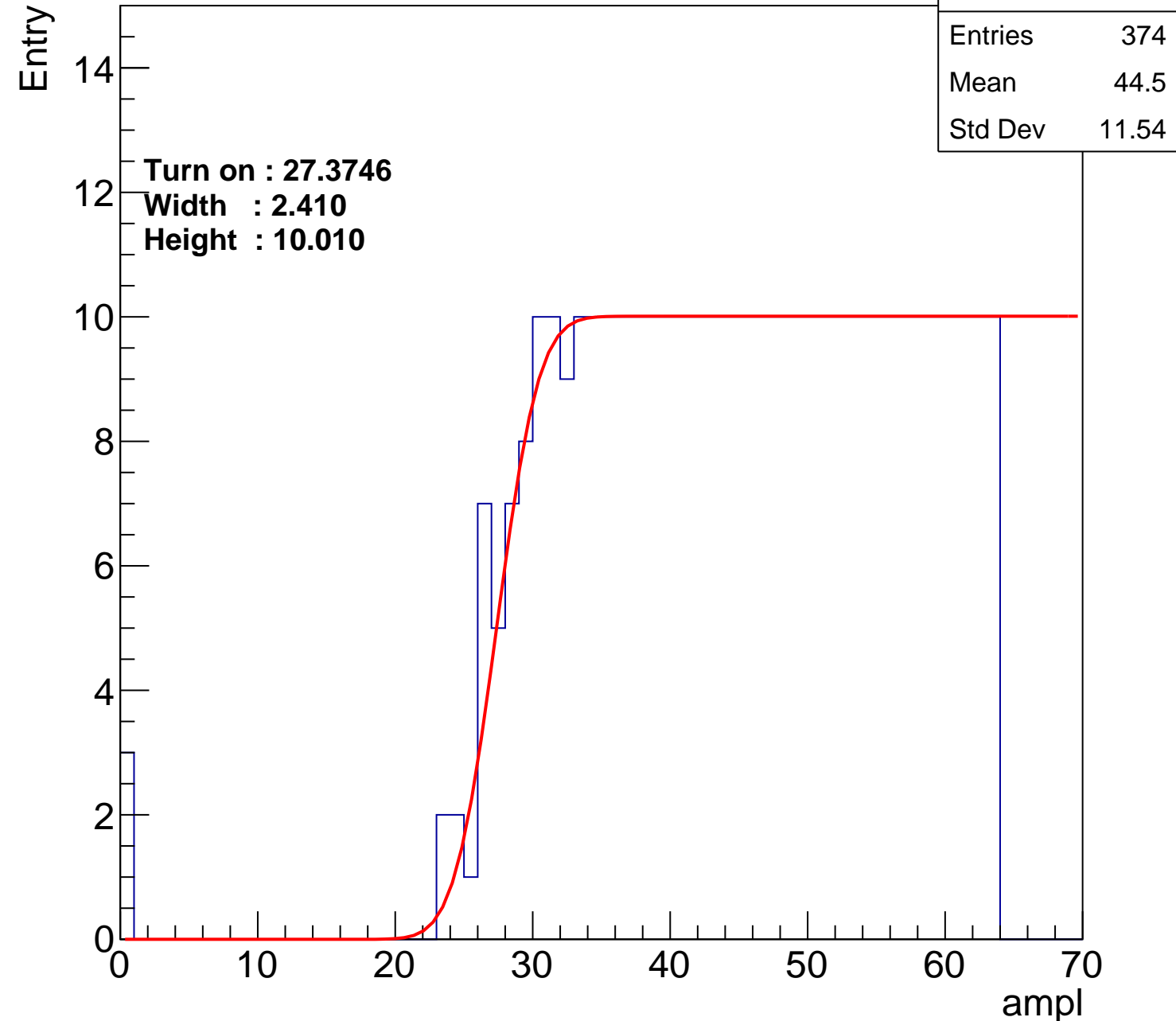
Width : 2.410

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch49

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	45.01
Std Dev	11.32

Turn on : 28.4484

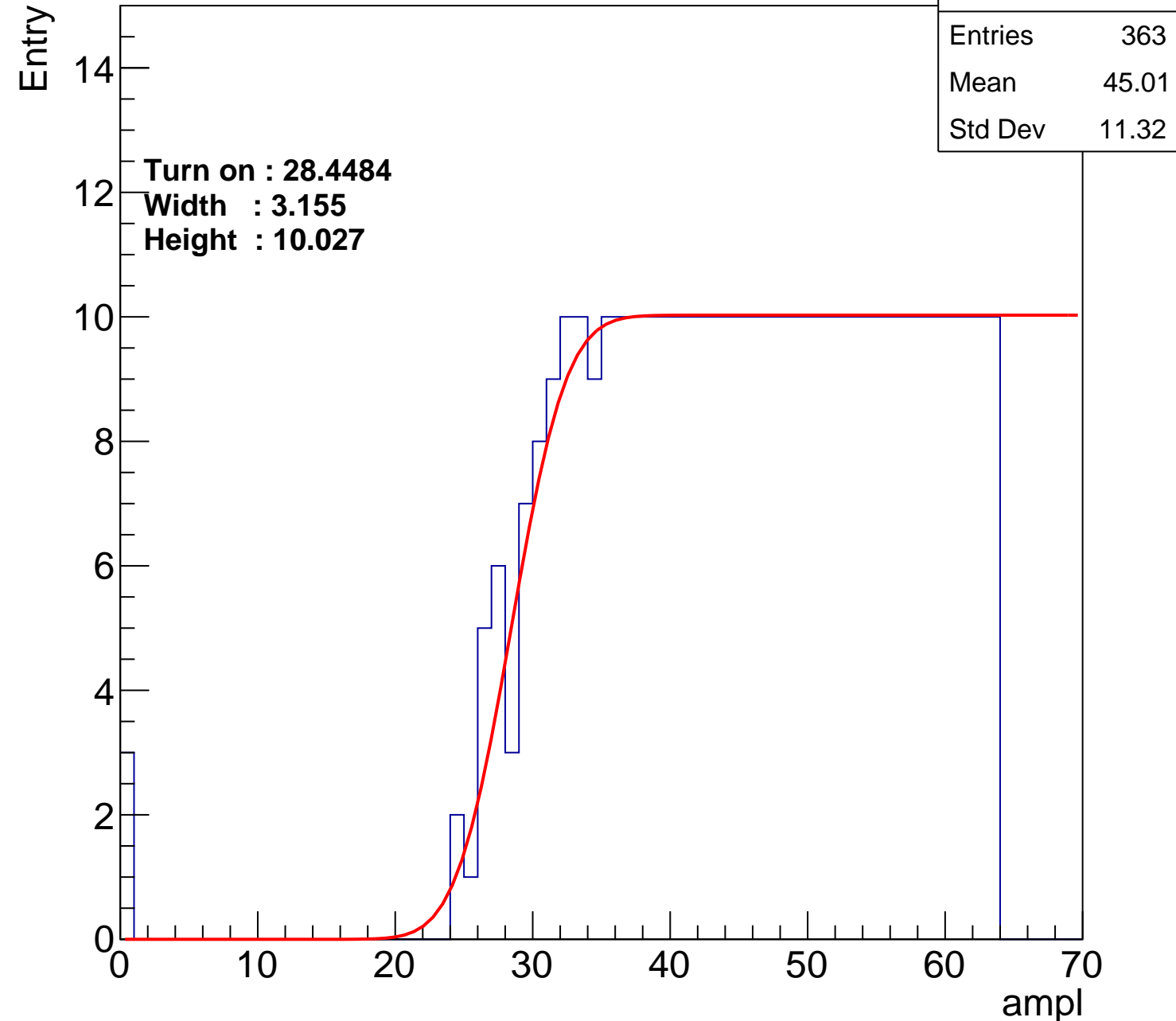
Width : 3.155

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch50

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.88
Std Dev	11.75

Turn on : 26.1342

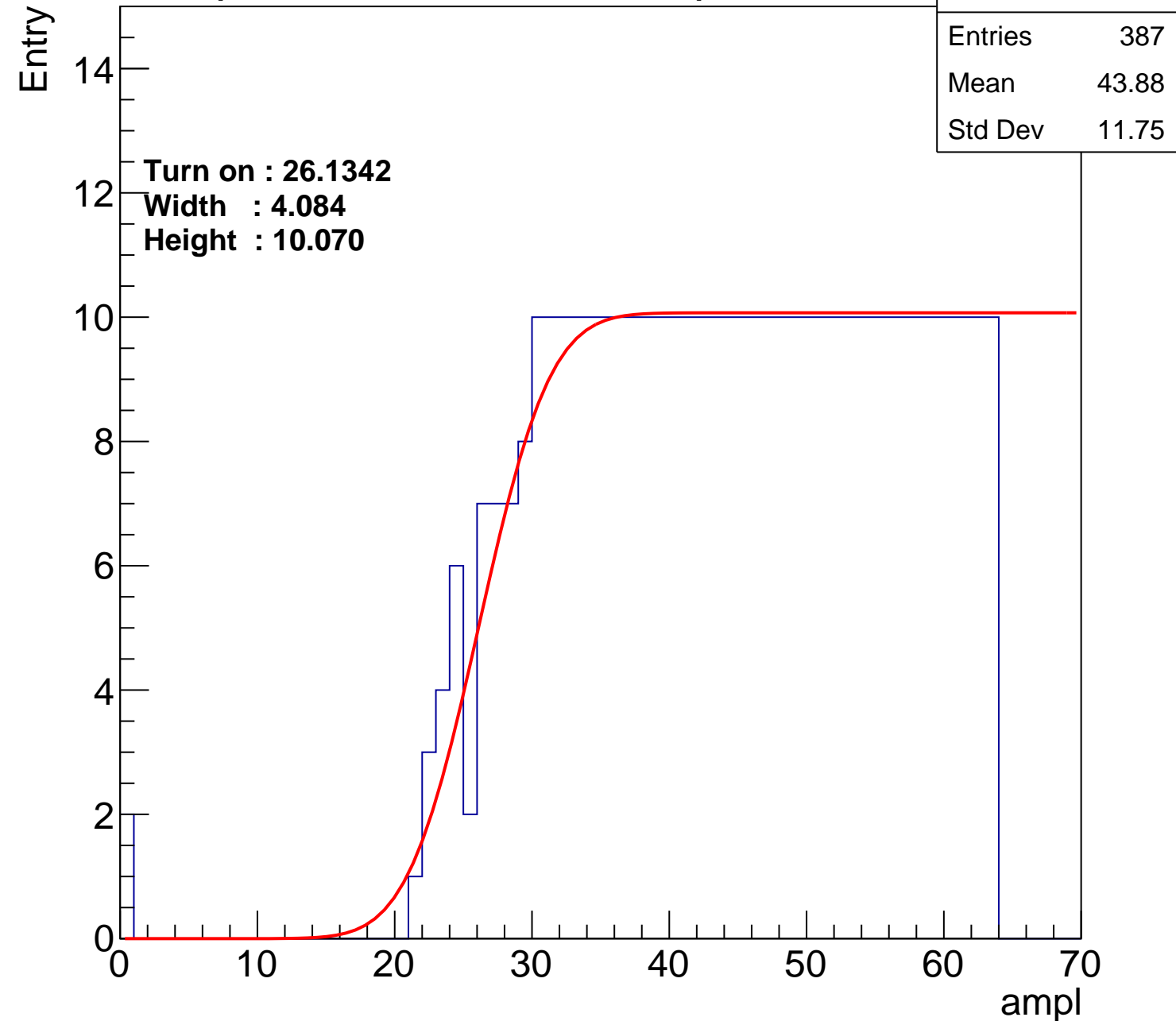
Width : 4.084

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch51

calib_packv5_042523_0143.root, FC#4, port A2

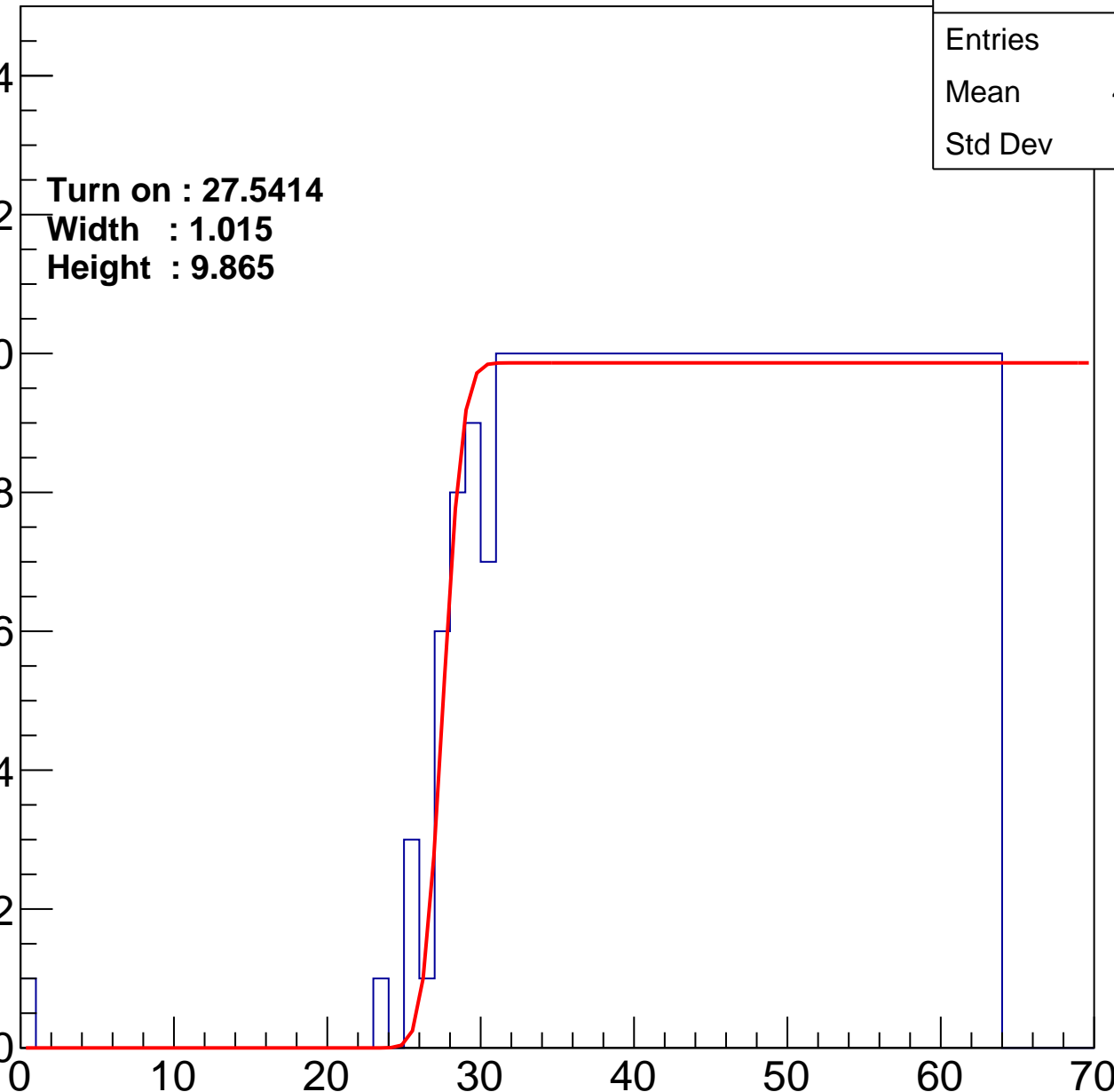
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5414
Width : 1.015
Height : 9.865

Entries	366
Mean	45.06
Std Dev	10.89

ampl



B1L100S, U9-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.1105

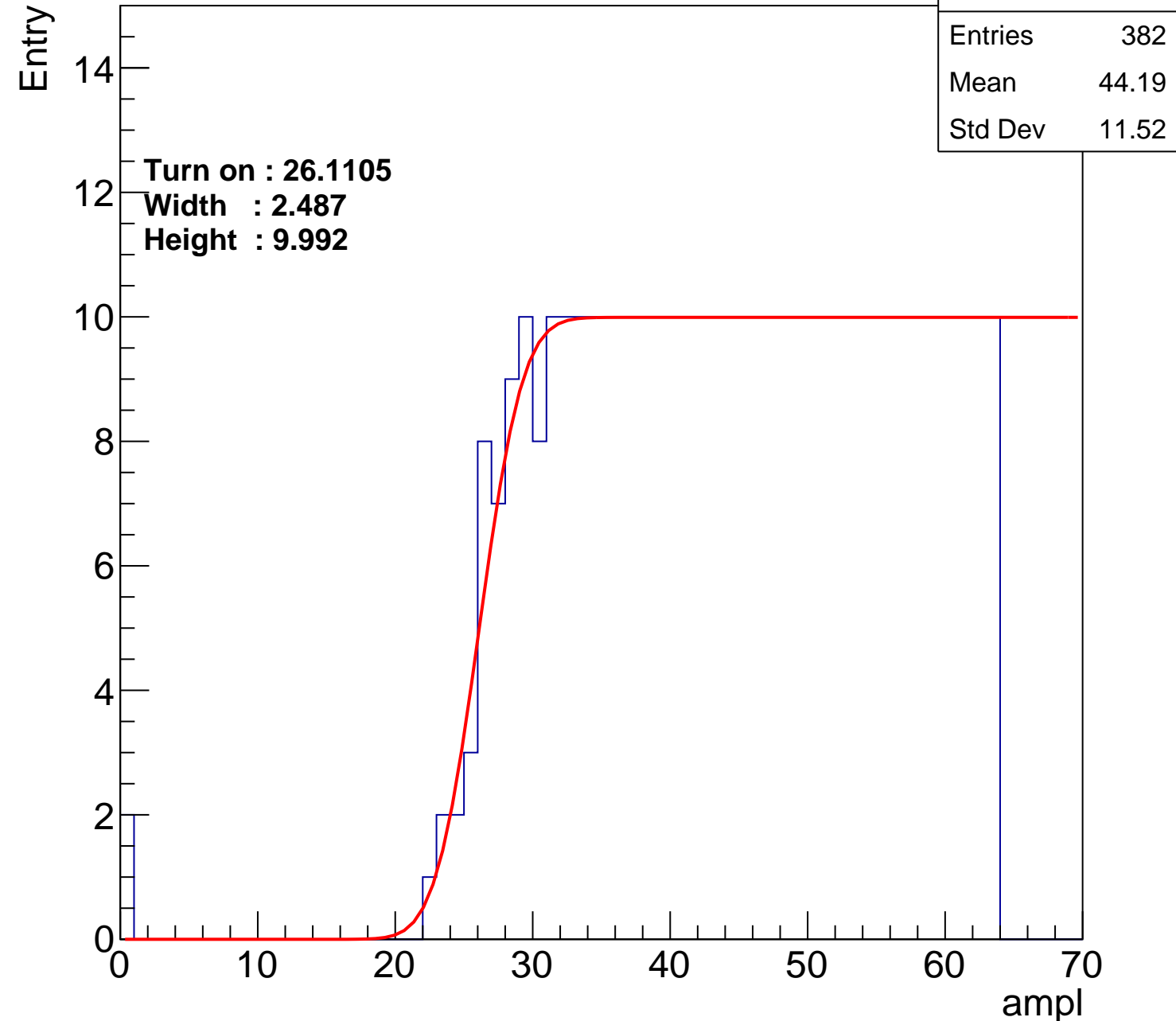
Width : 2.487

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.51
Std Dev	11.98

Turn on : 24.8682

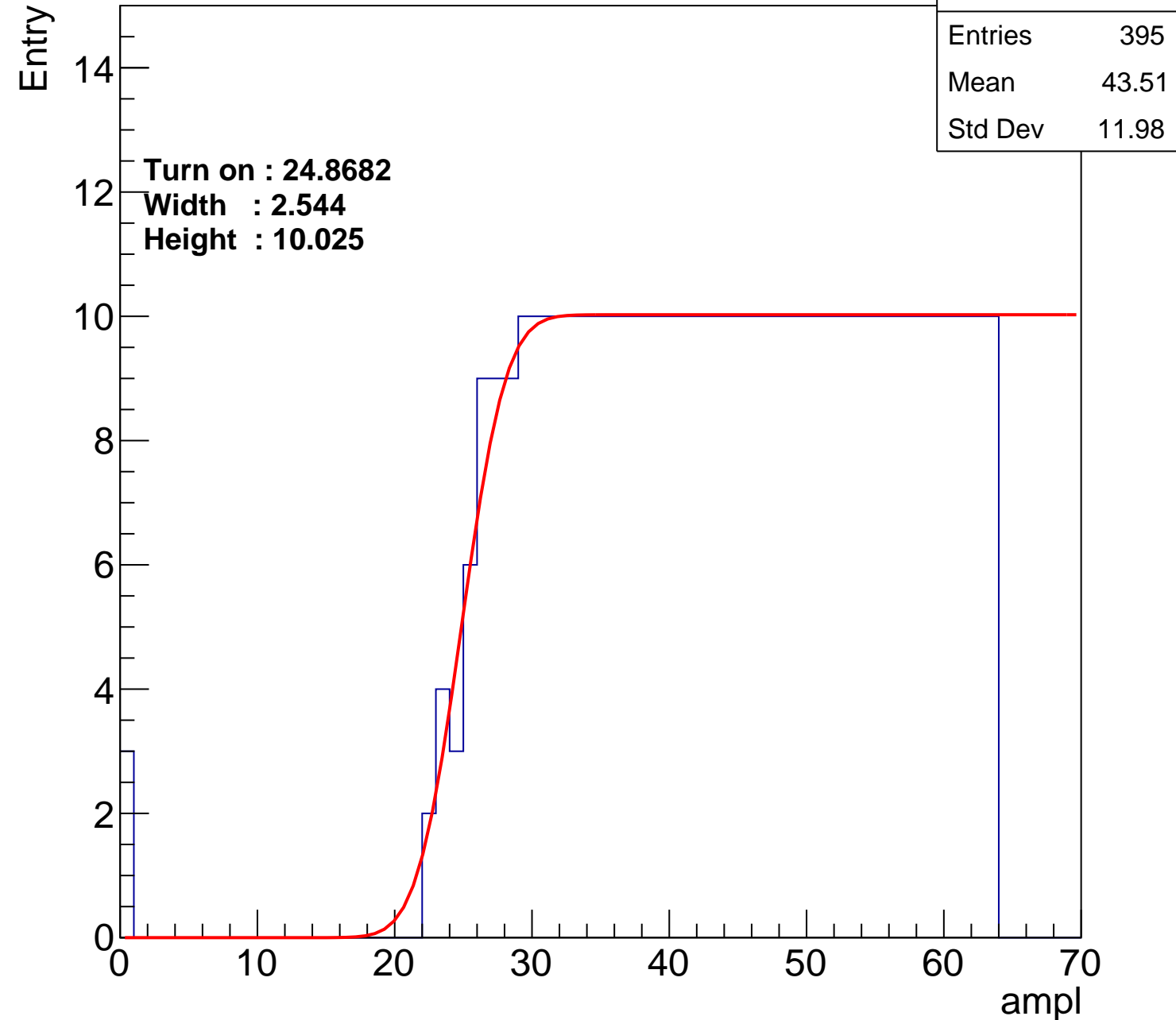
Width : 2.544

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch54

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.6166

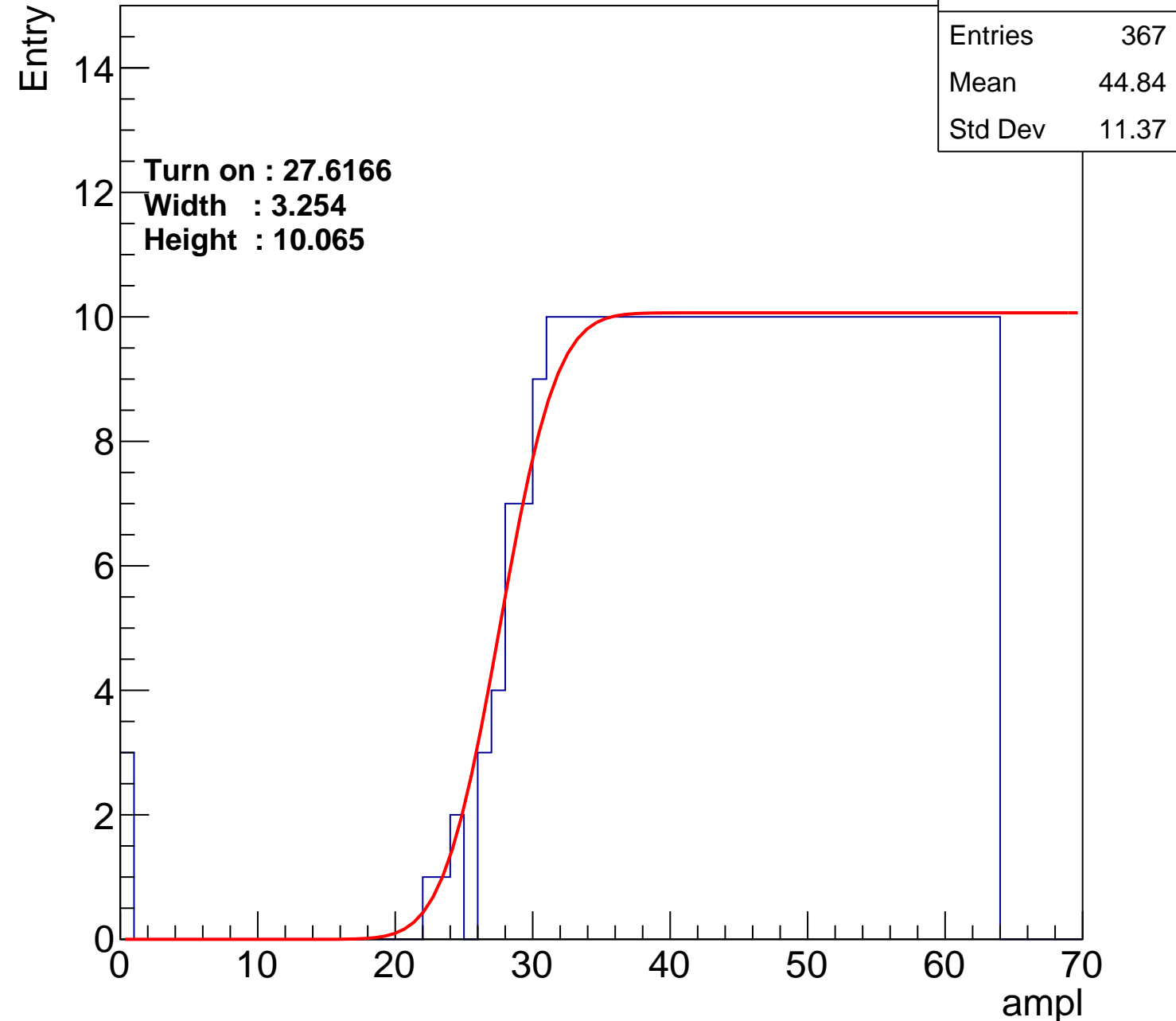
Width : 3.254

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch55

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.42
Std Dev	12.09

Turn on : 25.5409

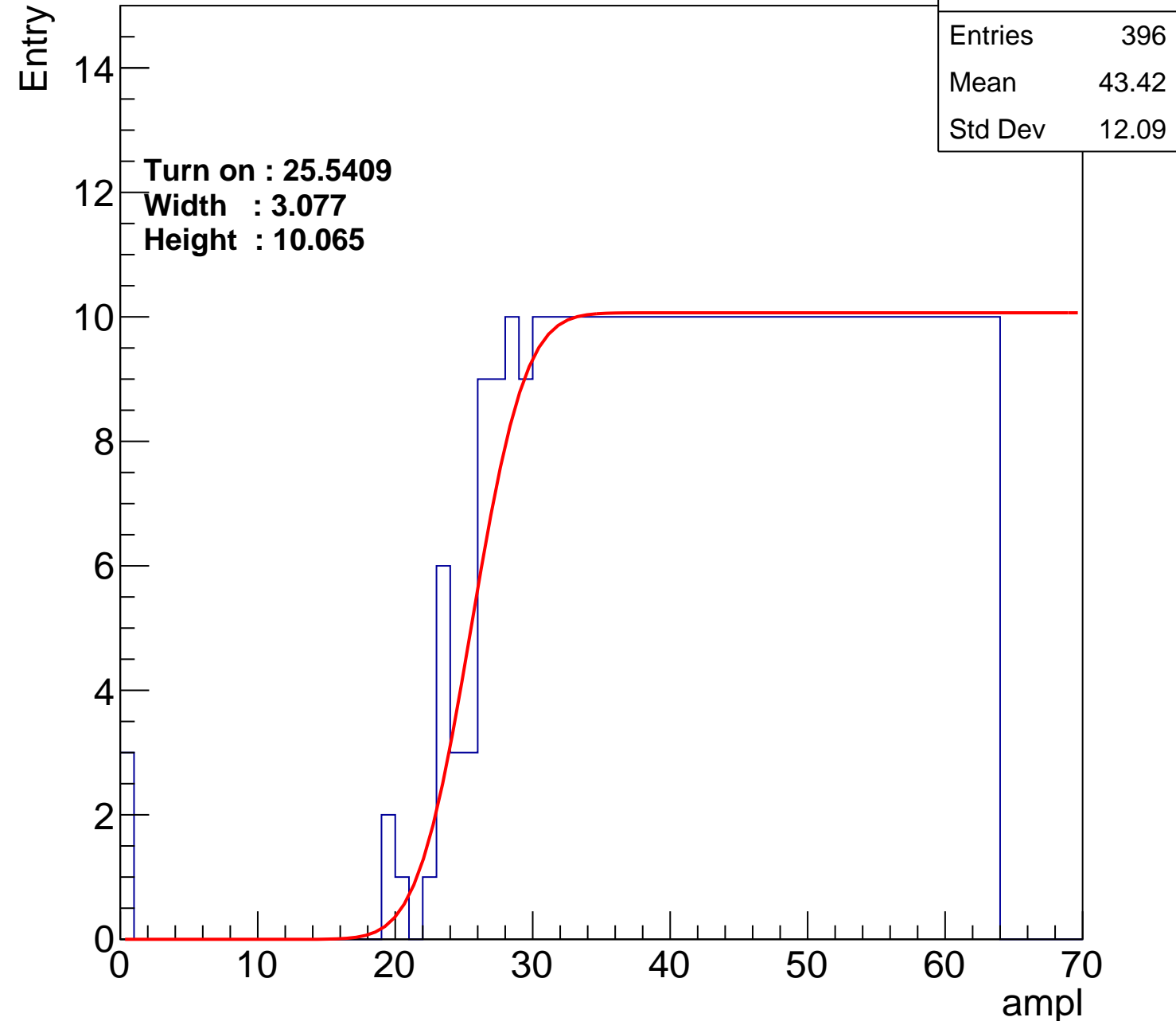
Width : 3.077

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch56

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	44.89
Std Dev	11.54

Turn on : 28.2444

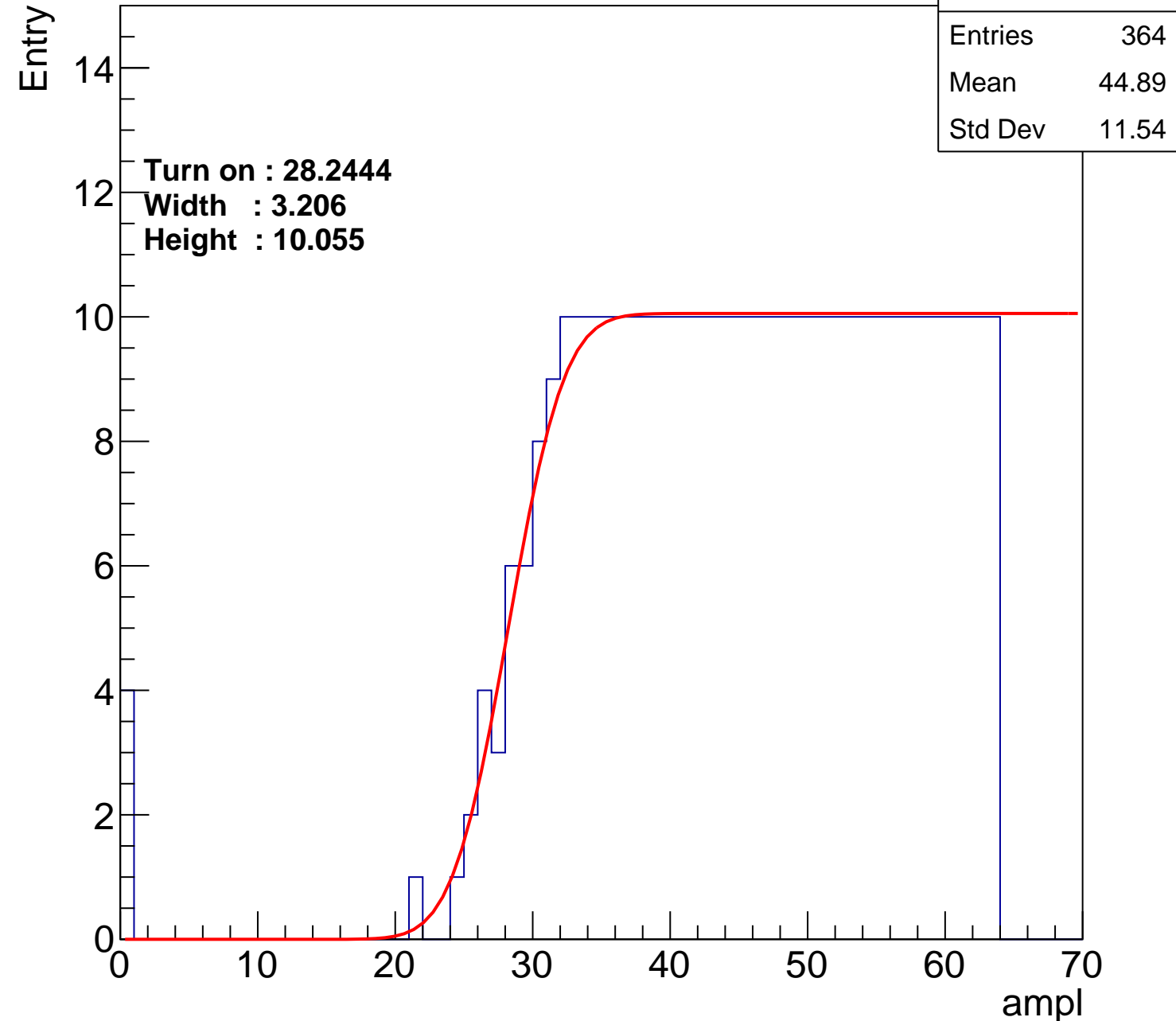
Width : 3.206

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.5780

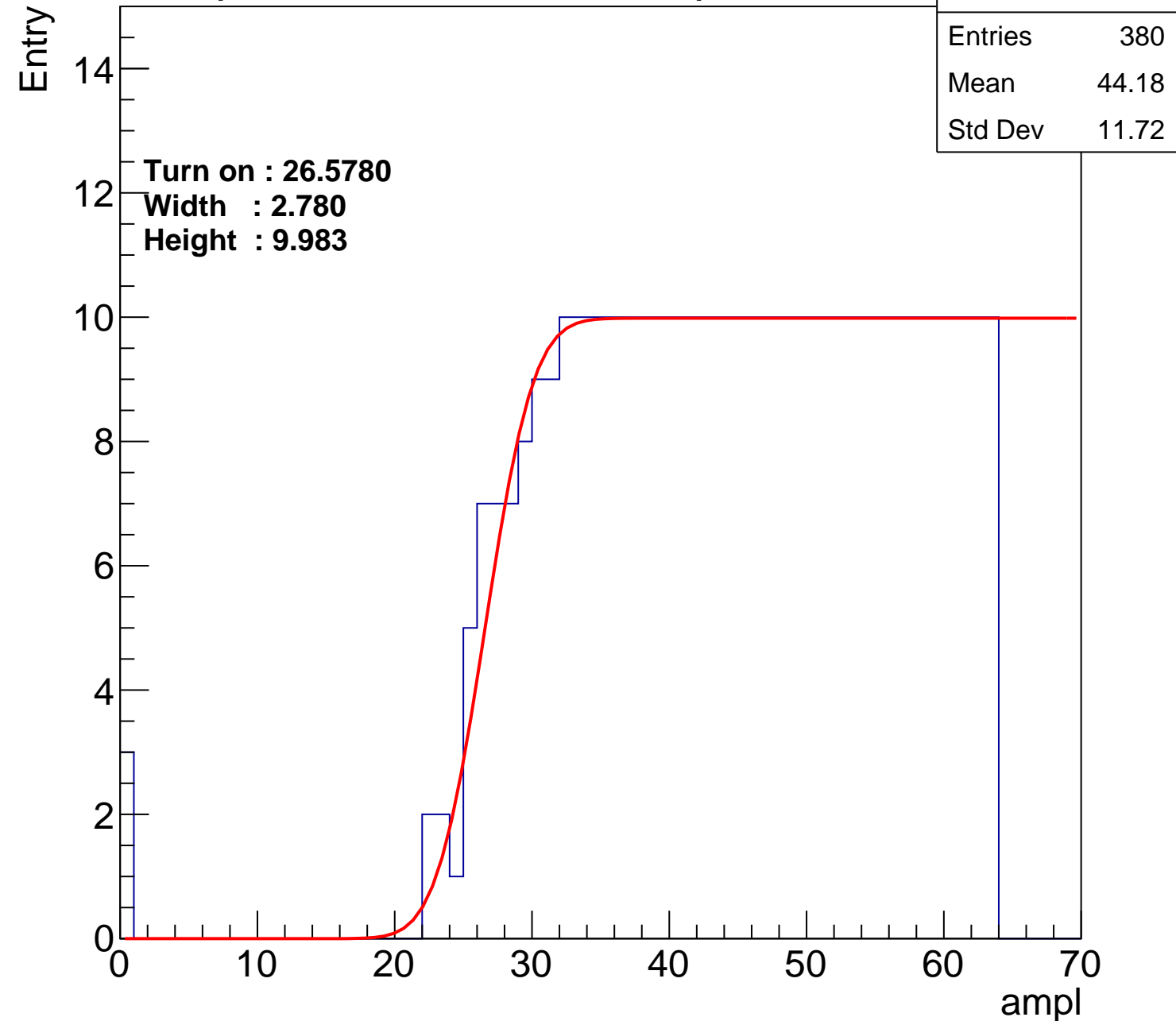
Width : 2.780

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch58

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.34
Std Dev	11.81

Turn on : 27.1154

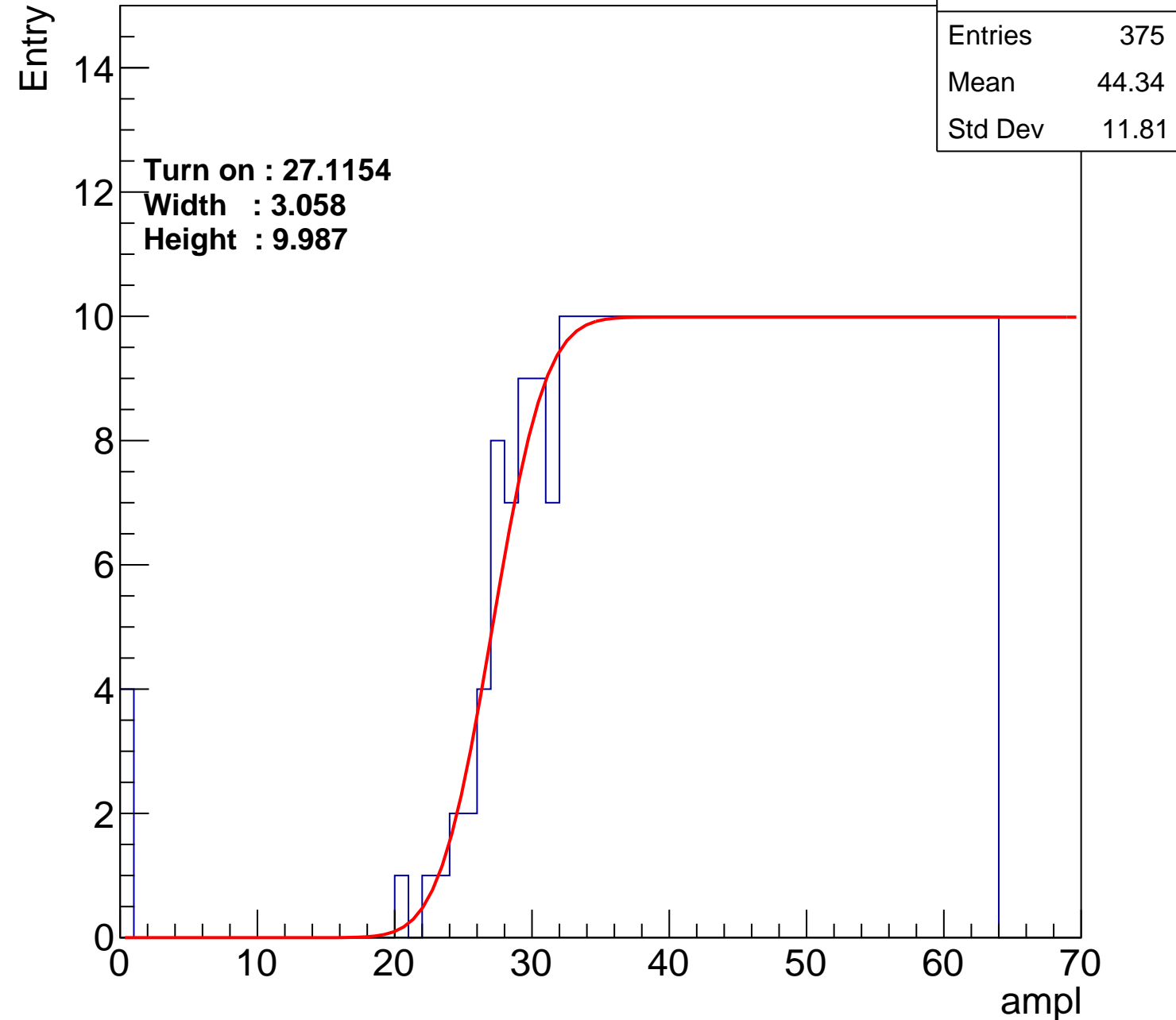
Width : 3.058

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch59

calib_packv5_042523_0143.root, FC#4, port A2

Entries	343
Mean	45.98
Std Dev	10.86

Turn on : 30.1505

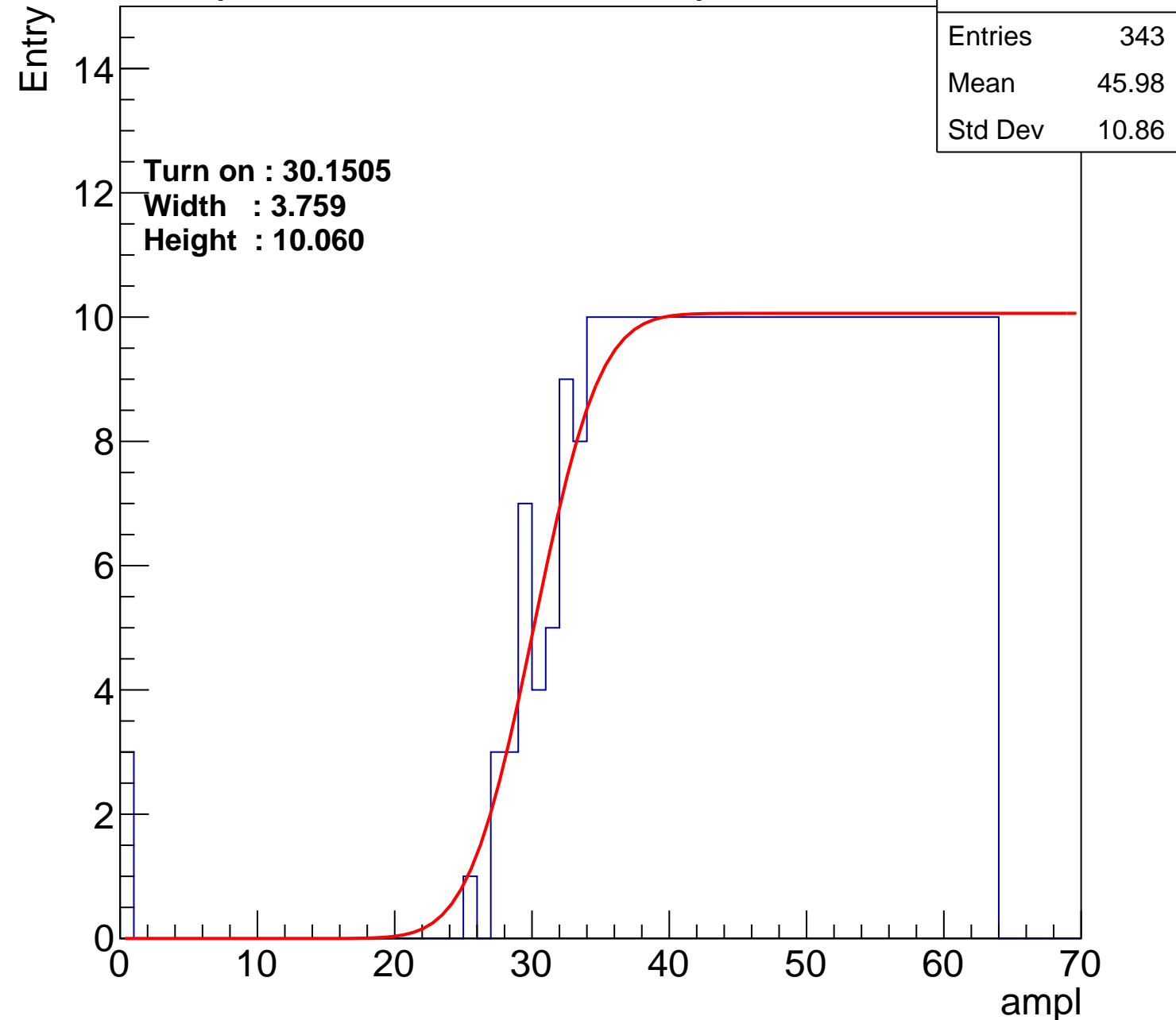
Width : 3.759

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch60

calib_packv5_042523_0143.root, FC#4, port A2

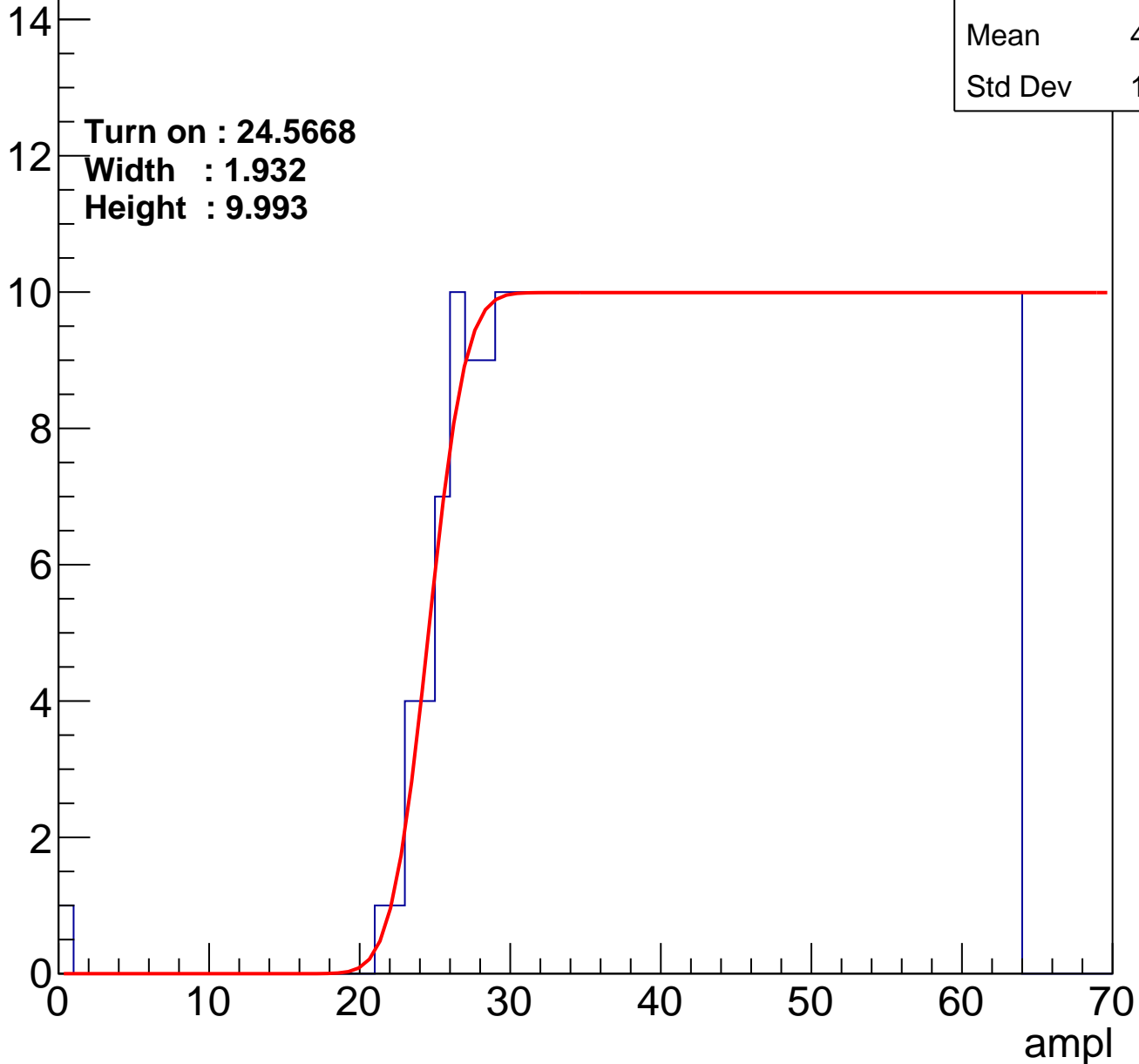
Entries	396
Mean	43.59
Std Dev	11.68

Turn on : 24.5668

Width : 1.932

Height : 9.993

Entry



B1L100S, U9-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.6249

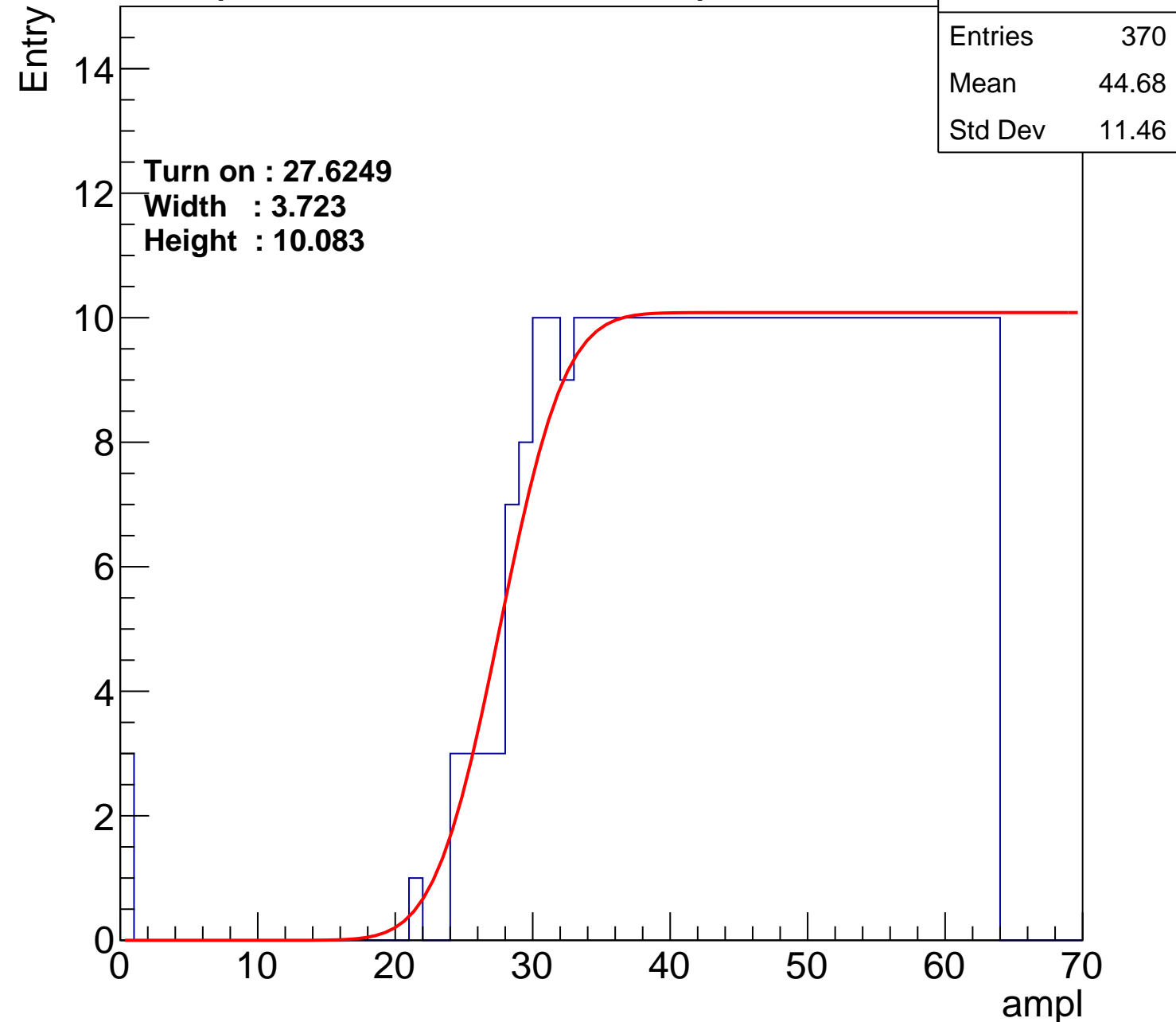
Width : 3.723

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch62

calib_packv5_042523_0143.root, FC#4, port A2

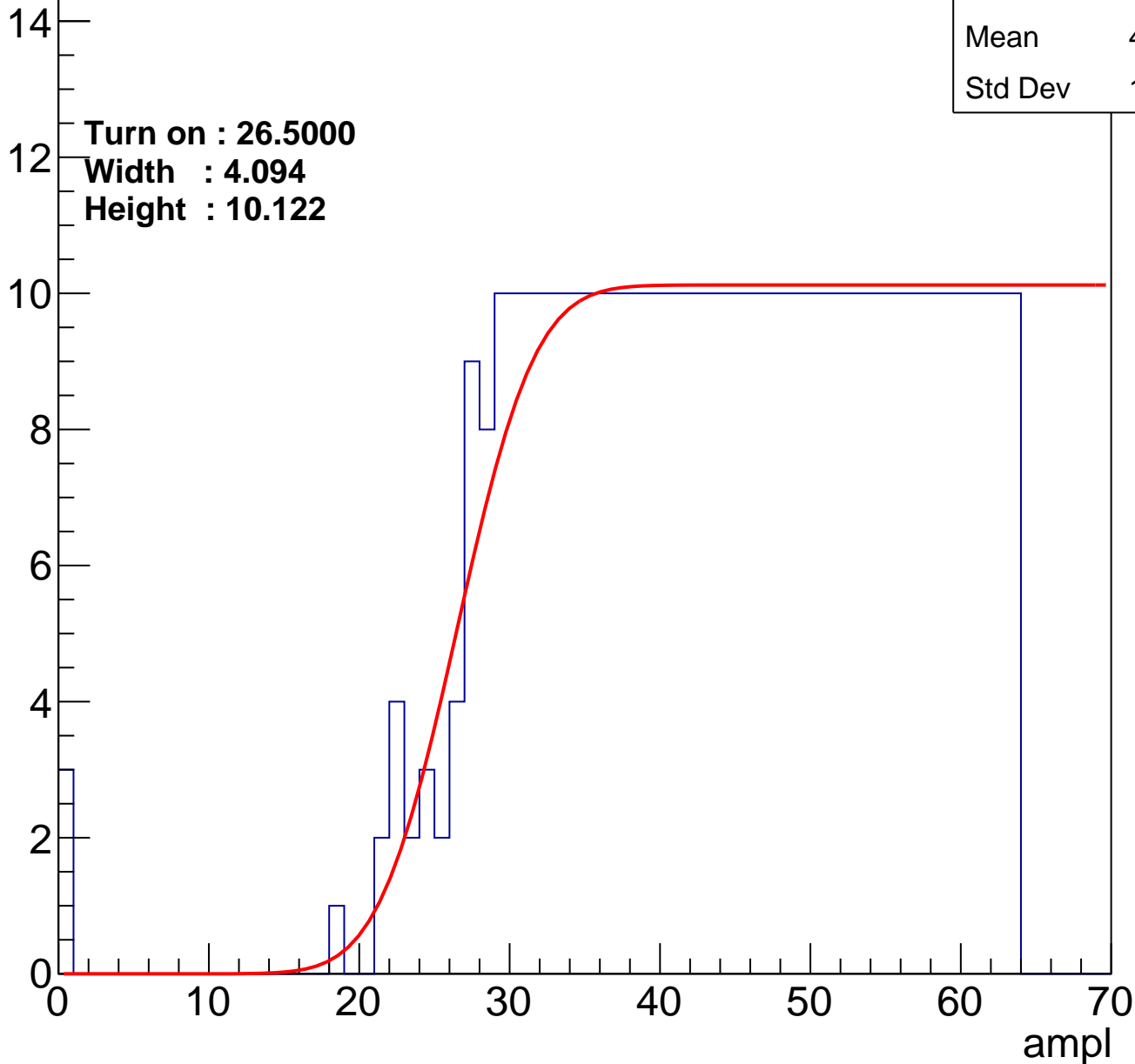
Entries	388
Mean	43.78
Std Dev	11.94

Turn on : 26.5000

Width : 4.094

Height : 10.122

Entry



B1L100S, U9-ch63

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.1376

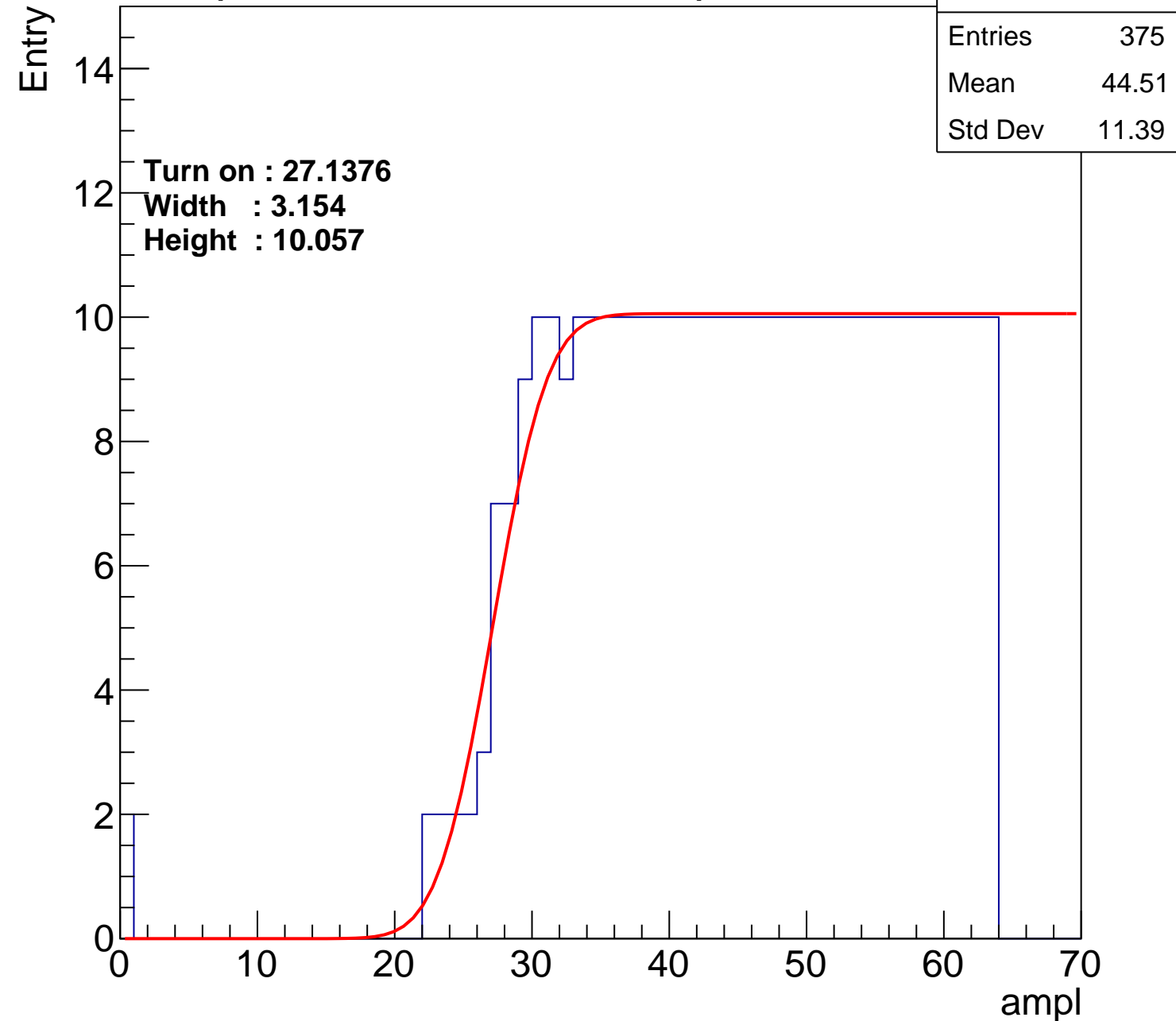
Width : 3.154

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.4081

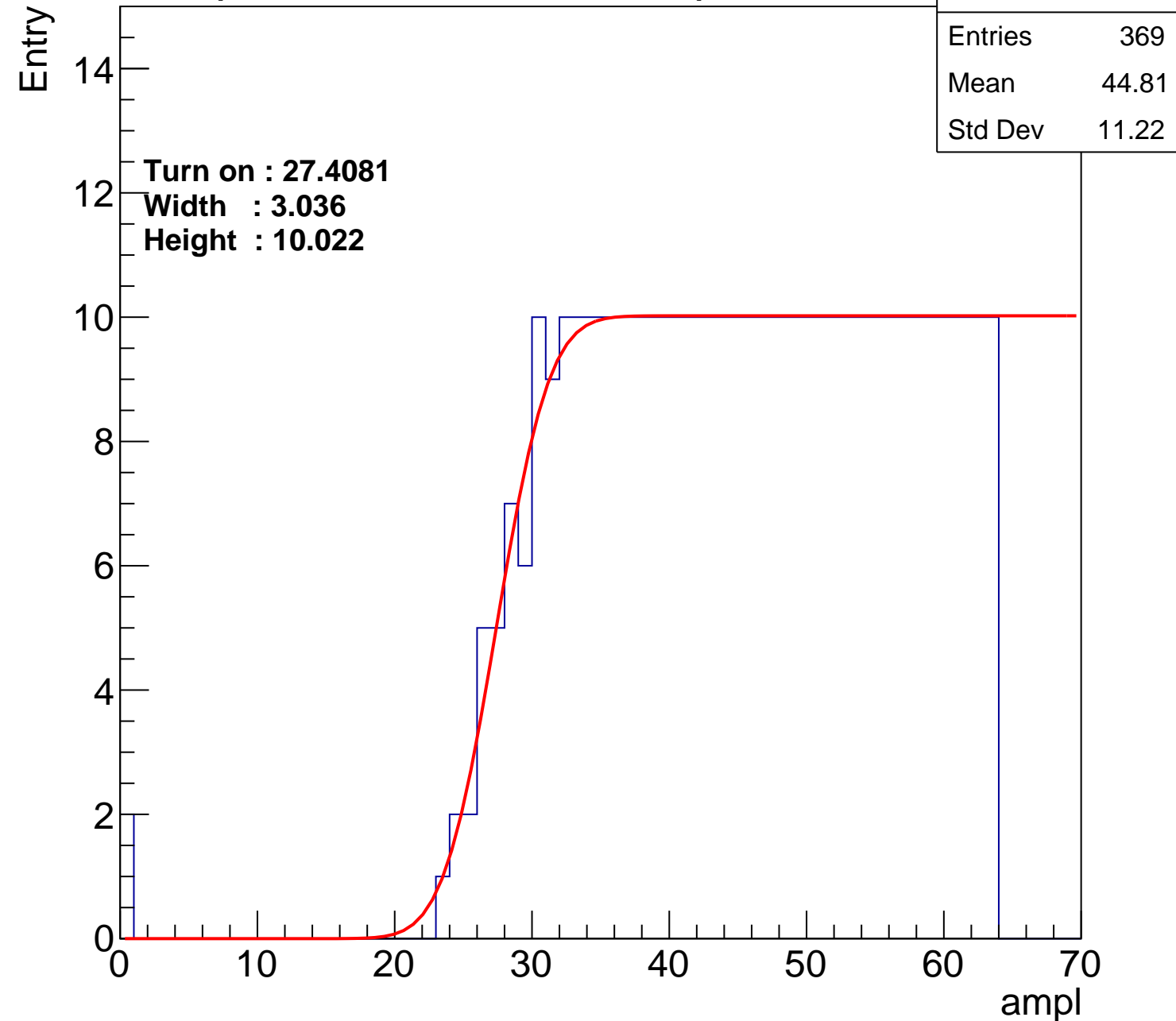
Width : 3.036

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.52
Std Dev	11.51

Turn on : 26.6066

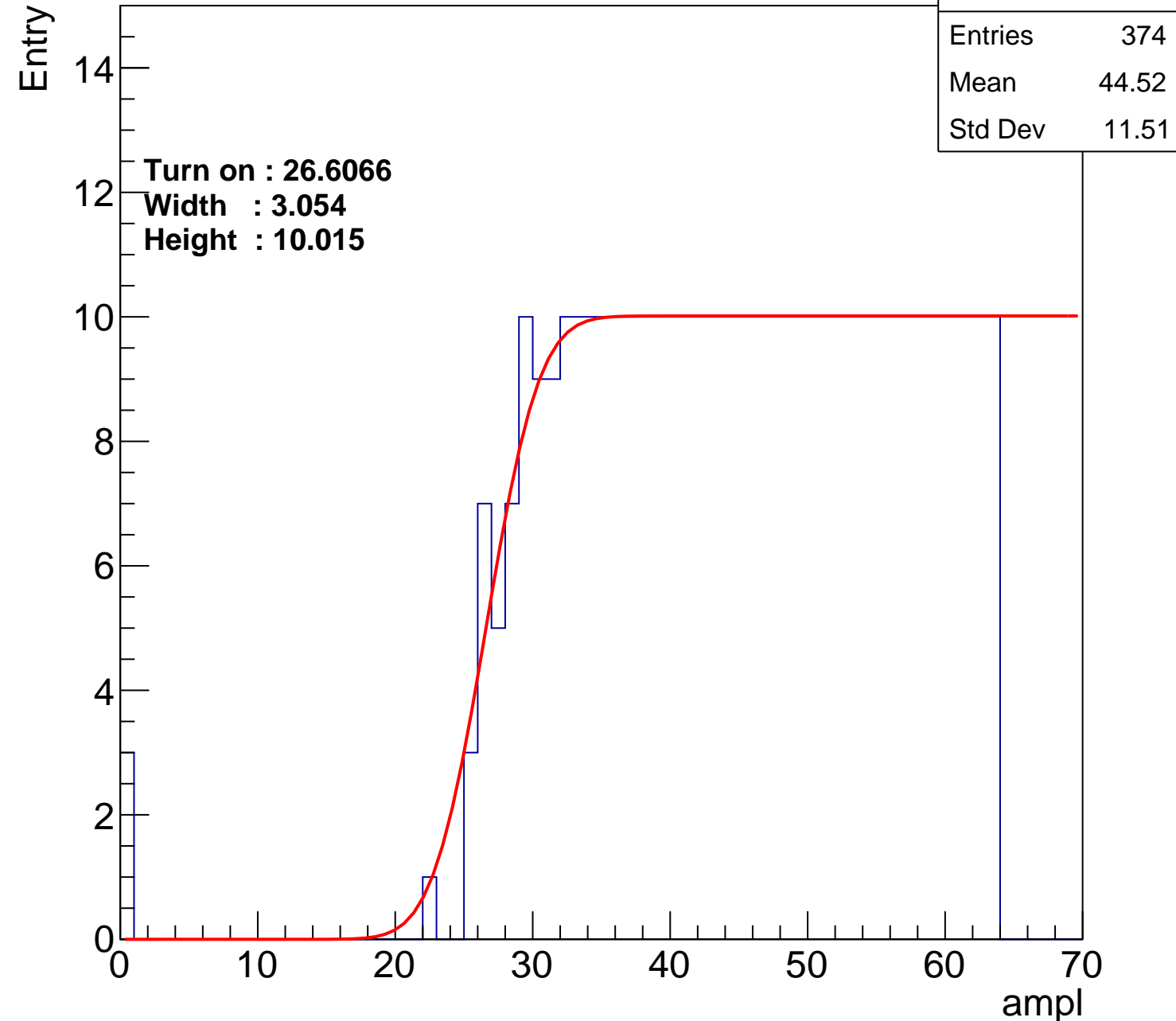
Width : 3.054

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch66

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.52
Std Dev	11.28

Turn on : 26.8302

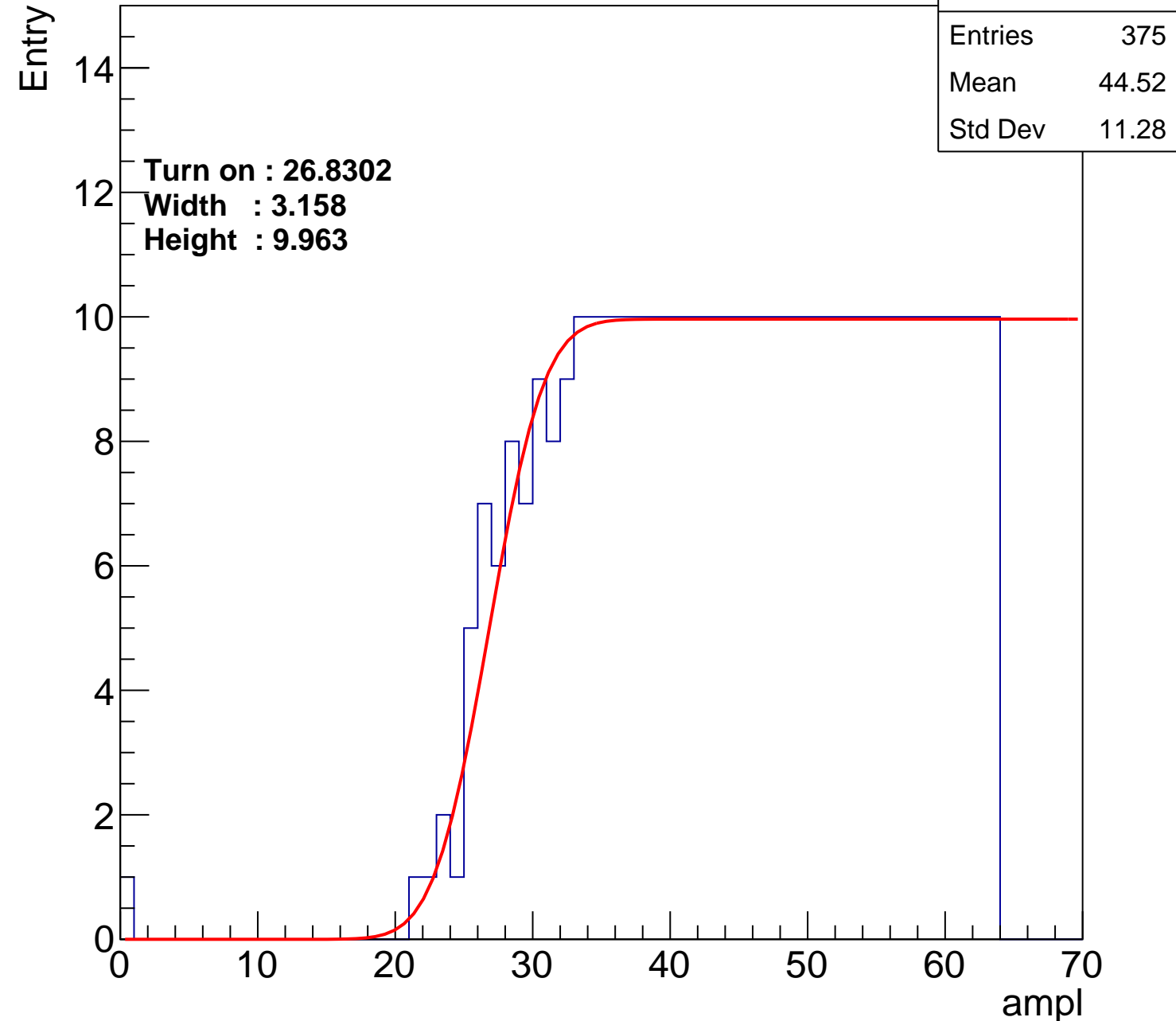
Width : 3.158

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch67

calib_packv5_042523_0143.root, FC#4, port A2

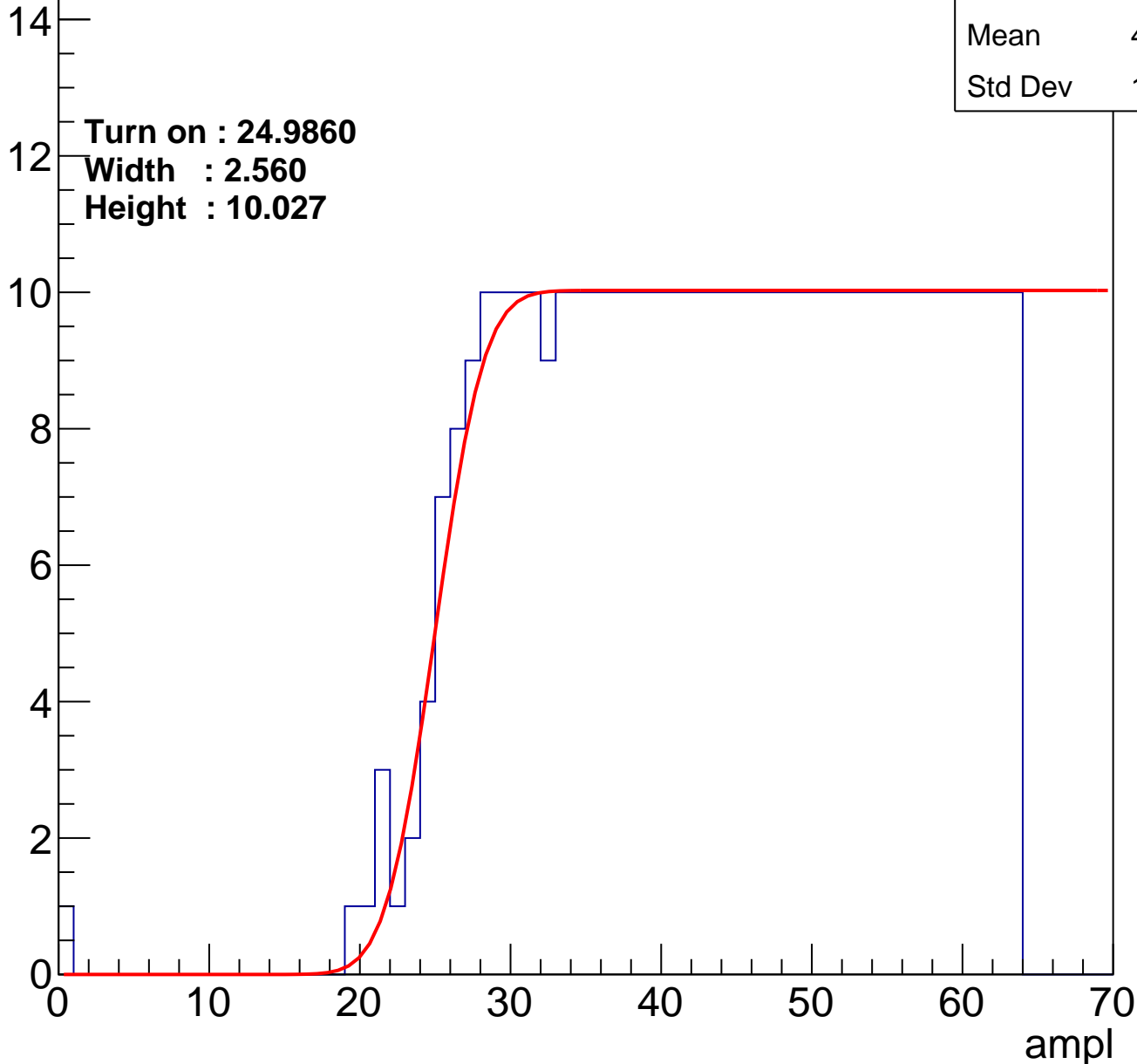
Entries	396
Mean	43.54
Std Dev	11.77

Turn on : 24.9860

Width : 2.560

Height : 10.027

Entry



B1L100S, U9-ch68

calib_packv5_042523_0143.root, FC#4, port A2

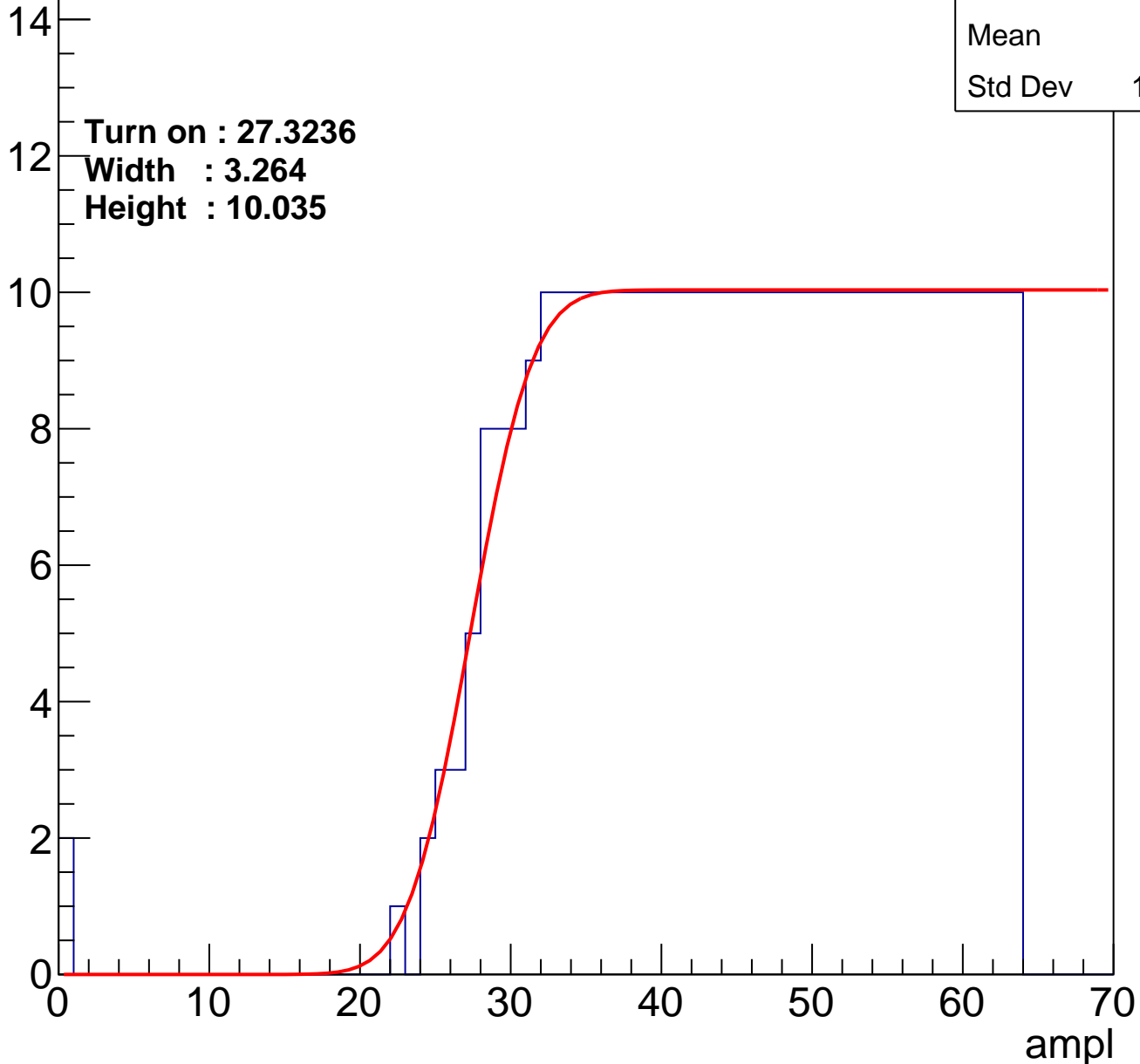
Entries	369
Mean	44.8
Std Dev	11.23

Turn on : 27.3236

Width : 3.264

Height : 10.035

Entry



B1L100S, U9-ch69

calib_packv5_042523_0143.root, FC#4, port A2

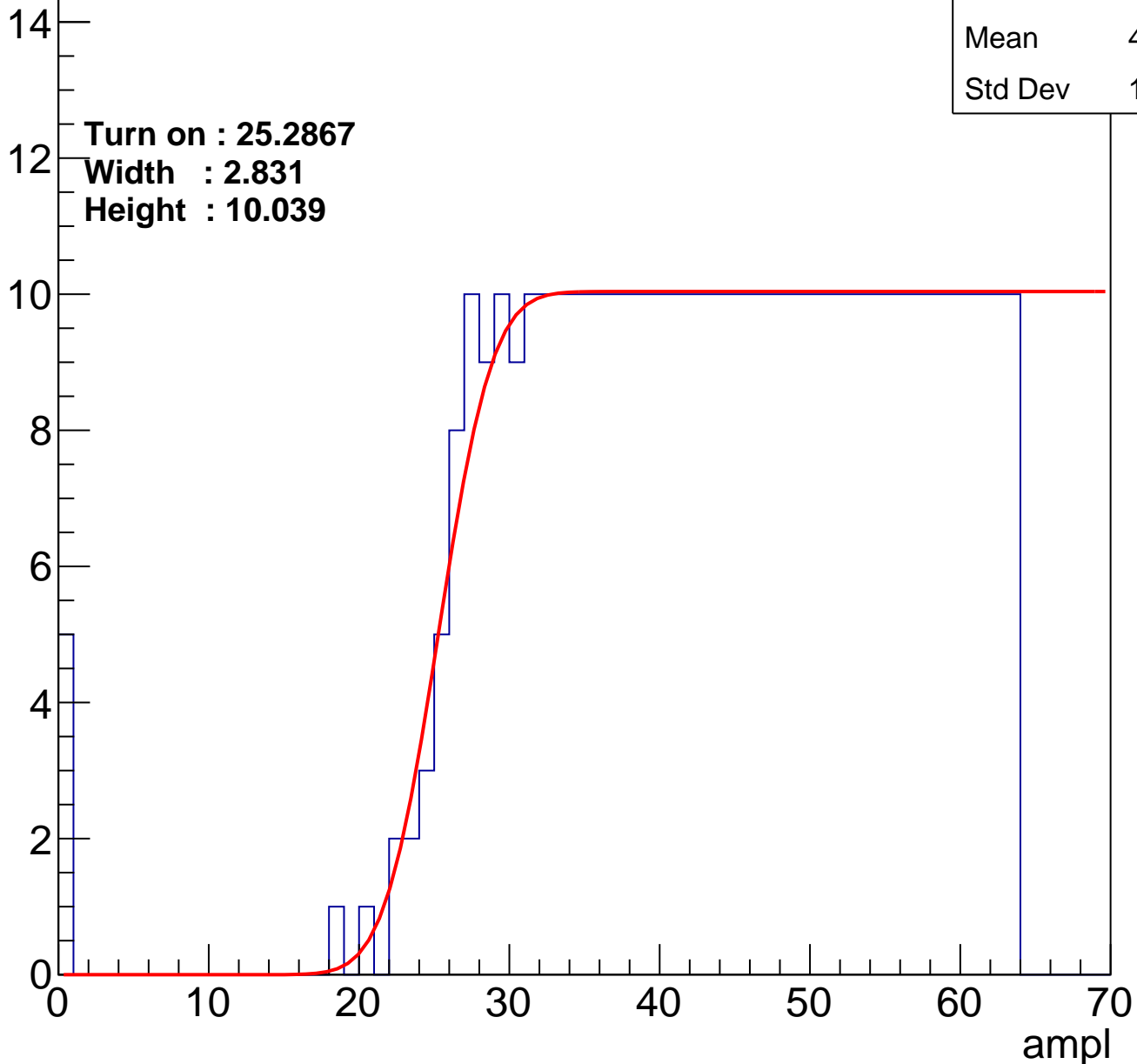
Entries	395
Mean	43.35
Std Dev	12.36

Turn on : 25.2867

Width : 2.831

Height : 10.039

Entry



B1L100S, U9-ch70

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.08
Std Dev	11.31

Turn on : 28.5454

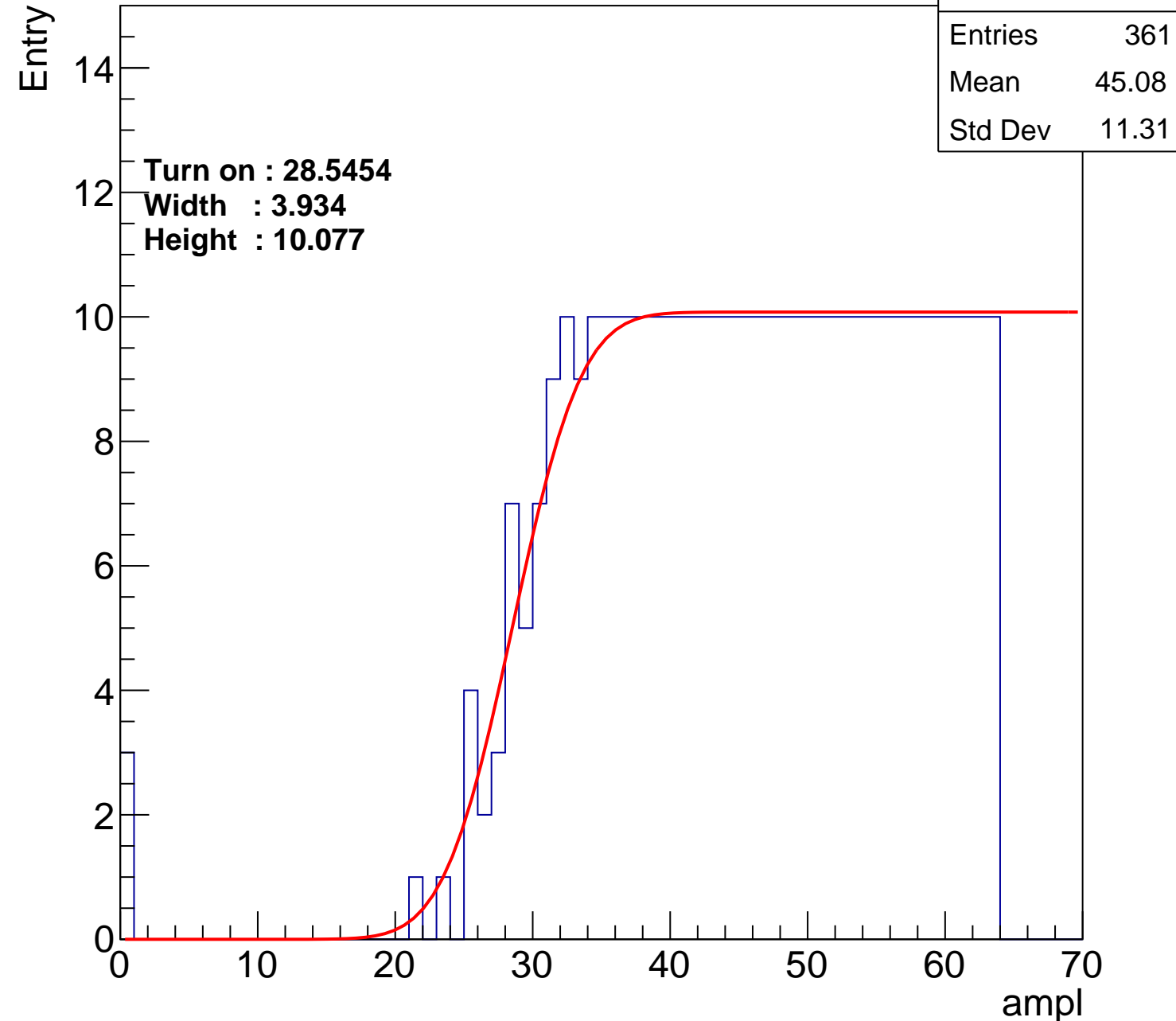
Width : 3.934

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.1433

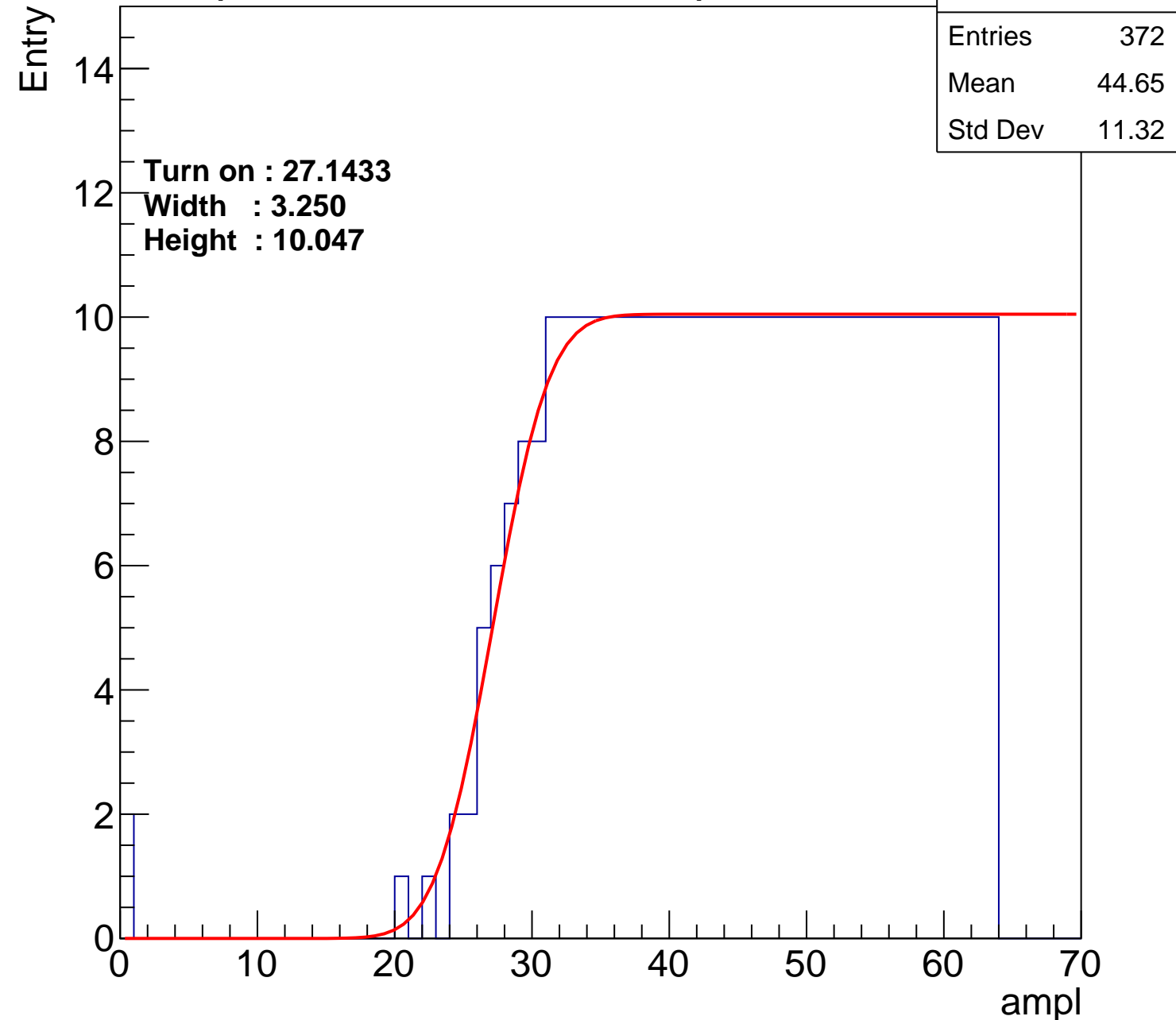
Width : 3.250

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch72

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.4653

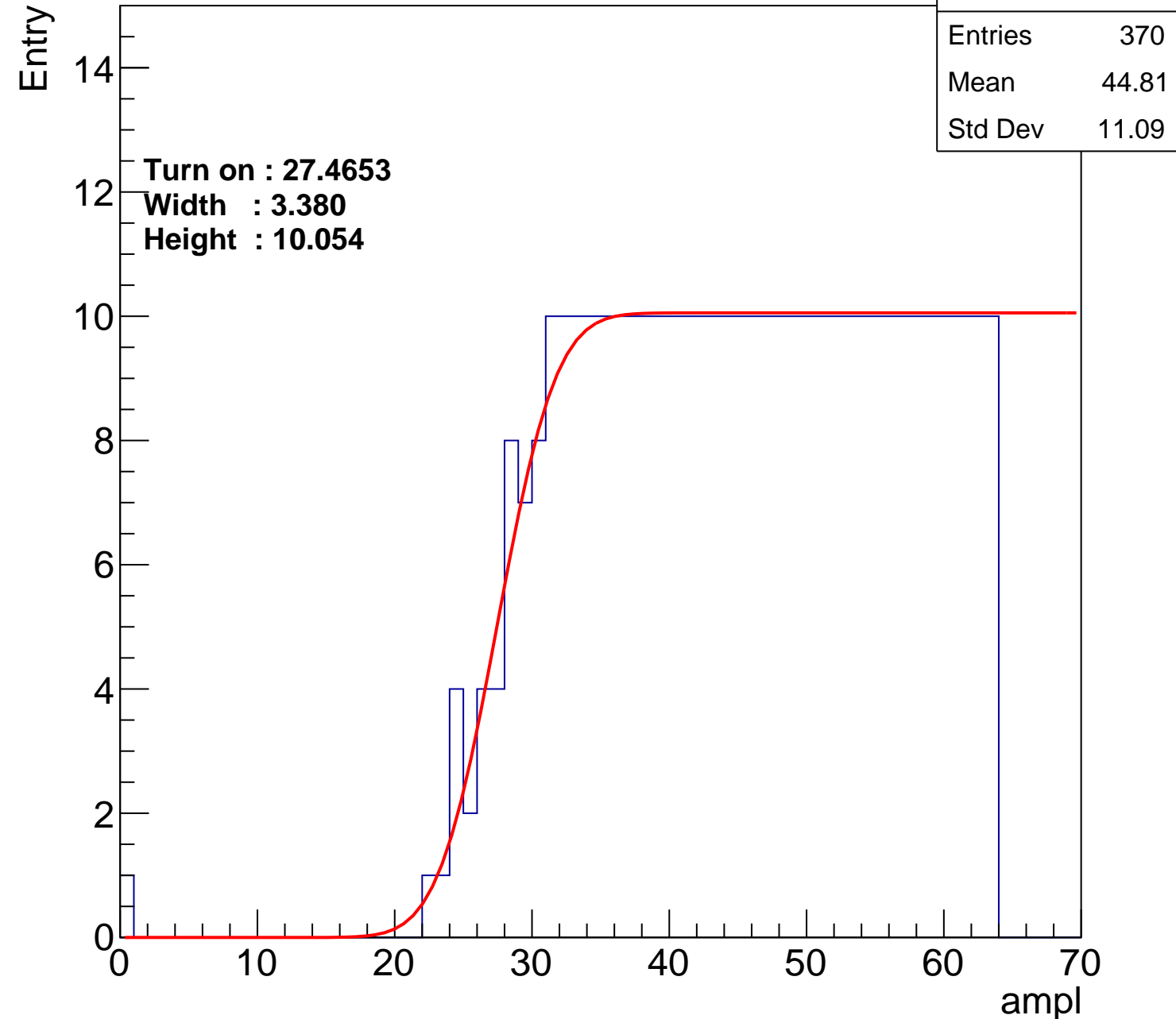
Width : 3.380

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.75
Std Dev	11.92

Turn on : 25.8422

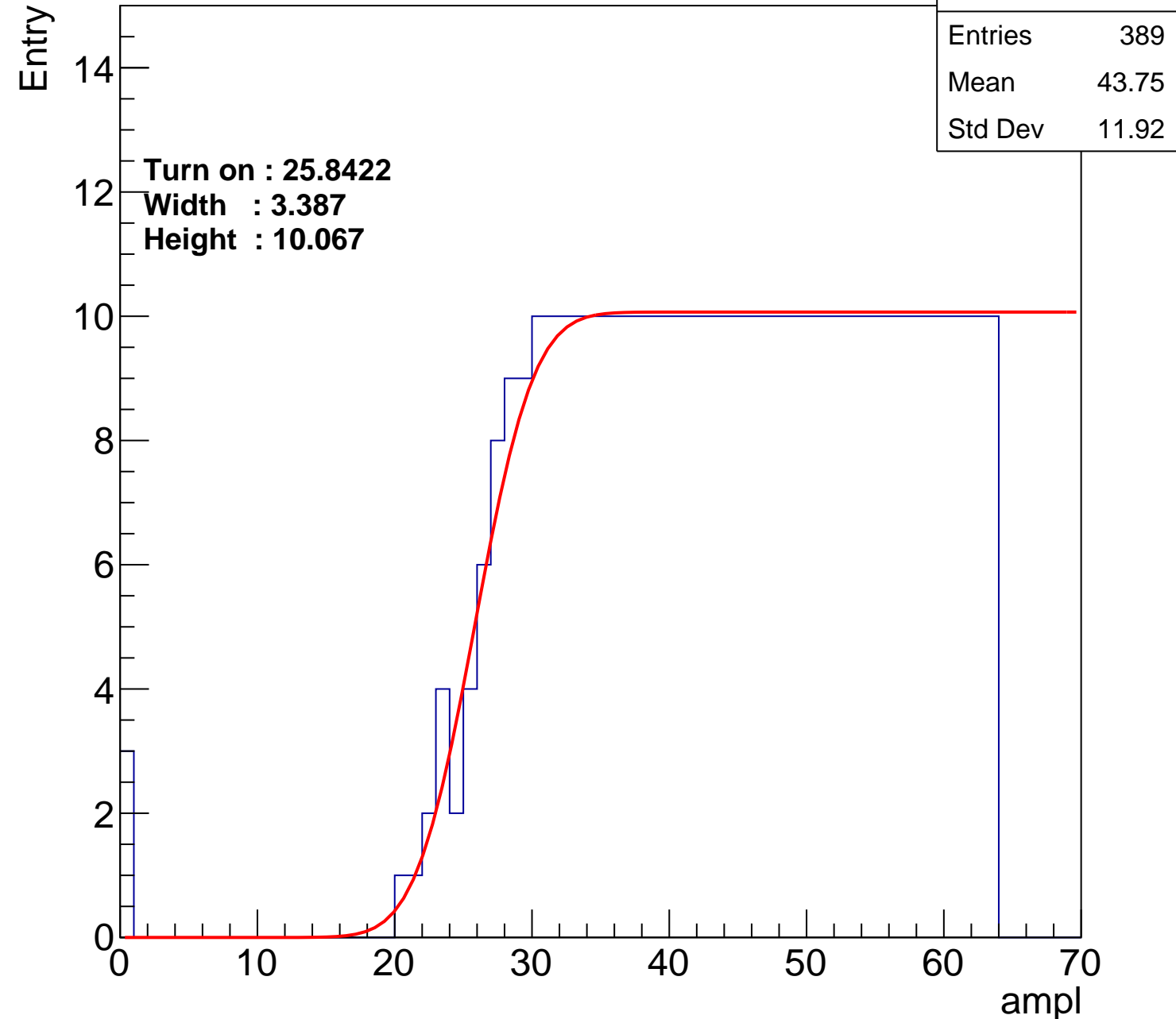
Width : 3.387

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch74

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.34
Std Dev	11.58

Turn on : 26.6111

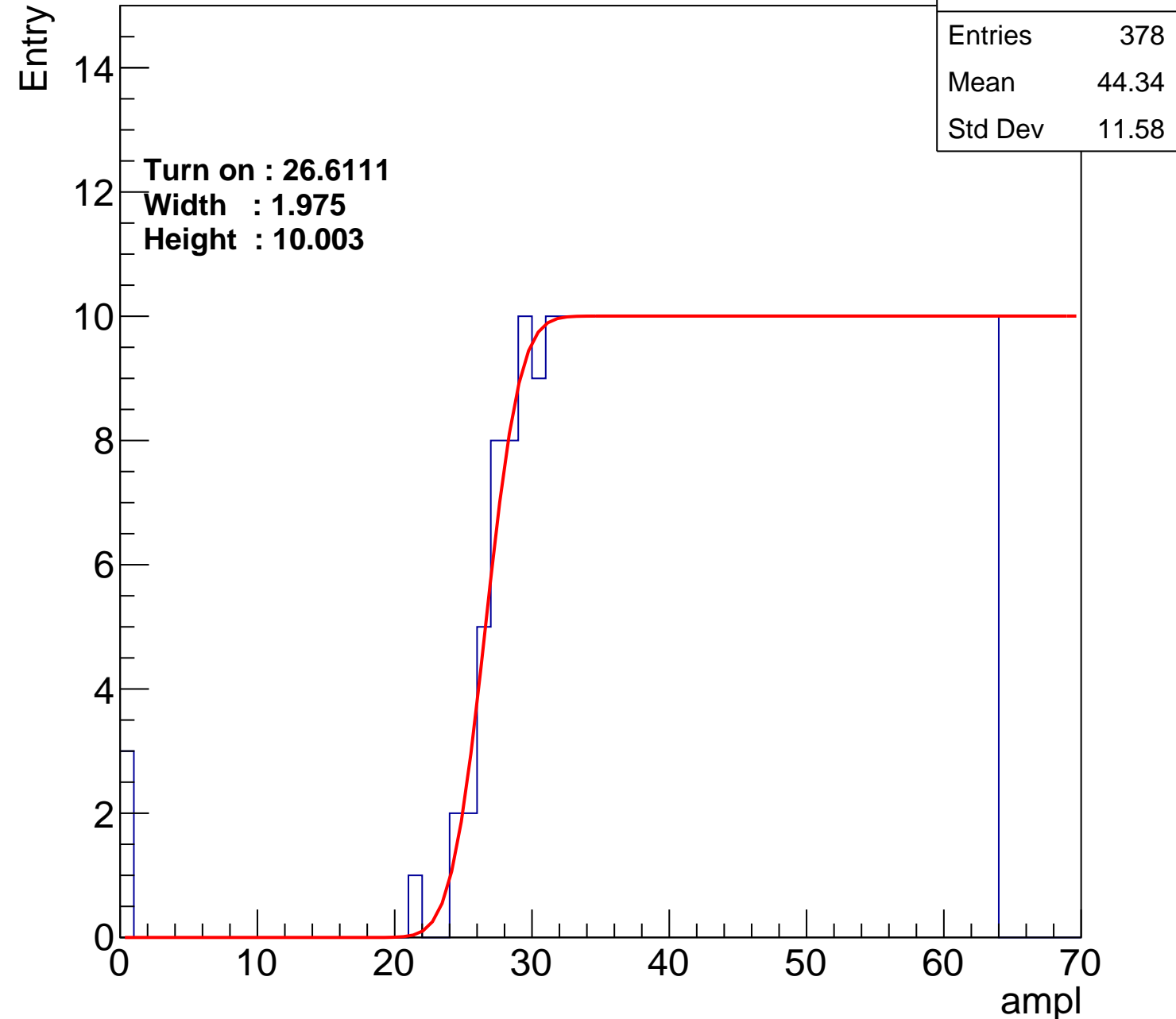
Width : 1.975

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch75

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 25.9080

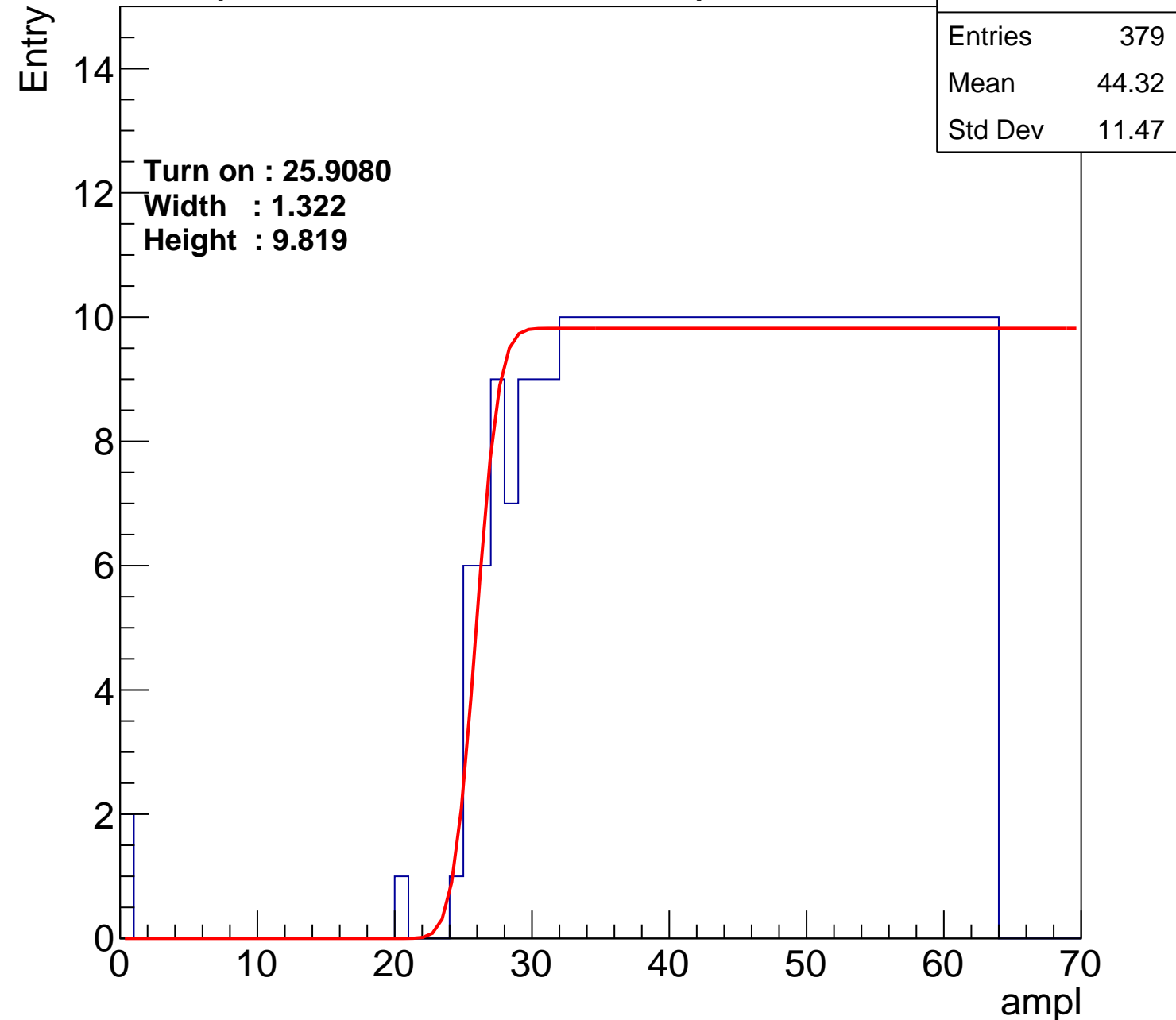
Width : 1.322

Height : 9.819

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.87
Std Dev	11.37

Turn on : 27.8239

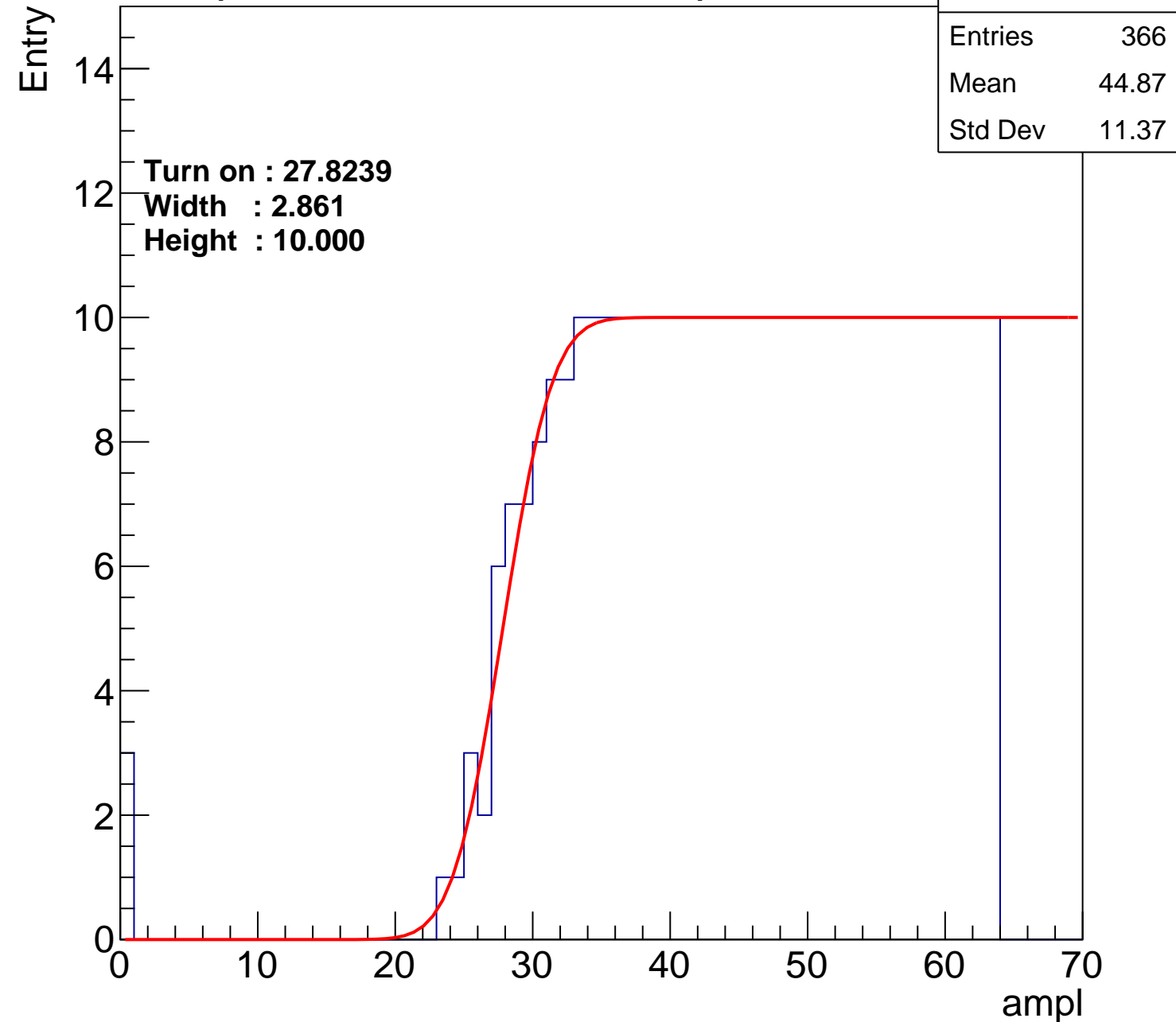
Width : 2.861

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.3
Std Dev	11.77

Turn on : 26.8701

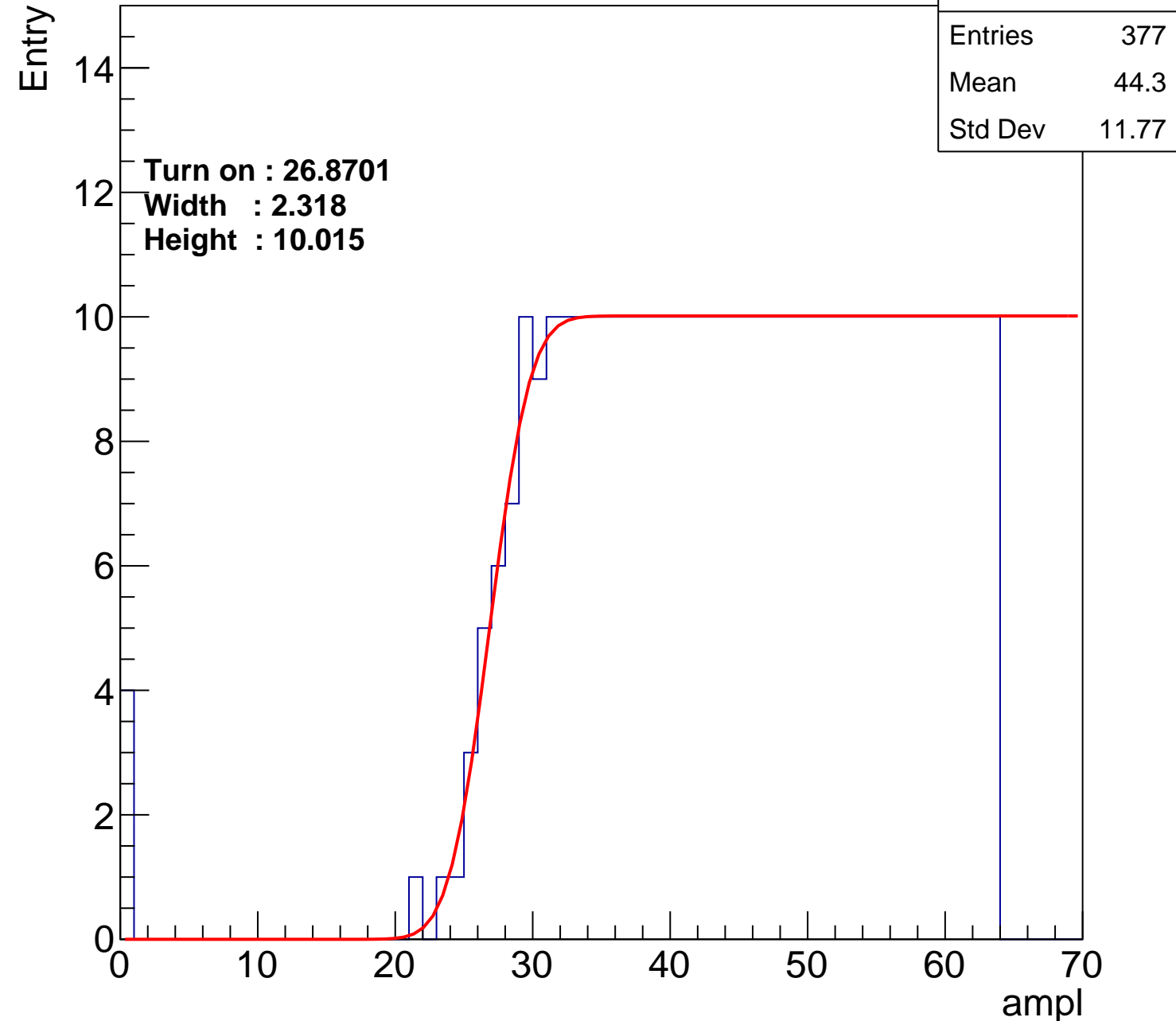
Width : 2.318

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch78

calib_packv5_042523_0143.root, FC#4, port A2

Entries	394
Mean	43.52
Std Dev	11.97

Turn on : 24.9745

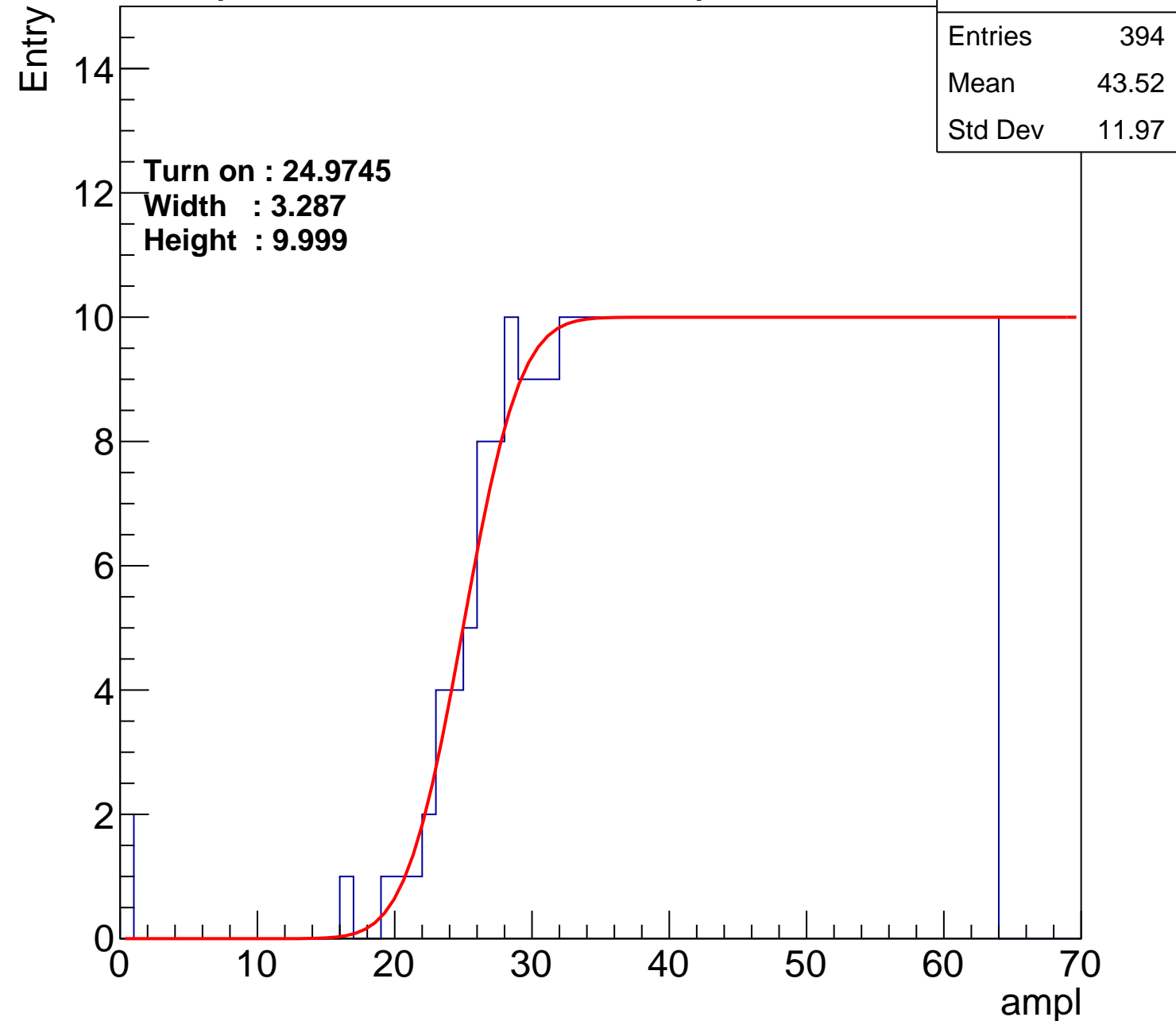
Width : 3.287

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.8038

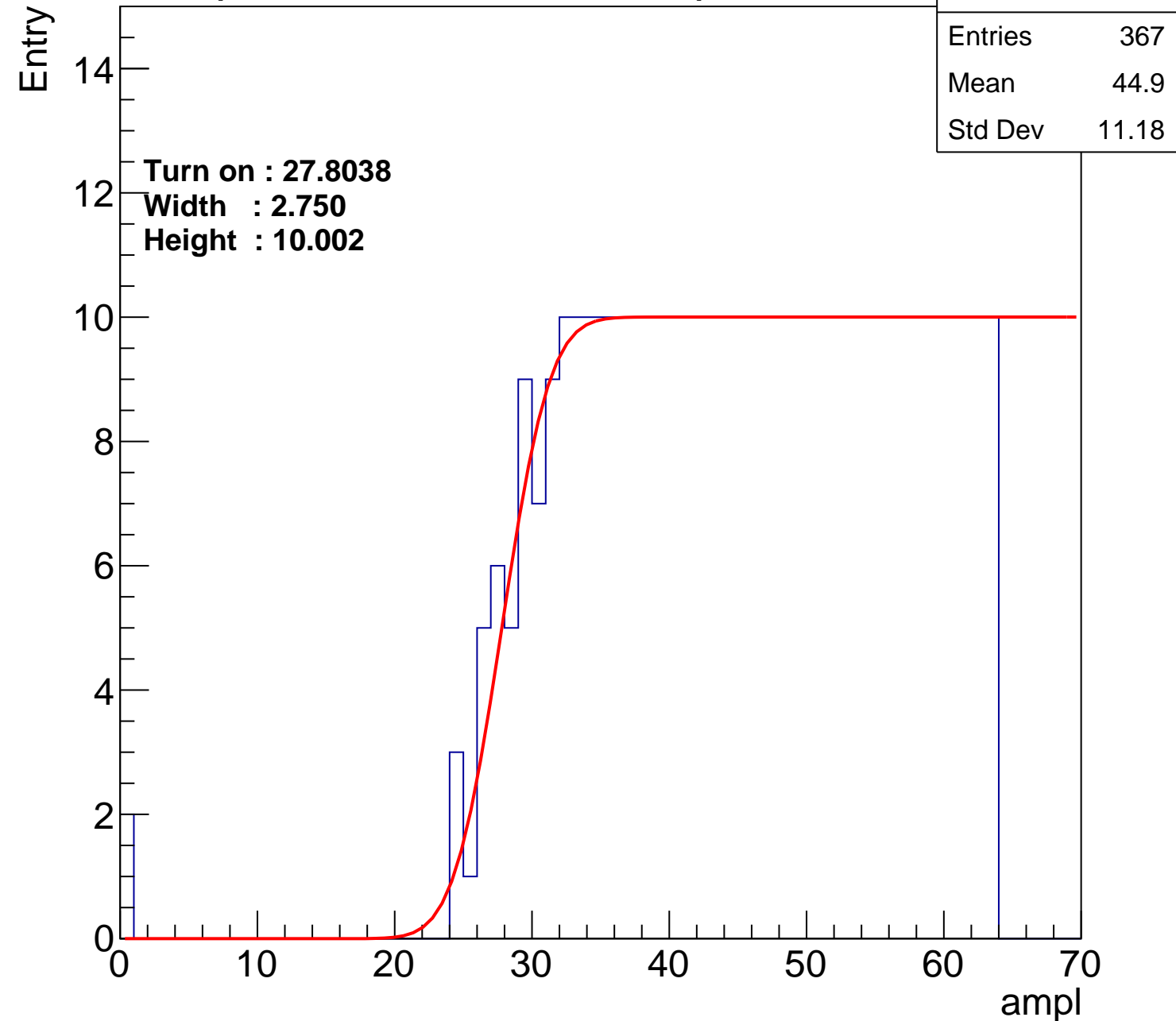
Width : 2.750

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch80

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.1825

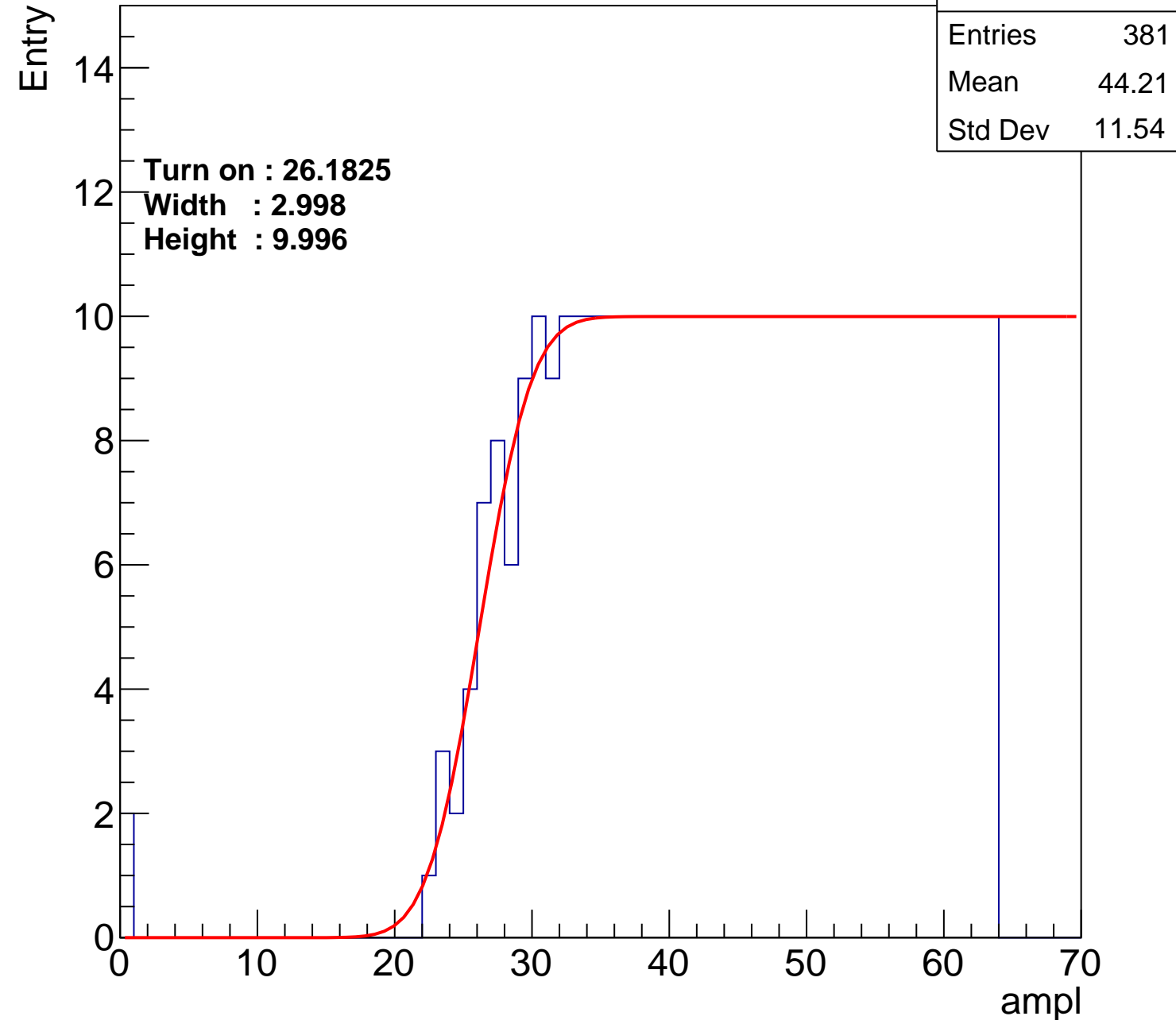
Width : 2.998

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch81

calib_packv5_042523_0143.root, FC#4, port A2

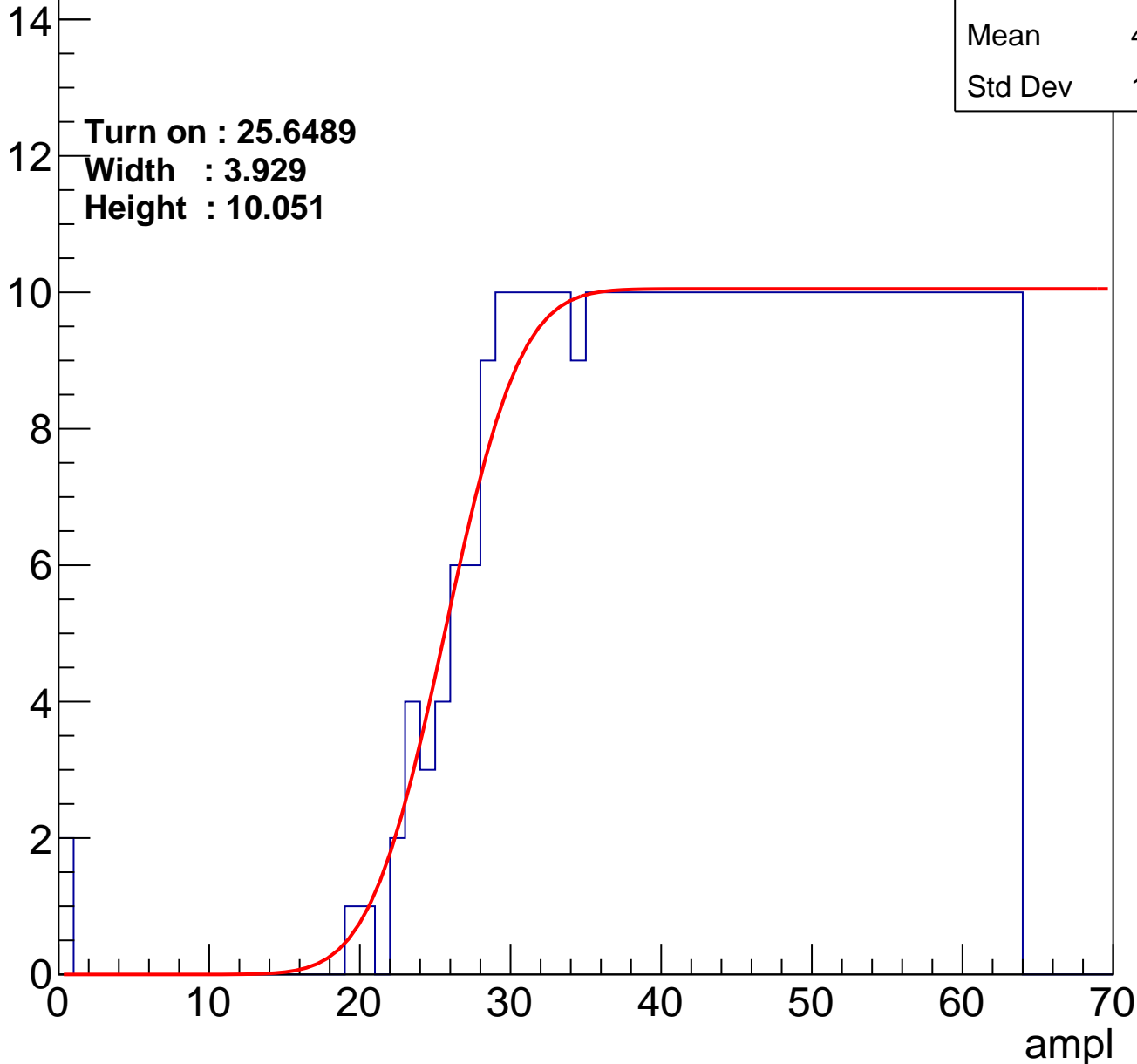
Entries	387
Mean	43.88
Std Dev	11.75

Turn on : 25.6489

Width : 3.929

Height : 10.051

Entry



B1L100S, U9-ch82

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.4181

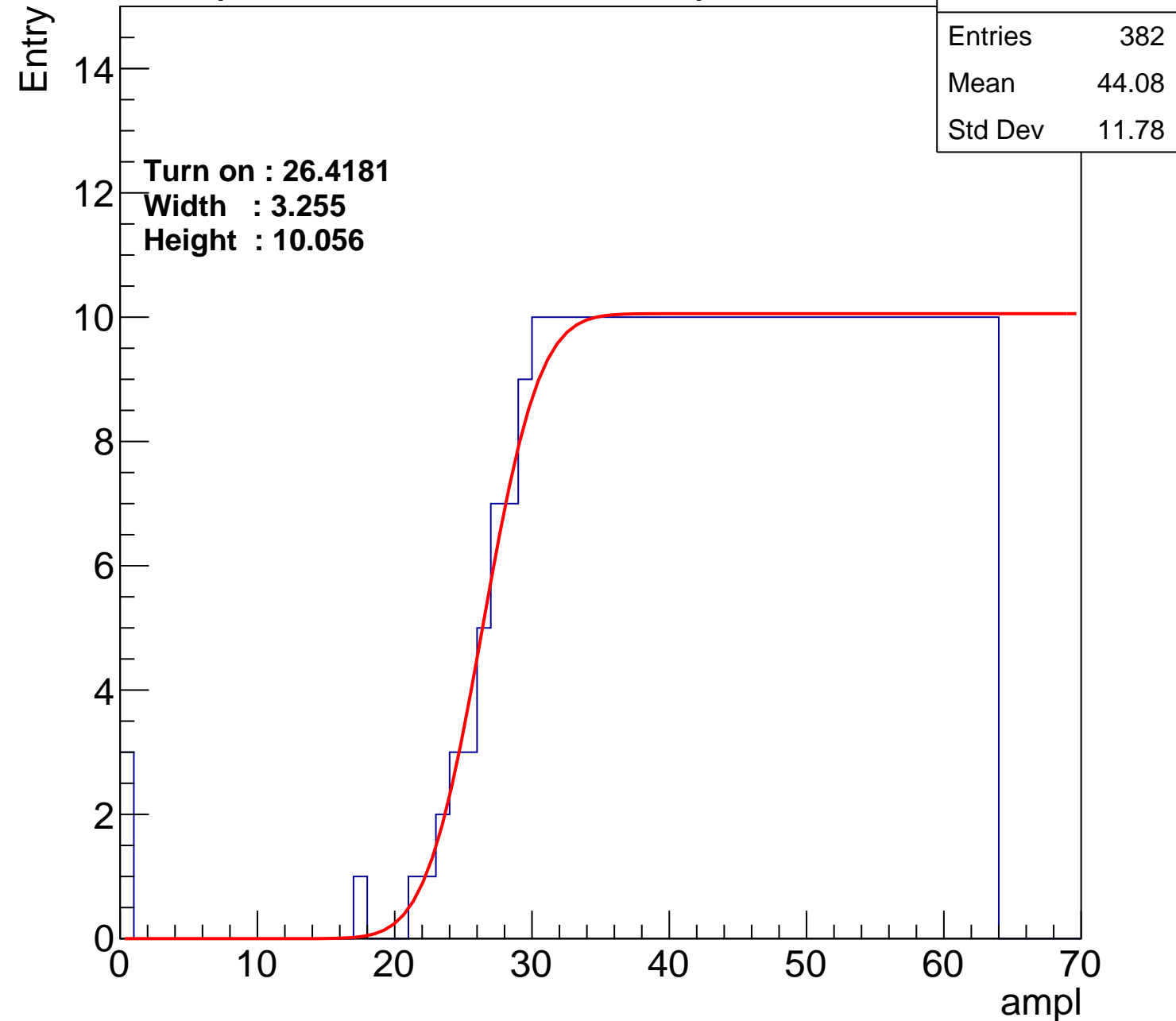
Width : 3.255

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.1955

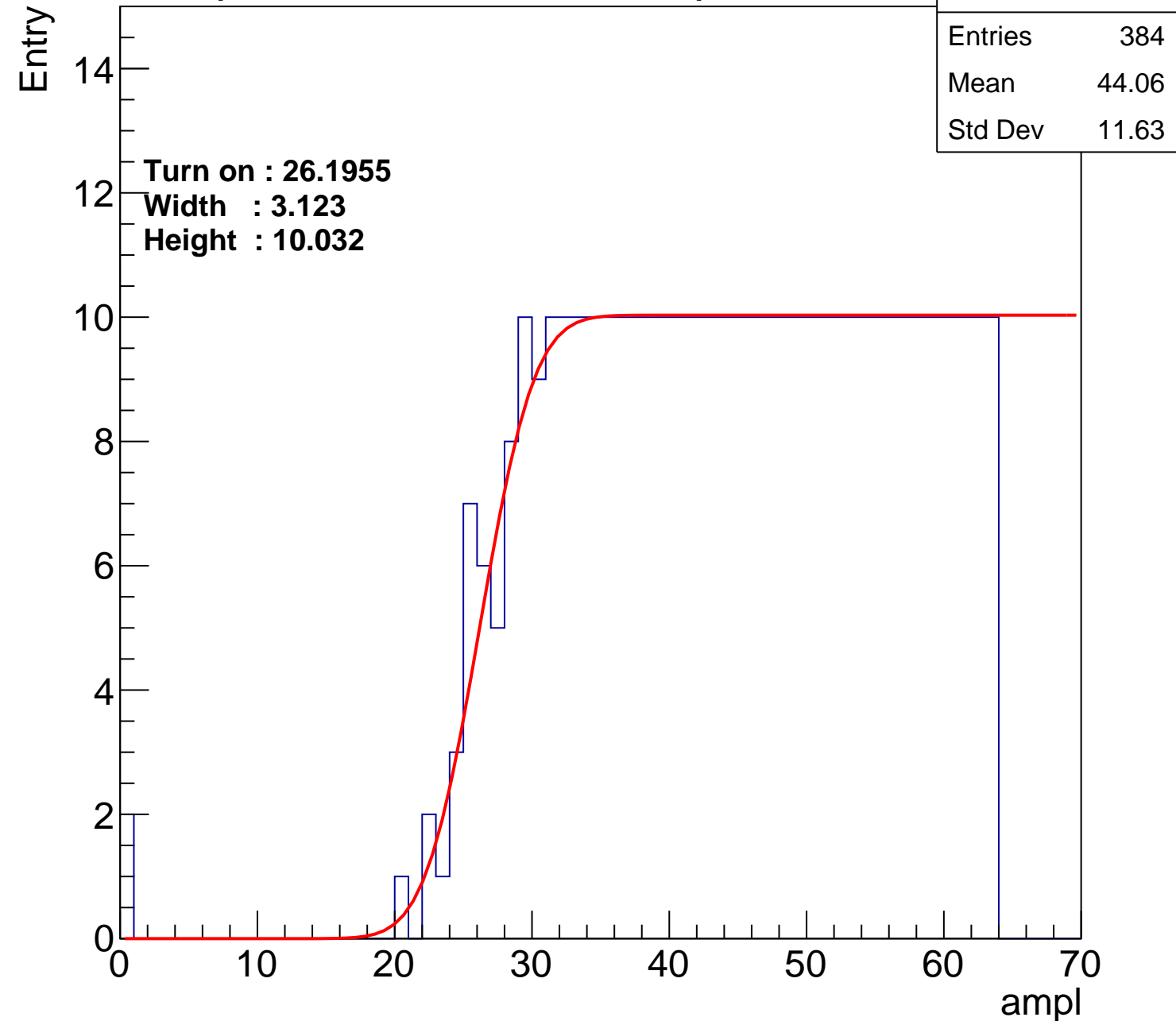
Width : 3.123

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.3117

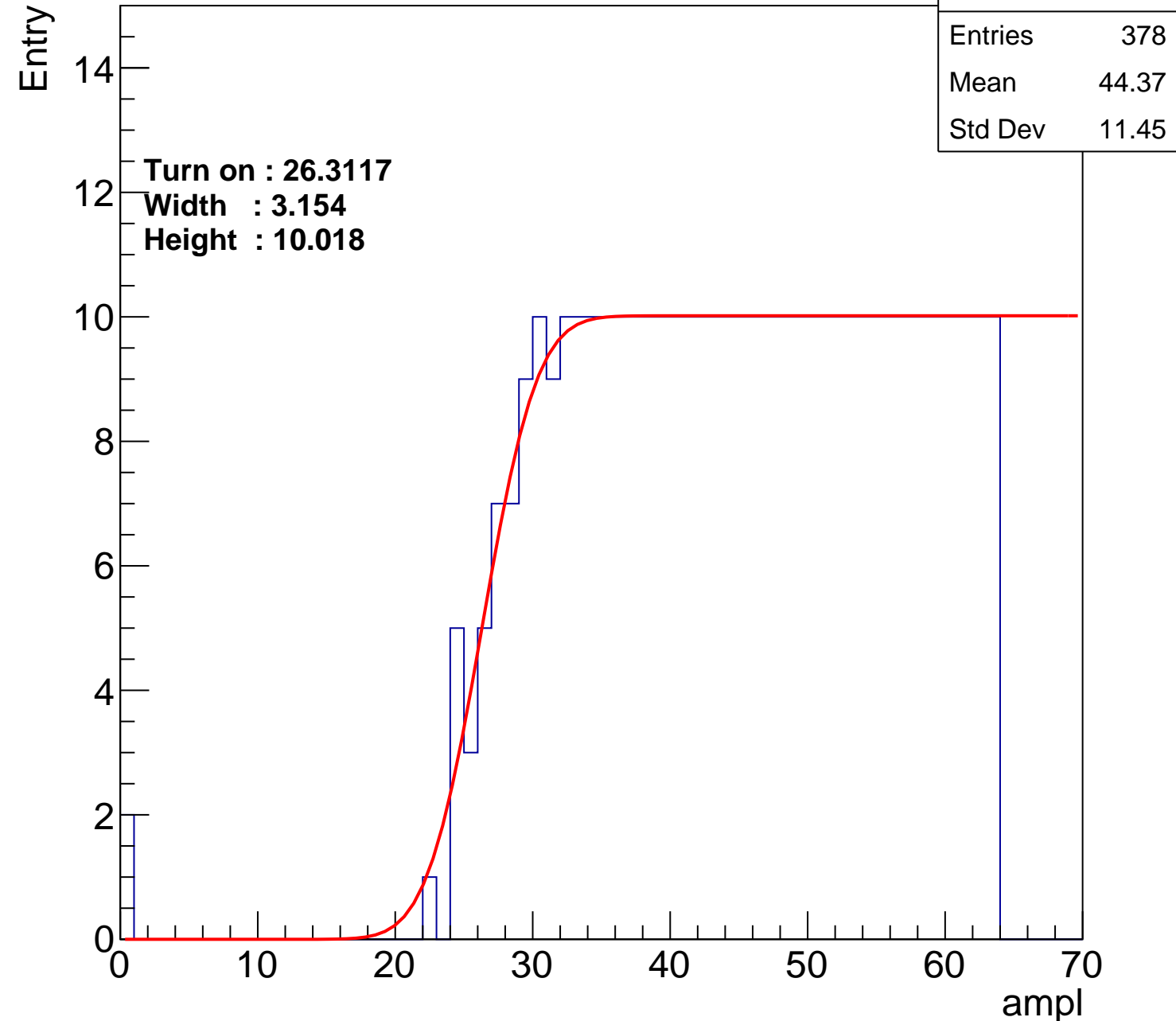
Width : 3.154

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch85

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.8559

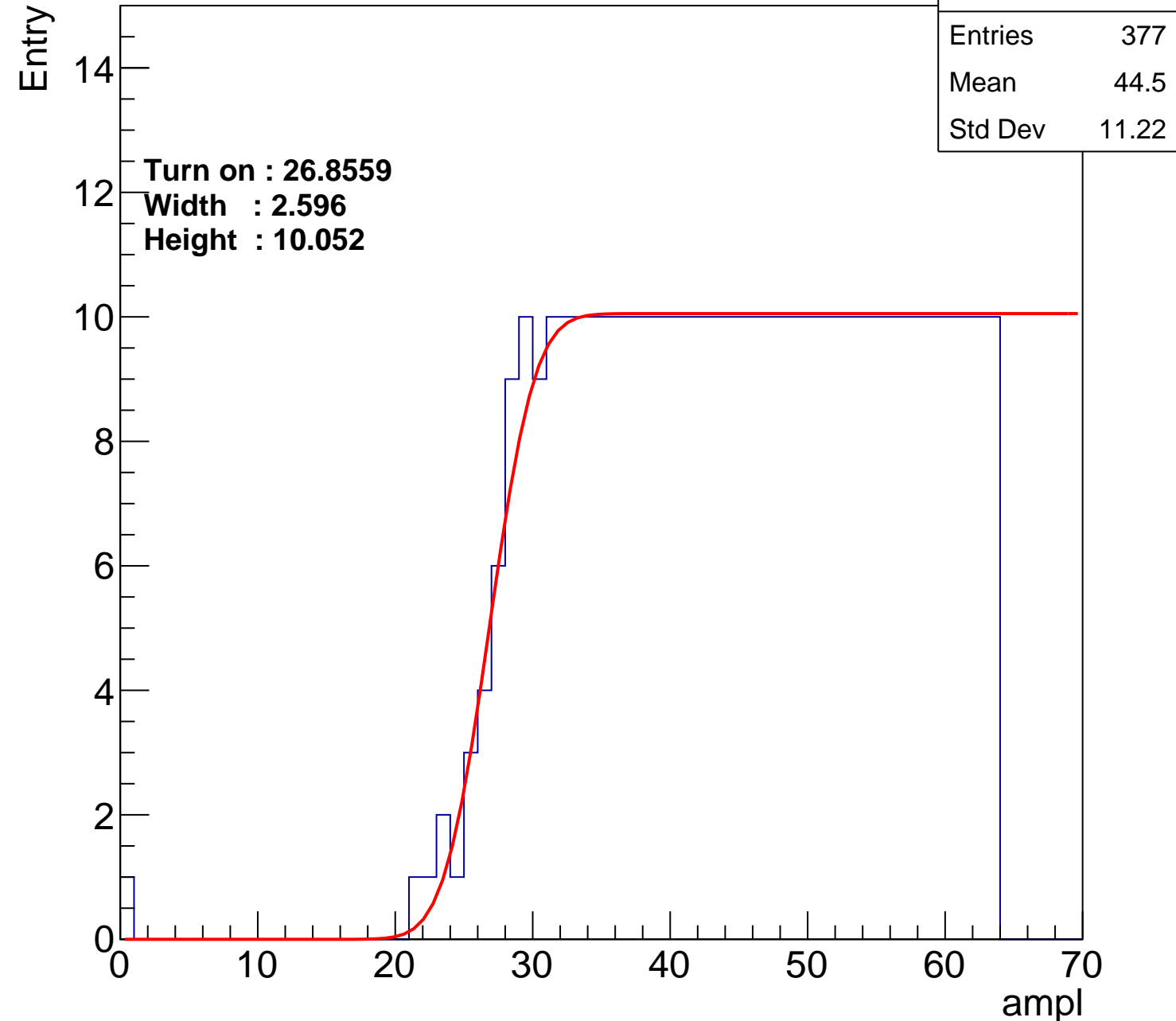
Width : 2.596

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch86

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.4
Std Dev	11.4

Turn on : 26.3003

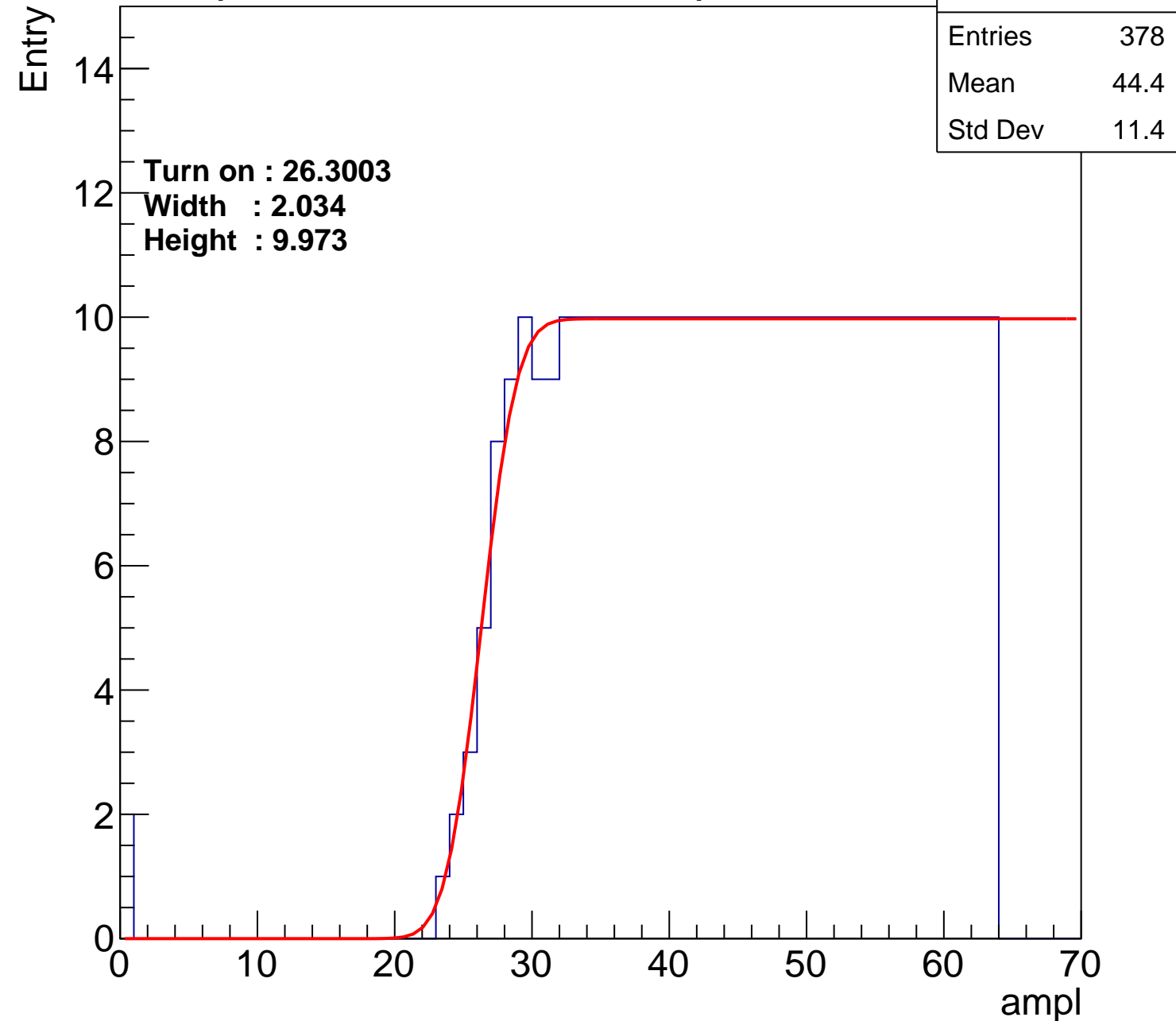
Width : 2.034

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 27.9299

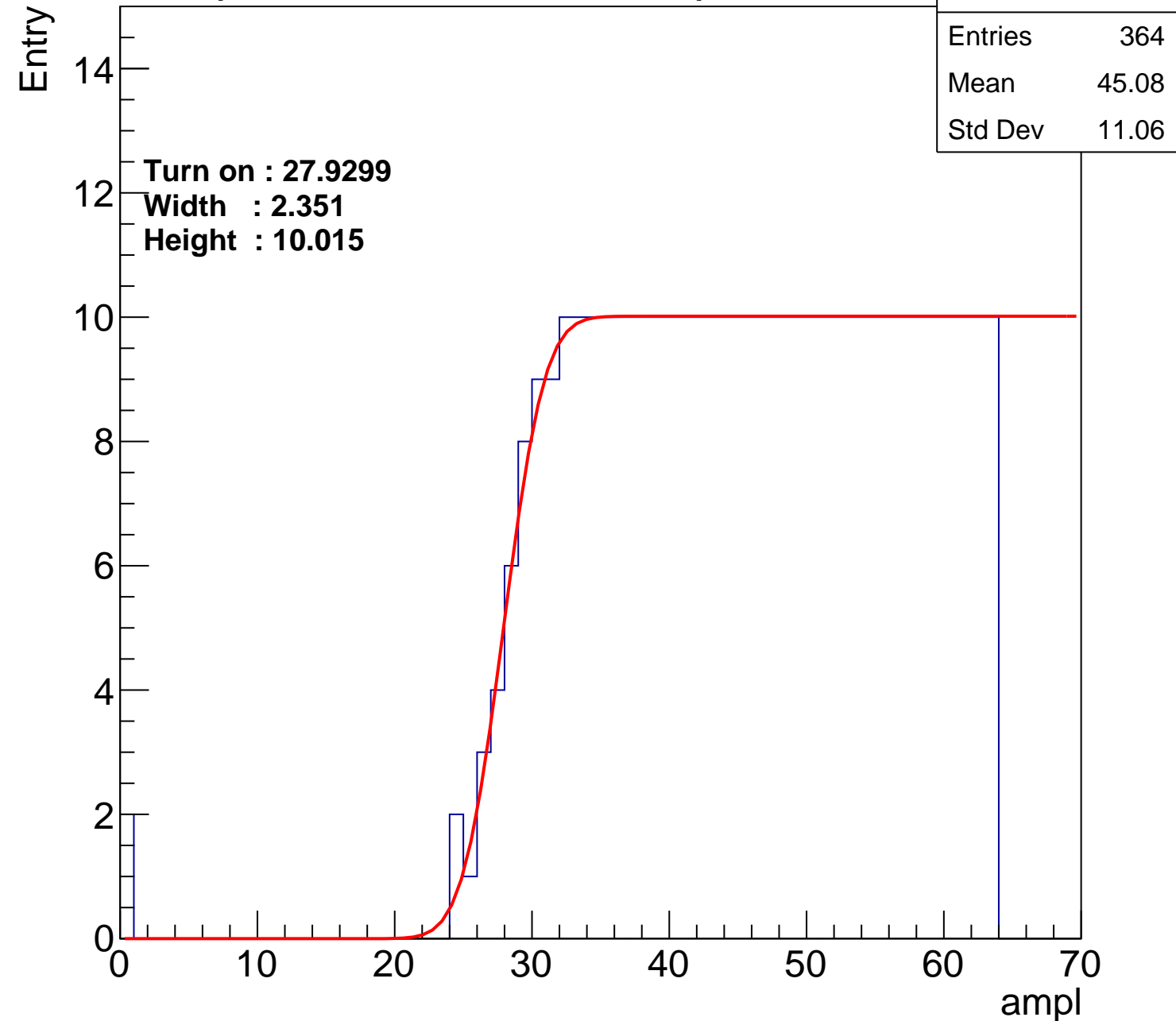
Width : 2.351

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch88

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 26.9762

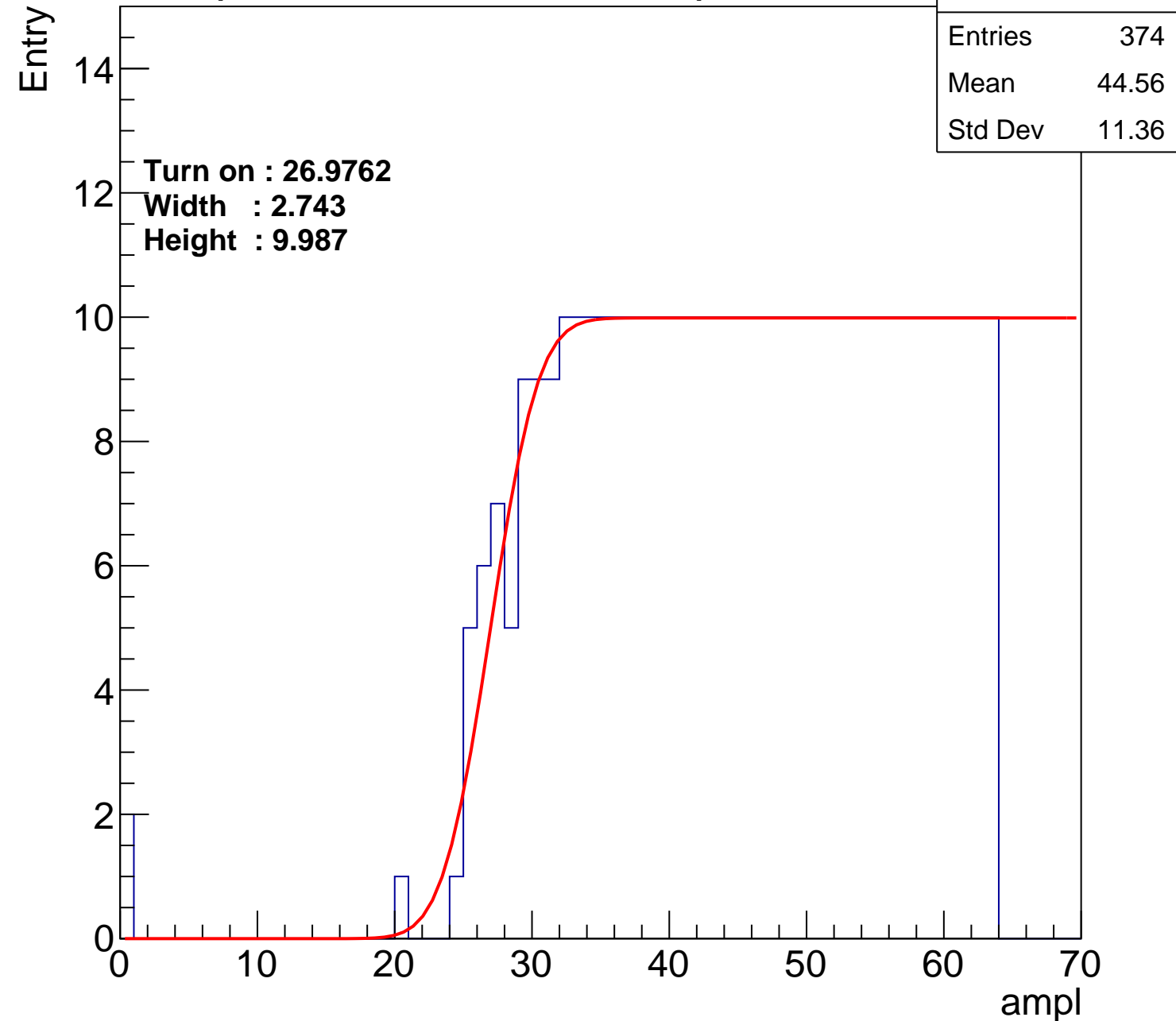
Width : 2.743

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch89

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.2649

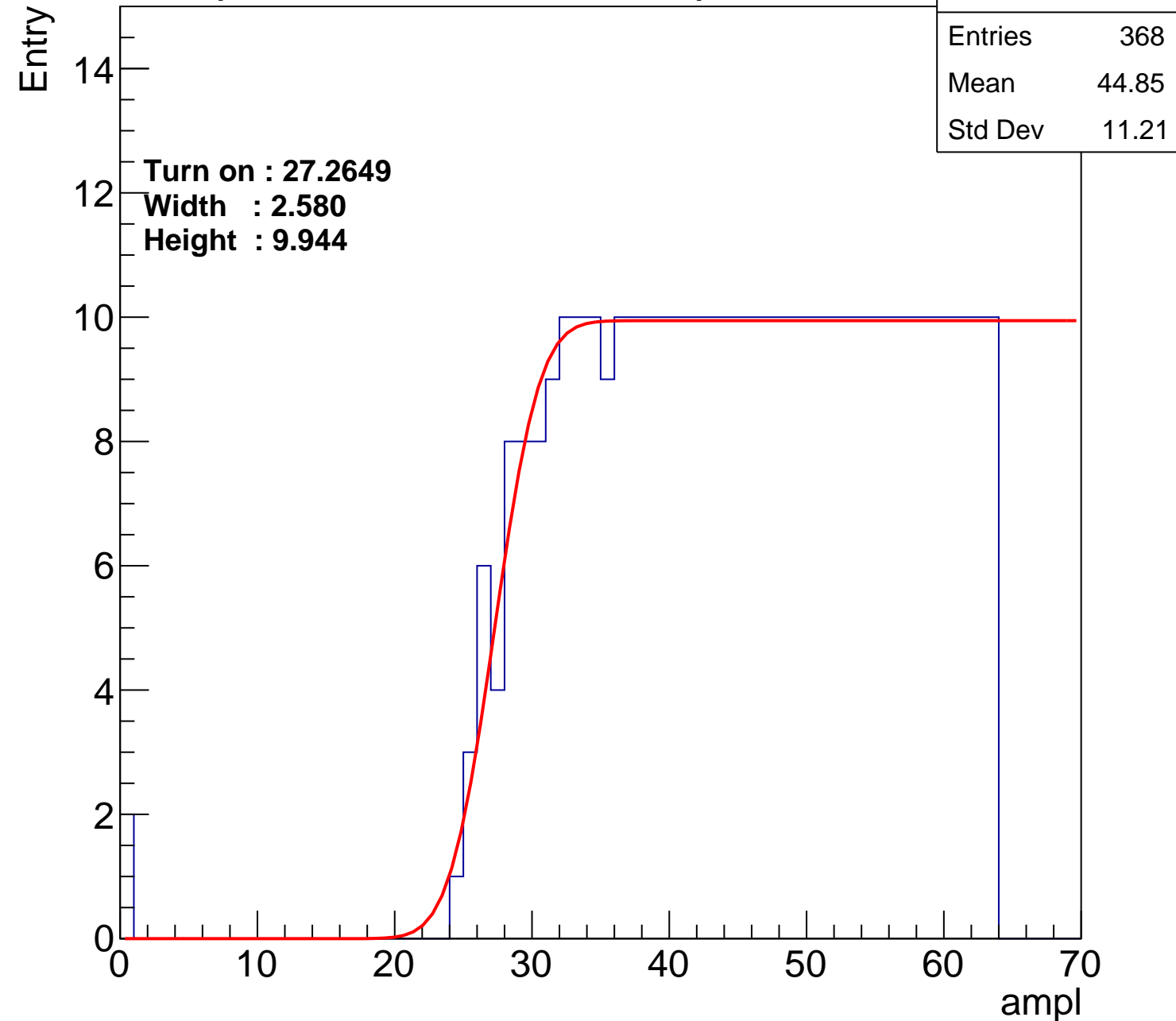
Width : 2.580

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch90

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.9825

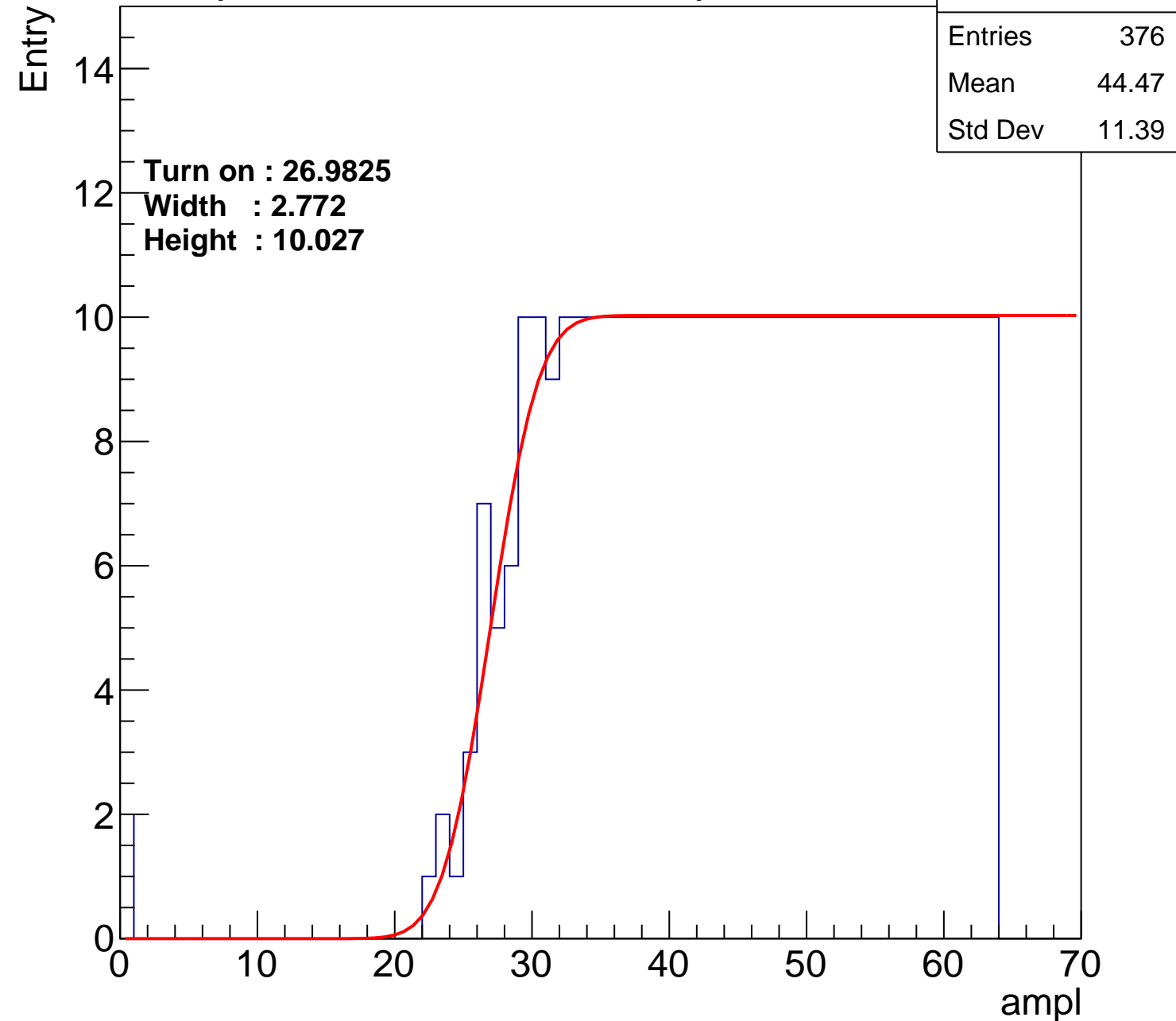
Width : 2.772

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch91

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.04
Std Dev	11.79

Turn on : 26.2389

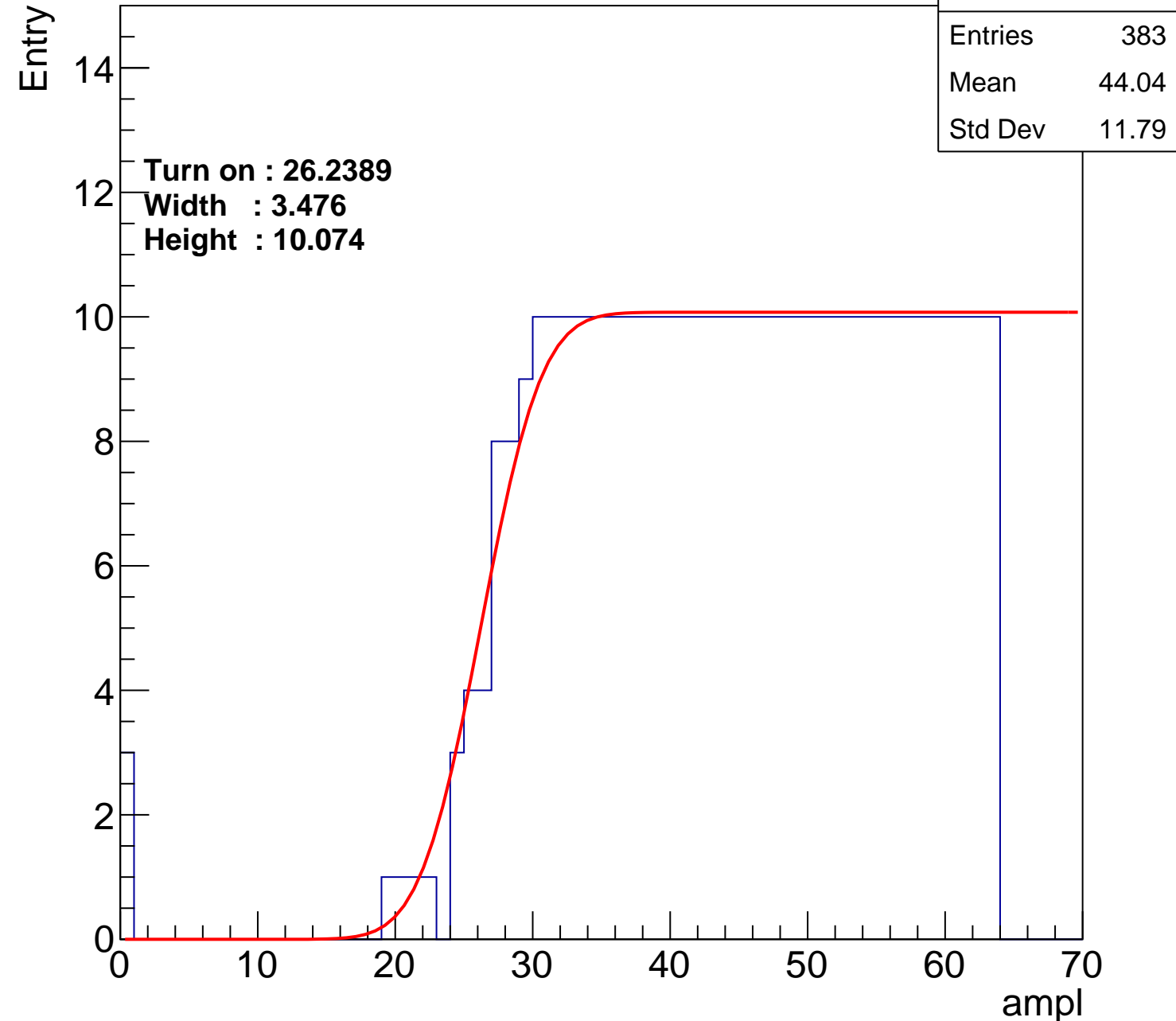
Width : 3.476

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch92

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.04
Std Dev	10.99

Turn on : 28.0140

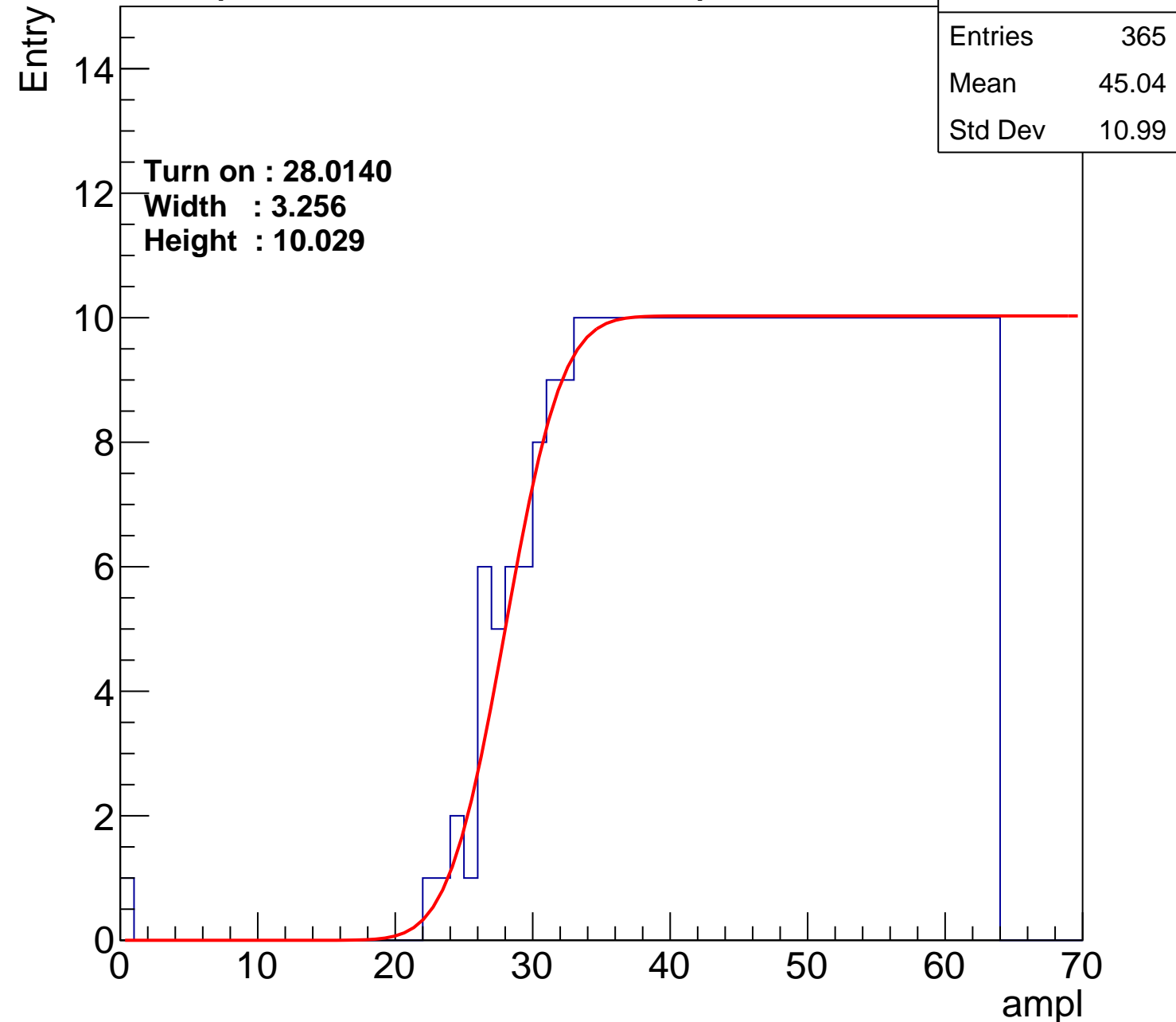
Width : 3.256

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.2872

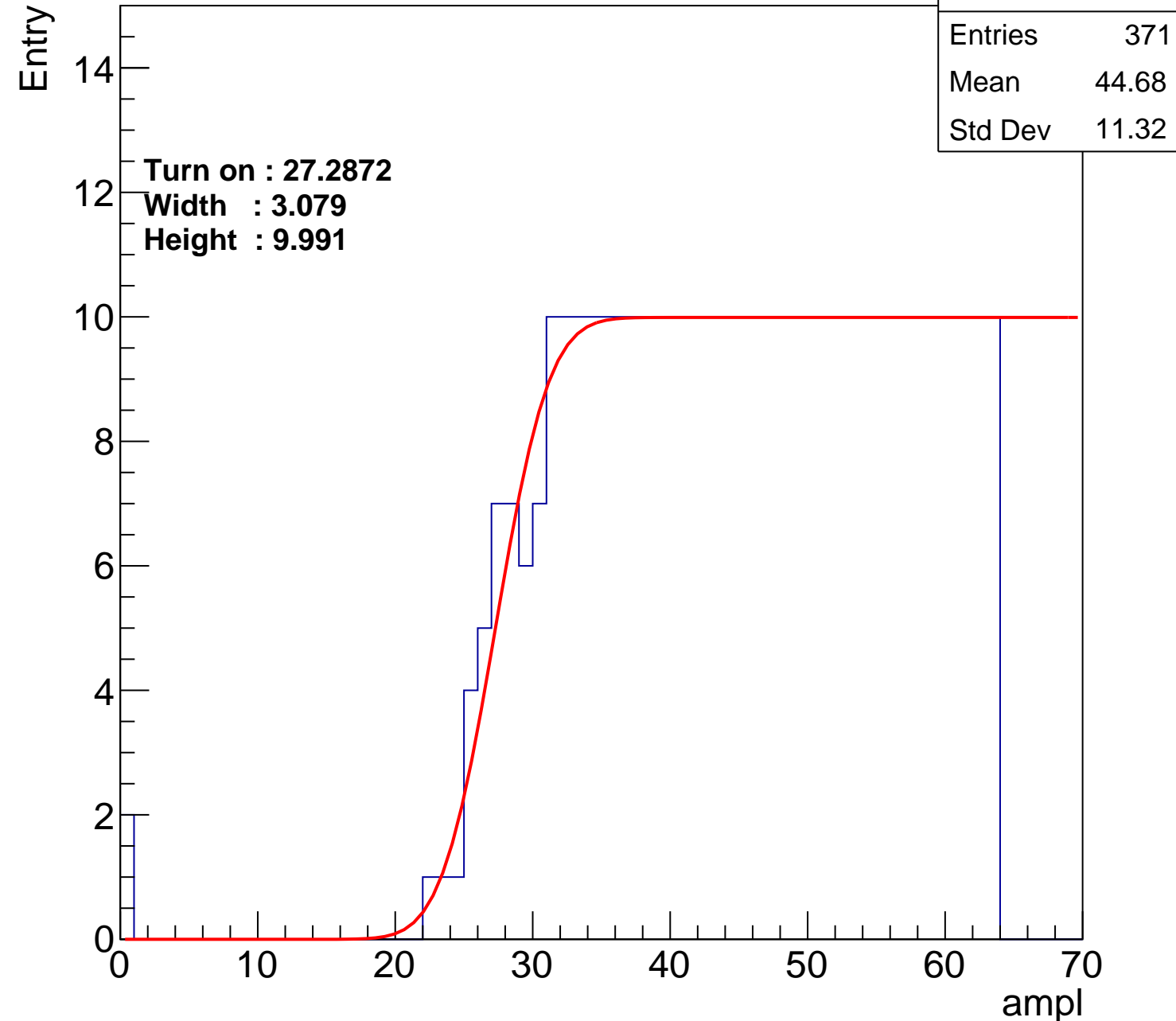
Width : 3.079

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.2422

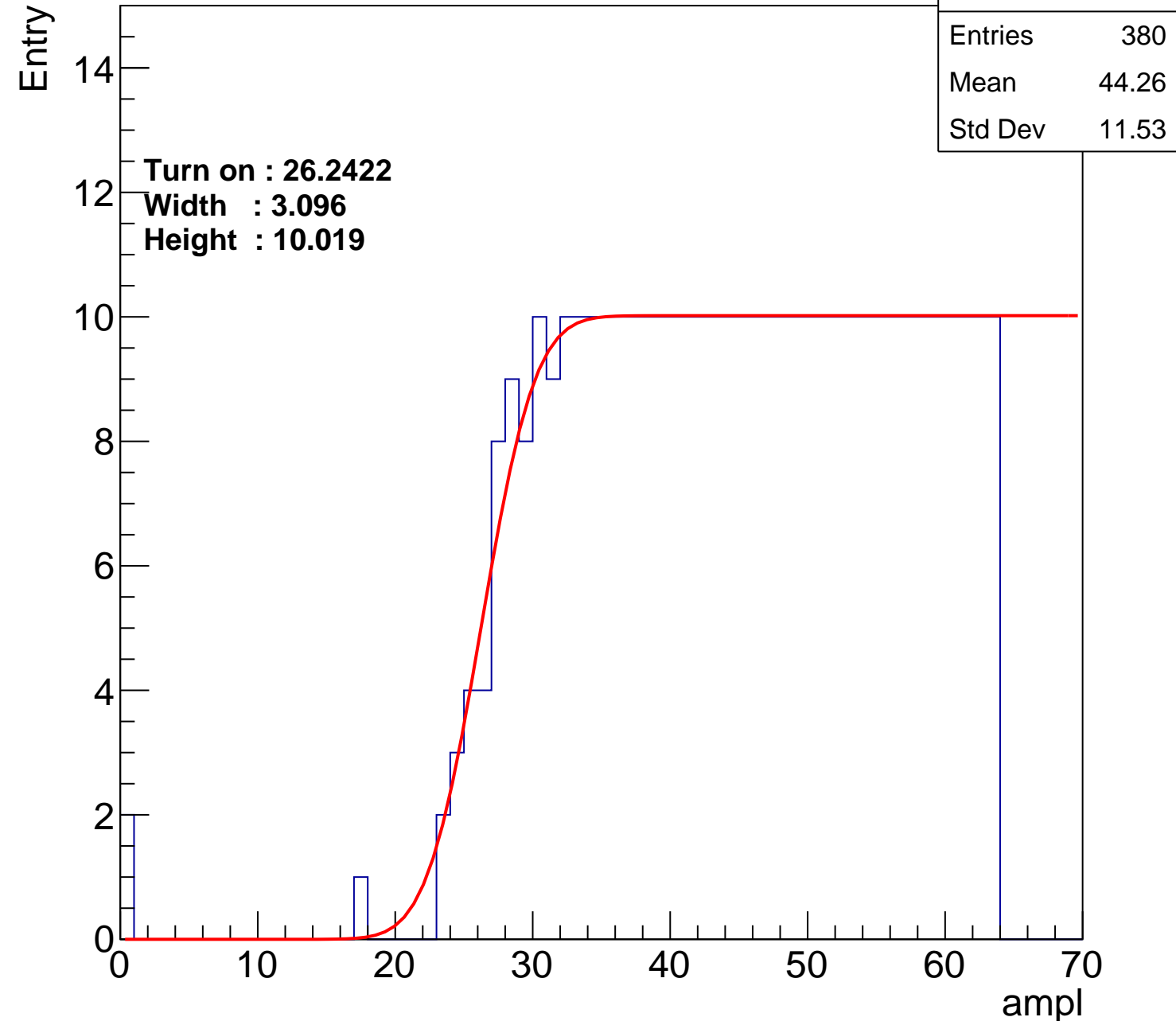
Width : 3.096

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch95

calib_packv5_042523_0143.root, FC#4, port A2

Entries	397
Mean	43.23
Std Dev	12.44

Turn on : 25.0511

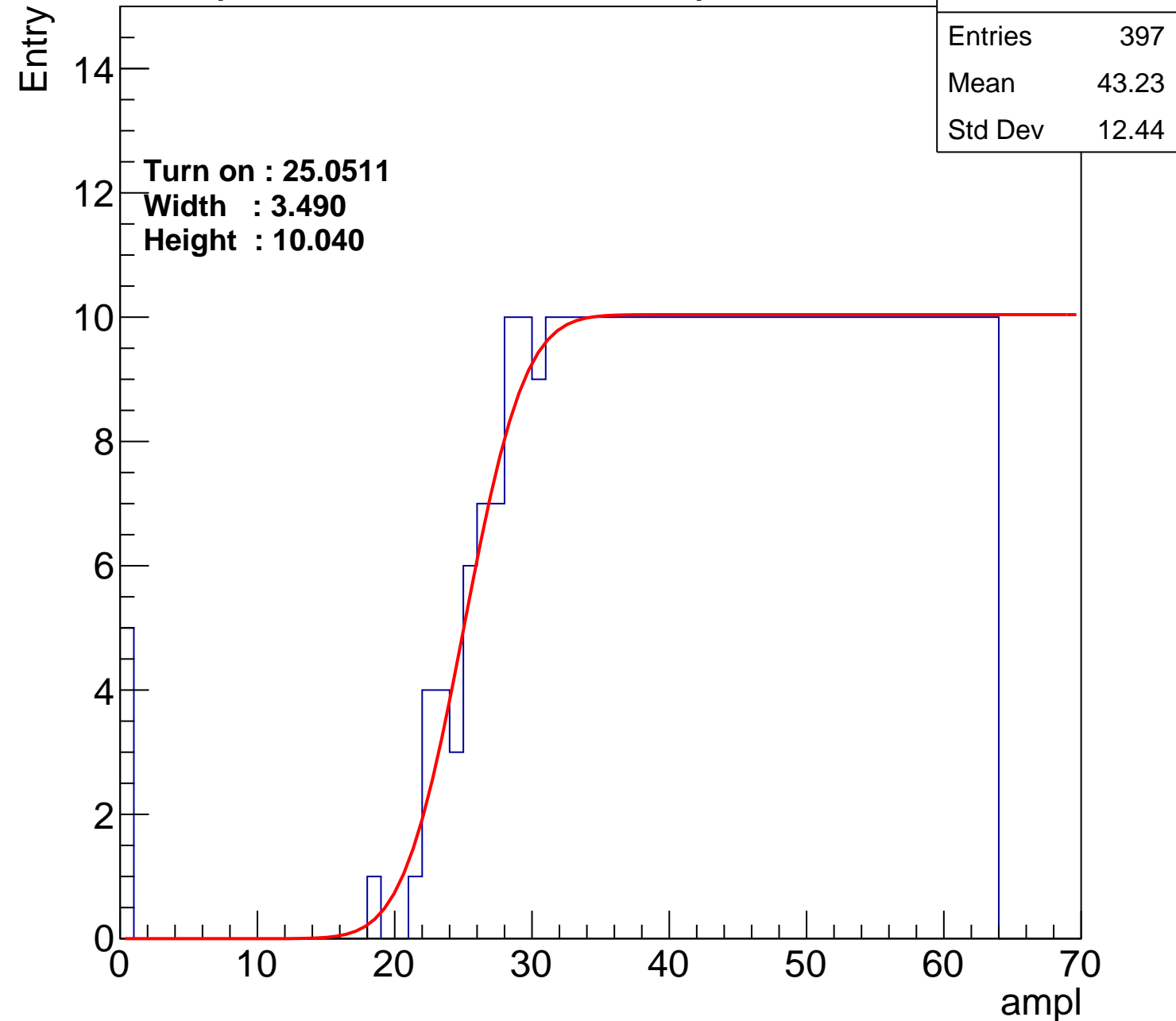
Width : 3.490

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch96

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.52
Std Dev	11.25

Turn on : 26.4358

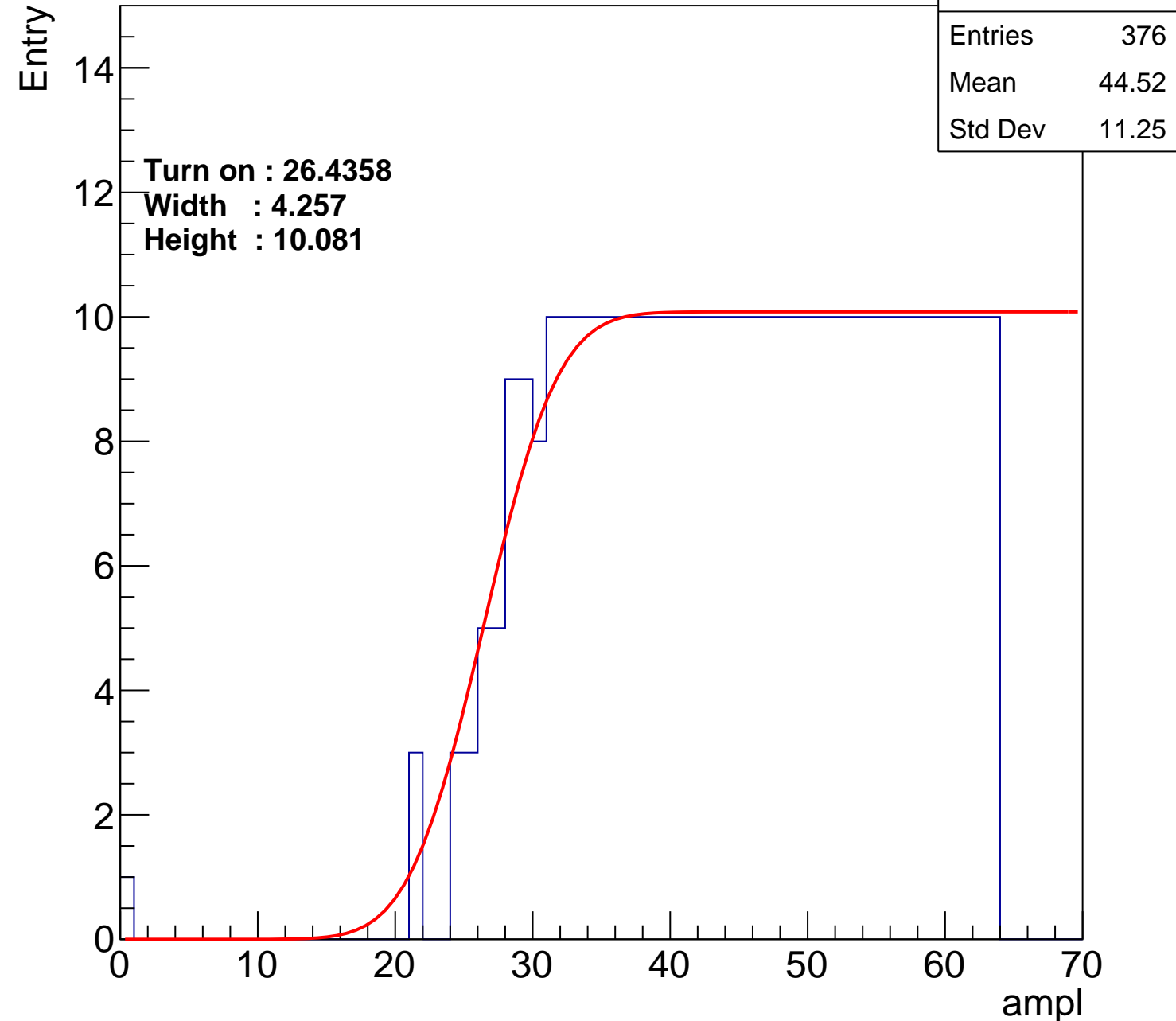
Width : 4.257

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.65
Std Dev	11.8

Turn on : 25.4362

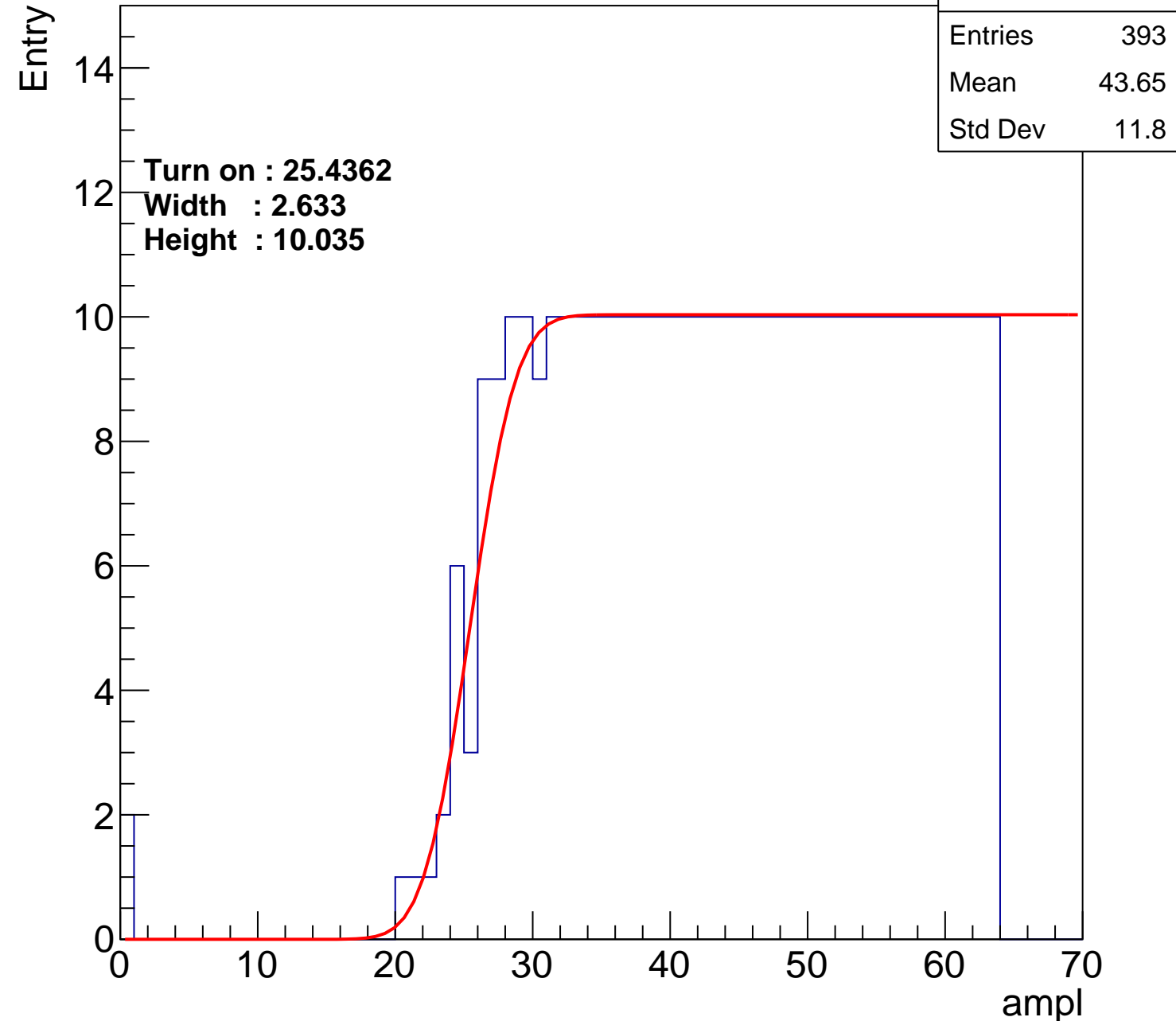
Width : 2.633

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch98

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 26.0712

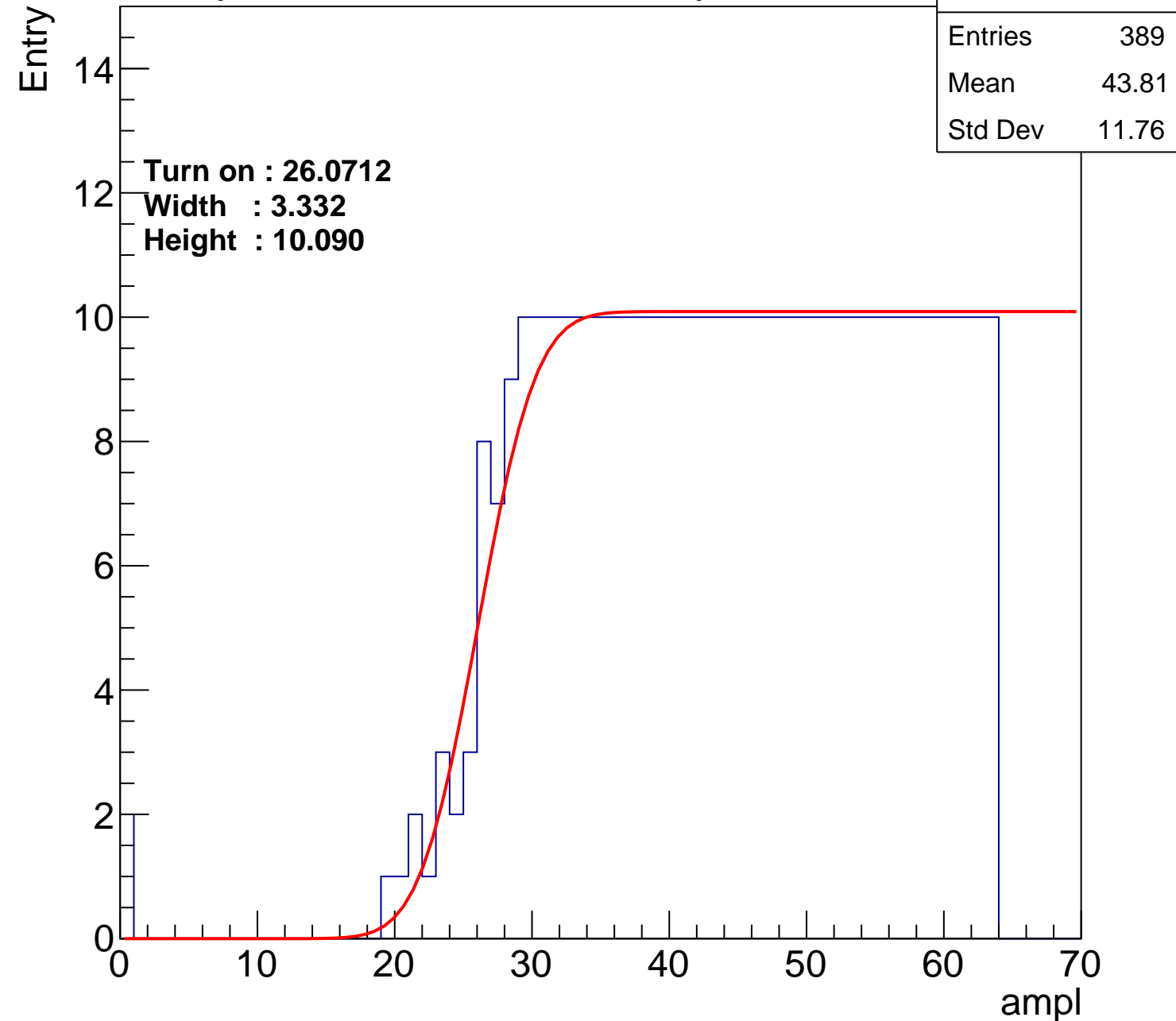
Width : 3.332

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch99

calib_packv5_042523_0143.root, FC#4, port A2

Entries	347
Mean	45.7
Std Dev	11.18

Turn on : 29.7973

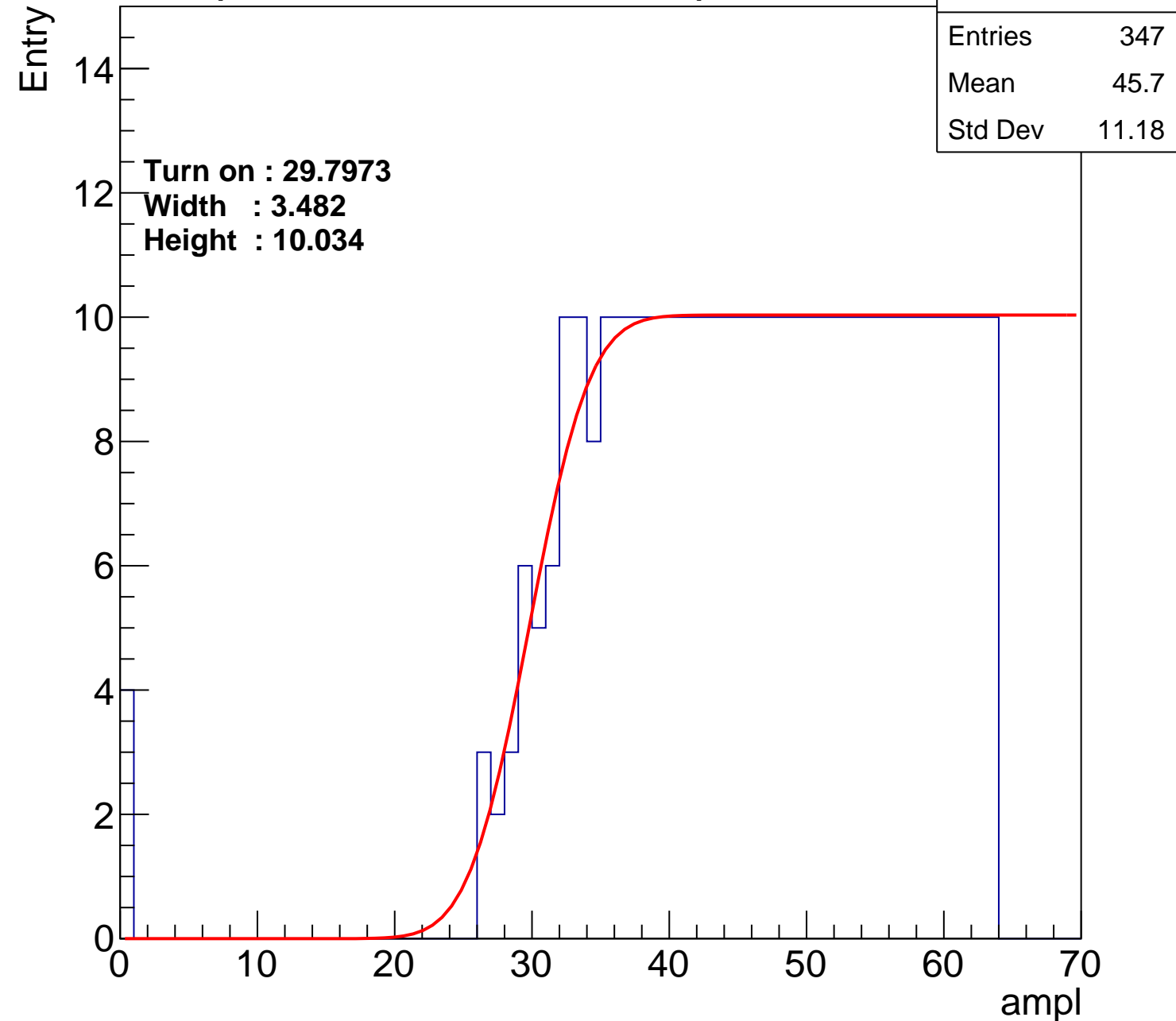
Width : 3.482

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch100

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.25
Std Dev	10.85

Turn on : 28.4397

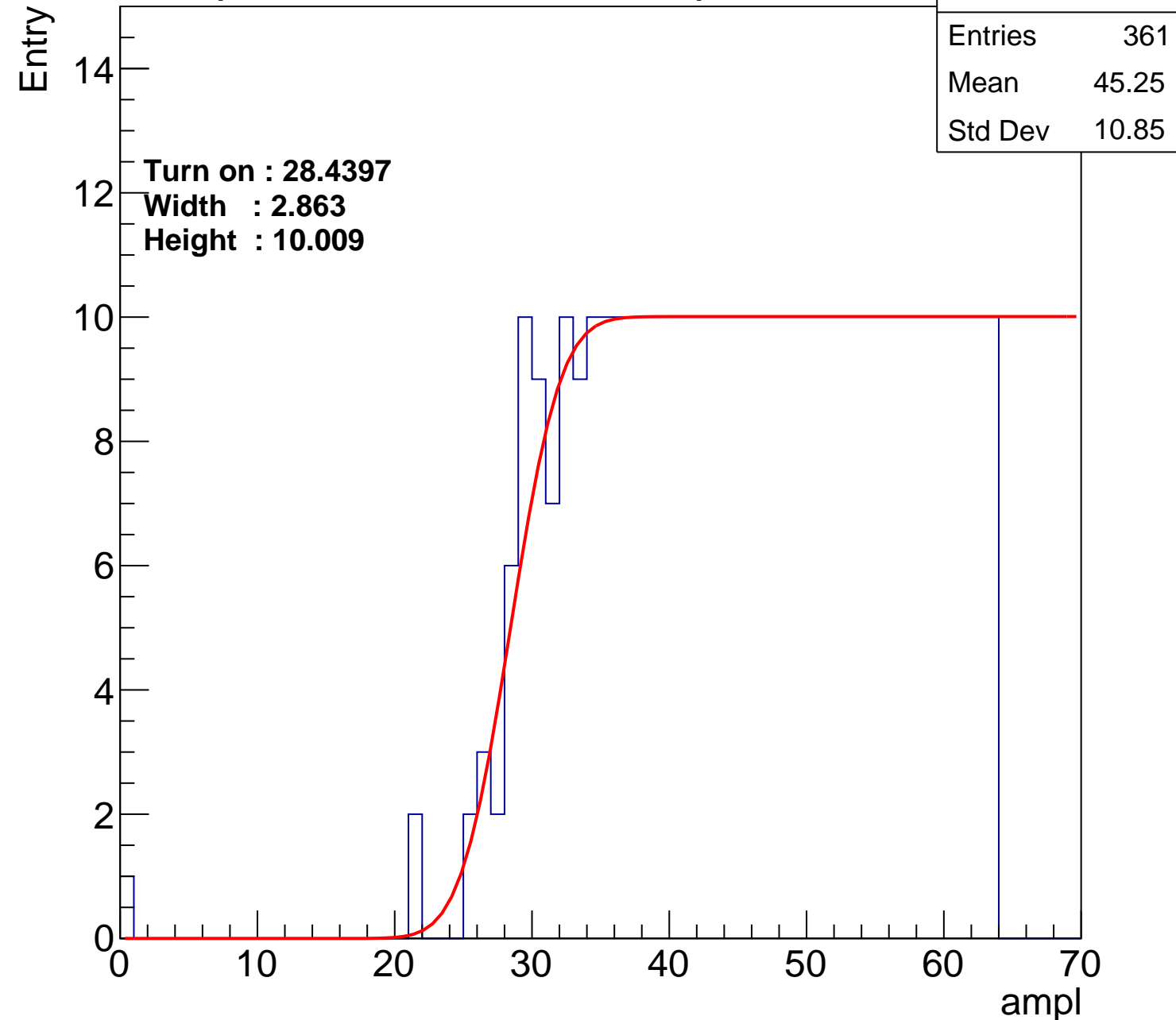
Width : 2.863

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch101

calib_packv5_042523_0143.root, FC#4, port A2

Entries	353
Mean	45.18
Std Dev	11.94

Turn on : 29.2792

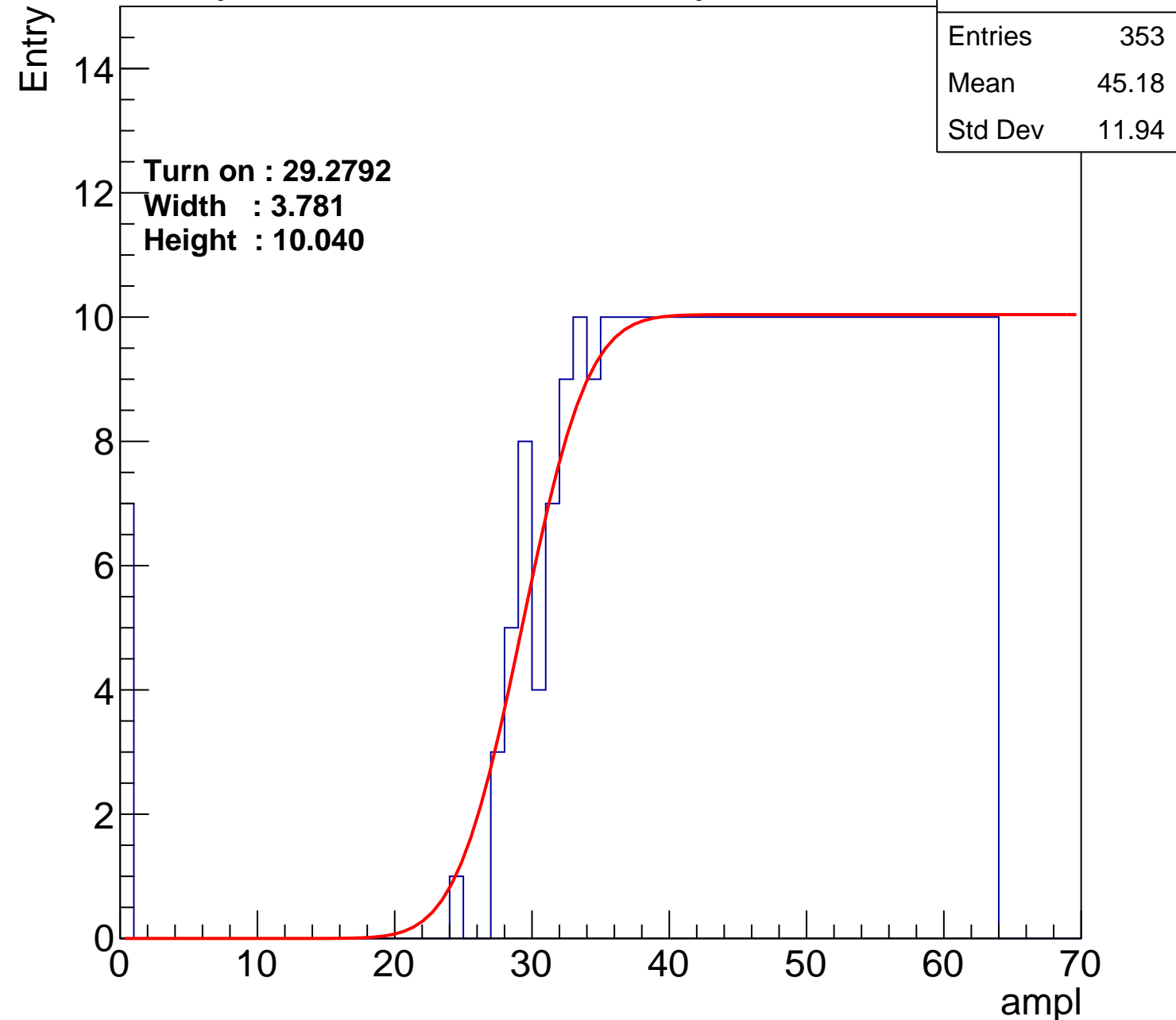
Width : 3.781

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	401
Mean	43.28
Std Dev	11.92

Turn on : 24.0744

Width : 2.096

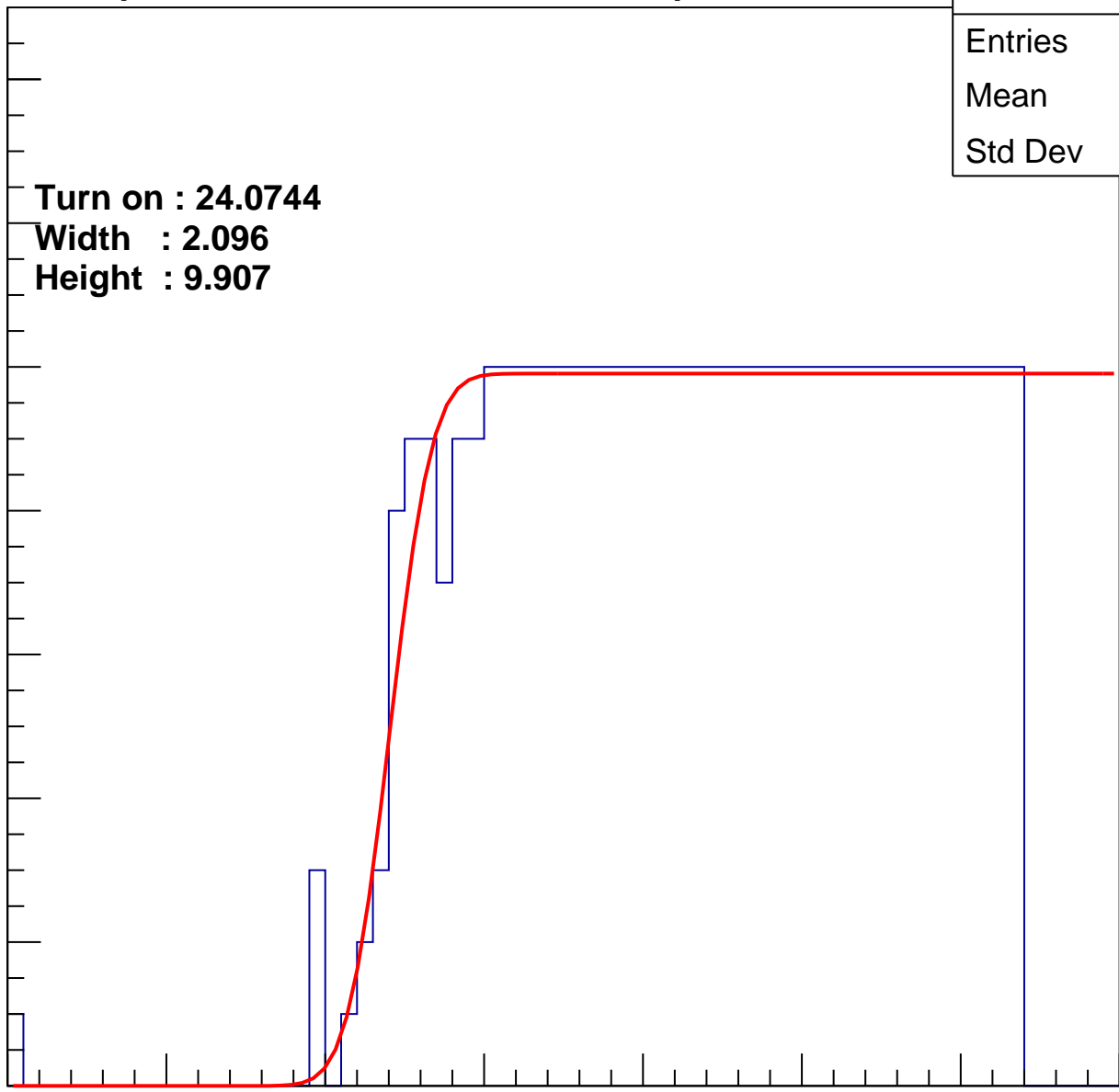
Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U9-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	44.71
Std Dev	11.96

Turn on : 28.3422

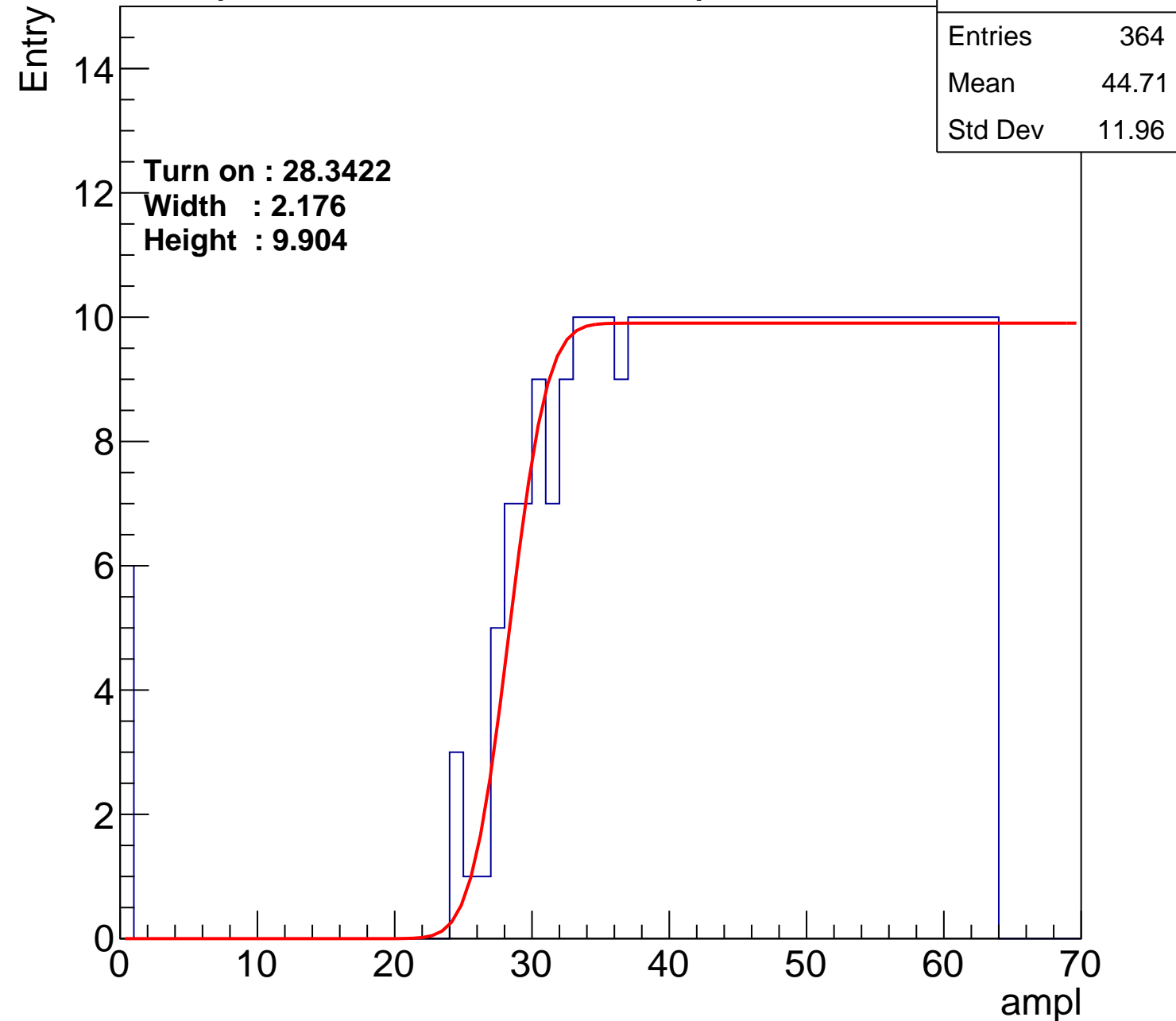
Width : 2.176

Height : 9.904

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch104

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.9641

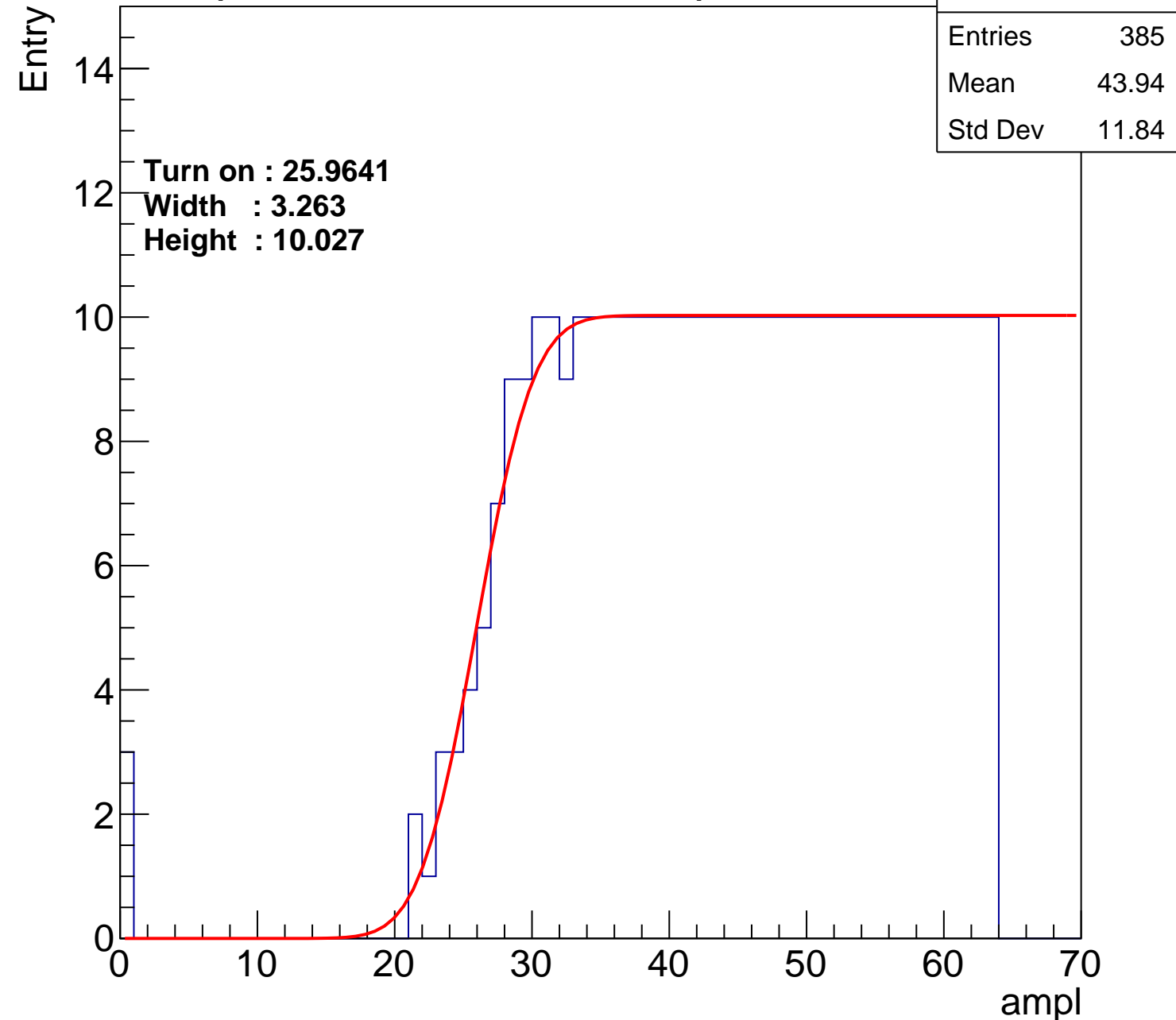
Width : 3.263

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	349
Mean	45.77
Std Dev	10.76

Turn on : 28.9056

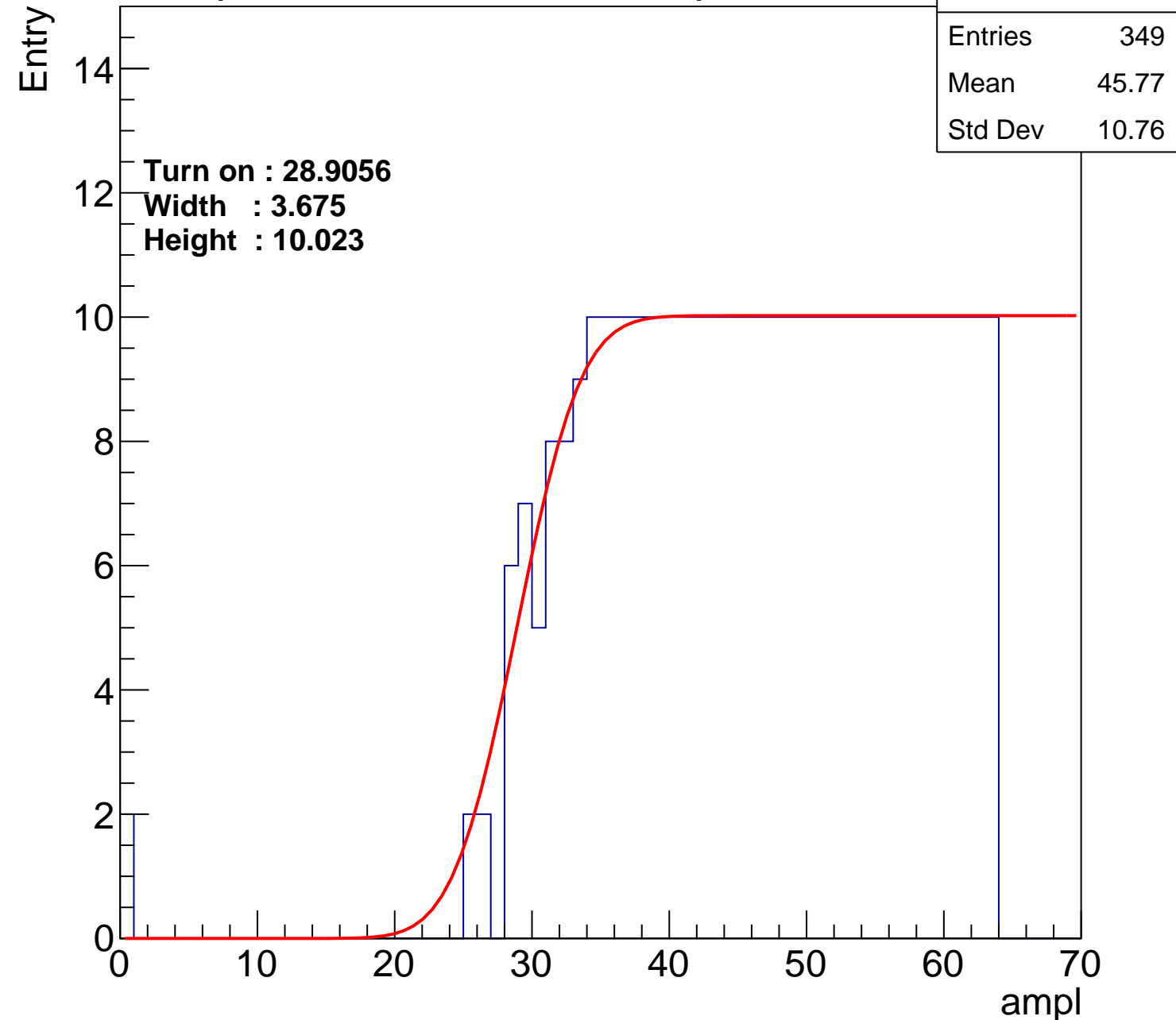
Width : 3.675

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch106

calib_packv5_042523_0143.root, FC#4, port A2

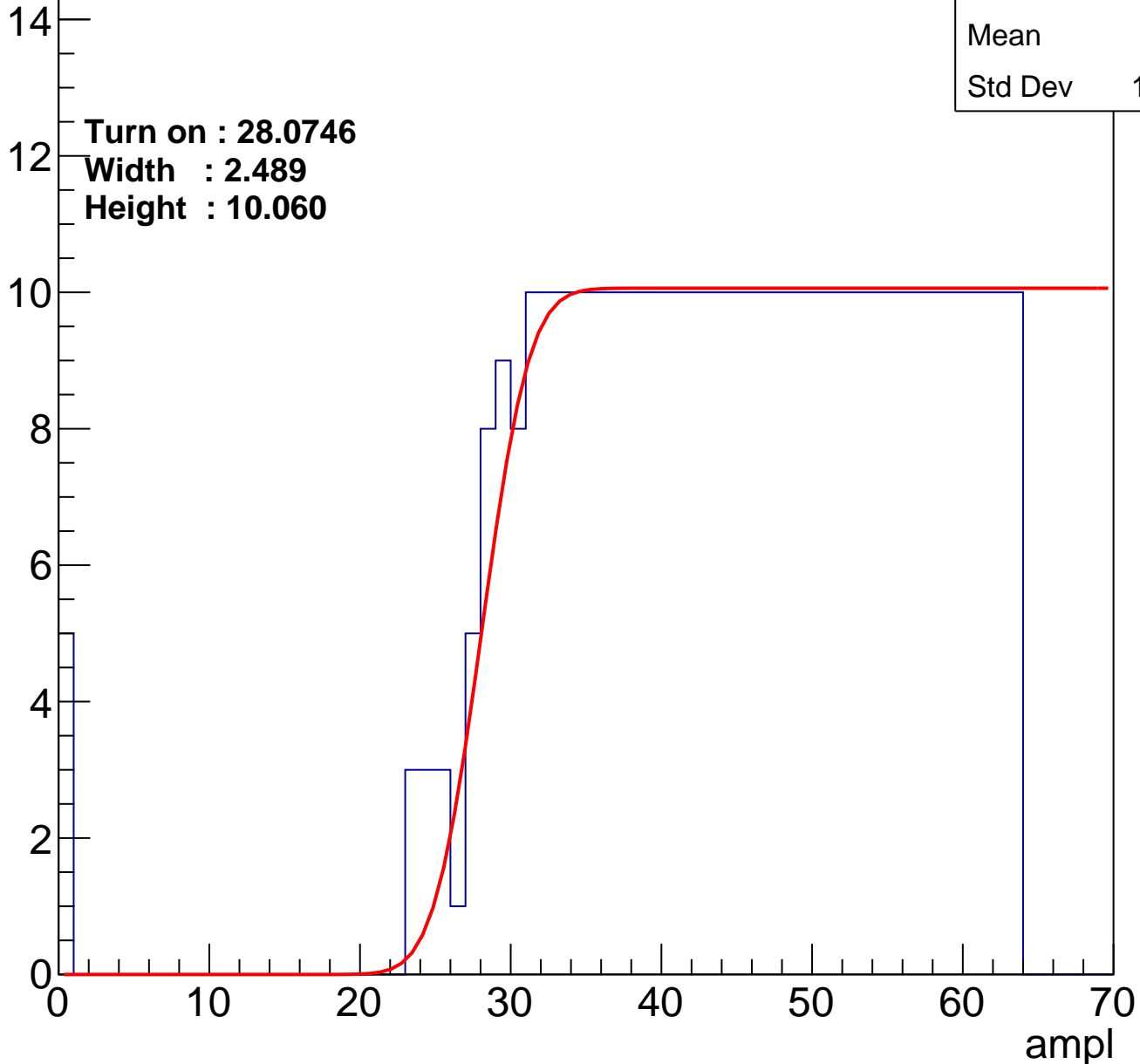
Entries	375
Mean	44.3
Std Dev	11.95

Turn on : 28.0746

Width : 2.489

Height : 10.060

Entry



B1L100S, U9-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 28.3678

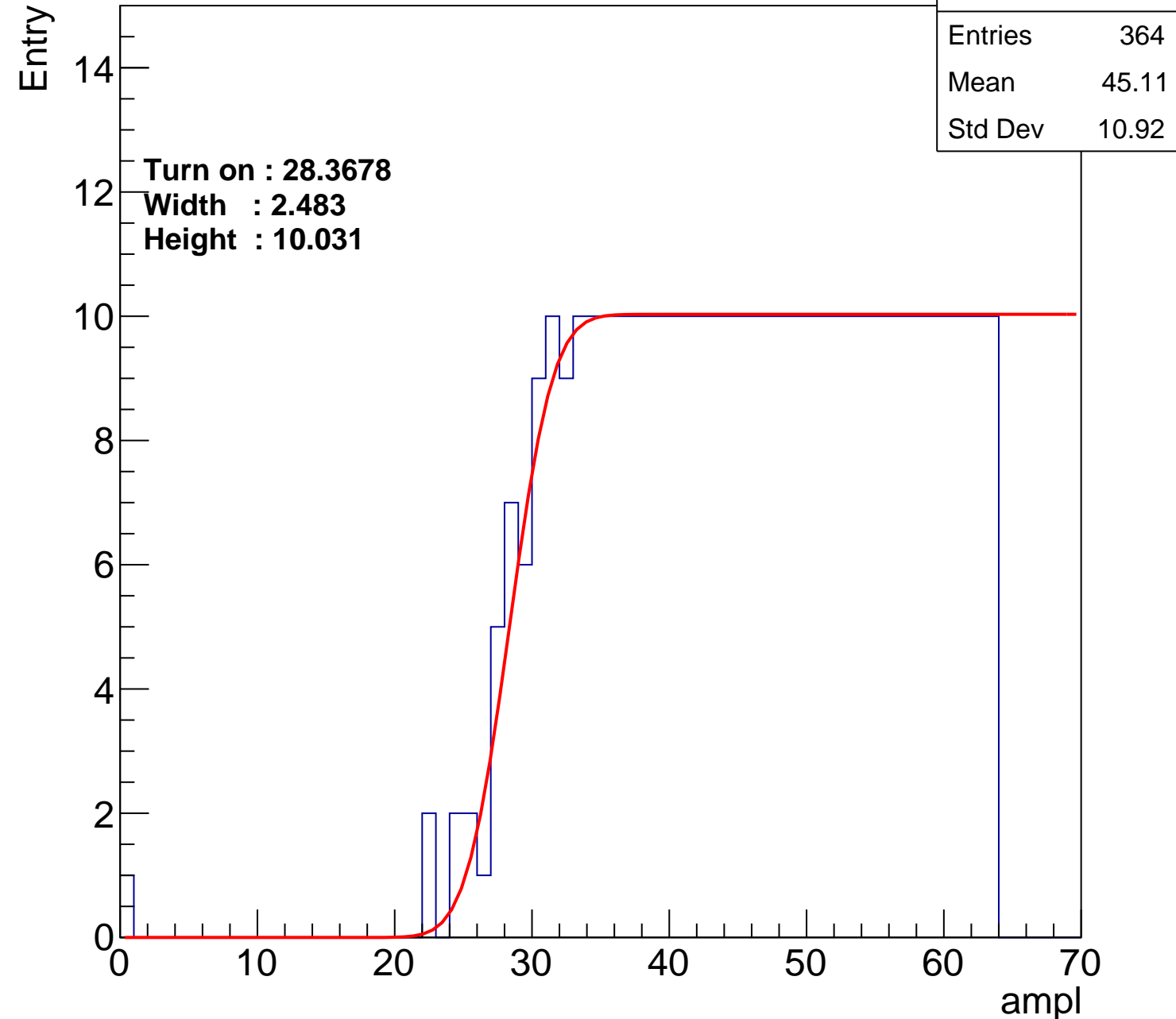
Width : 2.483

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch108

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.38
Std Dev	11.93

Turn on : 27.1999

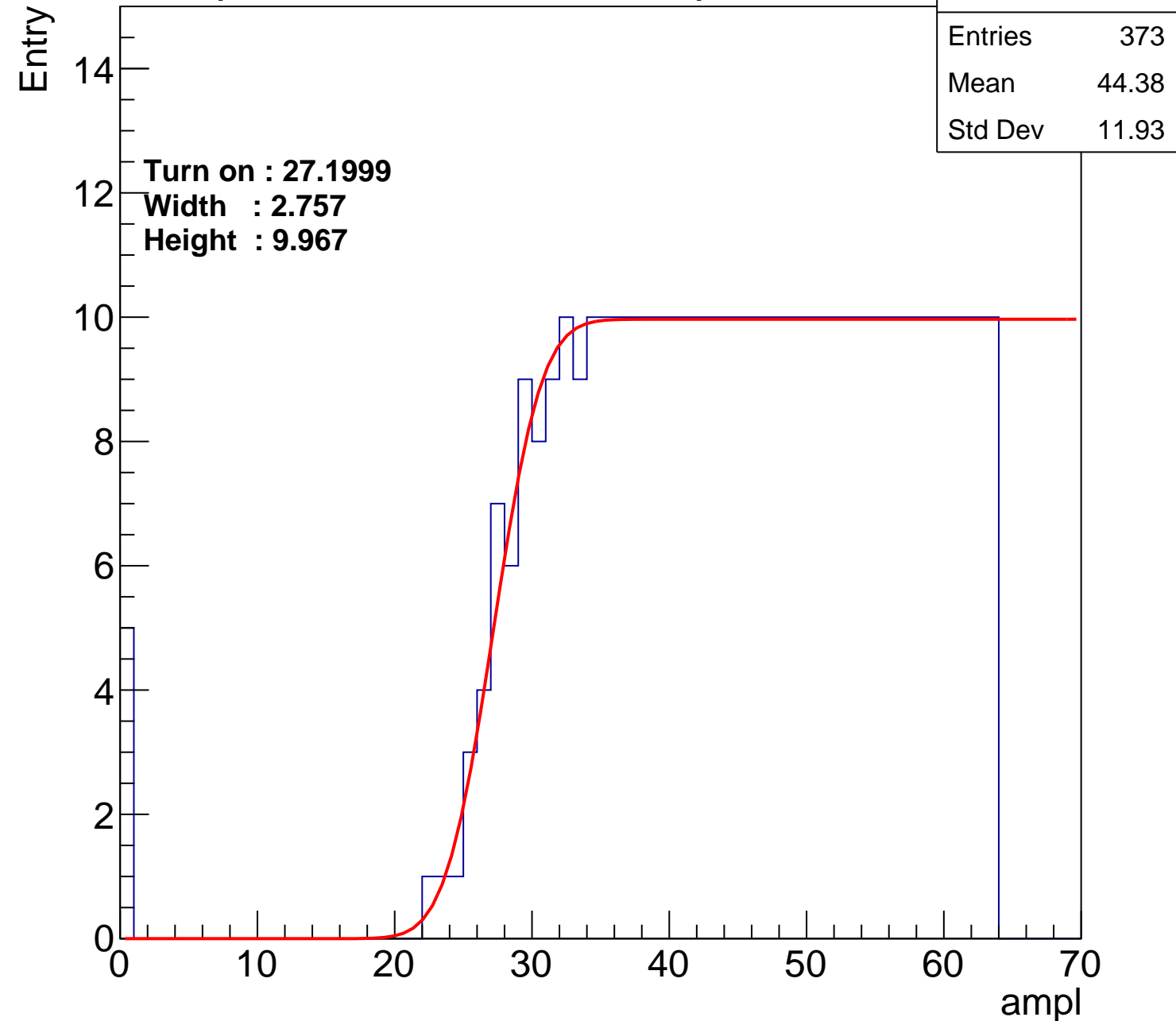
Width : 2.757

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl

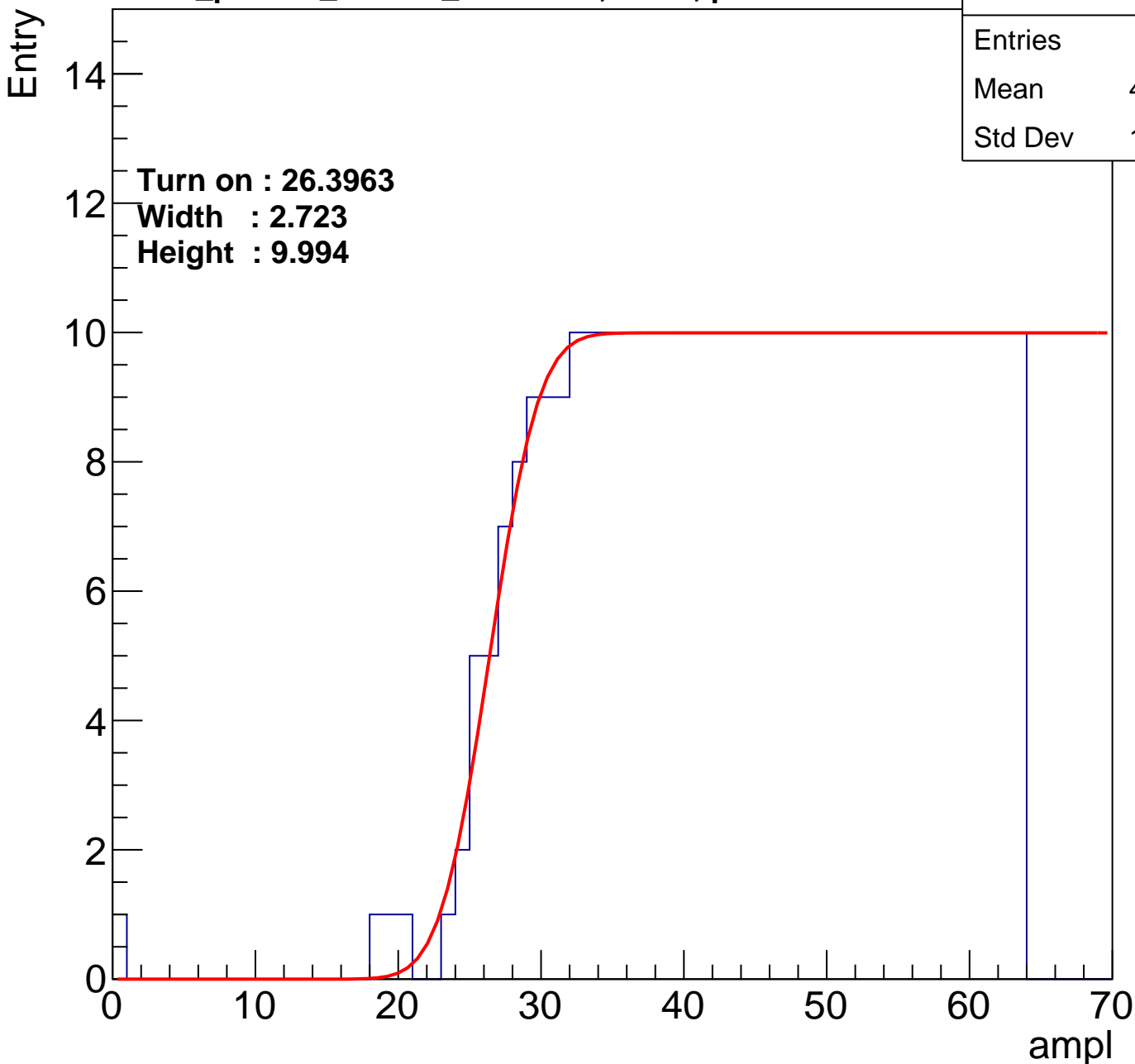


calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.34
Std Dev	11.37

Height : 9.994



B1L100S, U9-ch110

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.4
Std Dev	11.28

Turn on : 26.2744

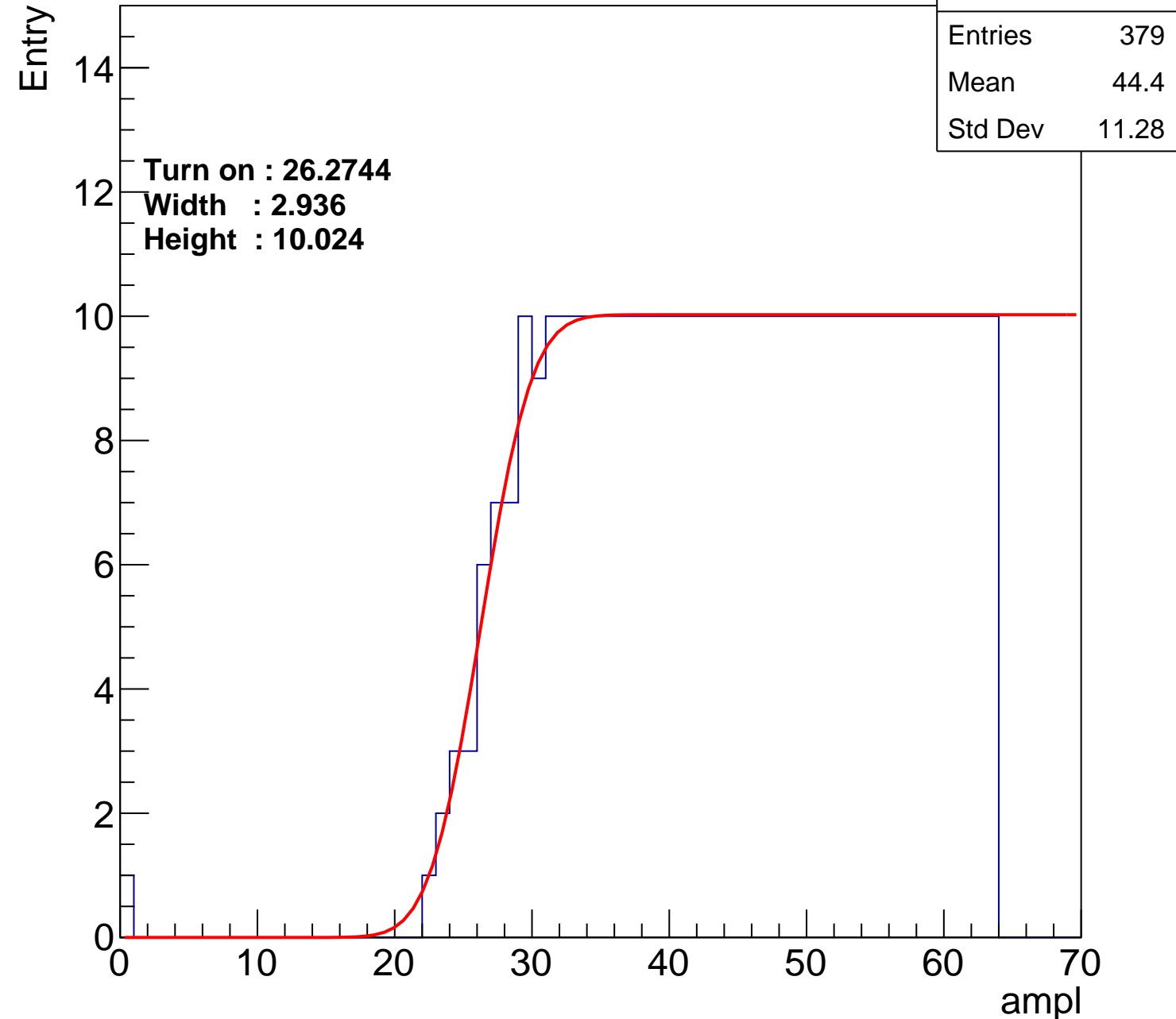
Width : 2.936

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	357
Mean	45.32
Std Dev	11.04

Turn on : 28.8660

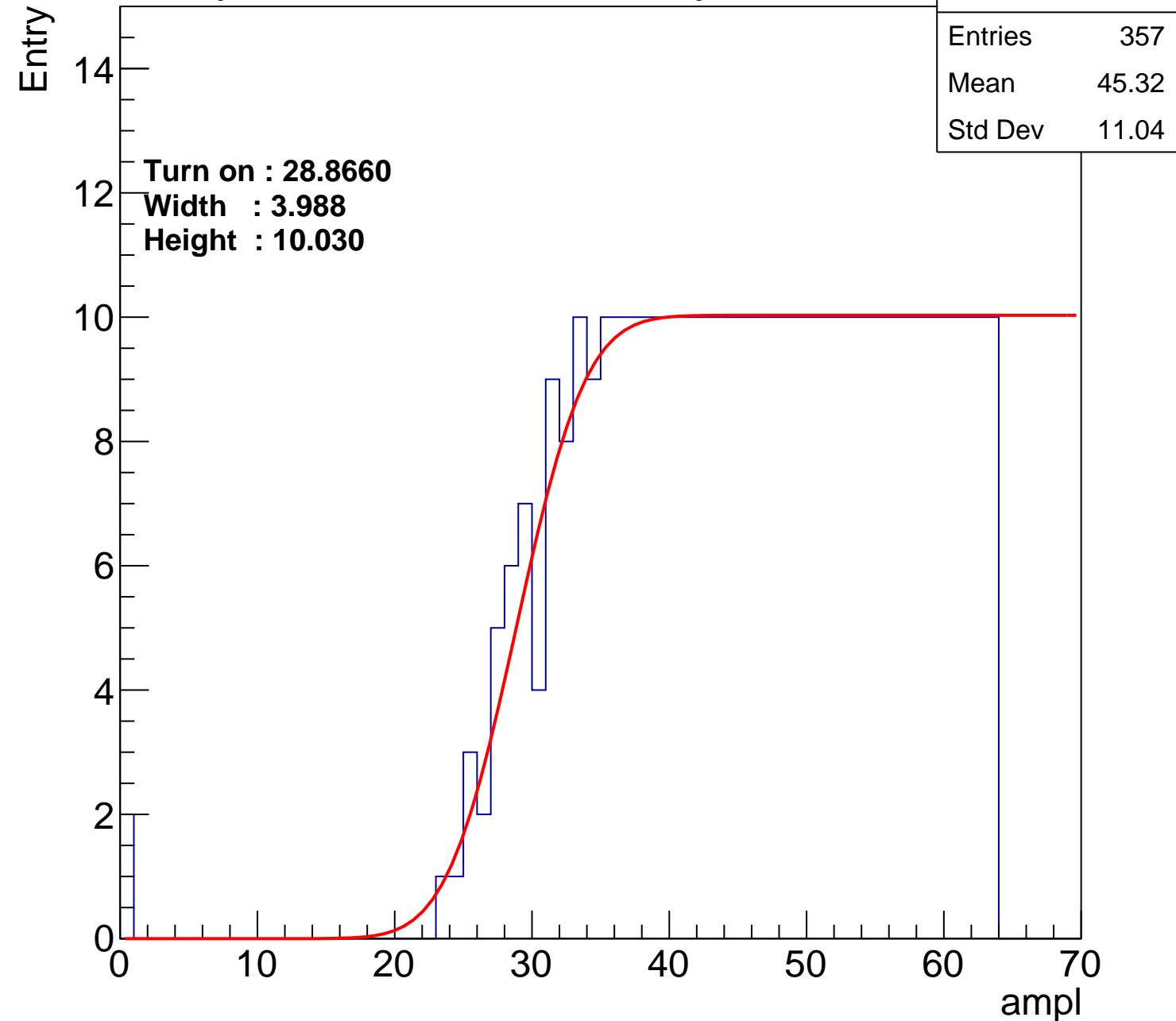
Width : 3.988

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch112

calib_packv5_042523_0143.root, FC#4, port A2

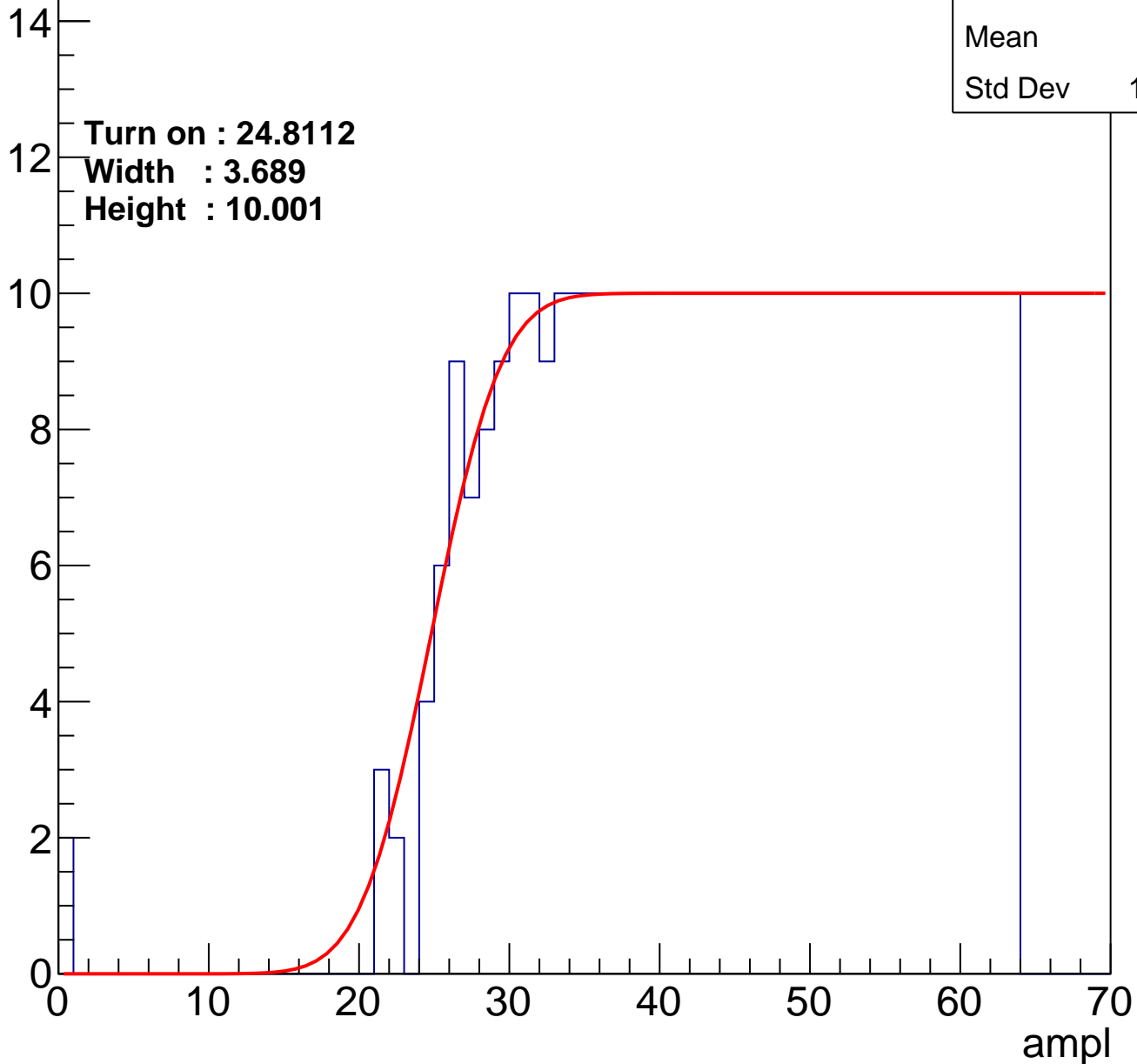
Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 24.8112

Width : 3.689

Height : 10.001

Entry



B1L100S, U9-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.2585

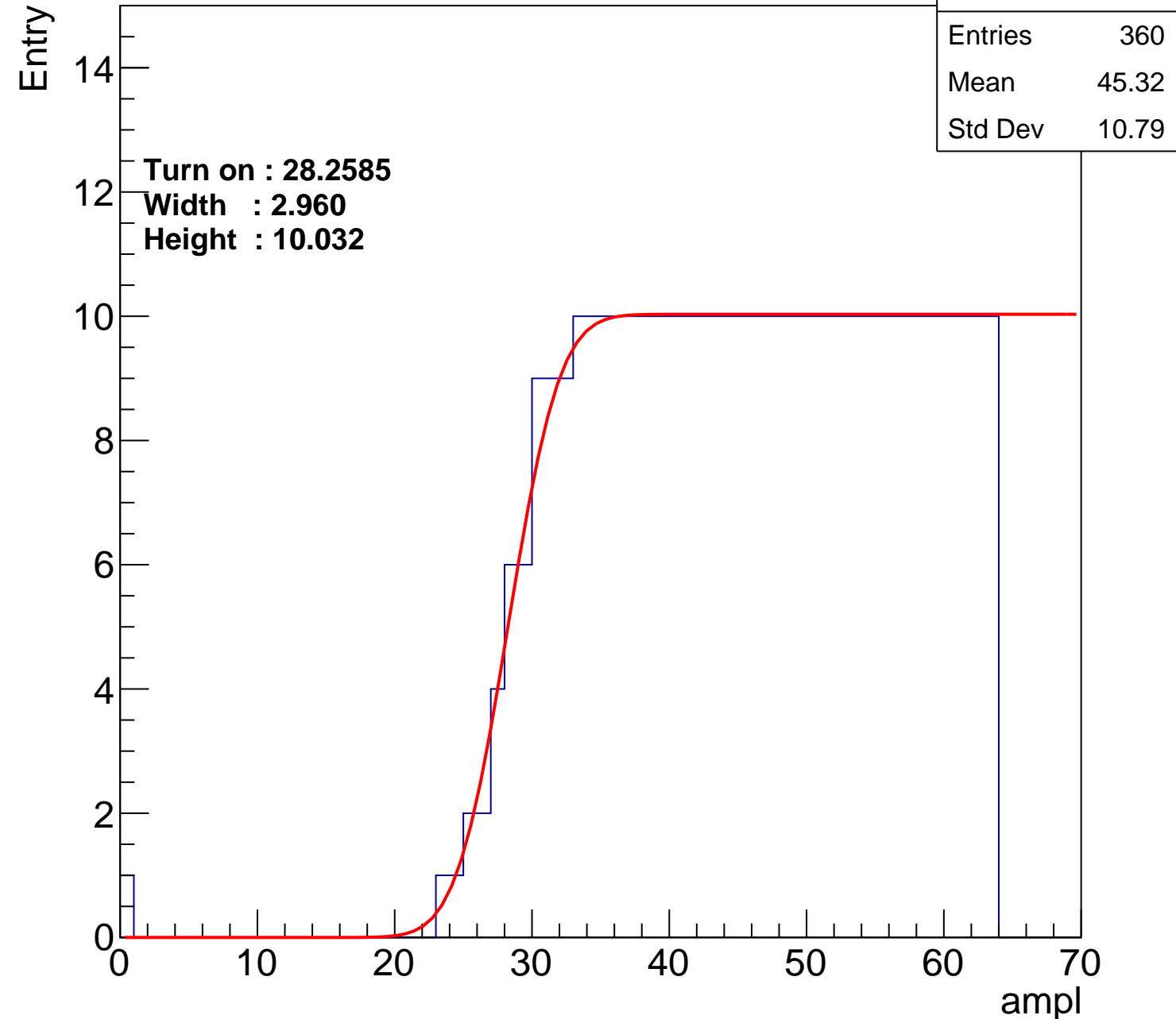
Width : 2.960

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.75
Std Dev	11.33

Turn on : 28.1843

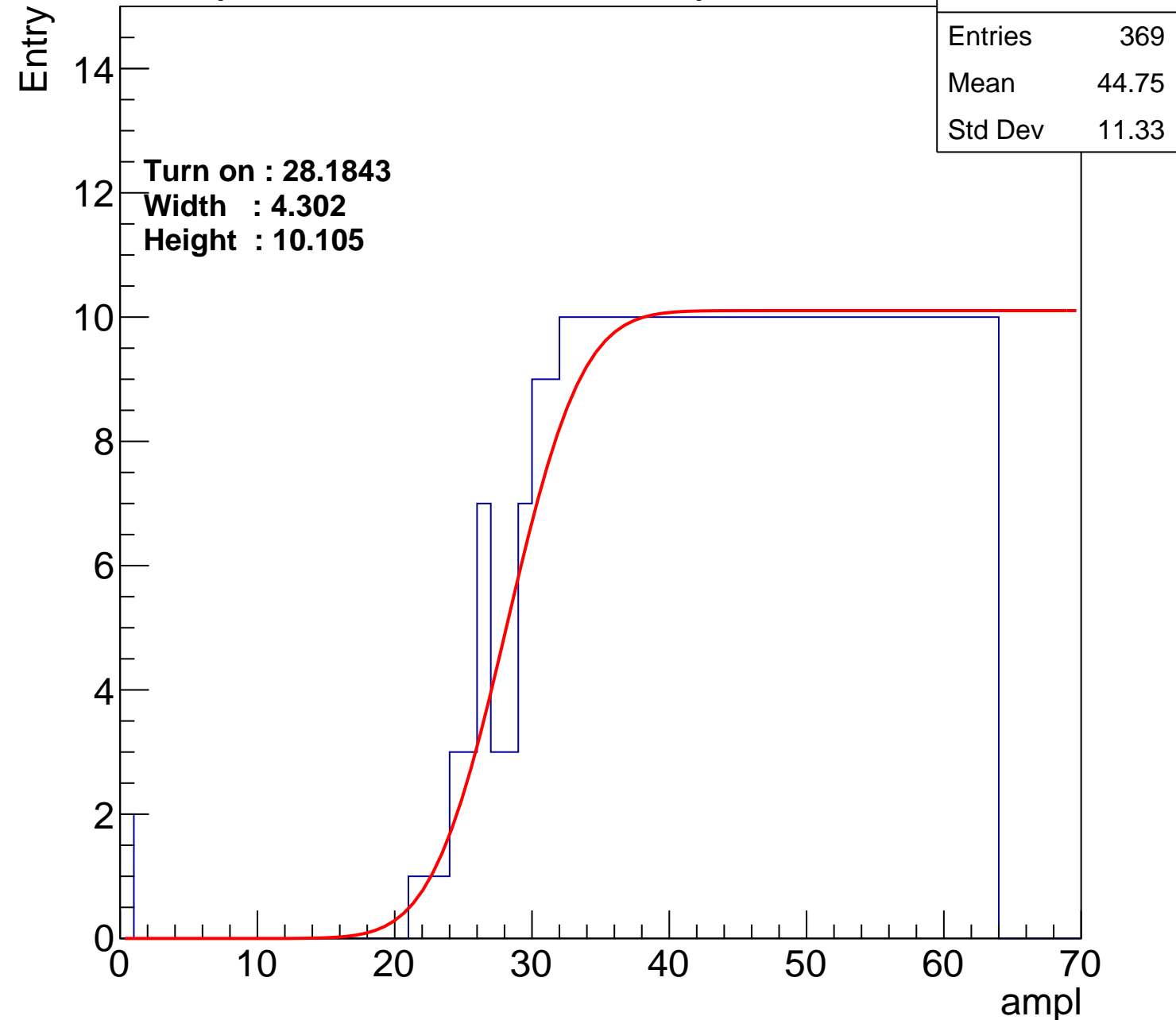
Width : 4.302

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch115

calib_packv5_042523_0143.root, FC#4, port A2

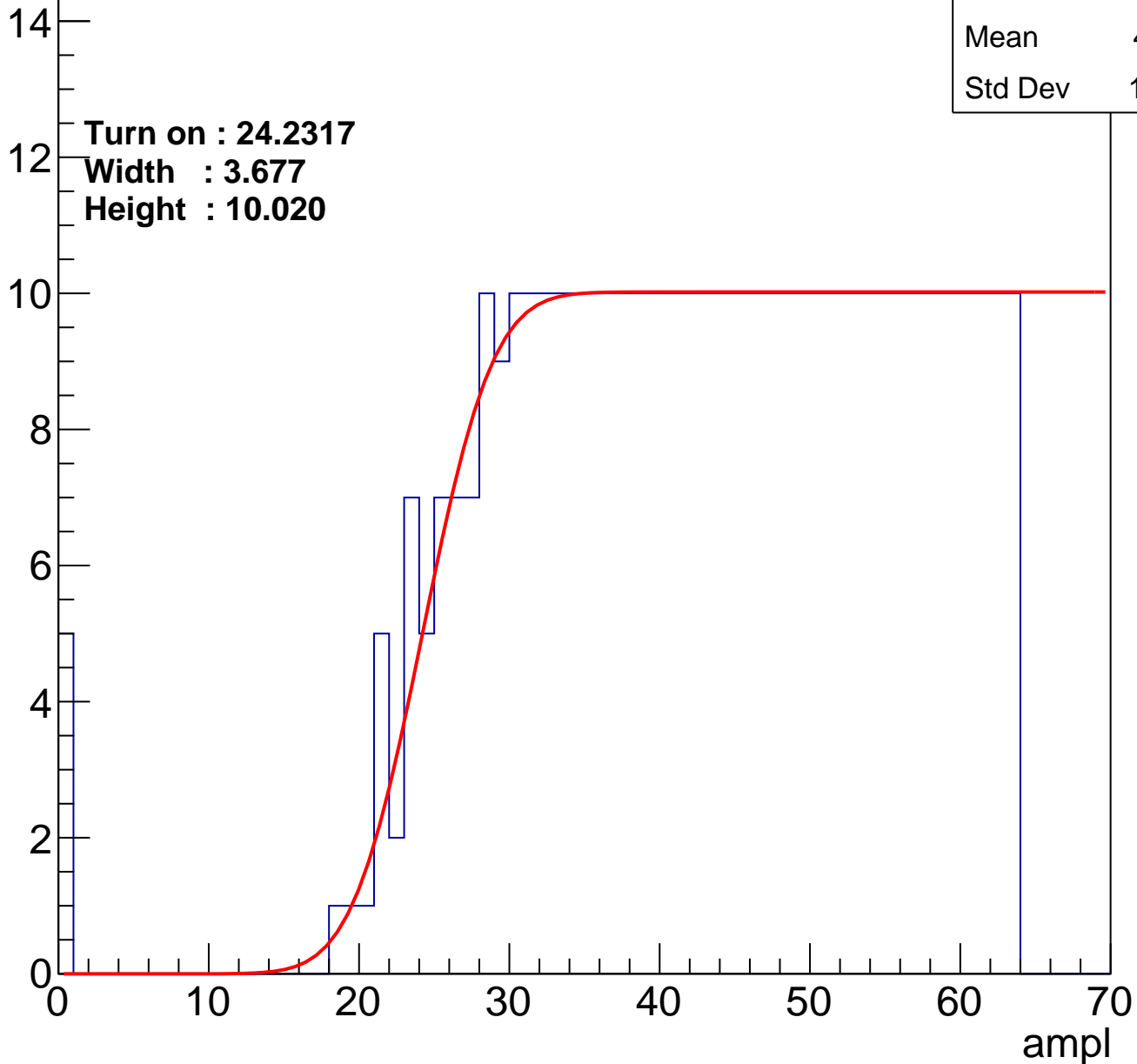
Entries	407
Mean	42.71
Std Dev	12.72

Turn on : 24.2317

Width : 3.677

Height : 10.020

Entry



B1L100S, U9-ch116

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.76
Std Dev	11.3

Turn on : 27.3603

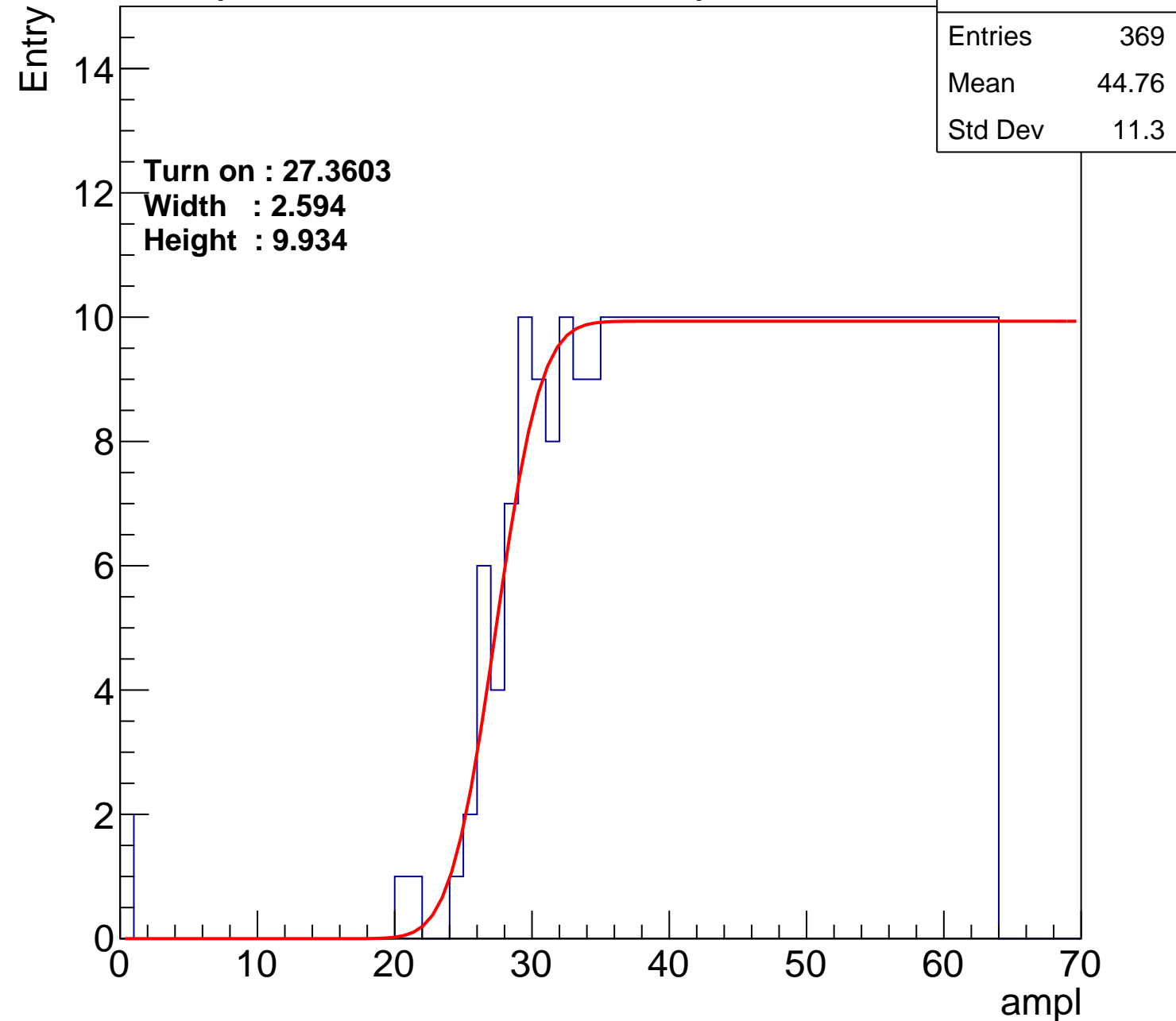
Width : 2.594

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch117

calib_packv5_042523_0143.root, FC#4, port A2

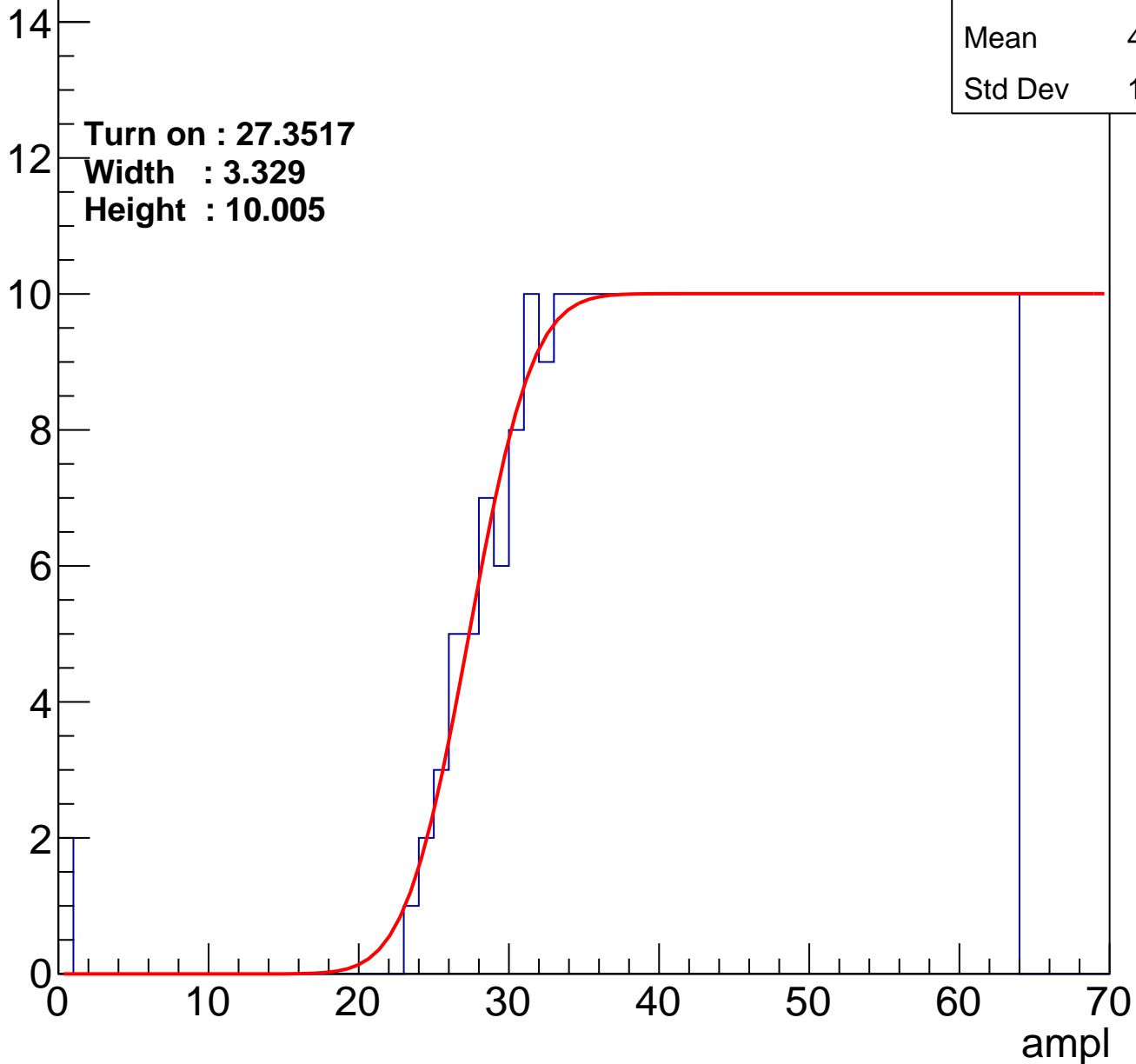
Entry

Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.3517

Width : 3.329

Height : 10.005



B1L100S, U9-ch118

calib_packv5_042523_0143.root, FC#4, port A2

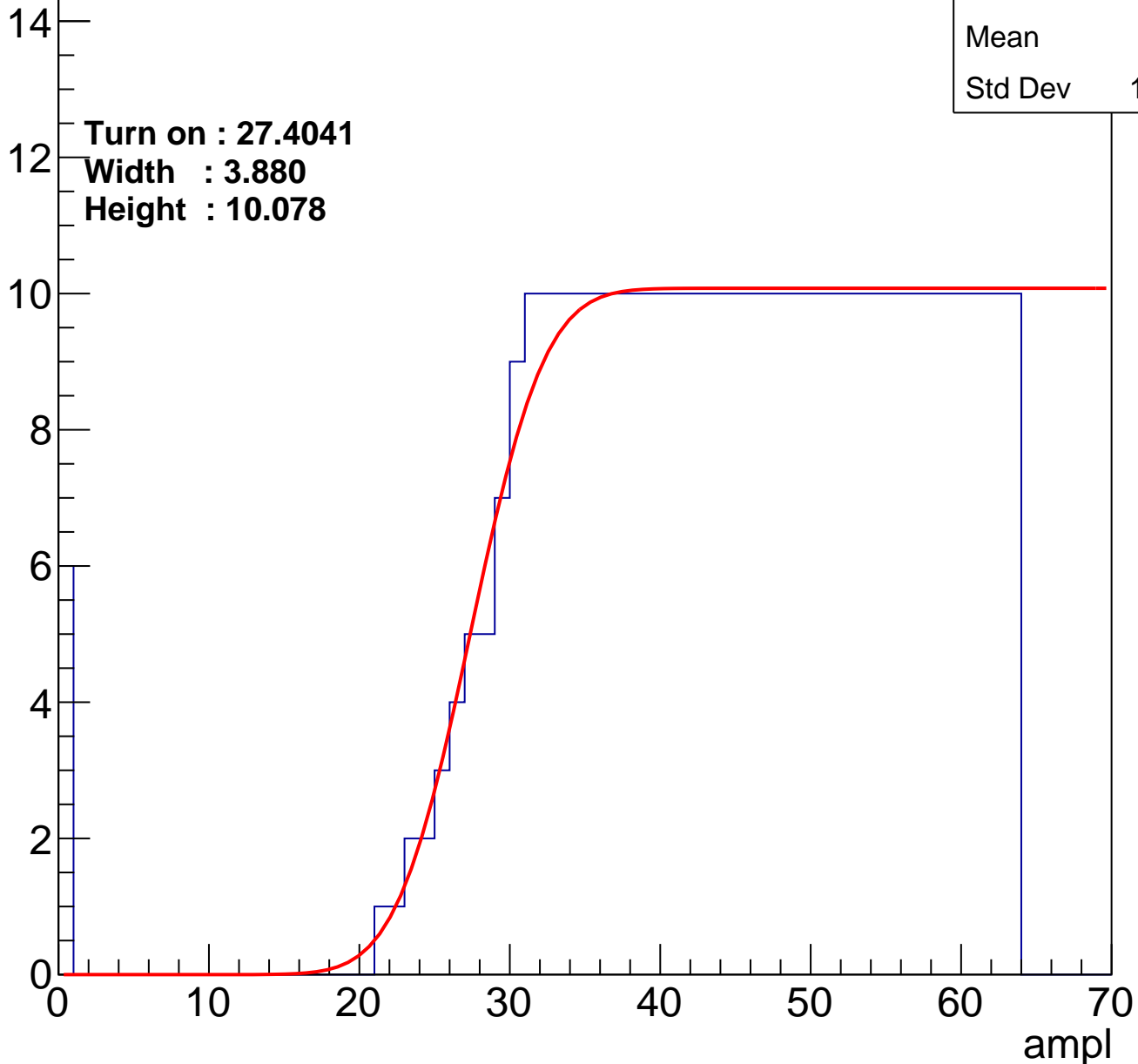
Entries	375
Mean	44.2
Std Dev	12.18

Turn on : 27.4041

Width : 3.880

Height : 10.078

Entry



B1L100S, U9-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.35
Std Dev	11.2

Turn on : 29.1202

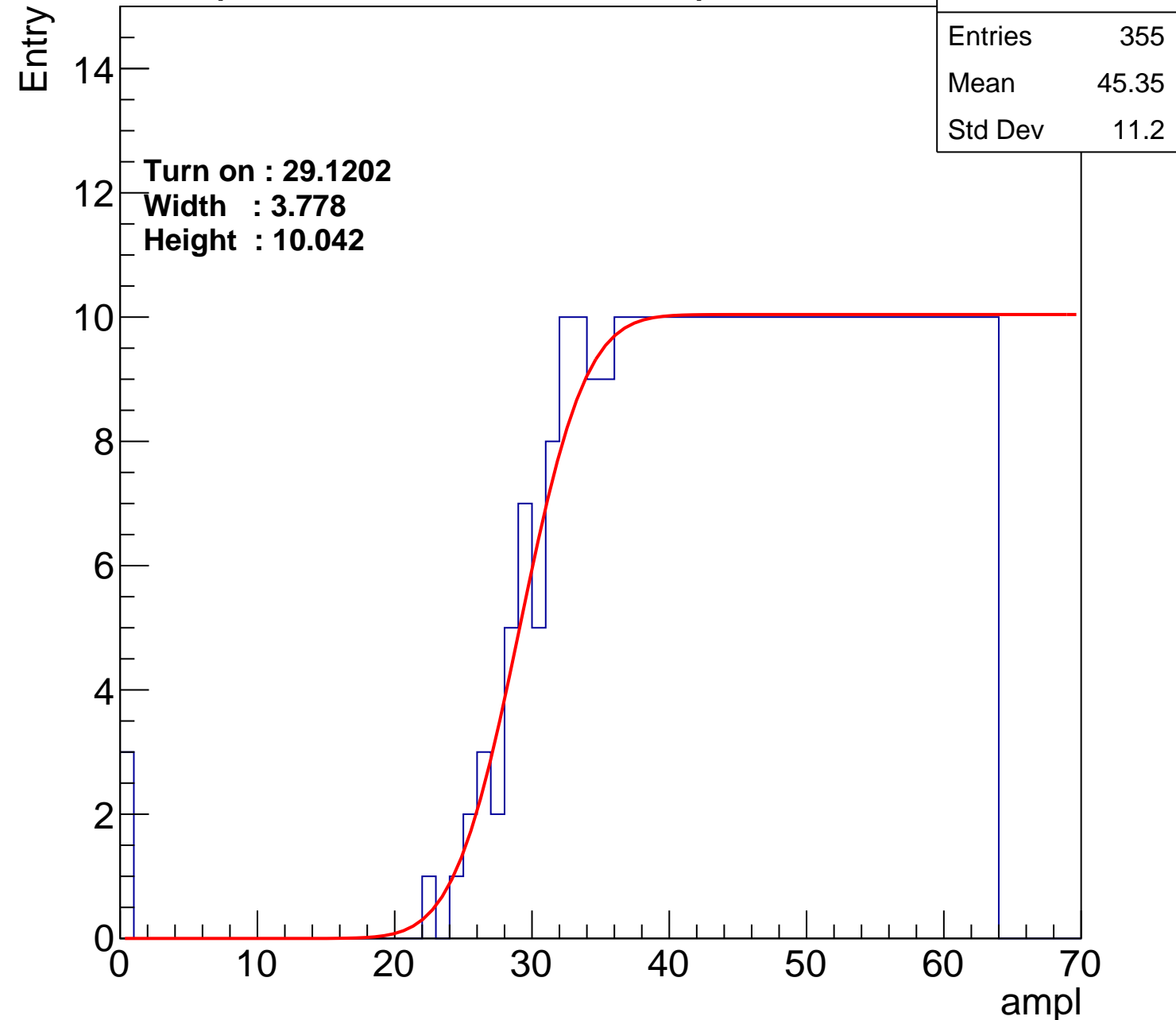
Width : 3.778

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch120

calib_packv5_042523_0143.root, FC#4, port A2

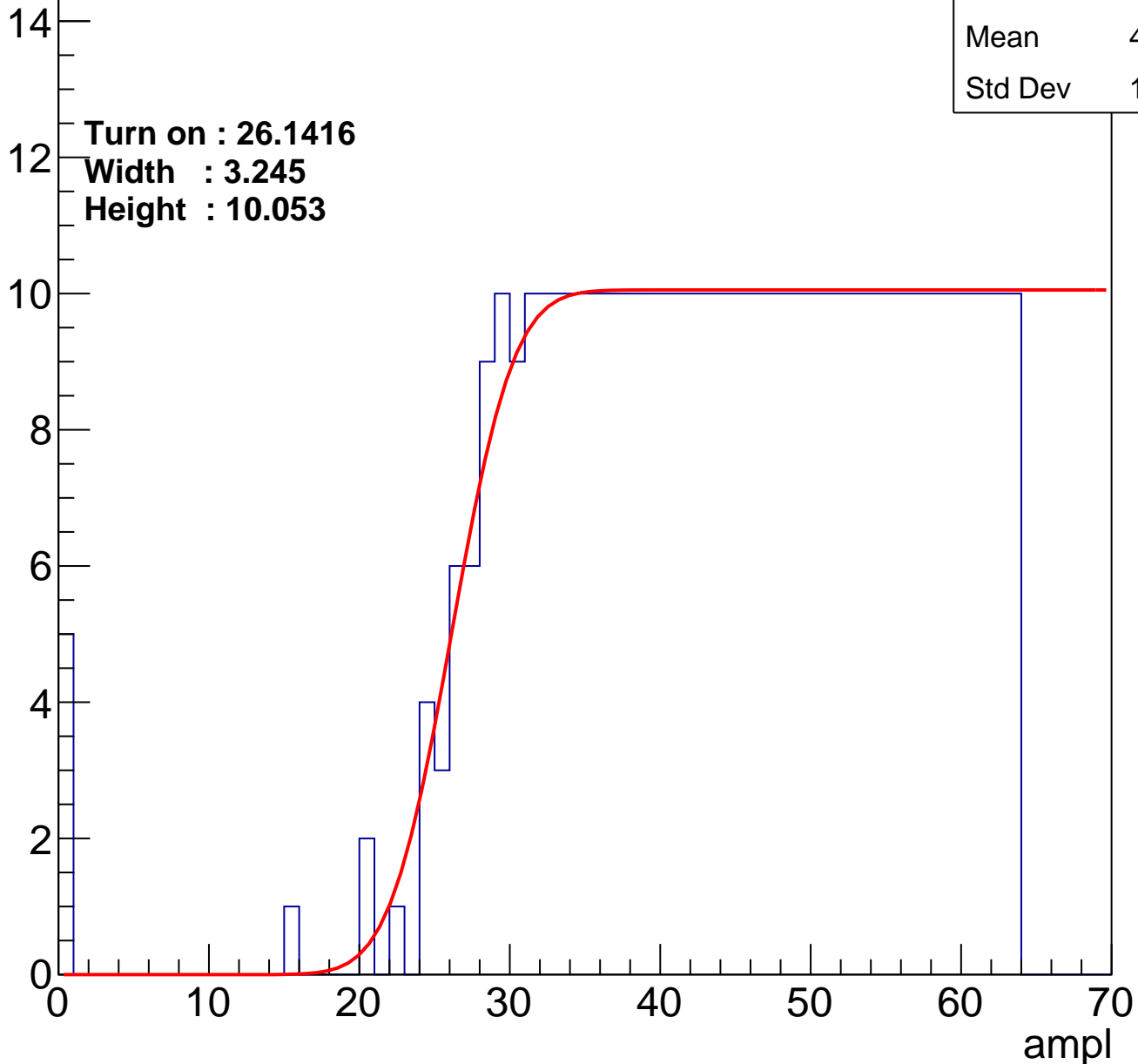
Entries	386
Mean	43.75
Std Dev	12.23

Turn on : 26.1416

Width : 3.245

Height : 10.053

Entry



B1L100S, U9-ch121

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.07
Std Dev	10.94

Turn on : 27.7884

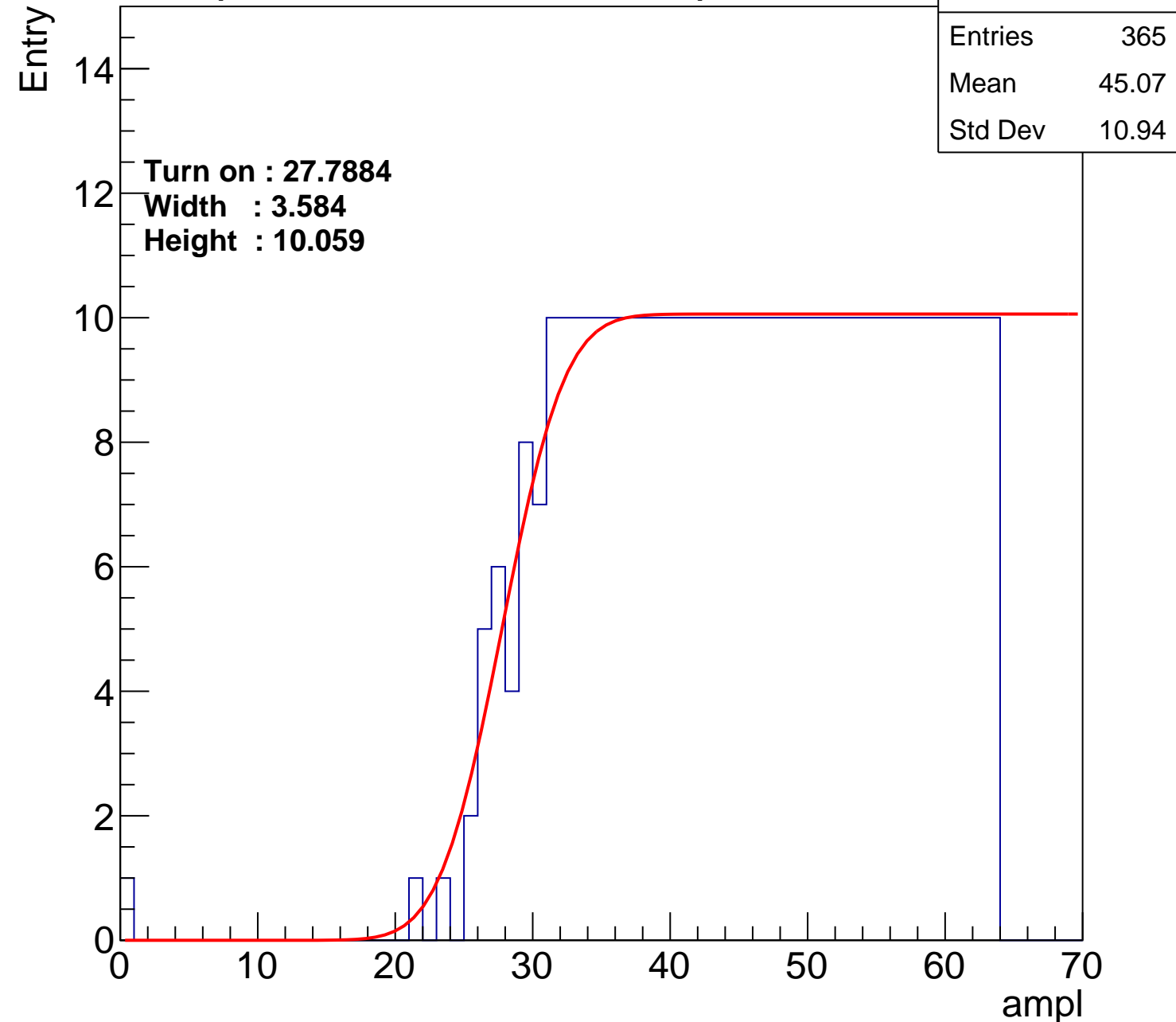
Width : 3.584

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch122

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.4
Std Dev	10.77

Turn on : 28.5363

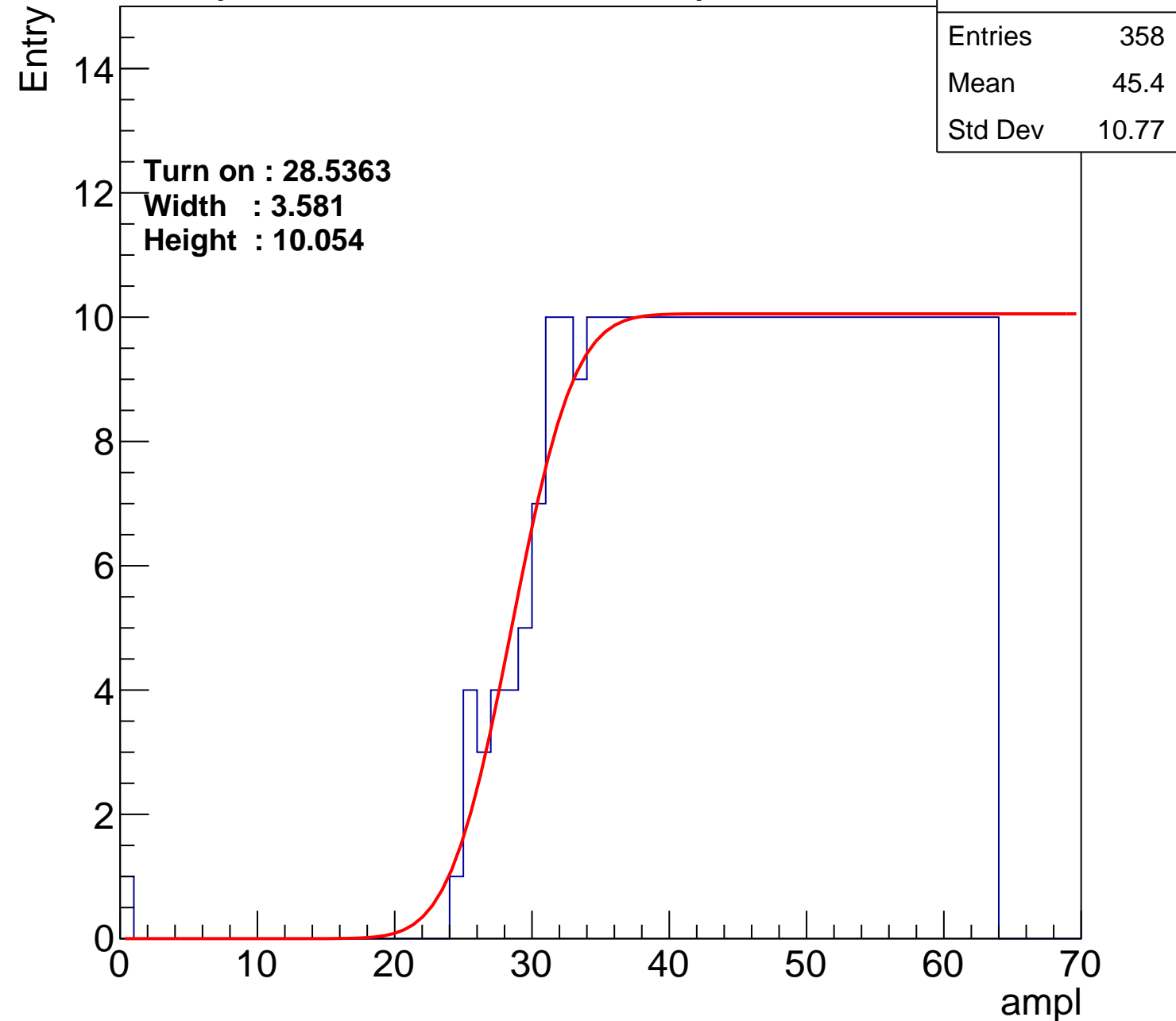
Width : 3.581

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch123

calib_packv5_042523_0143.root, FC#4, port A2

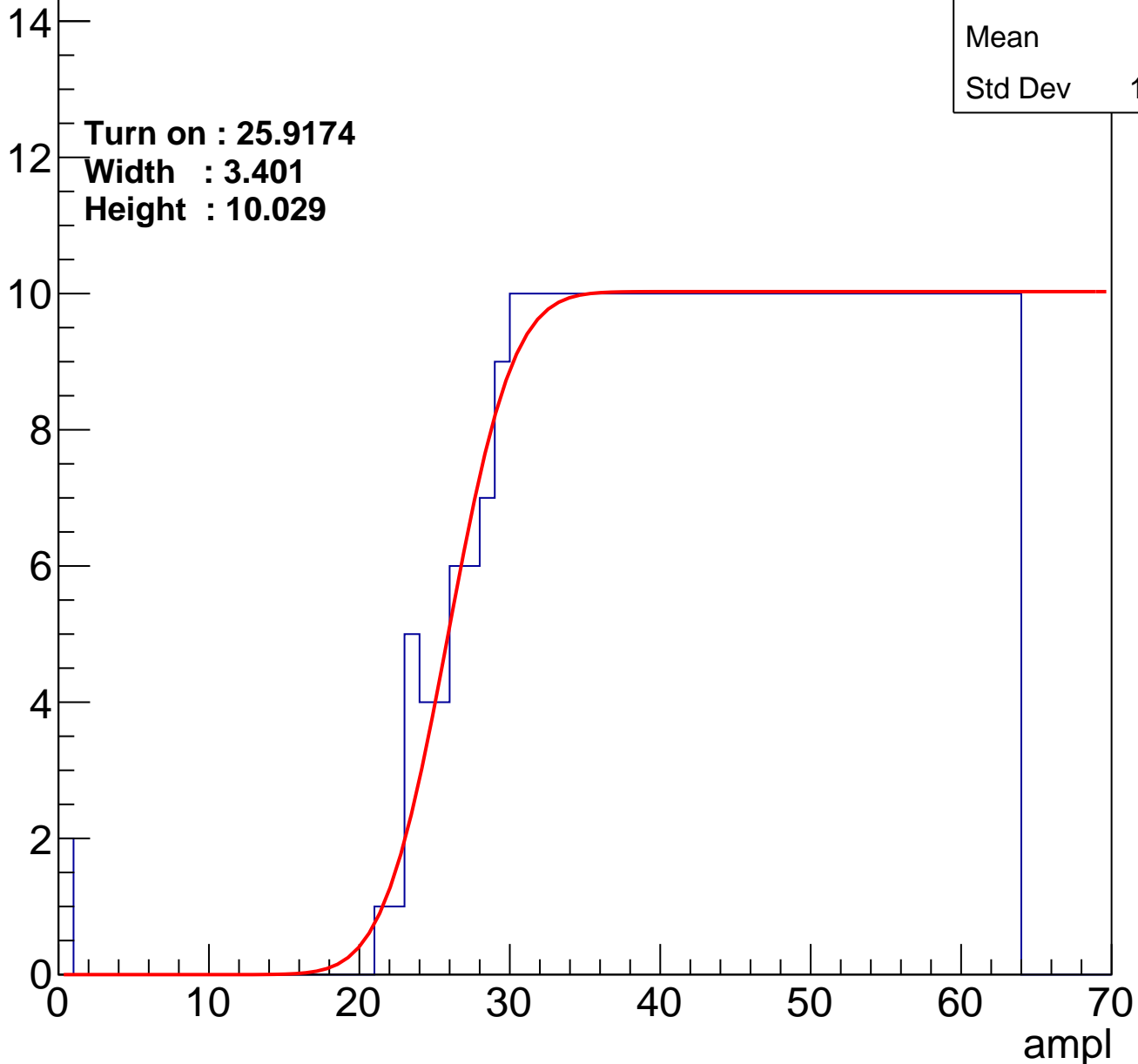
Entries	385
Mean	44
Std Dev	11.68

Turn on : 25.9174

Width : 3.401

Height : 10.029

Entry



B1L100S, U9-ch124

calib_packv5_042523_0143.root, FC#4, port A2

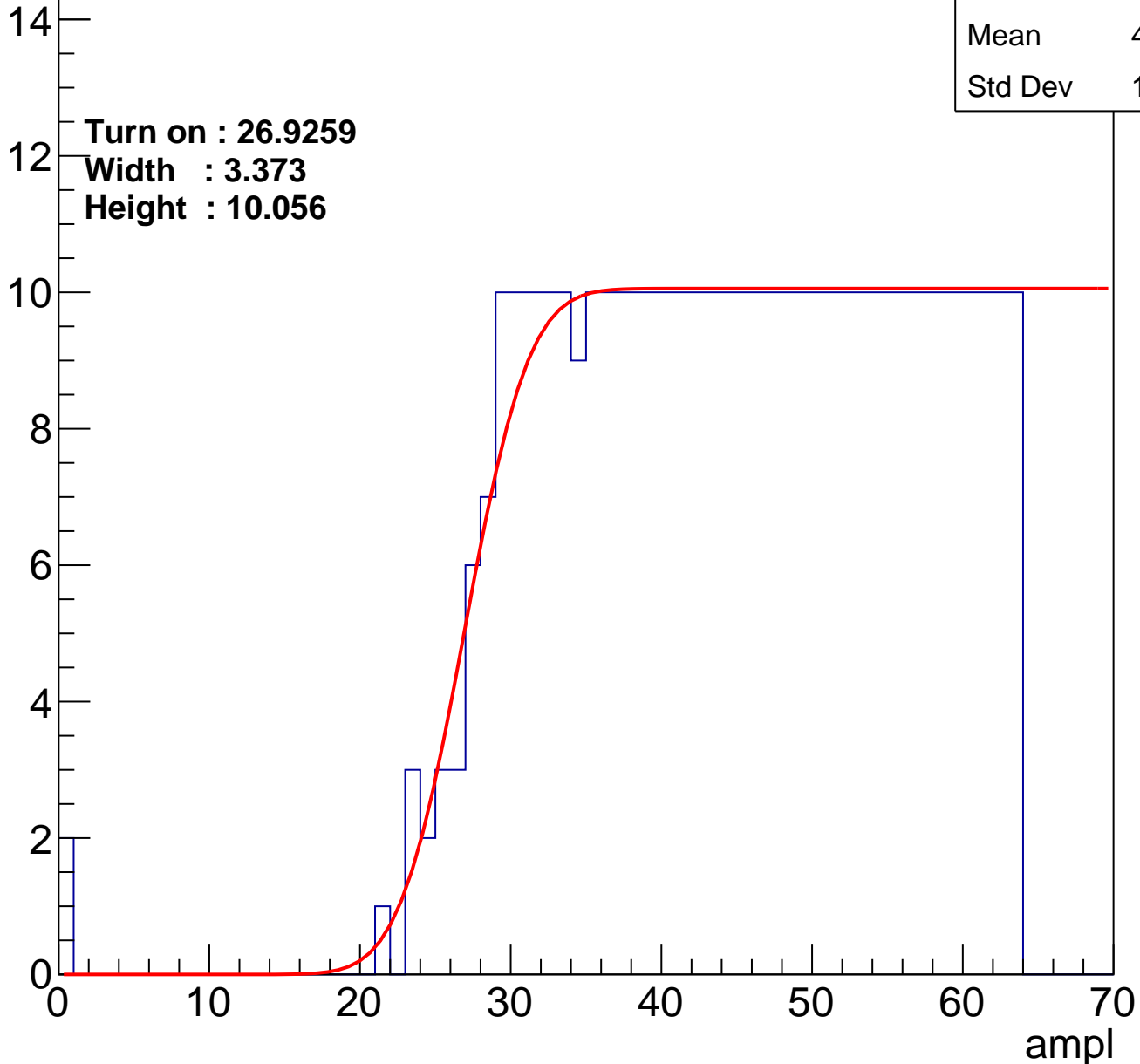
Entries	376
Mean	44.45
Std Dev	11.42

Turn on : 26.9259

Width : 3.373

Height : 10.056

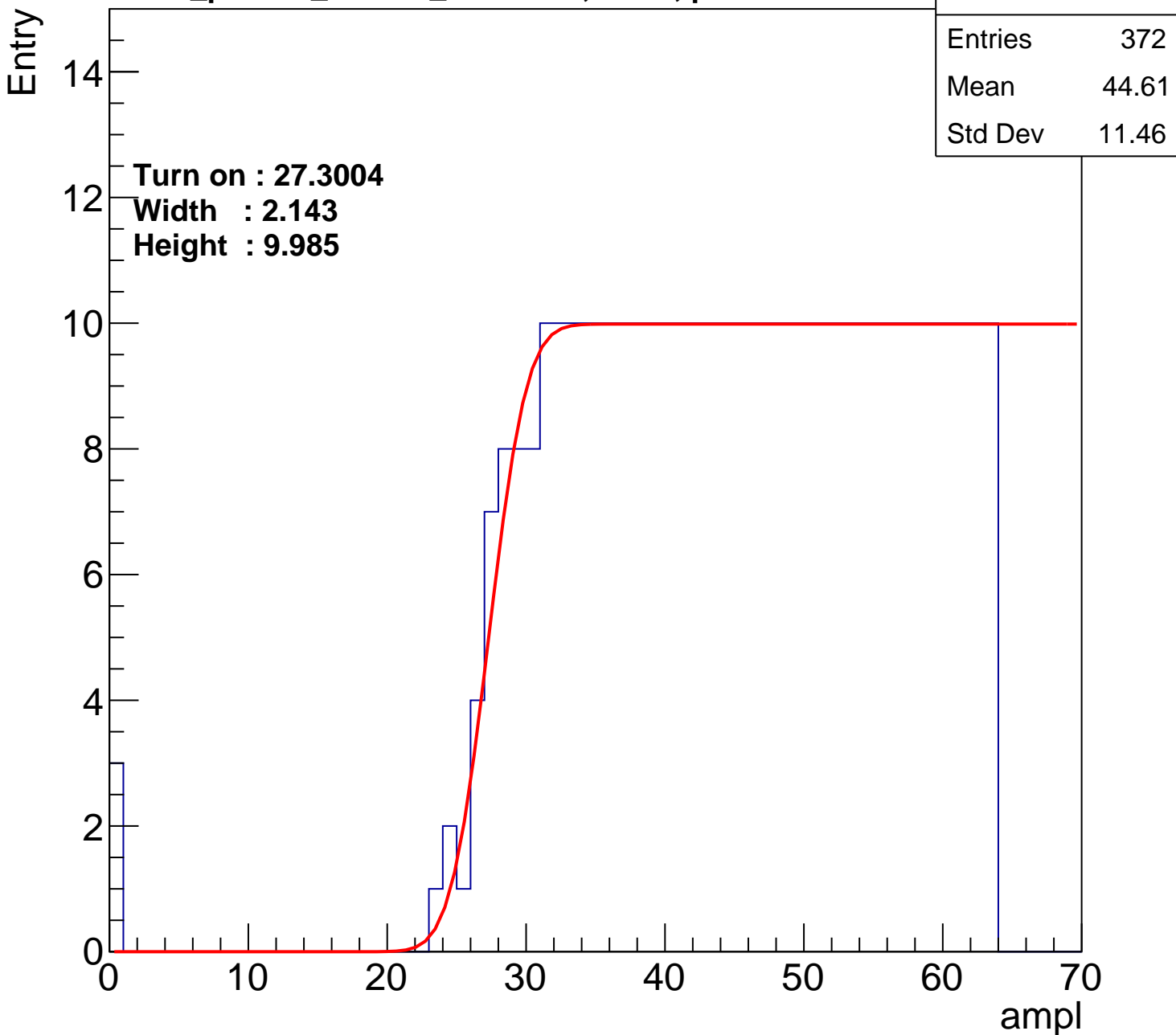
Entry



calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 27.3004
Width : 2.143
Height : 9.985



B1L100S, U9-ch126

calib_packv5_042523_0143.root, FC#4, port A2

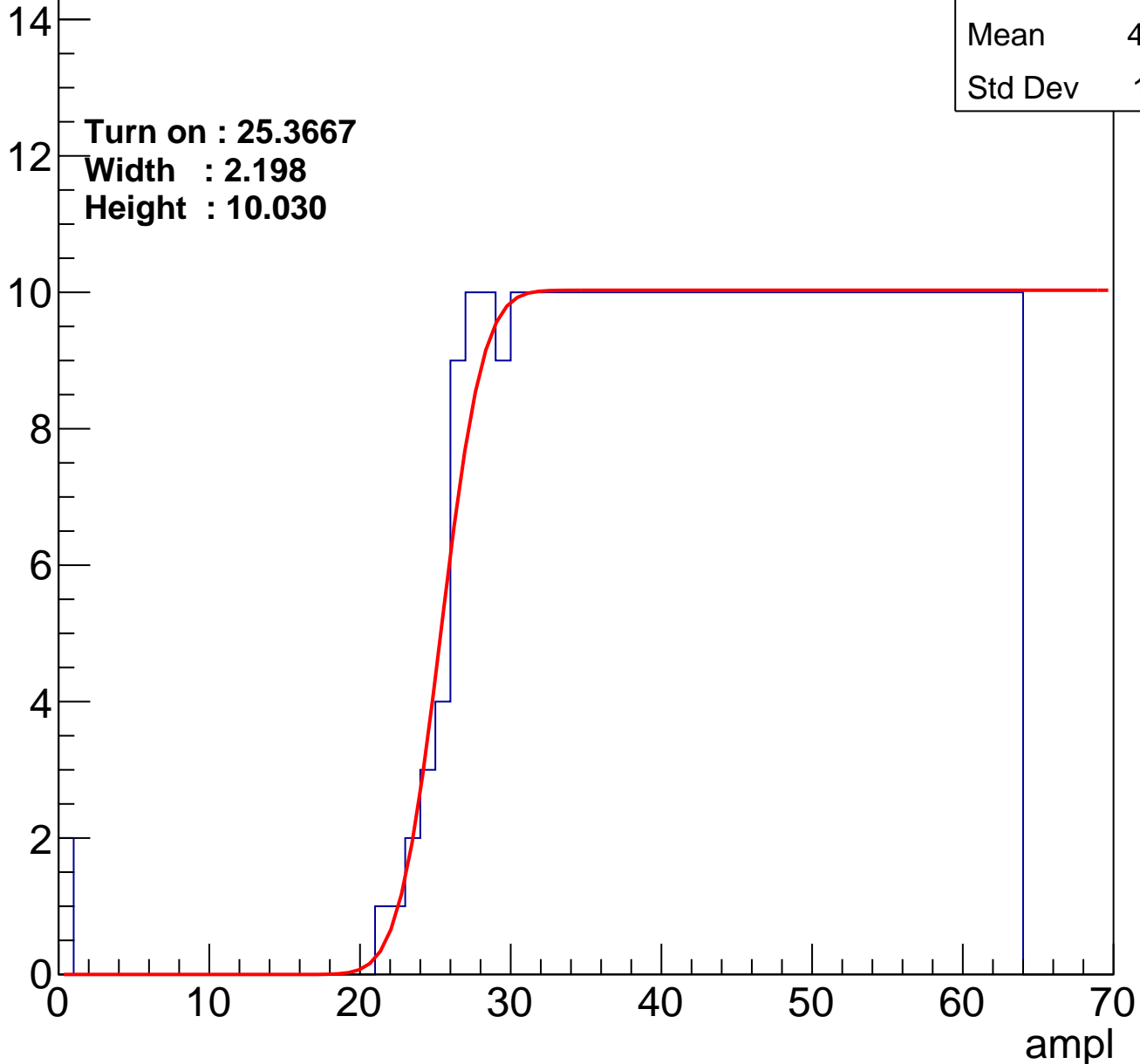
Entries	391
Mean	43.77
Std Dev	11.71

Turn on : 25.3667

Width : 2.198

Height : 10.030

Entry



B1L100S, U9-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.9464

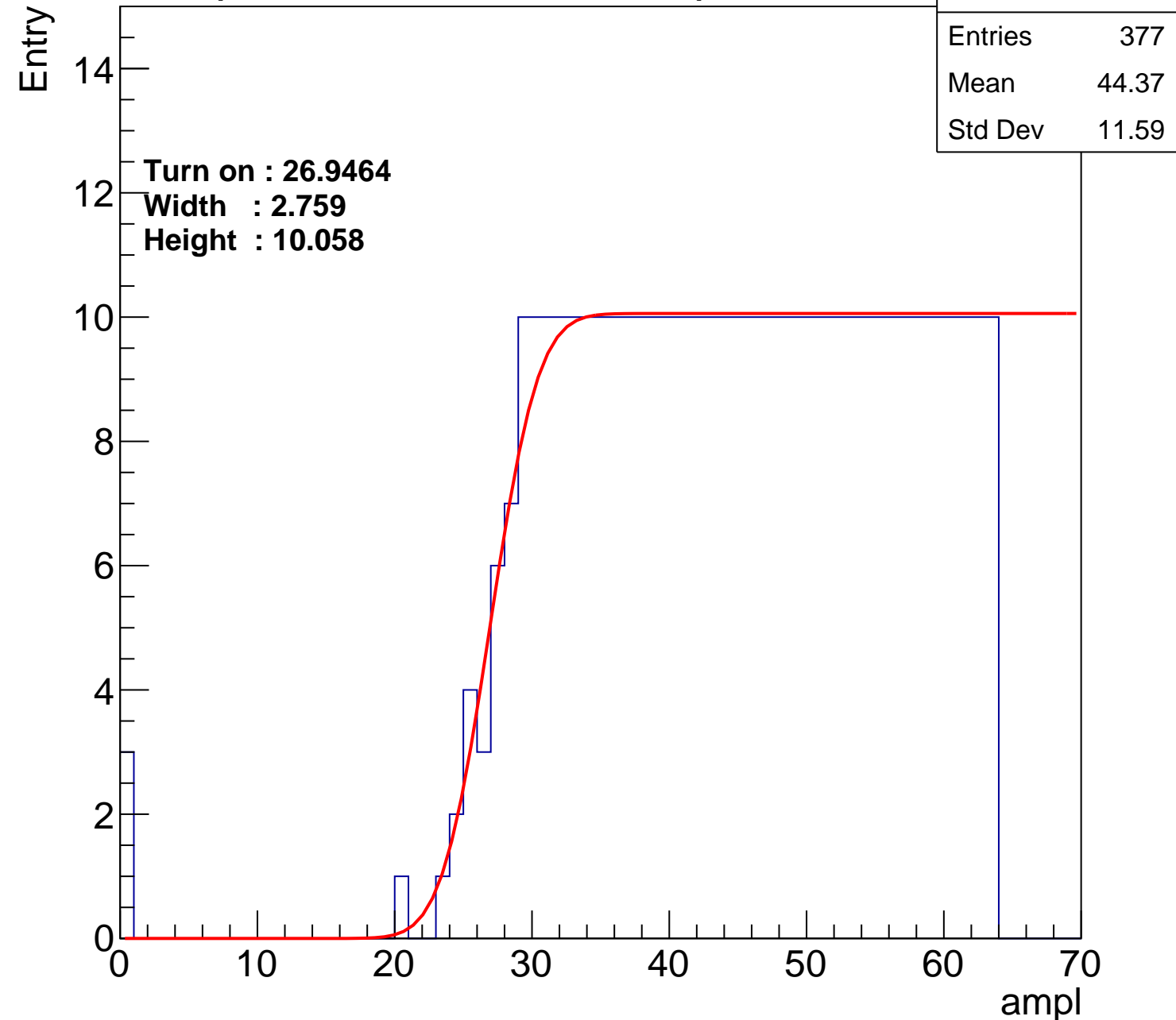
Width : 2.759

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U9-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.9464

Width : 2.759

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl

