

B0L102S, U17-ch0

calib_packv5_042523_0143.root, FC#12, port B1

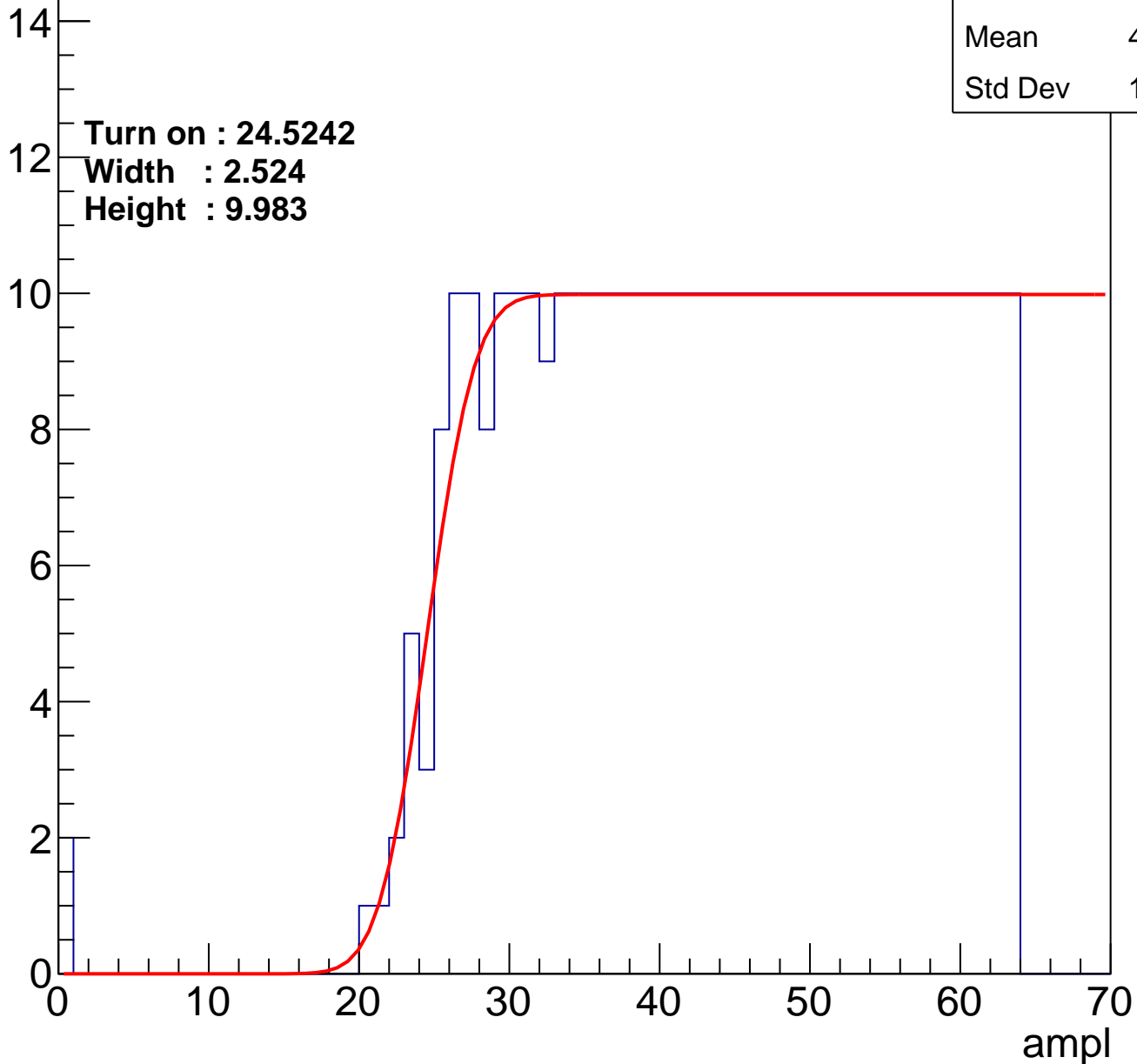
Entry

Entries	399
Mean	43.34
Std Dev	11.97

Turn on : 24.5242

Width : 2.524

Height : 9.983



B0L102S, U17-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.86
Std Dev	11.93

Turn on : 25.8942

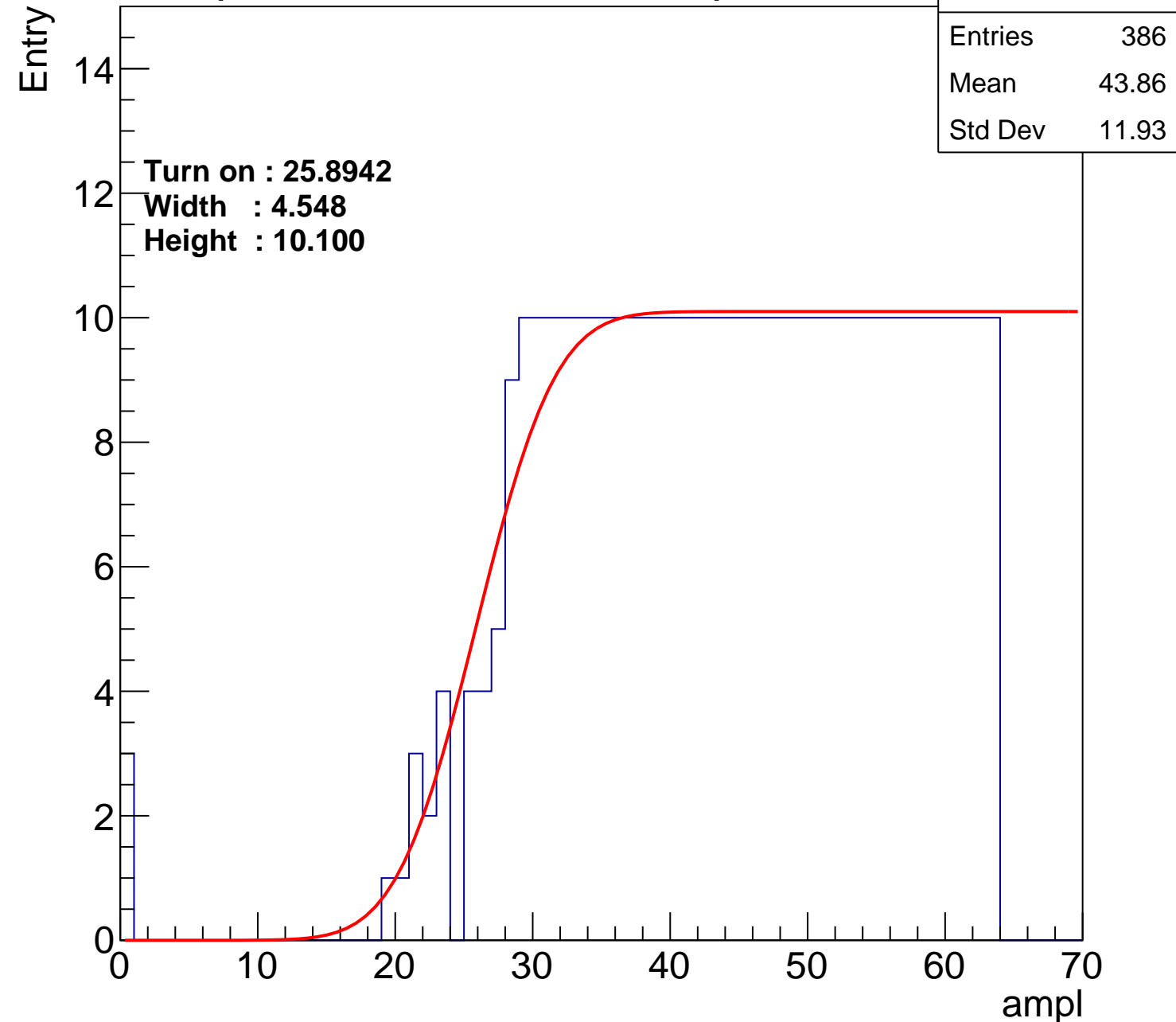
Width : 4.548

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	43.08
Std Dev	11.98

Turn on : 23.7280

Width : 2.274

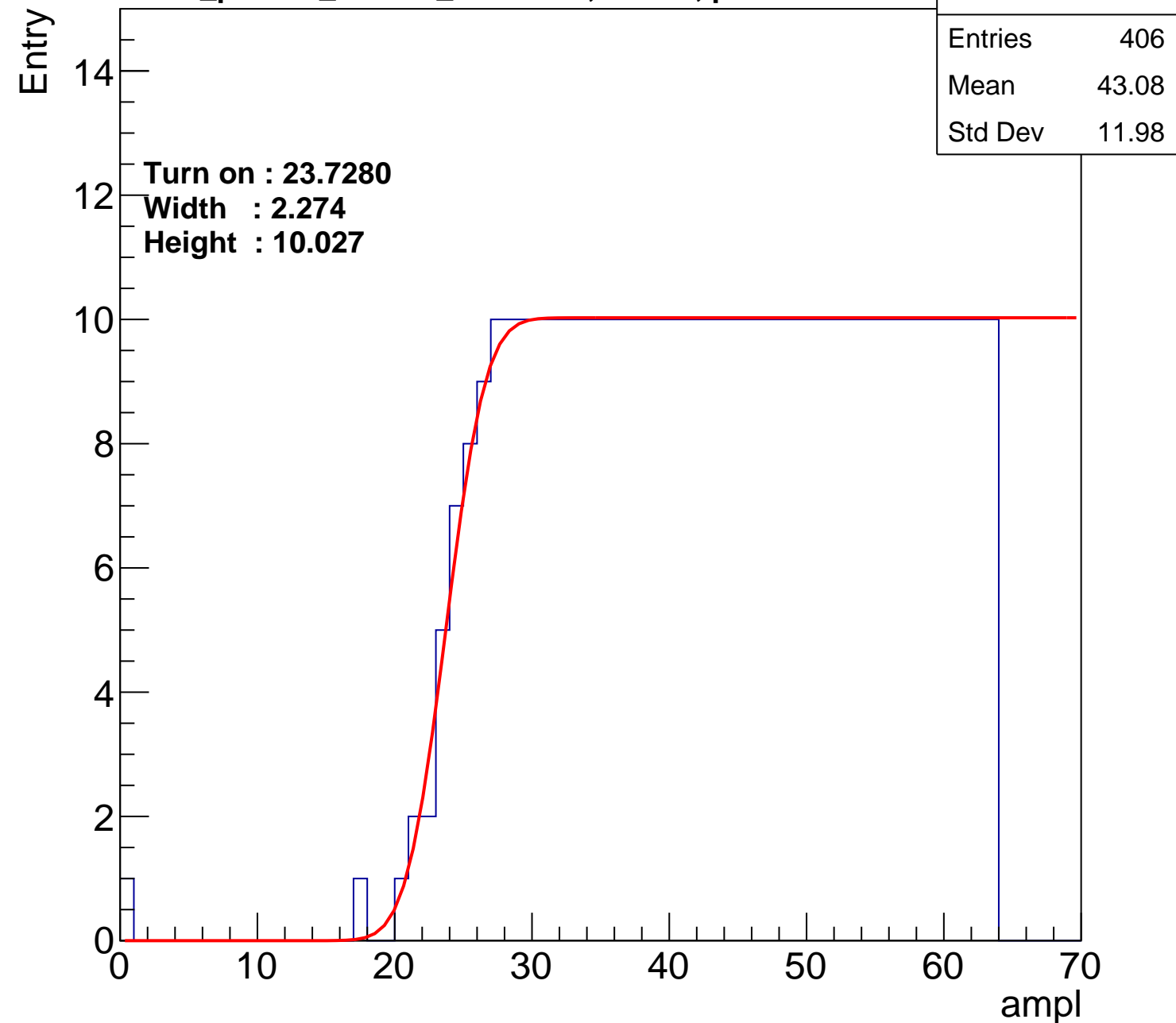
Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U17-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.16
Std Dev	11.03

Turn on : 27.6037

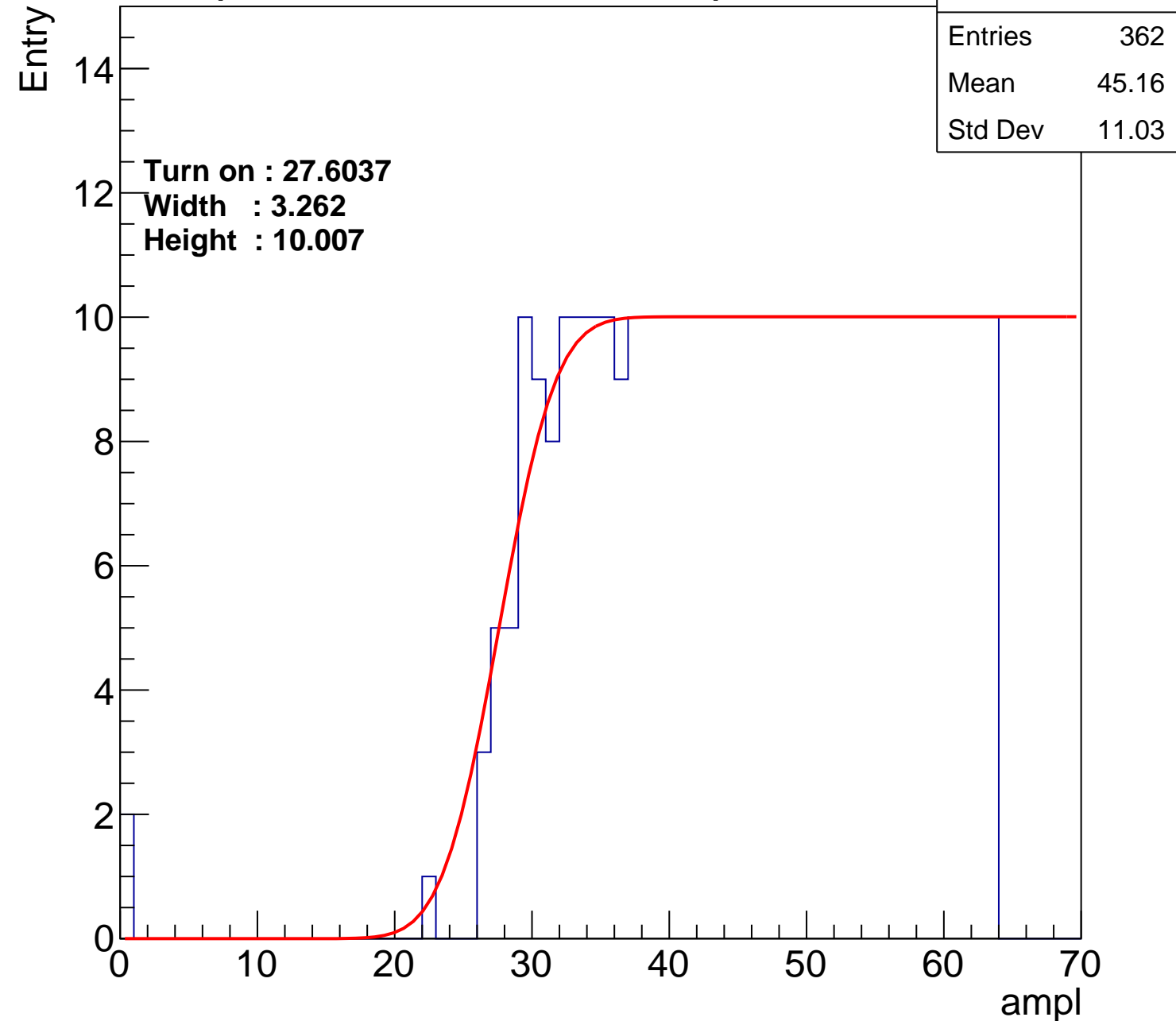
Width : 3.262

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.59
Std Dev	11.24

Turn on : 26.7218

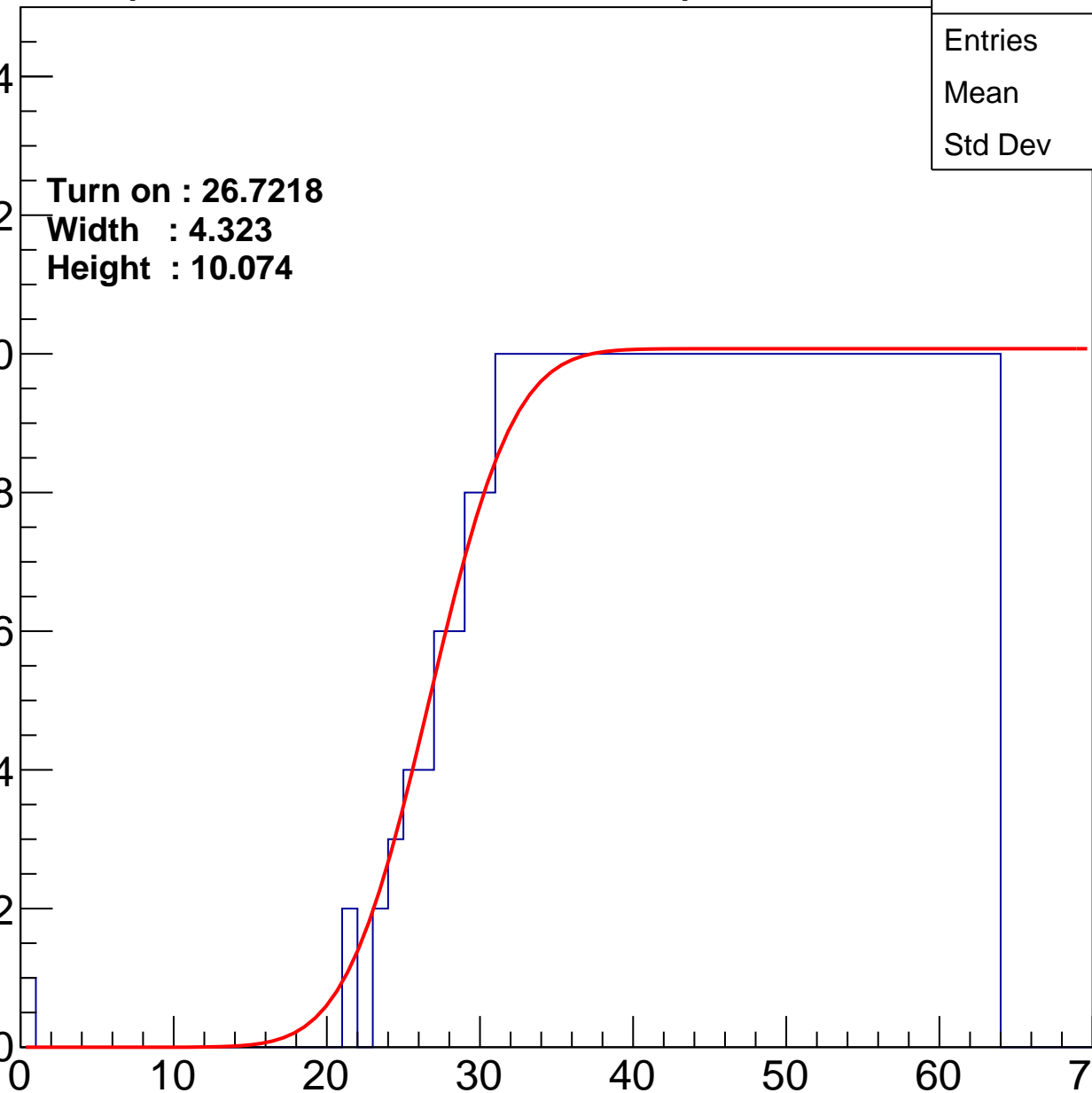
Width : 4.323

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch5

calib_packv5_042523_0143.root, FC#12, port B1

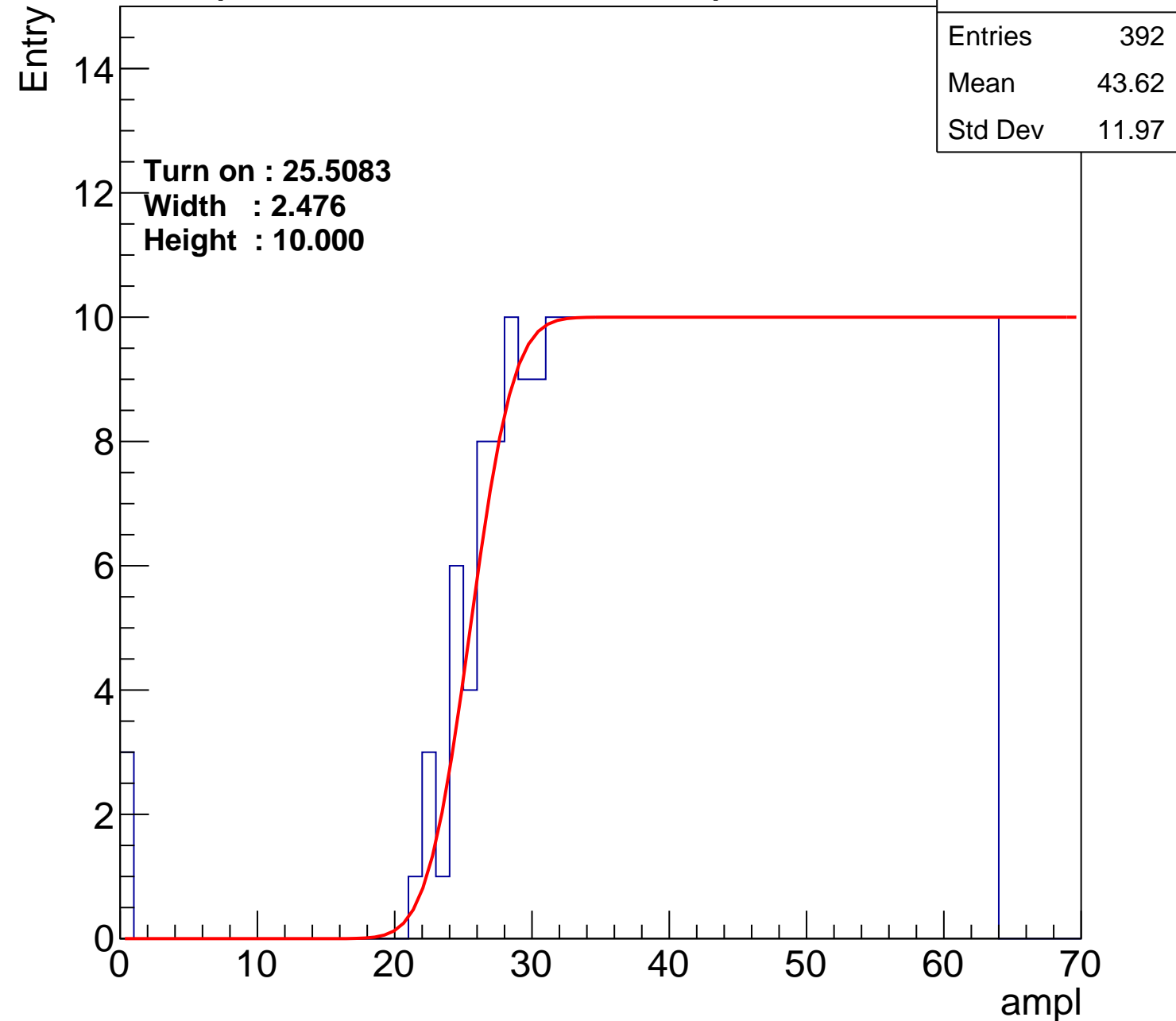
Entry

14
12
10
8
6
4
2
0

Turn on : 25.5083
Width : 2.476
Height : 10.000

Entries	392
Mean	43.62
Std Dev	11.97

ampl



B0L102S, U17-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.9
Std Dev	12.02

Turn on : 26.6949

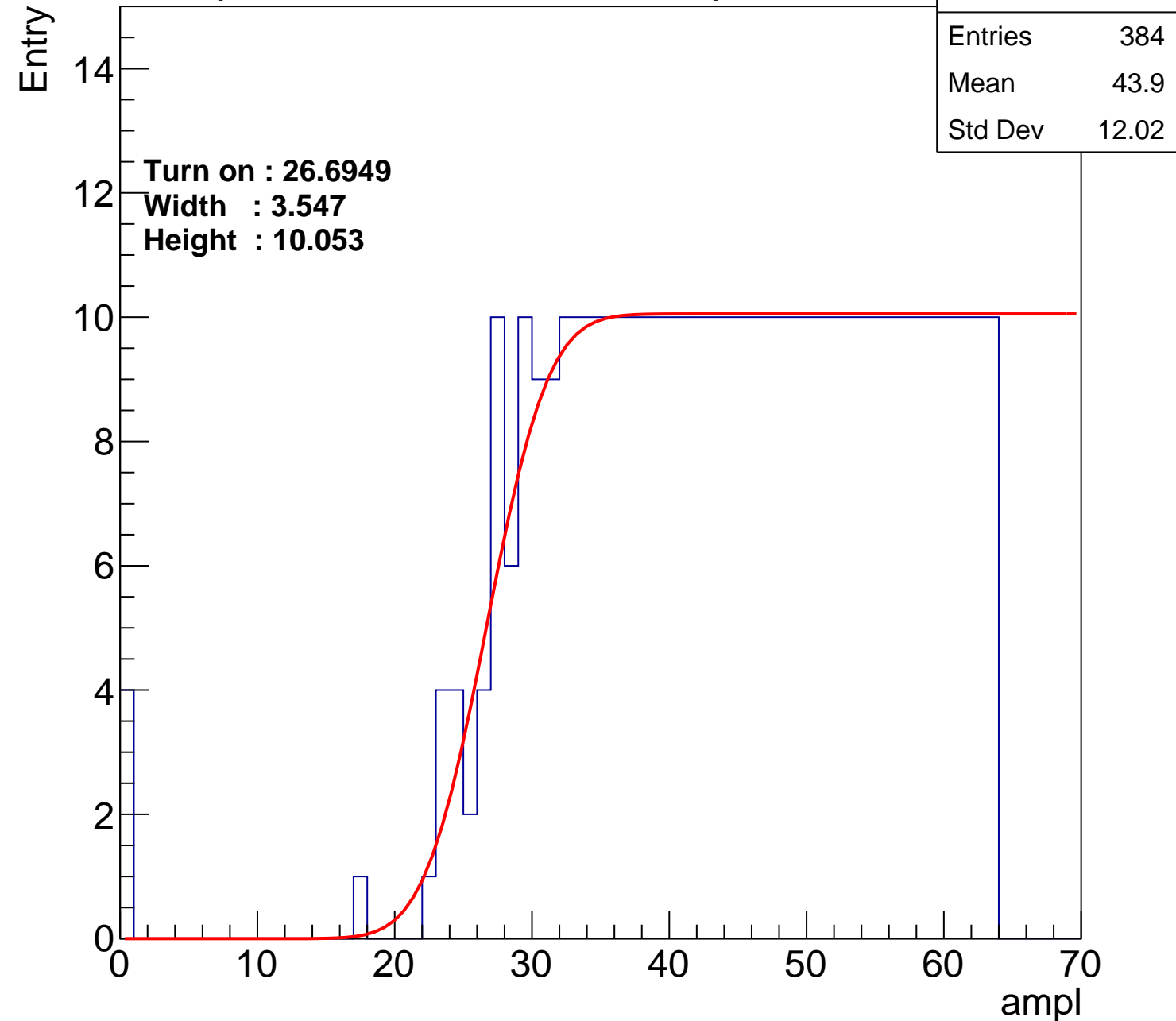
Width : 3.547

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.36
Std Dev	11.89

Turn on : 24.4042

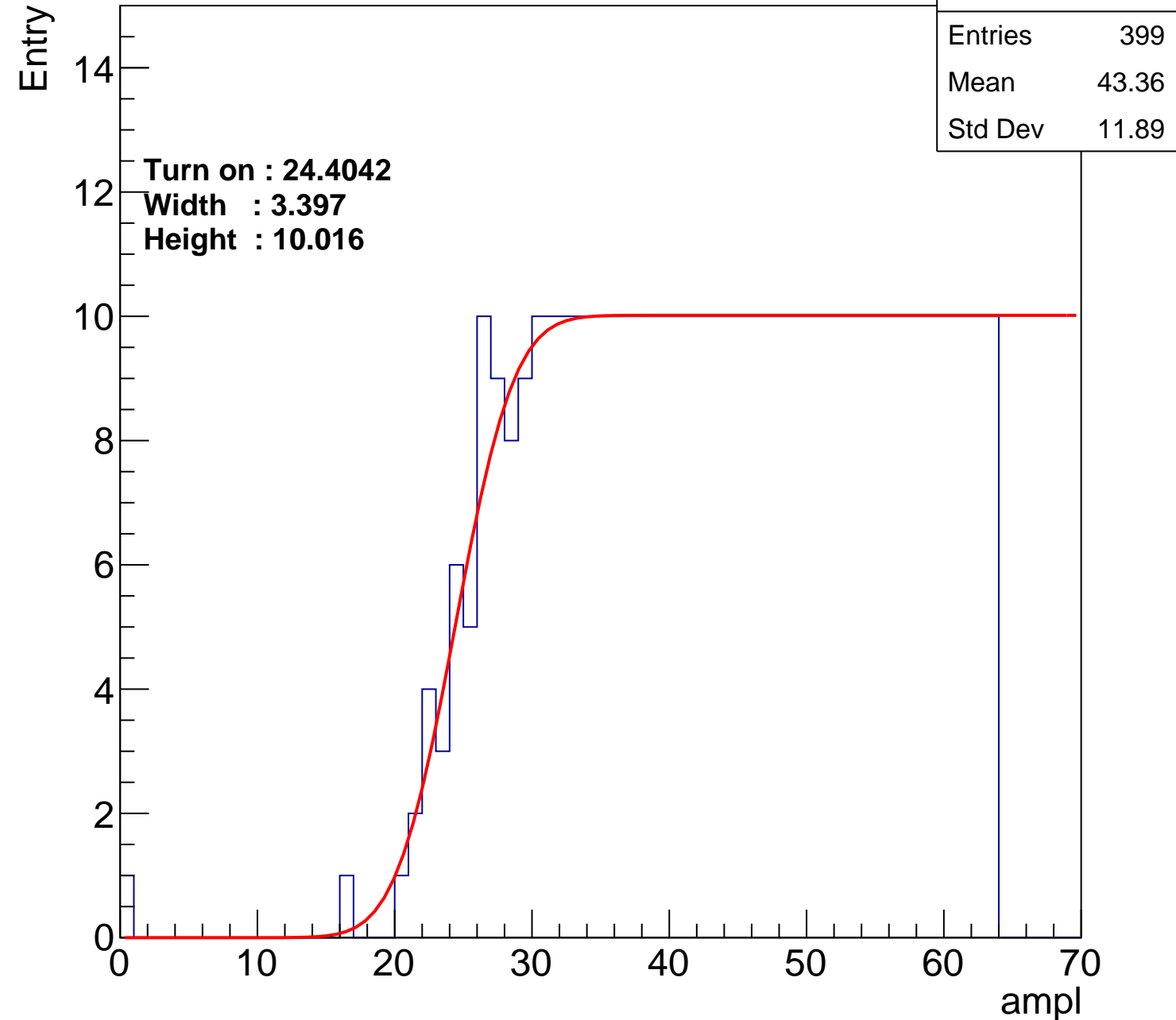
Width : 3.397

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch8

calib_packv5_042523_0143.root, FC#12, port B1

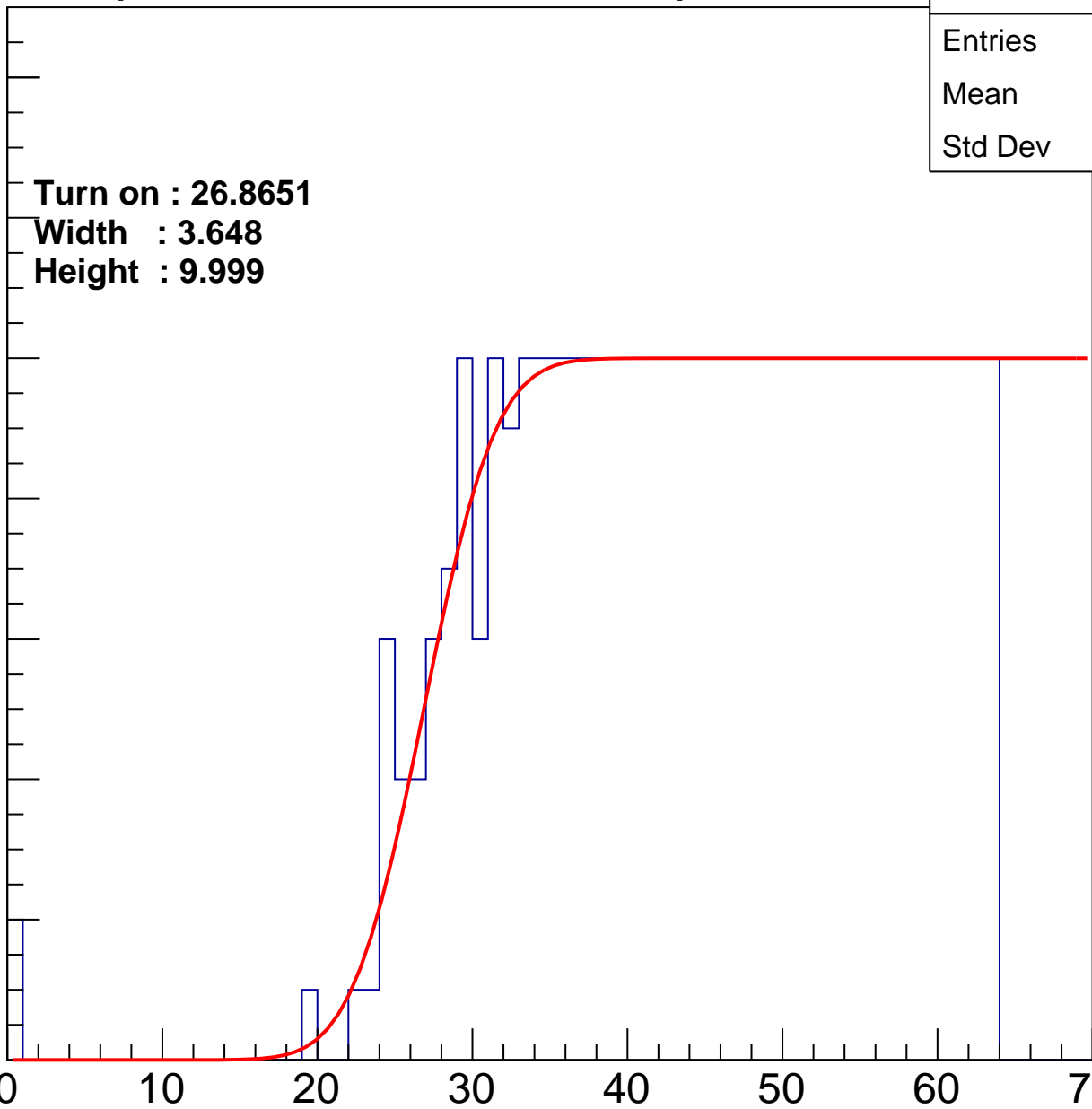
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8651
Width : 3.648
Height : 9.999

Entries	377
Mean	44.34
Std Dev	11.54

ampl



B0L102S, U17-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.6213

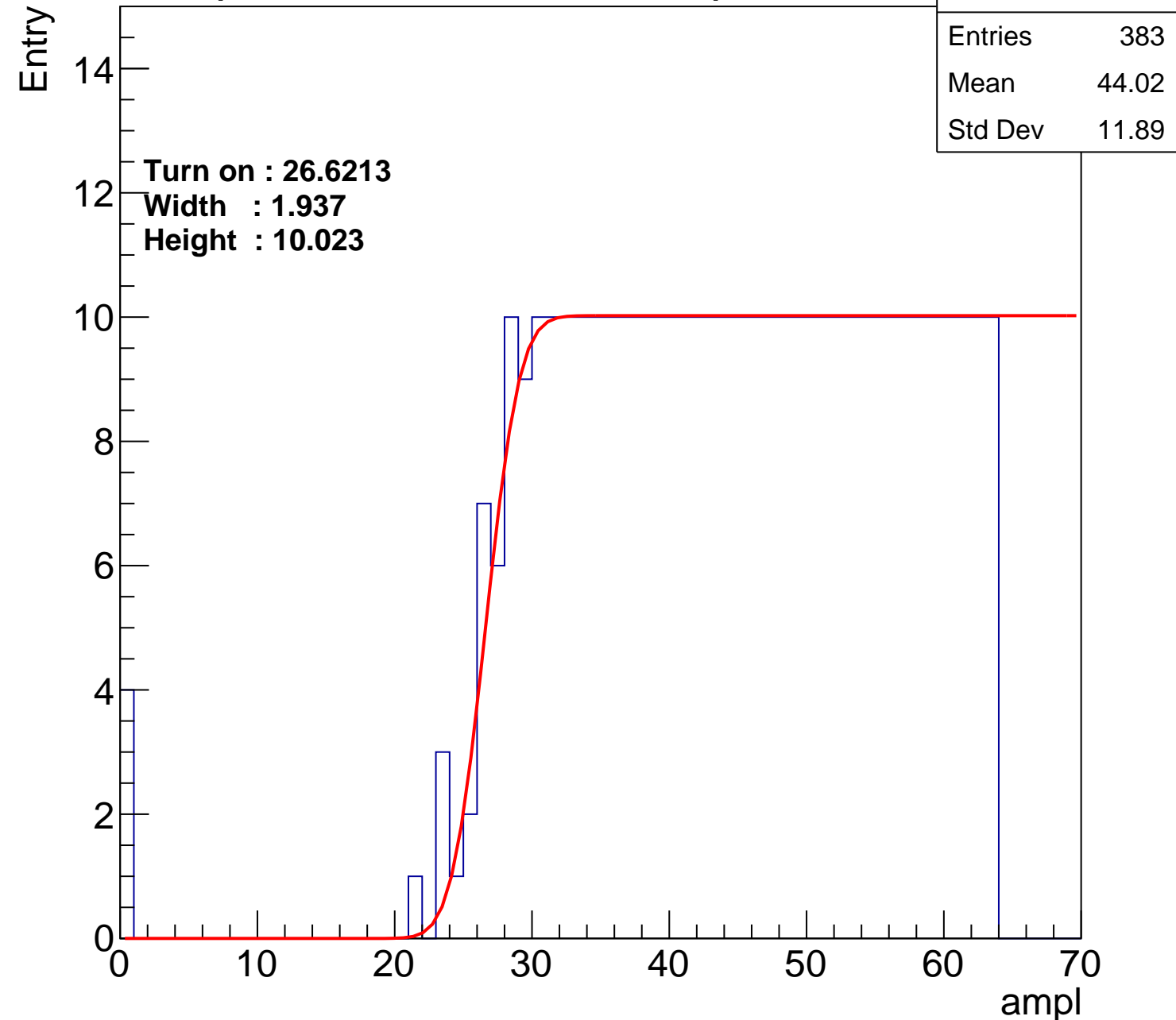
Width : 1.937

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.44
Std Dev	11.4

Turn on : 26.8211

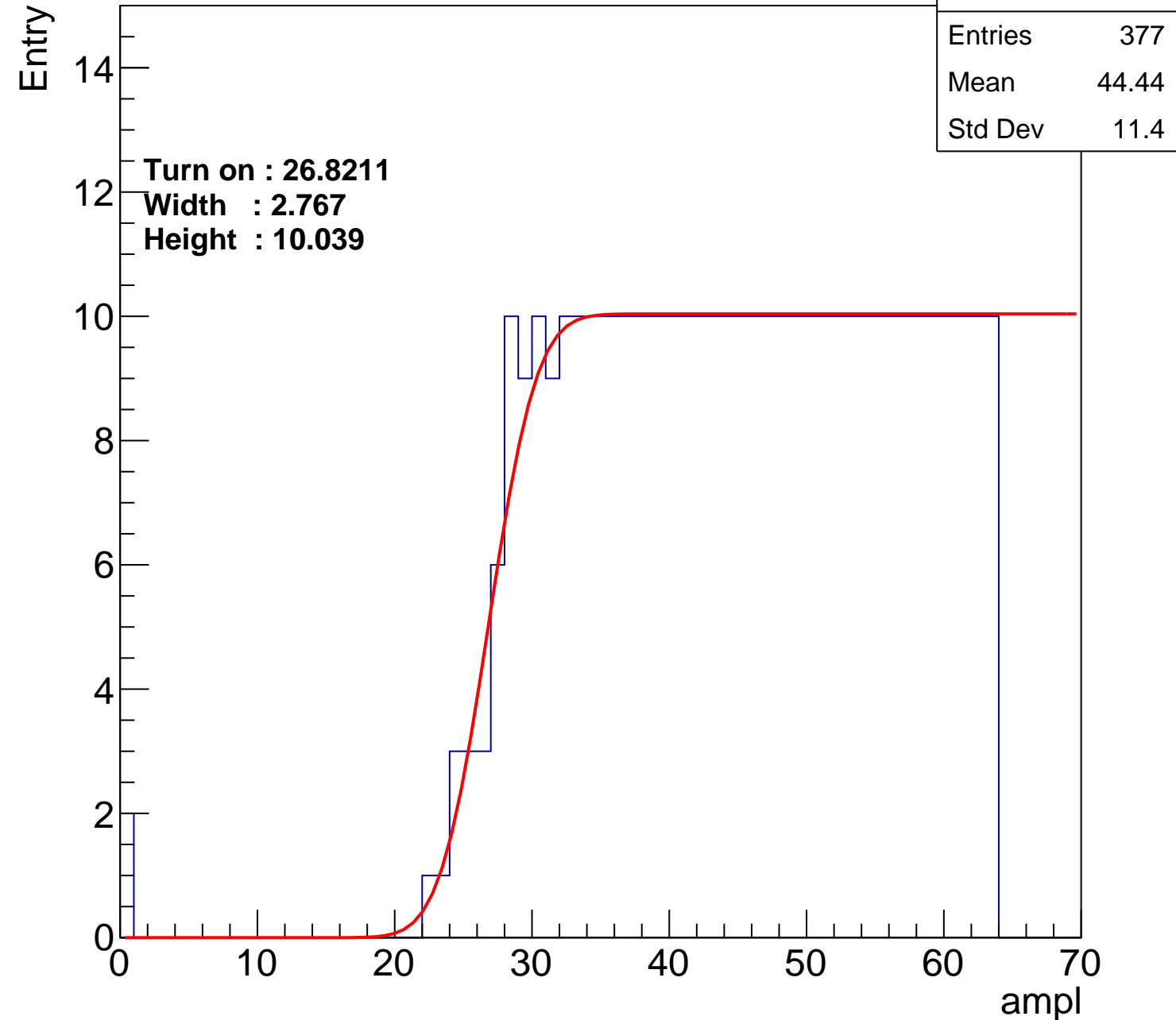
Width : 2.767

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.19
Std Dev	11.69

Turn on : 26.0260

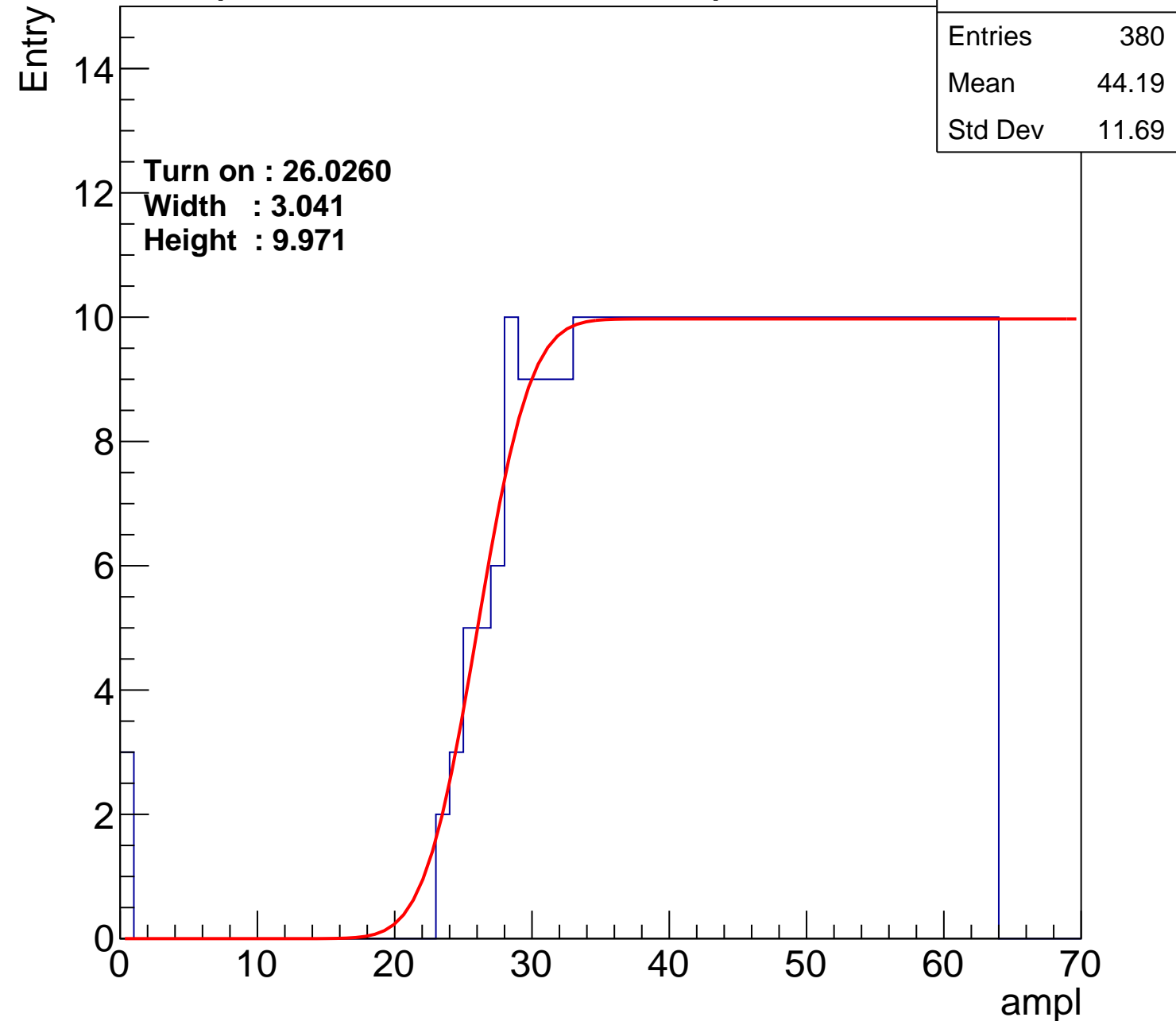
Width : 3.041

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch12

calib_packv5_042523_0143.root, FC#12, port B1

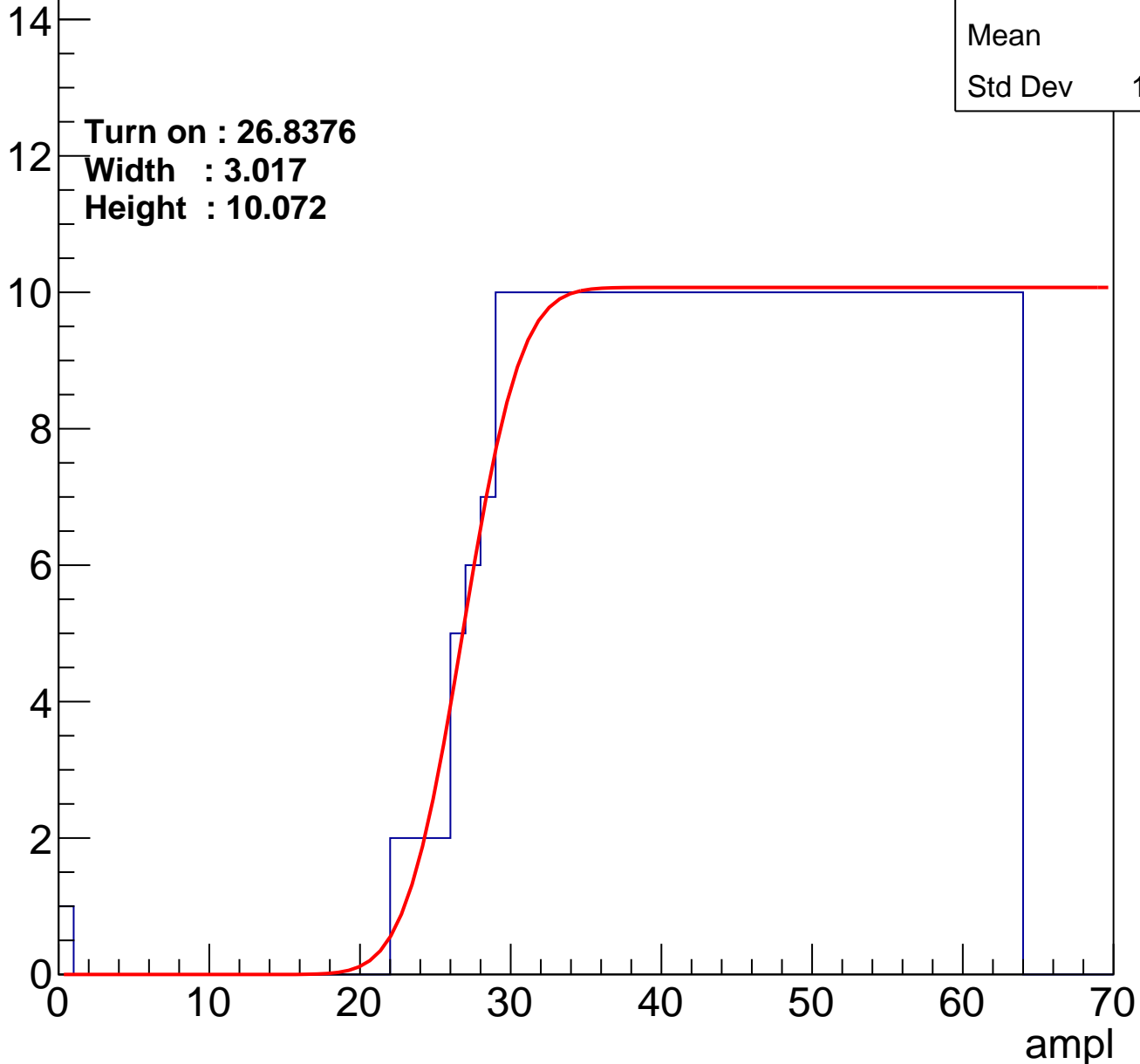
Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.8376

Width : 3.017

Height : 10.072

Entry



B0L102S, U17-ch13

calib_packv5_042523_0143.root, FC#12, port B1

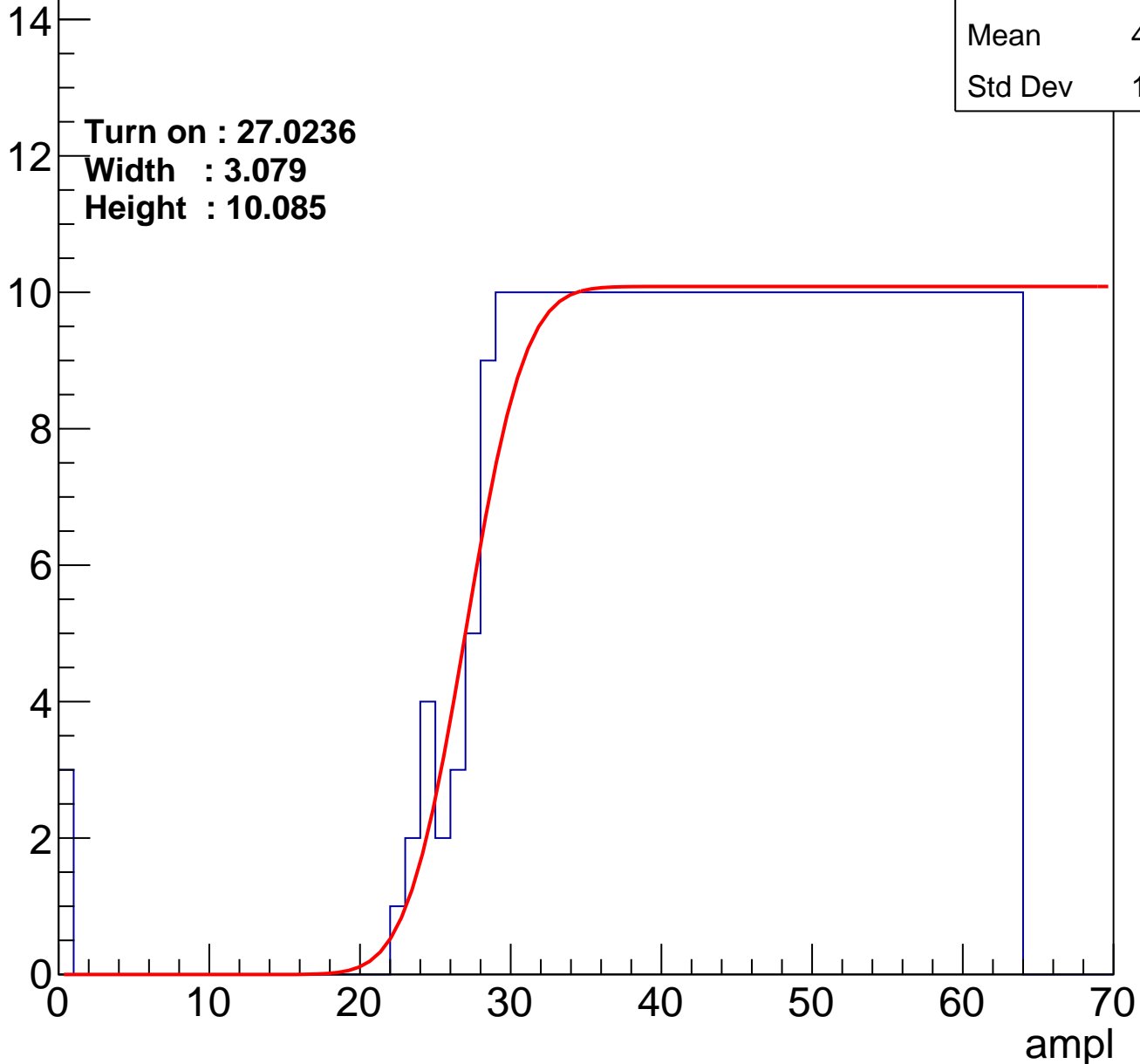
Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 27.0236

Width : 3.079

Height : 10.085

Entry



B0L102S, U17-ch14

calib_packv5_042523_0143.root, FC#12, port B1

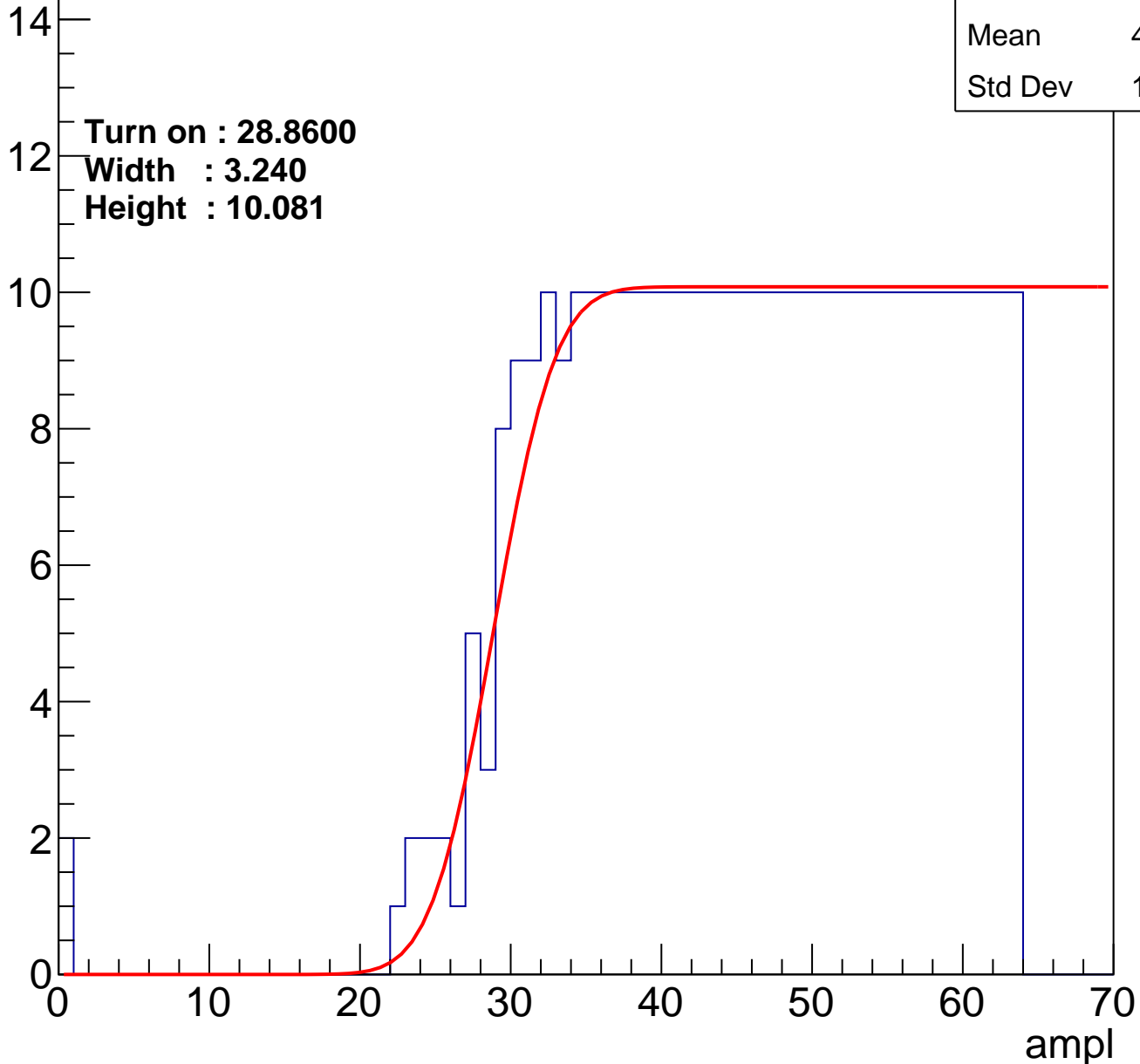
Entries	363
Mean	45.07
Std Dev	11.14

Turn on : 28.8600

Width : 3.240

Height : 10.081

Entry



B0L102S, U17-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.39
Std Dev	11.74

Turn on : 27.0641

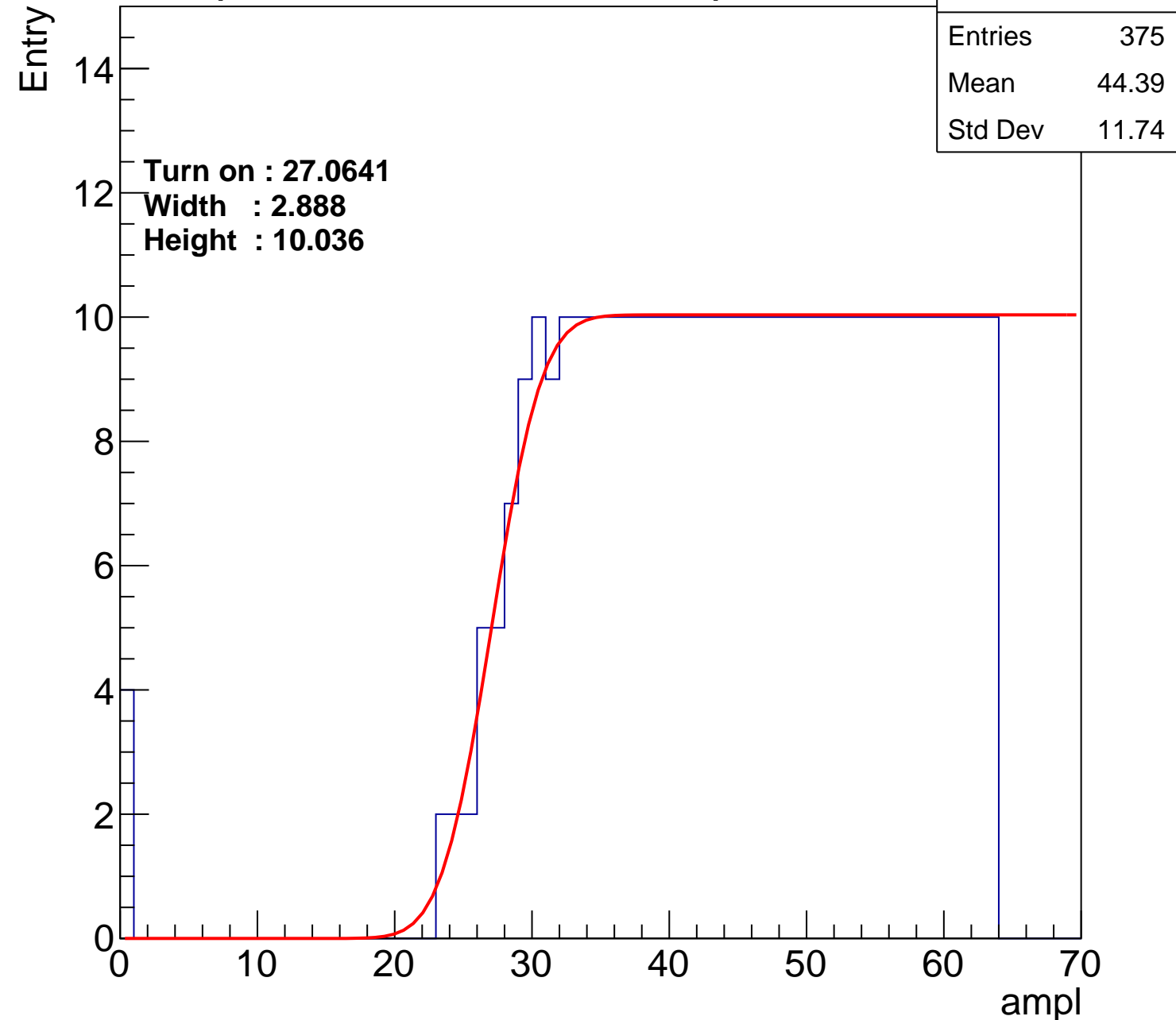
Width : 2.888

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch16

calib_packv5_042523_0143.root, FC#12, port B1

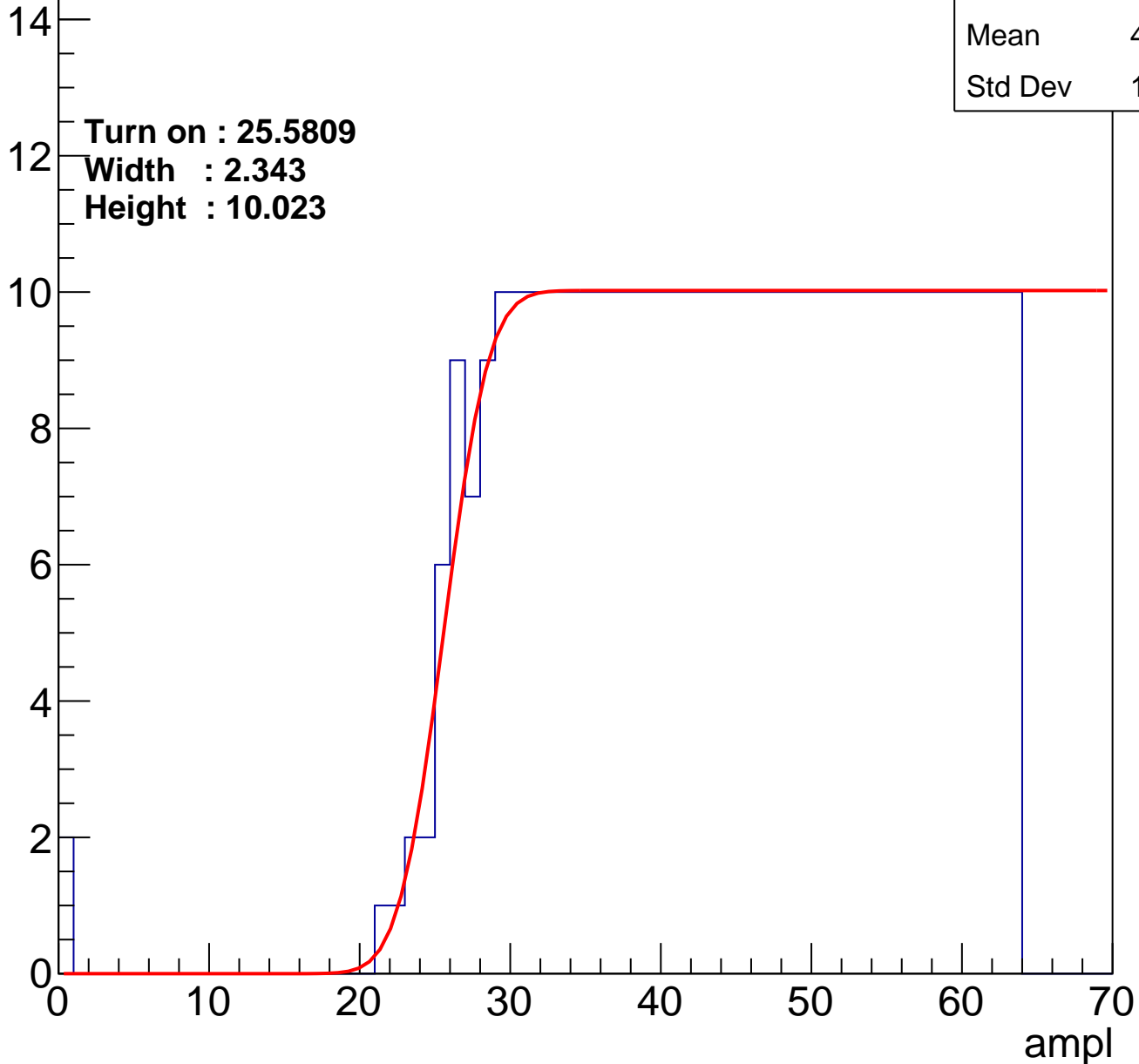
Entries	389
Mean	43.86
Std Dev	11.68

Turn on : 25.5809

Width : 2.343

Height : 10.023

Entry



B0L102S, U17-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.77
Std Dev	11.43

Turn on : 27.8404

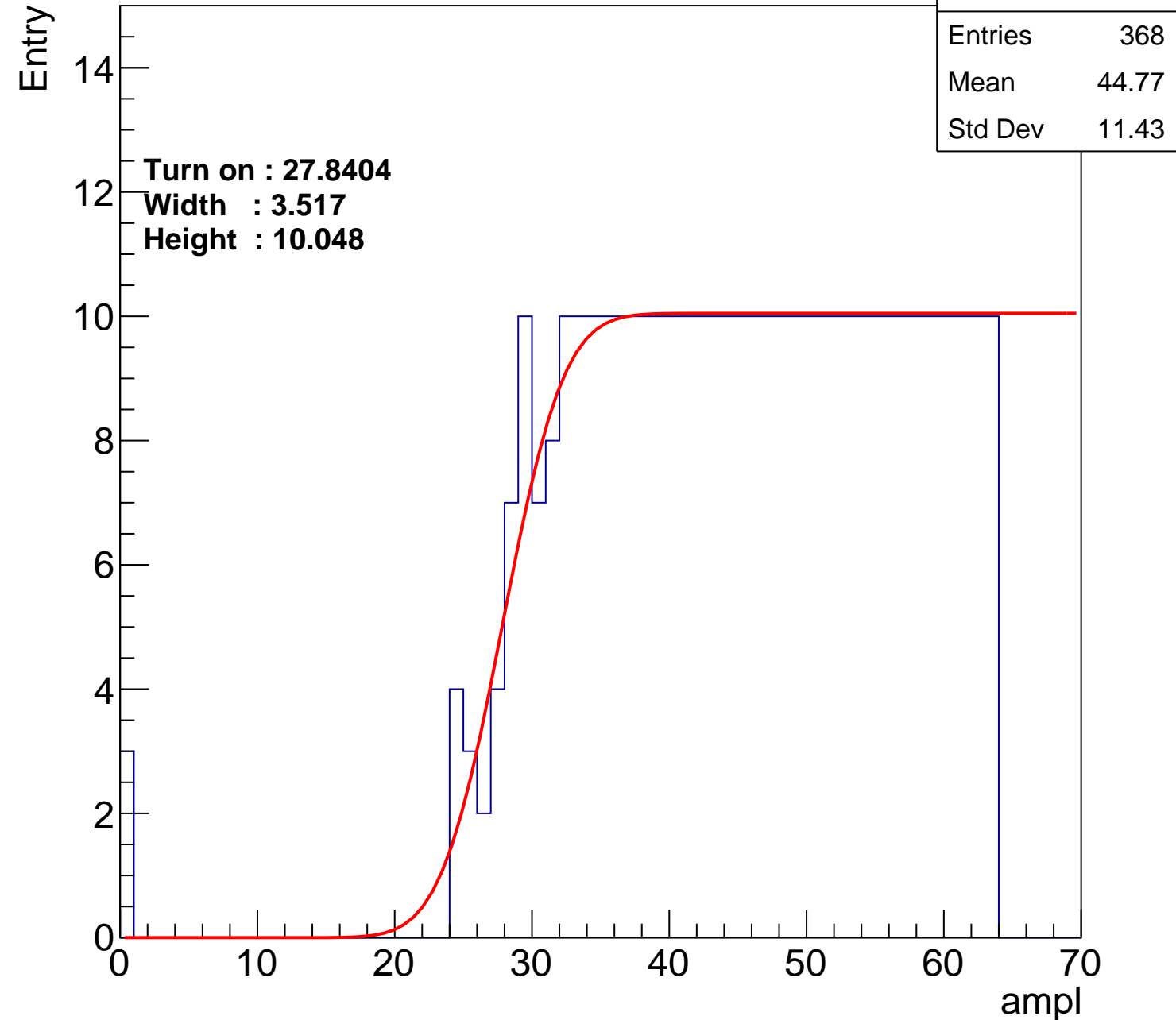
Width : 3.517

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.85
Std Dev	11.81

Turn on : 25.1929

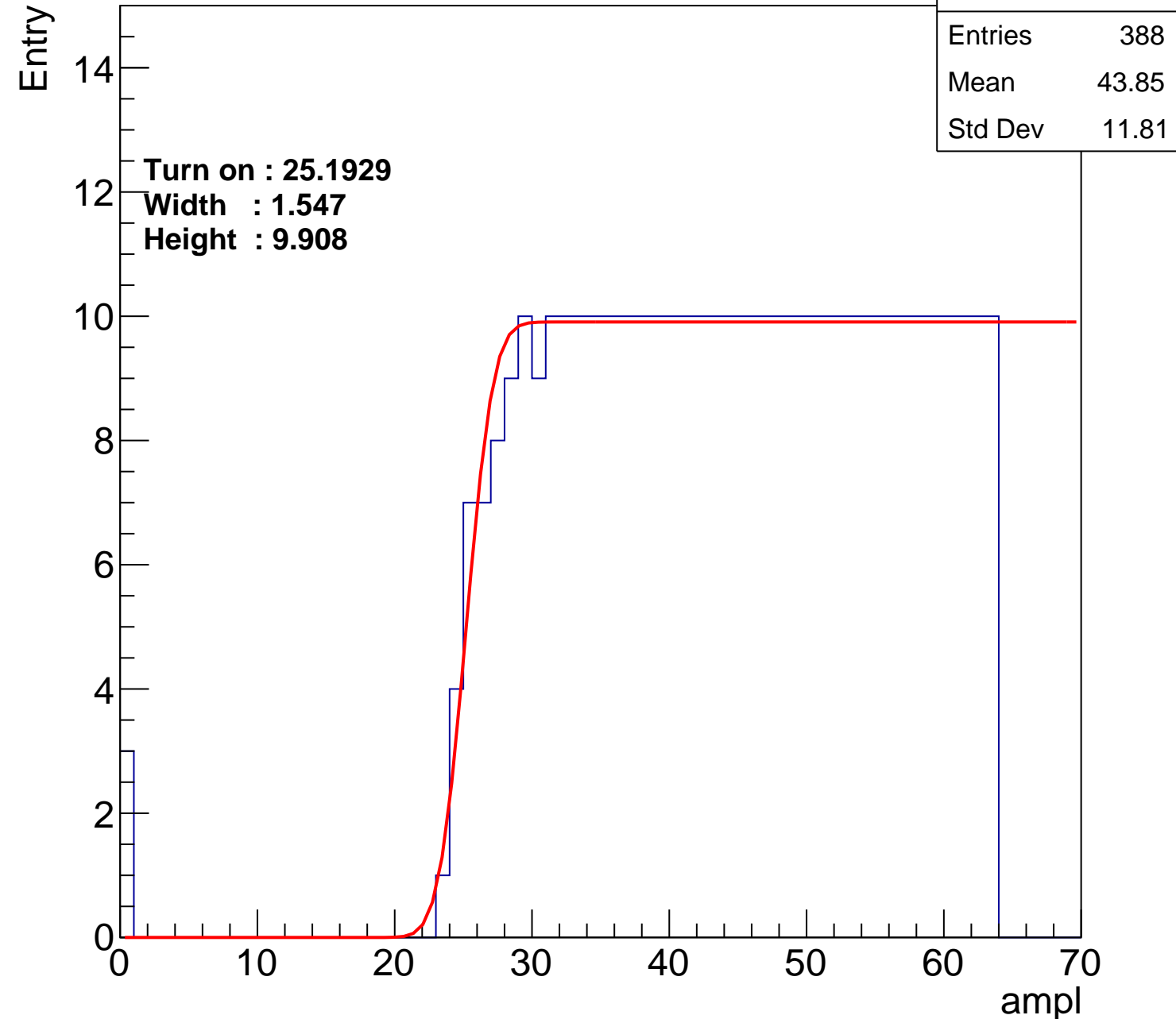
Width : 1.547

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch19

calib_packv5_042523_0143.root, FC#12, port B1

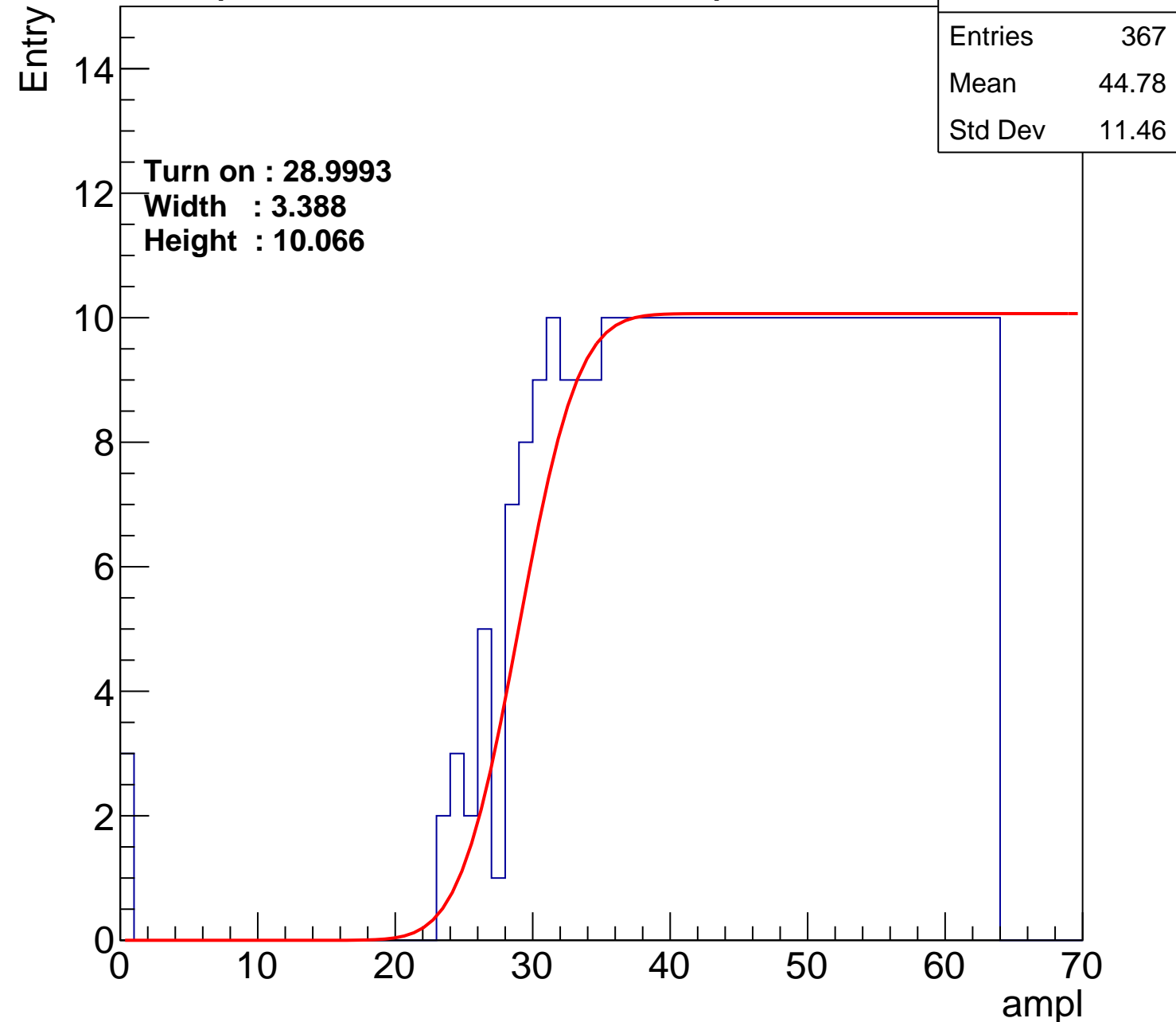
Entry

14
12
10
8
6
4
2
0

Turn on : 28.9993
Width : 3.388
Height : 10.066

Entries	367
Mean	44.78
Std Dev	11.46

ampl



B0L102S, U17-ch20

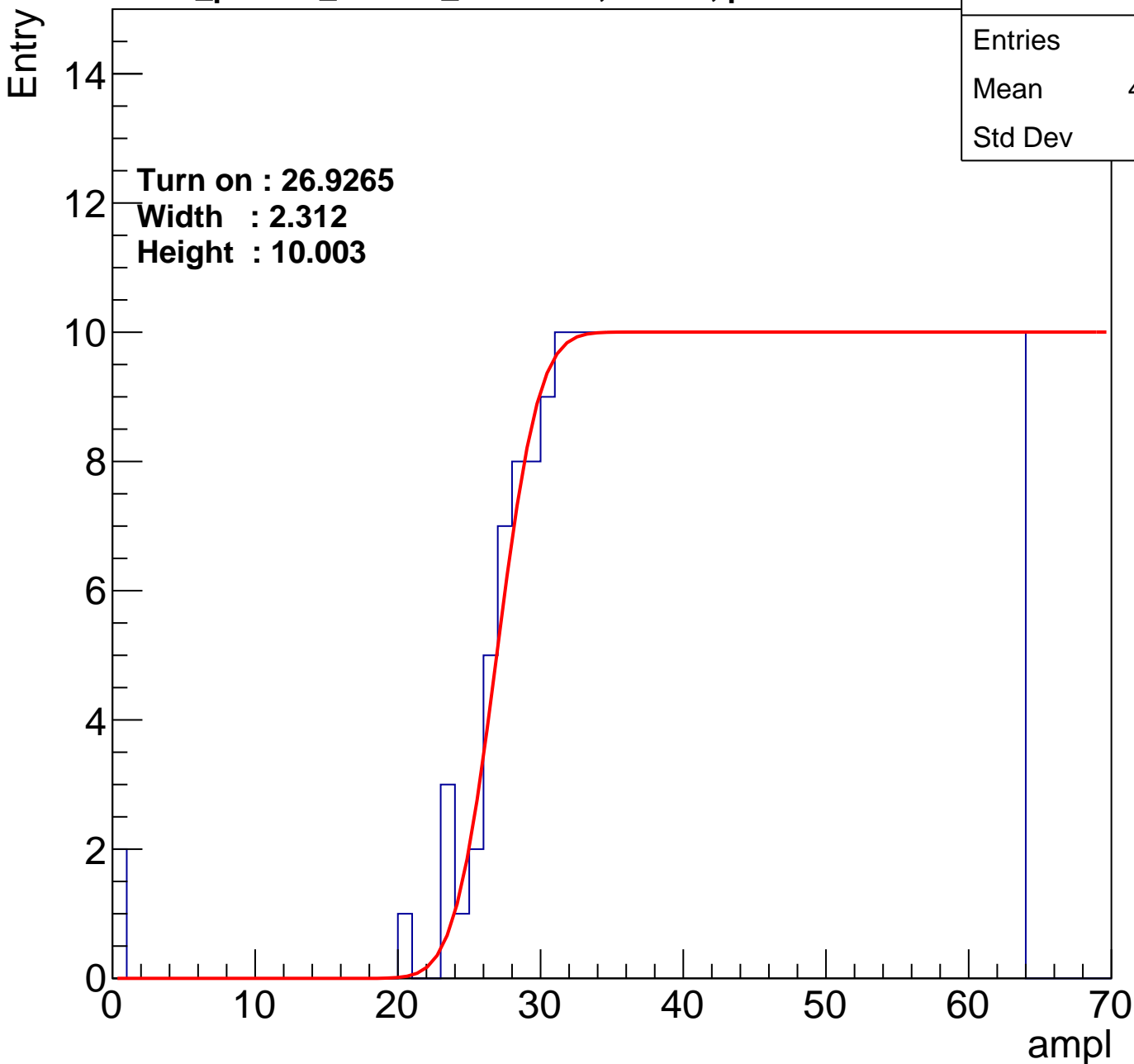
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.9265

Width : 2.312

Height : 10.003

Entries	376
Mean	44.46
Std Dev	11.41



B0L102S, U17-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 26.9294

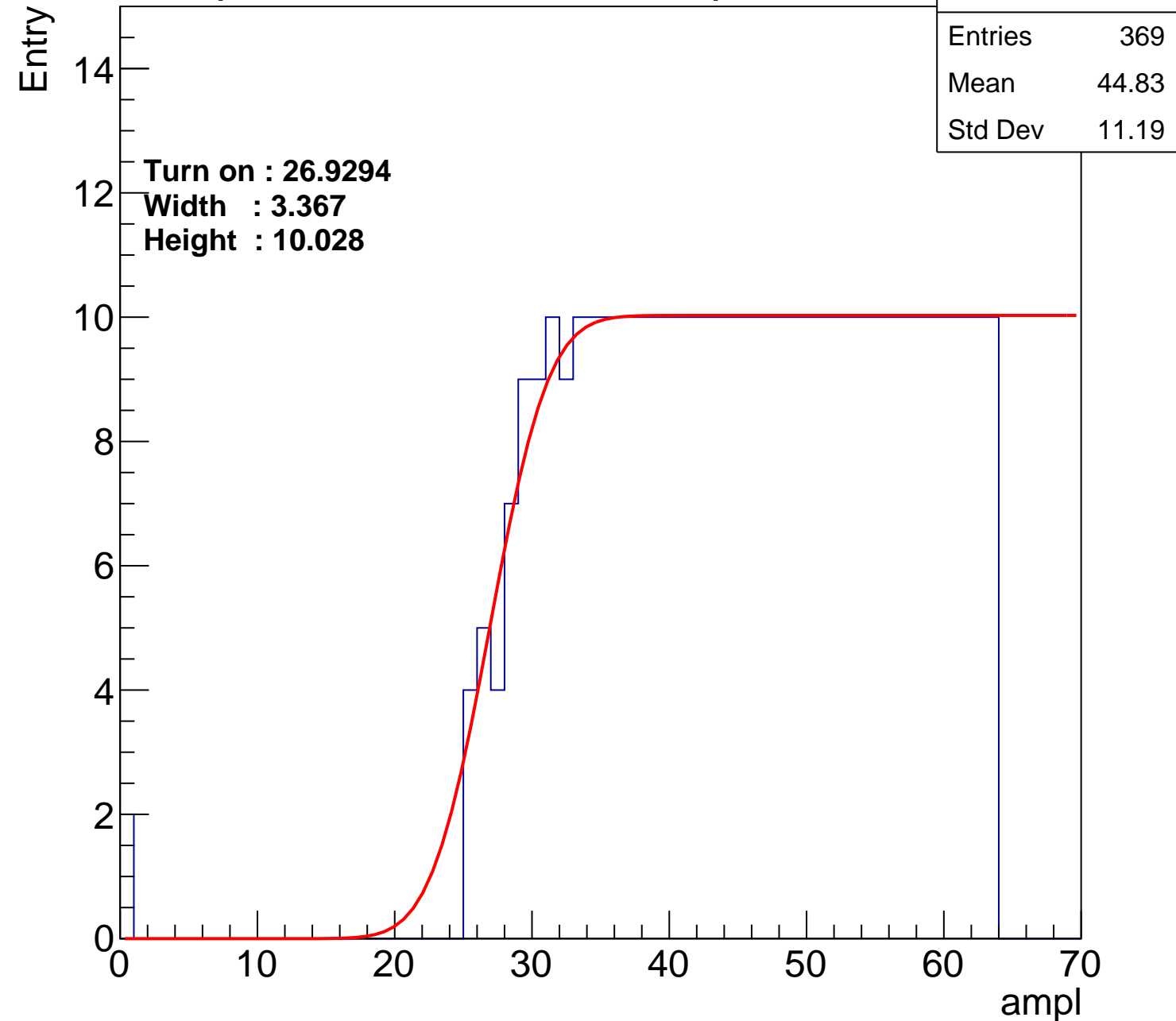
Width : 3.367

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.55
Std Dev	12.24

Turn on : 25.4858

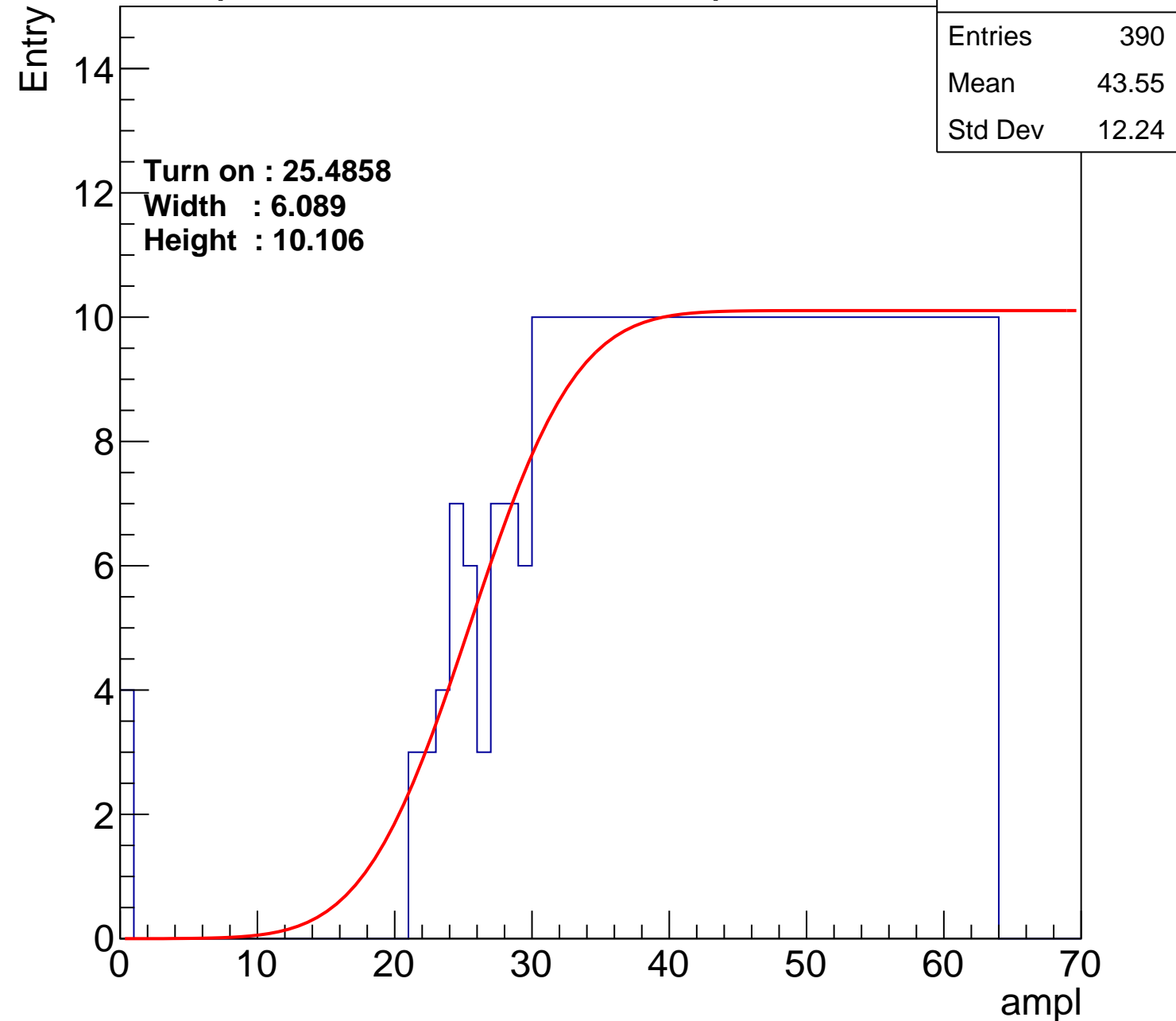
Width : 6.089

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.62
Std Dev	11.37

Turn on : 27.1902

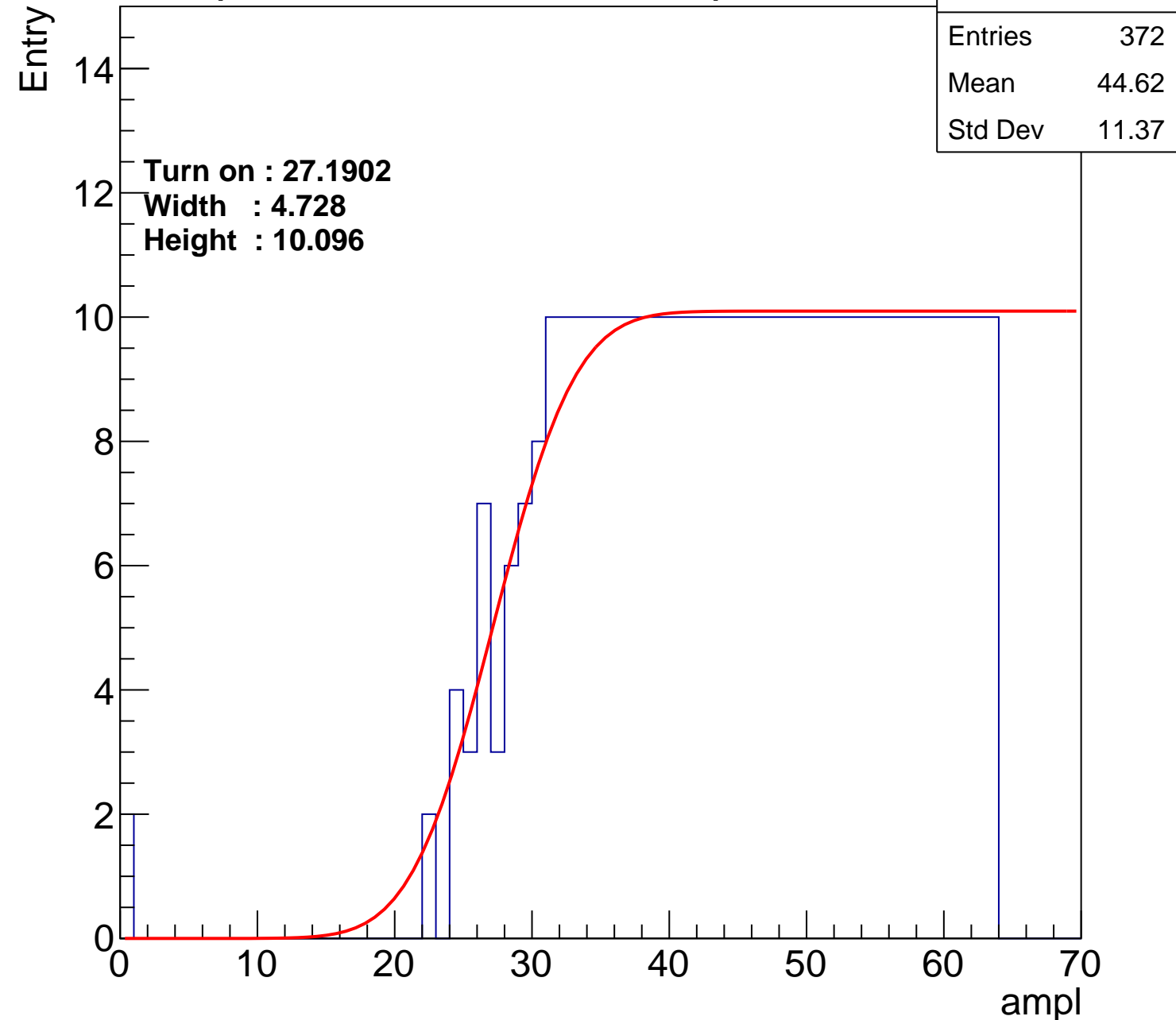
Width : 4.728

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 26.0005

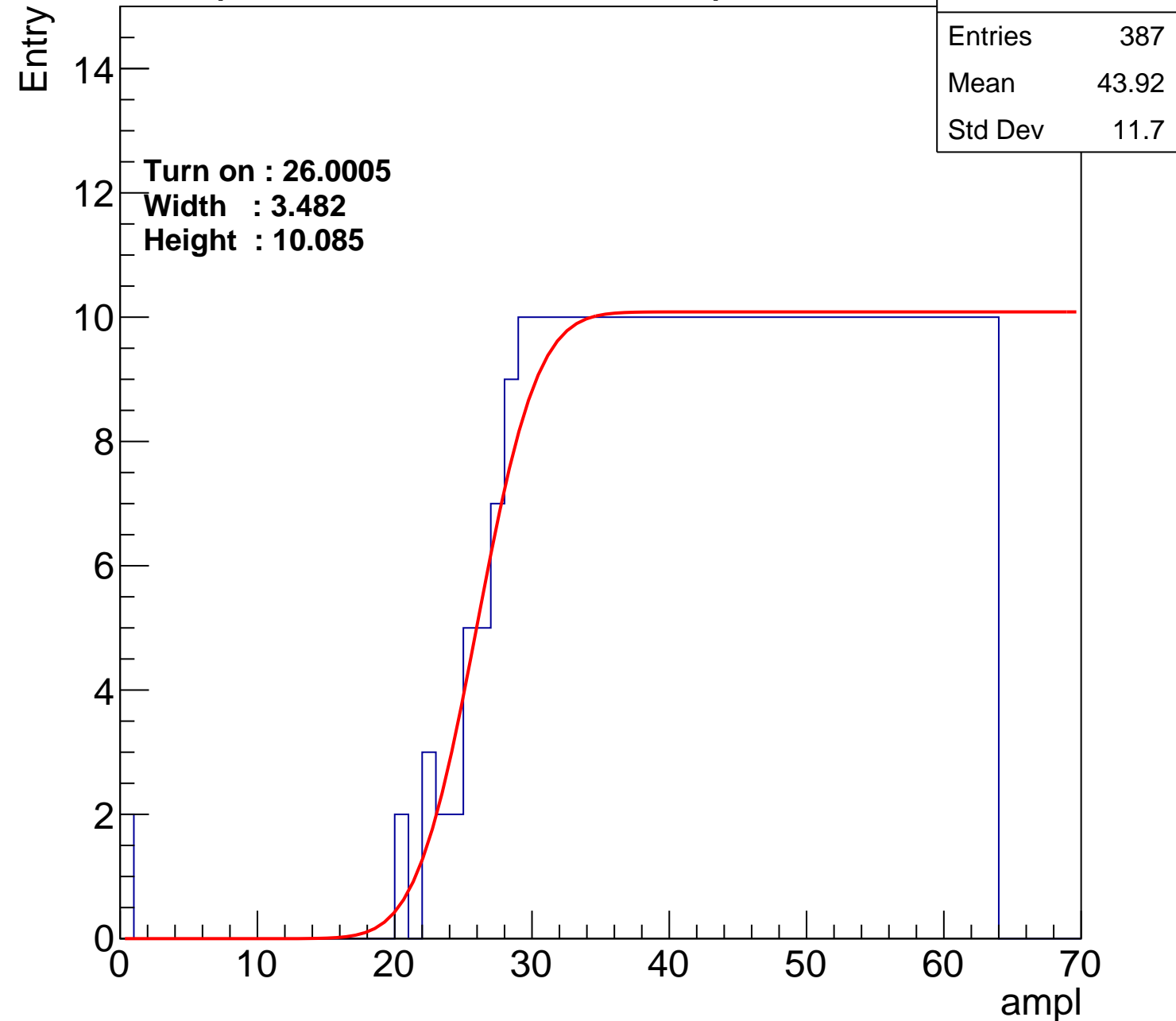
Width : 3.482

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.4
Std Dev	12.33

Turn on : 25.3834

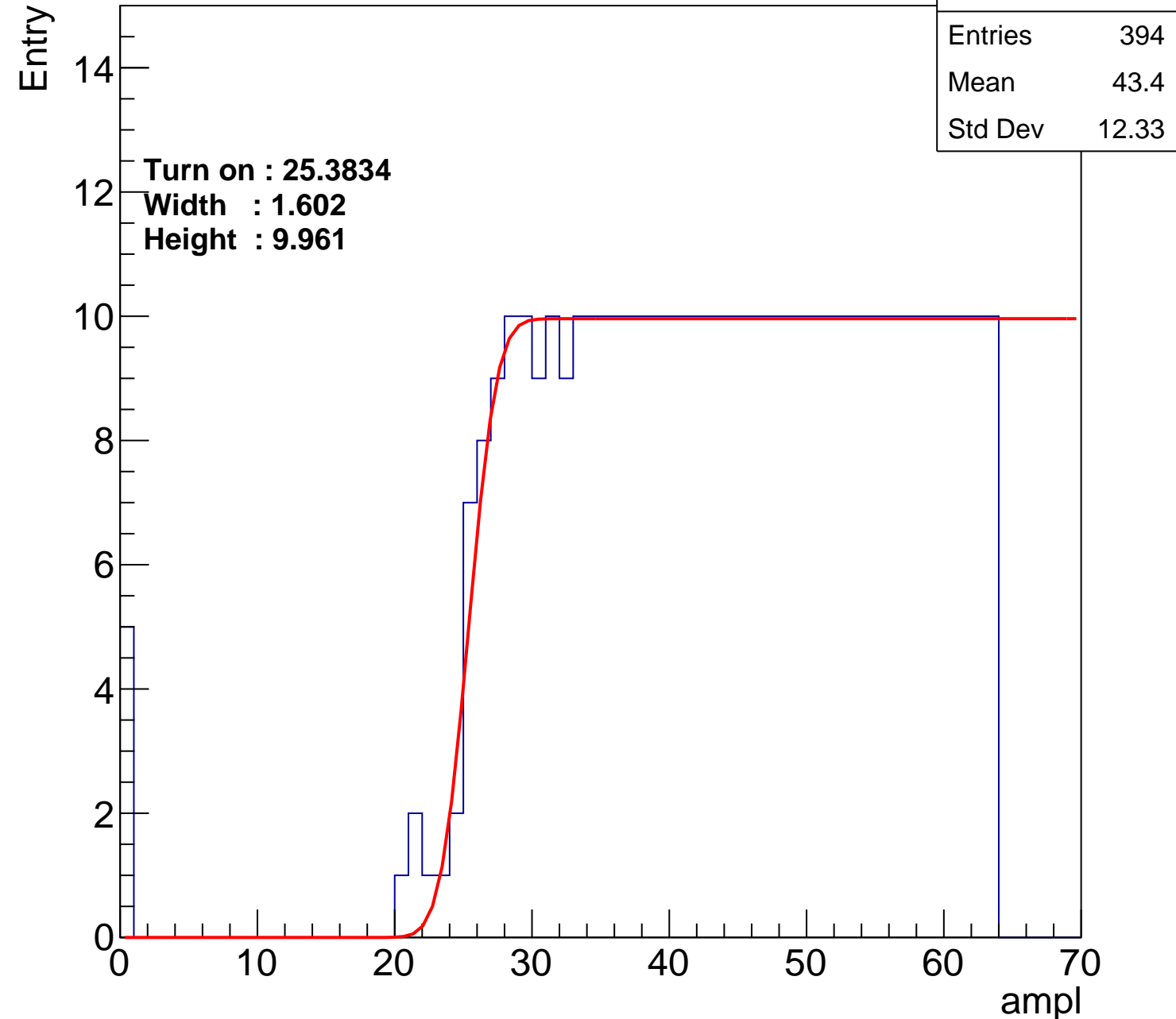
Width : 1.602

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.37
Std Dev	12.12

Turn on : 25.0140

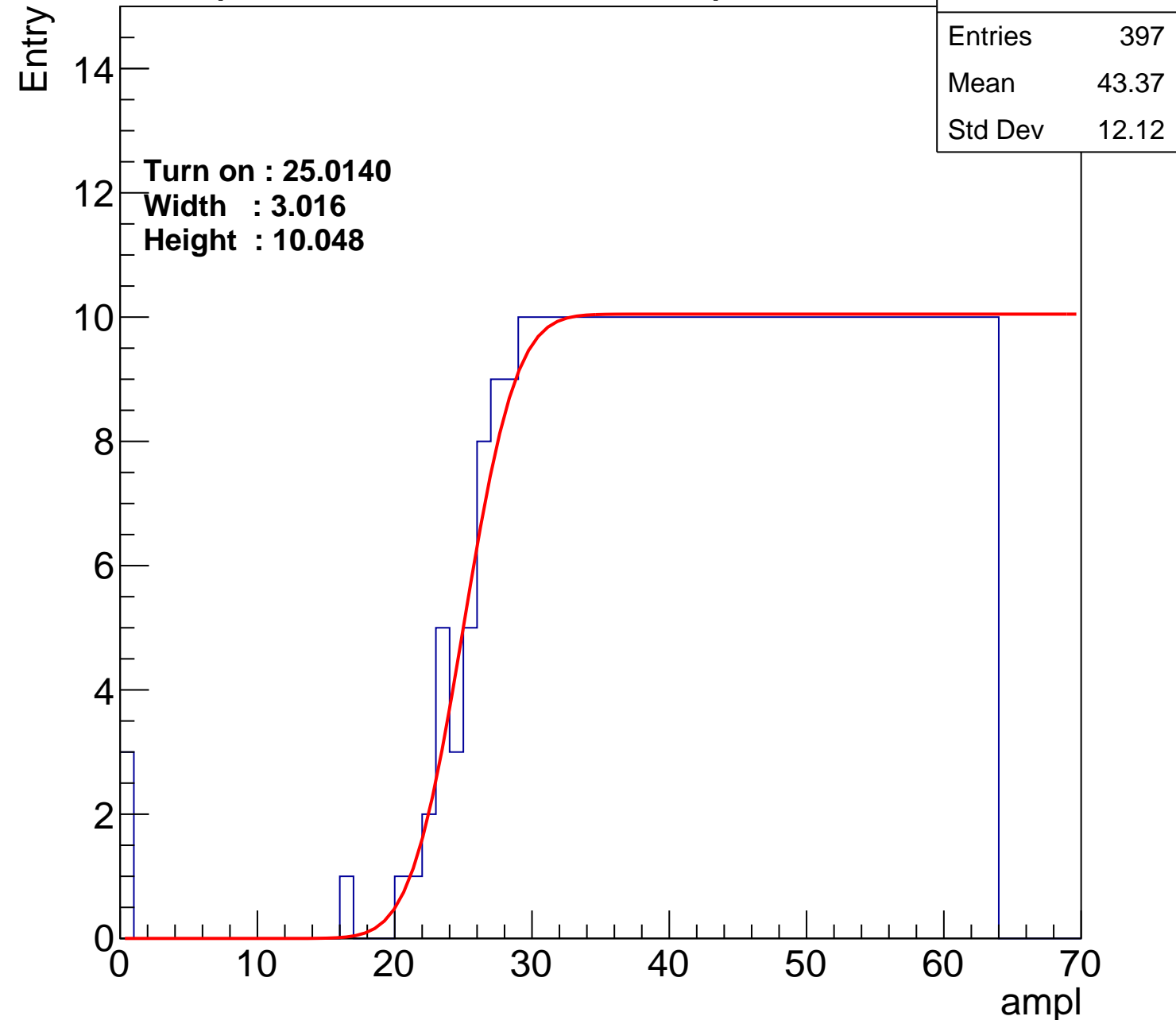
Width : 3.016

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.04
Std Dev	11.6

Turn on : 25.3812

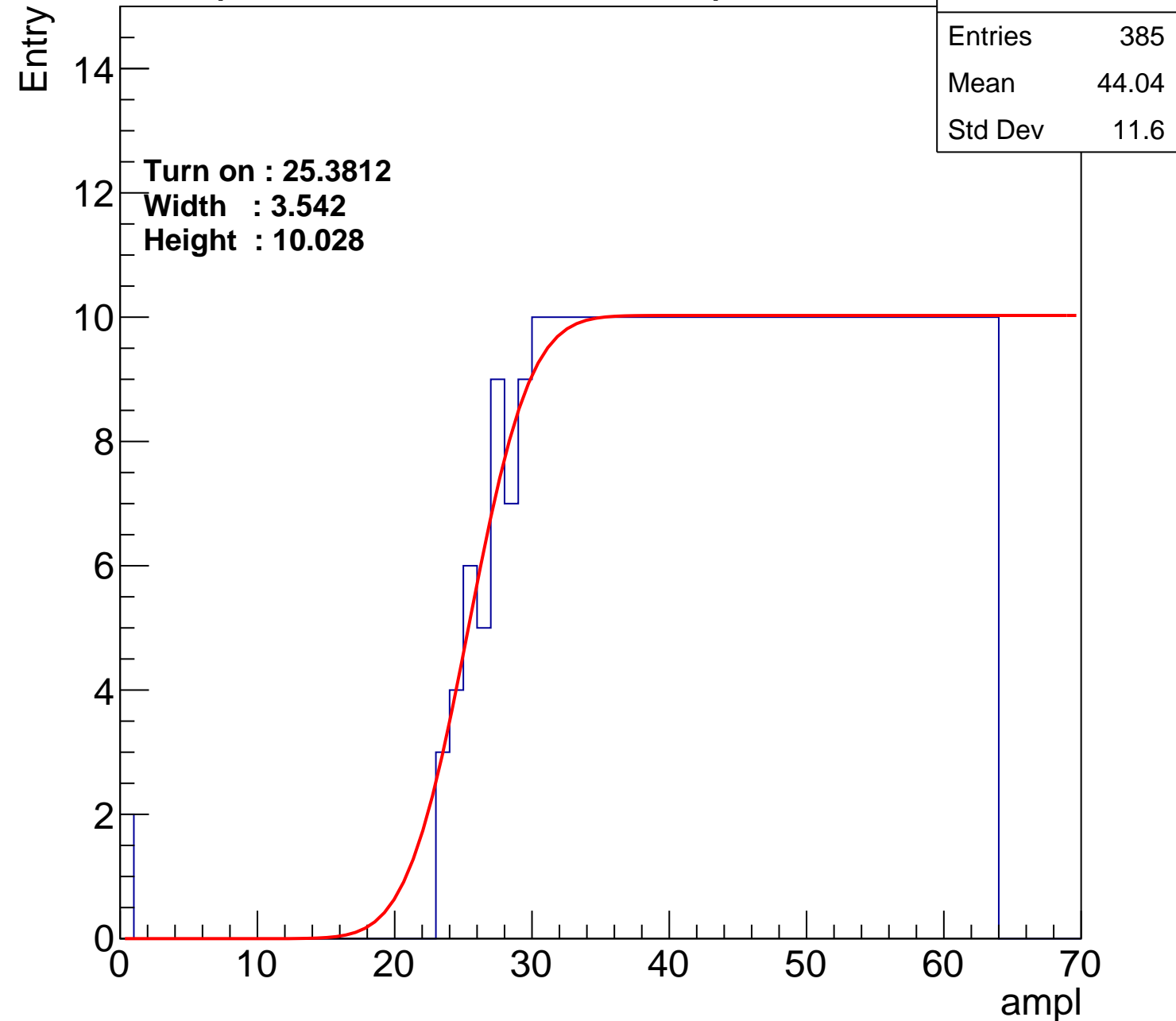
Width : 3.542

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch28

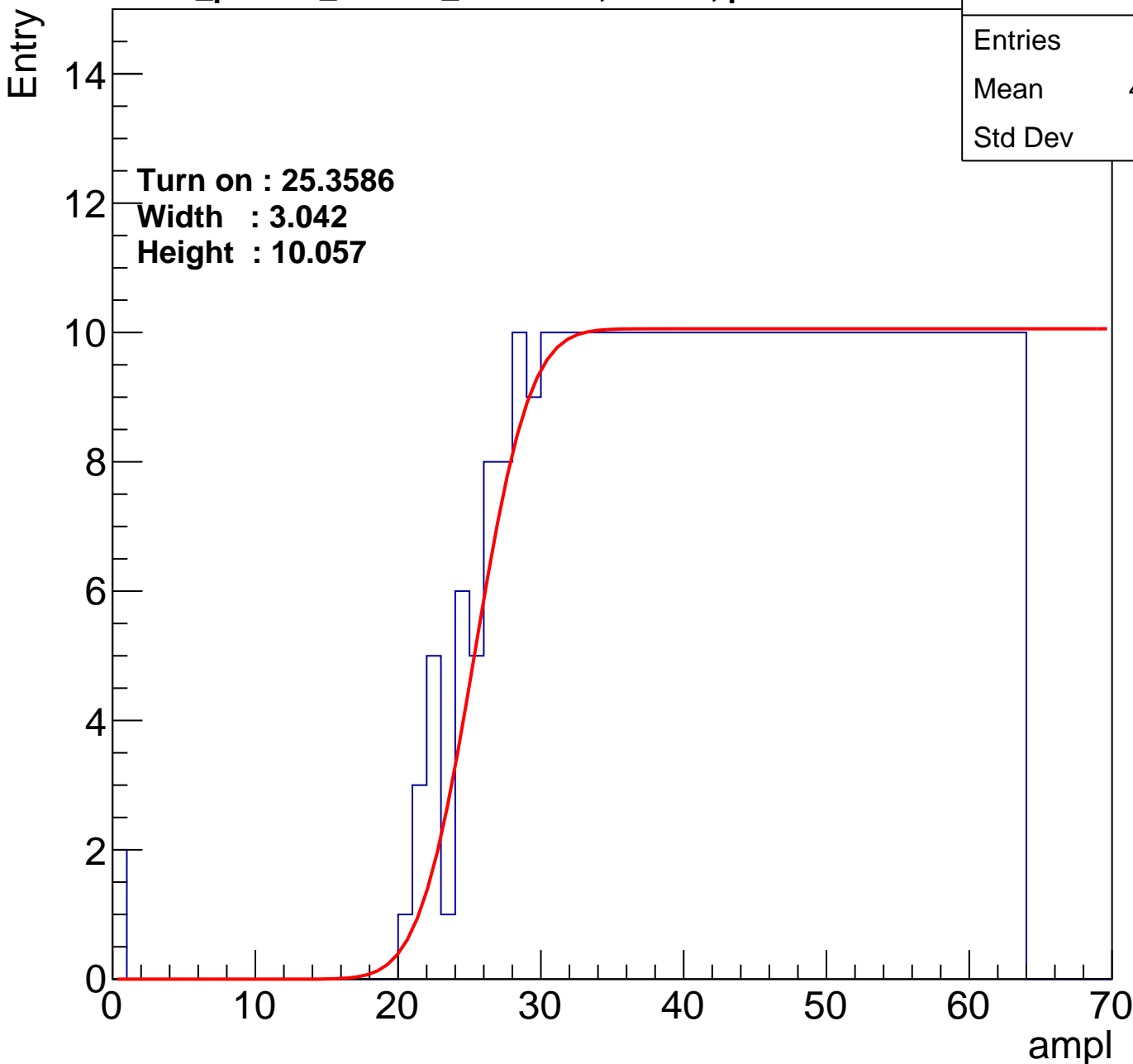
calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.37
Std Dev	12

Turn on : 25.3586

Width : 3.042

Height : 10.057



B0L102S, U17-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.84
Std Dev	11.51

Turn on : 27.6765

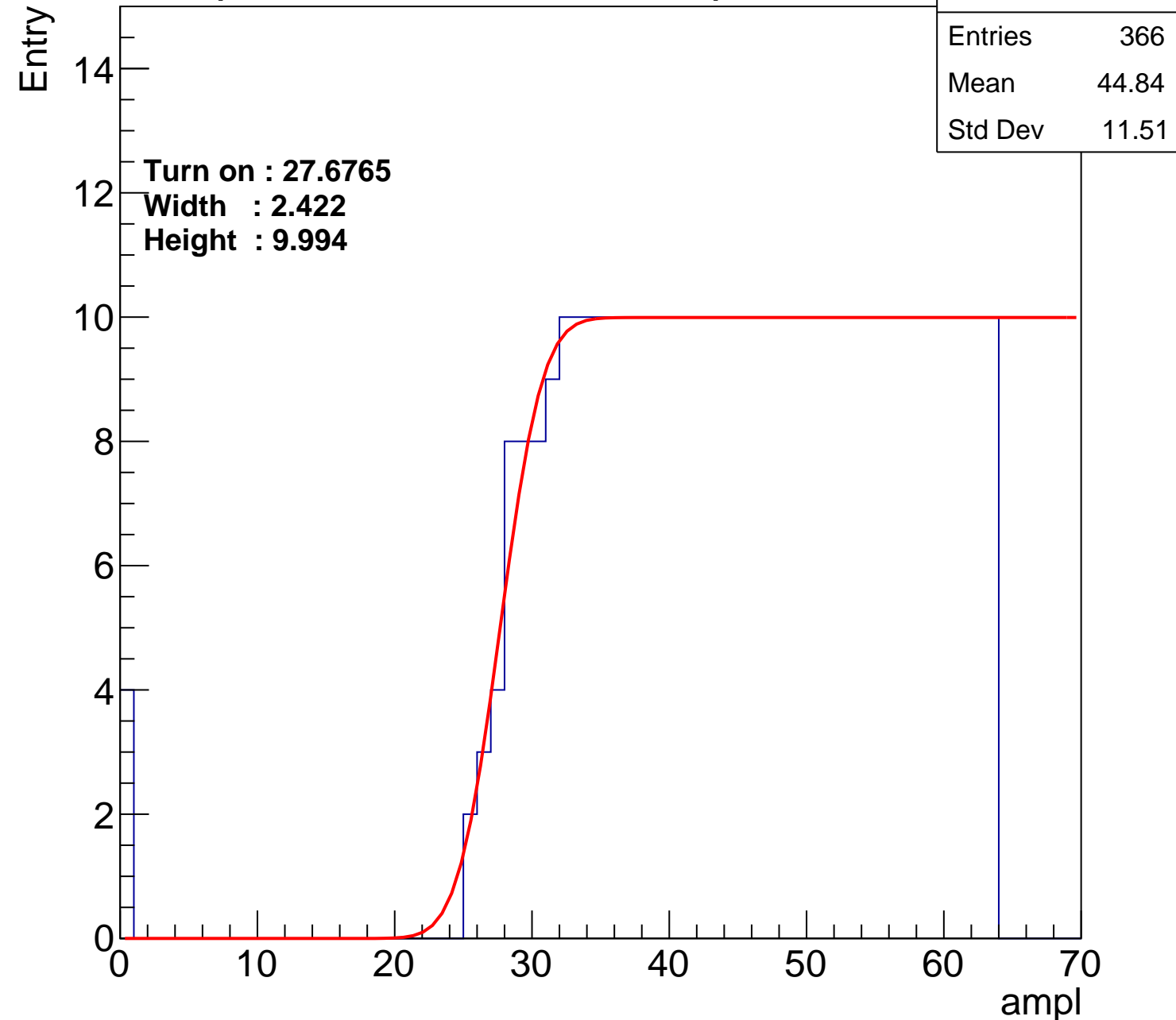
Width : 2.422

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.4
Std Dev	12.17

Turn on : 23.9934

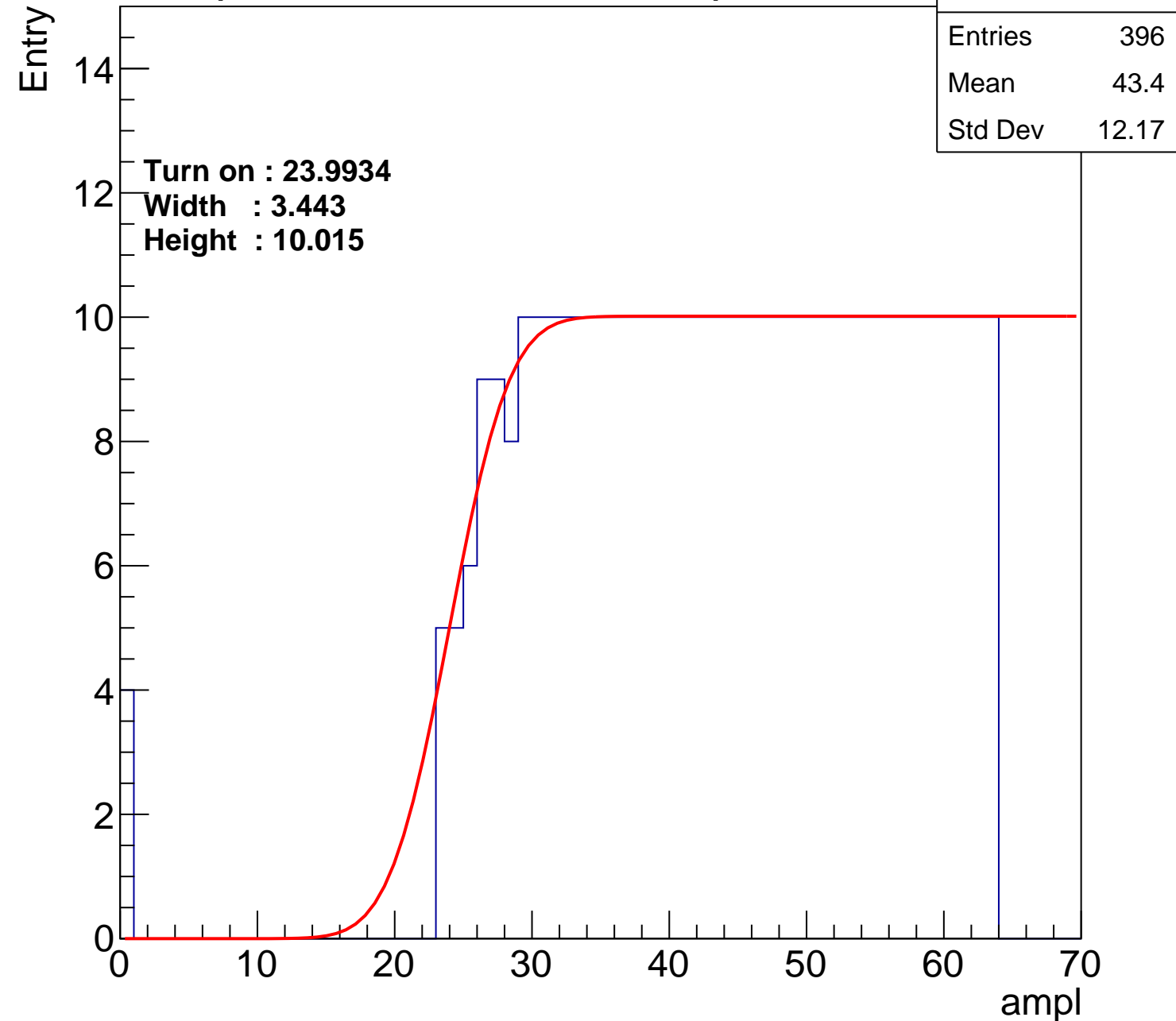
Width : 3.443

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.48
Std Dev	11.27

Turn on : 26.7780

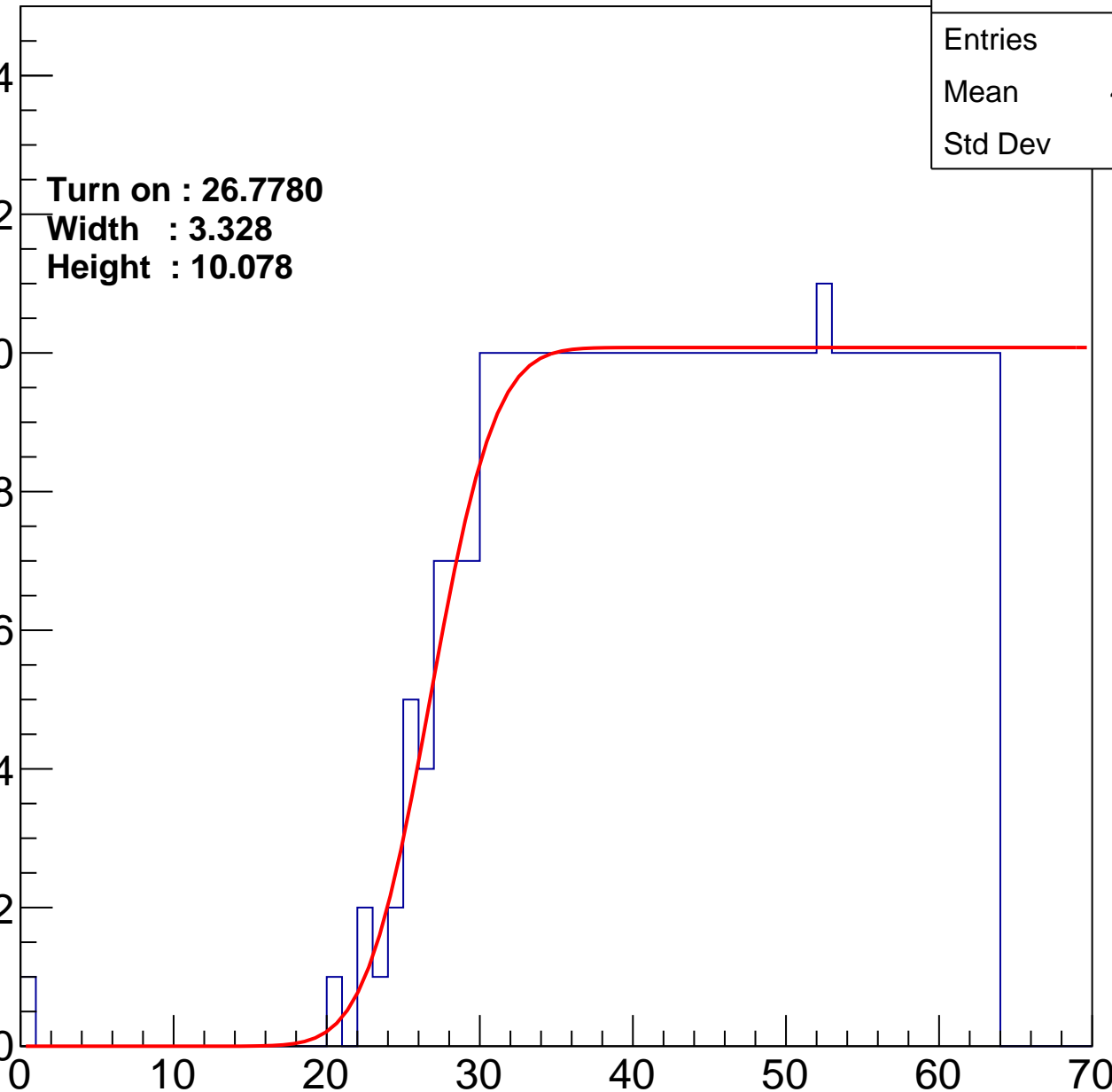
Width : 3.328

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.8000

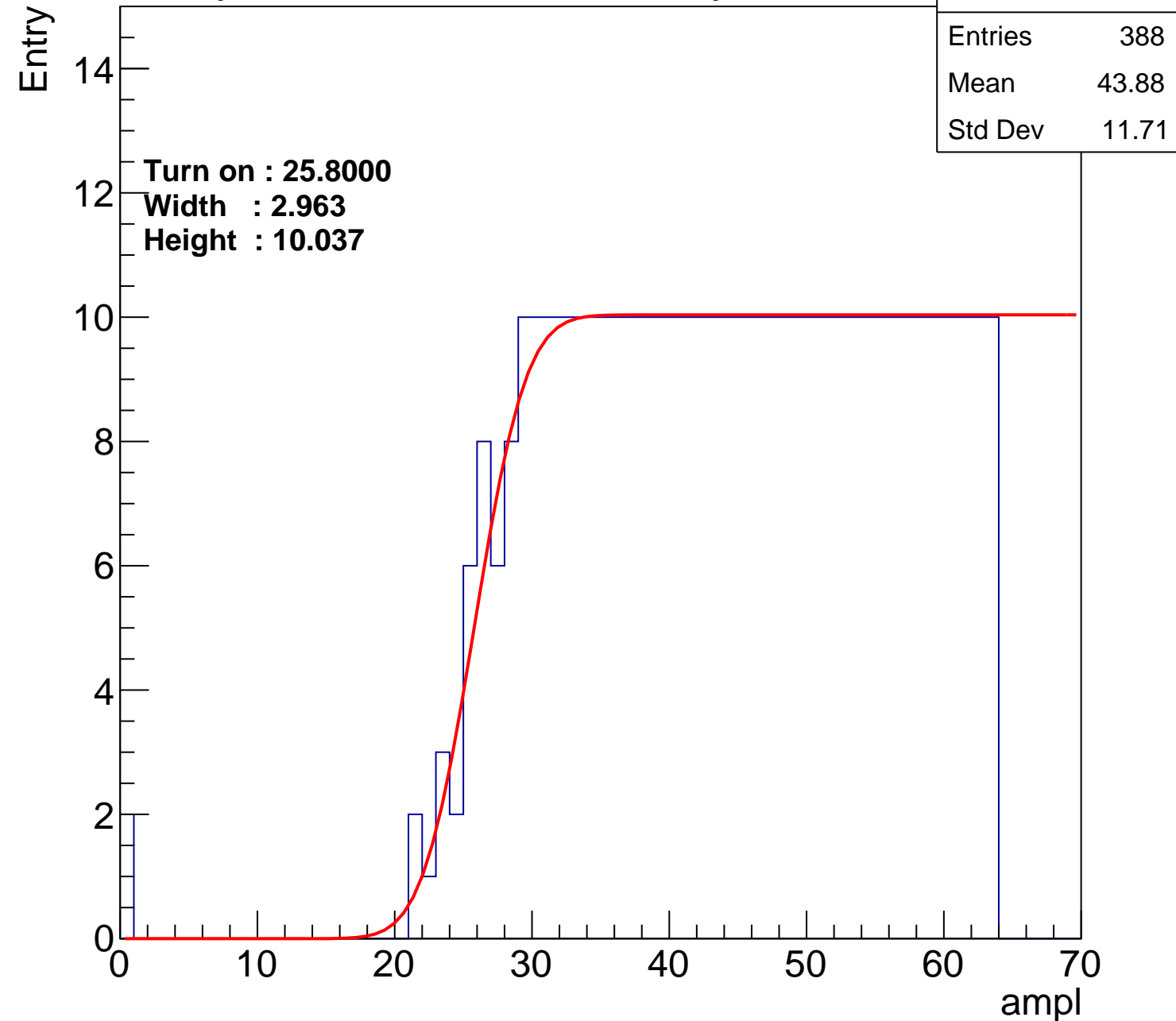
Width : 2.963

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch33

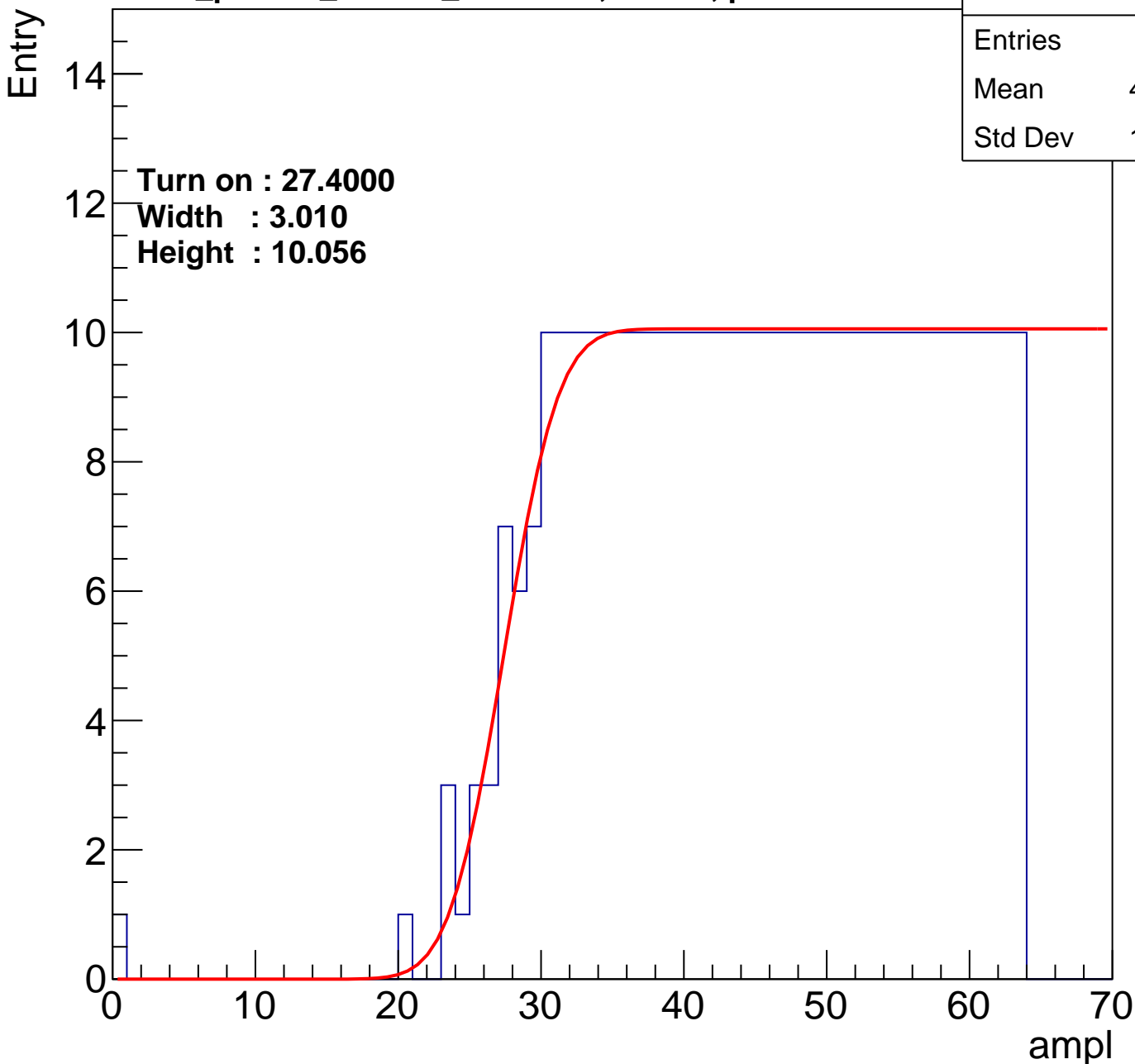
calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 27.4000

Width : 3.010

Height : 10.056



B0L102S, U17-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.08
Std Dev	11.89

Turn on : 26.4546

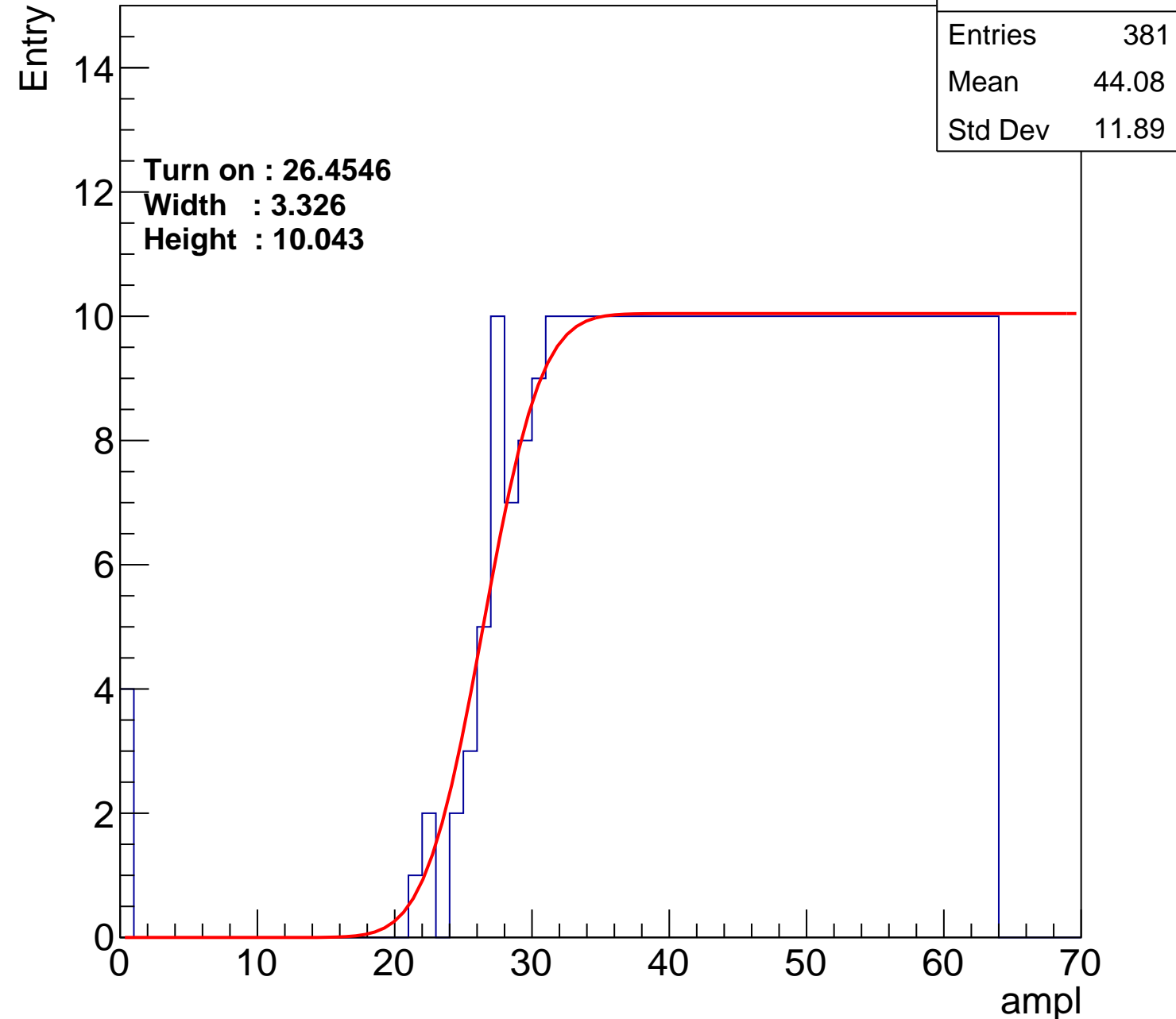
Width : 3.326

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.27
Std Dev	12.38

Turn on : 24.9710

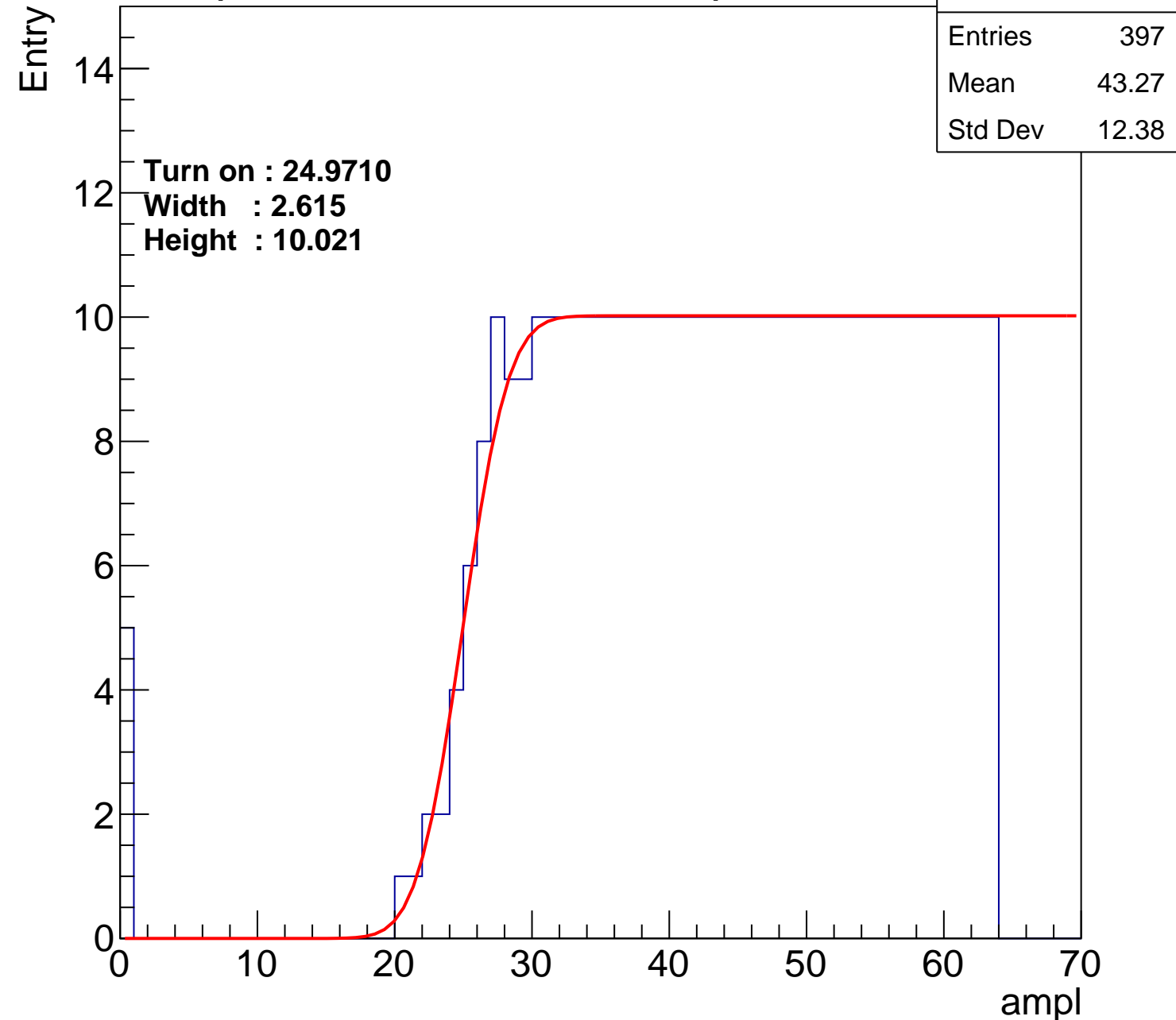
Width : 2.615

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch36

calib_packv5_042523_0143.root, FC#12, port B1

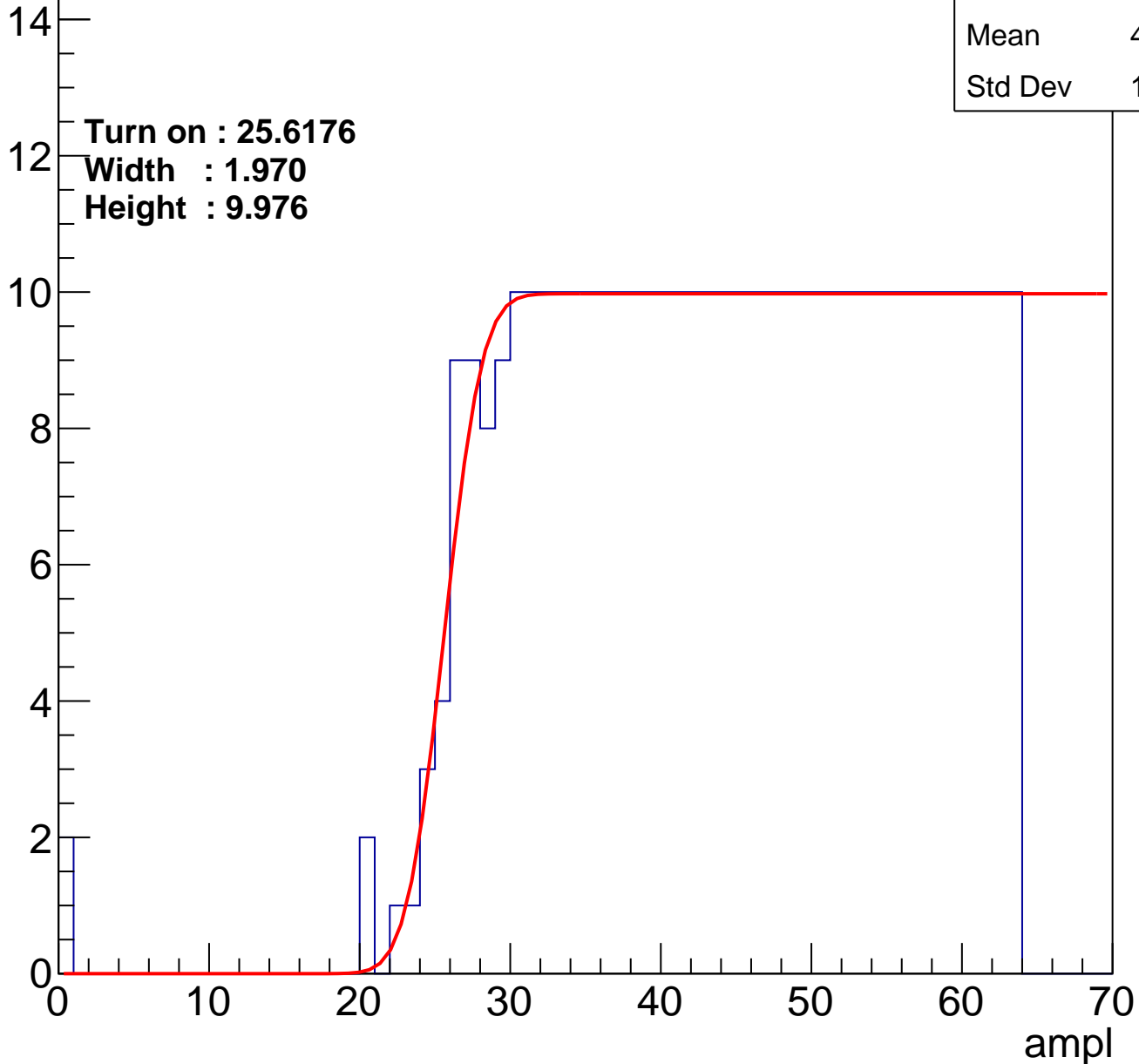
Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.6176

Width : 1.970

Height : 9.976

Entry



B0L102S, U17-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.1374

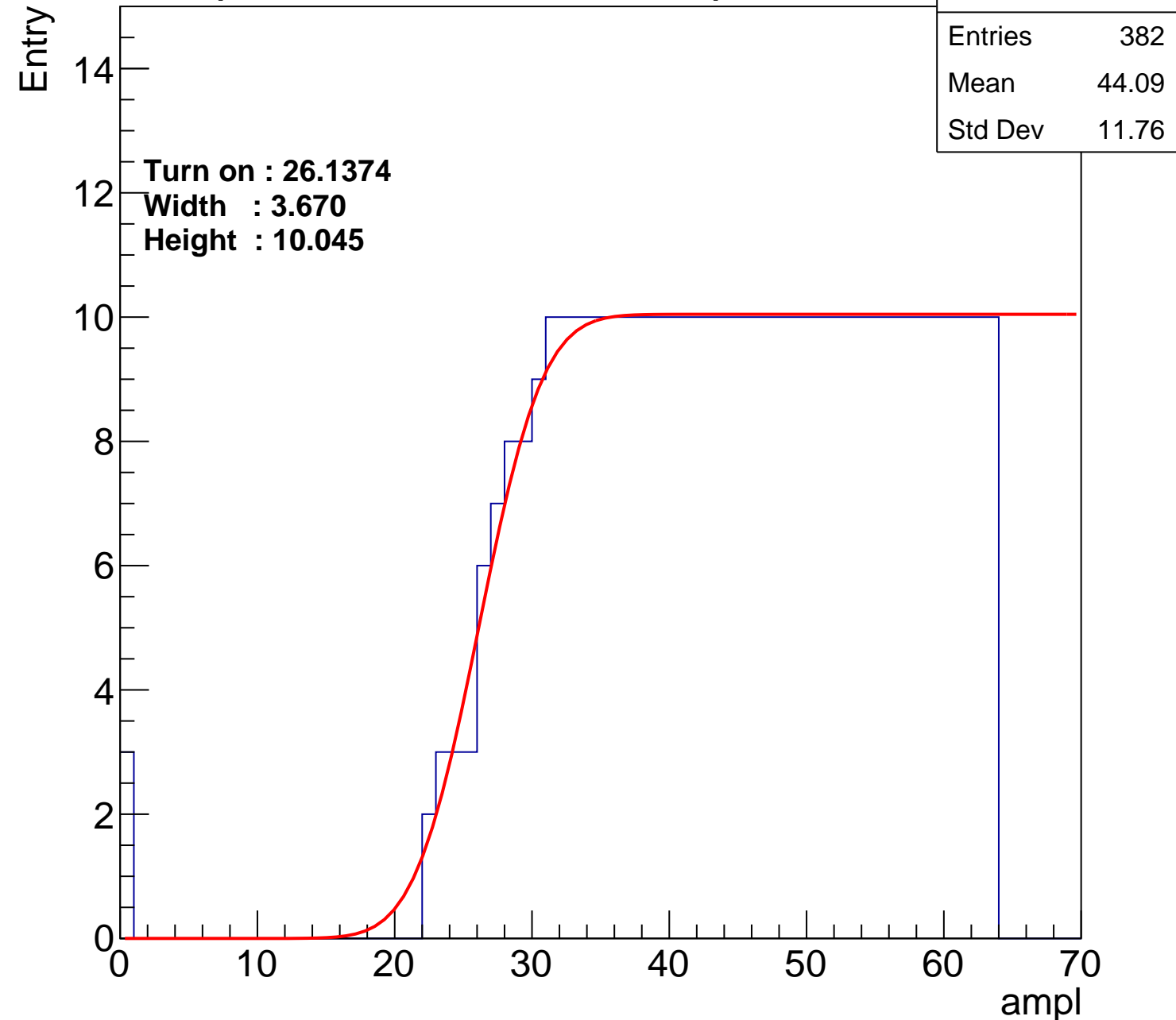
Width : 3.670

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.12
Std Dev	12.23

Turn on : 24.1795

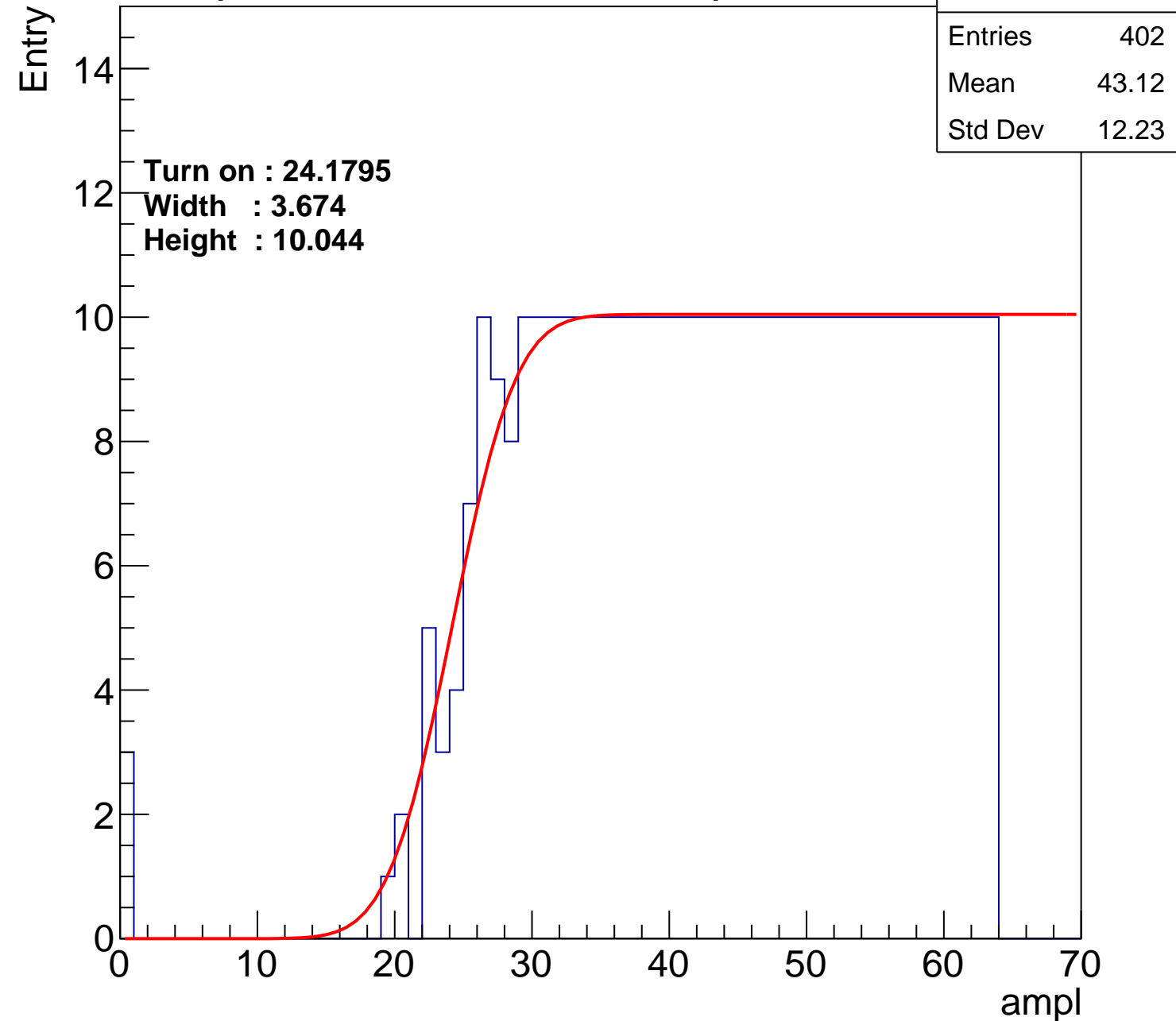
Width : 3.674

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.61
Std Dev	11.98

Turn on : 25.3432

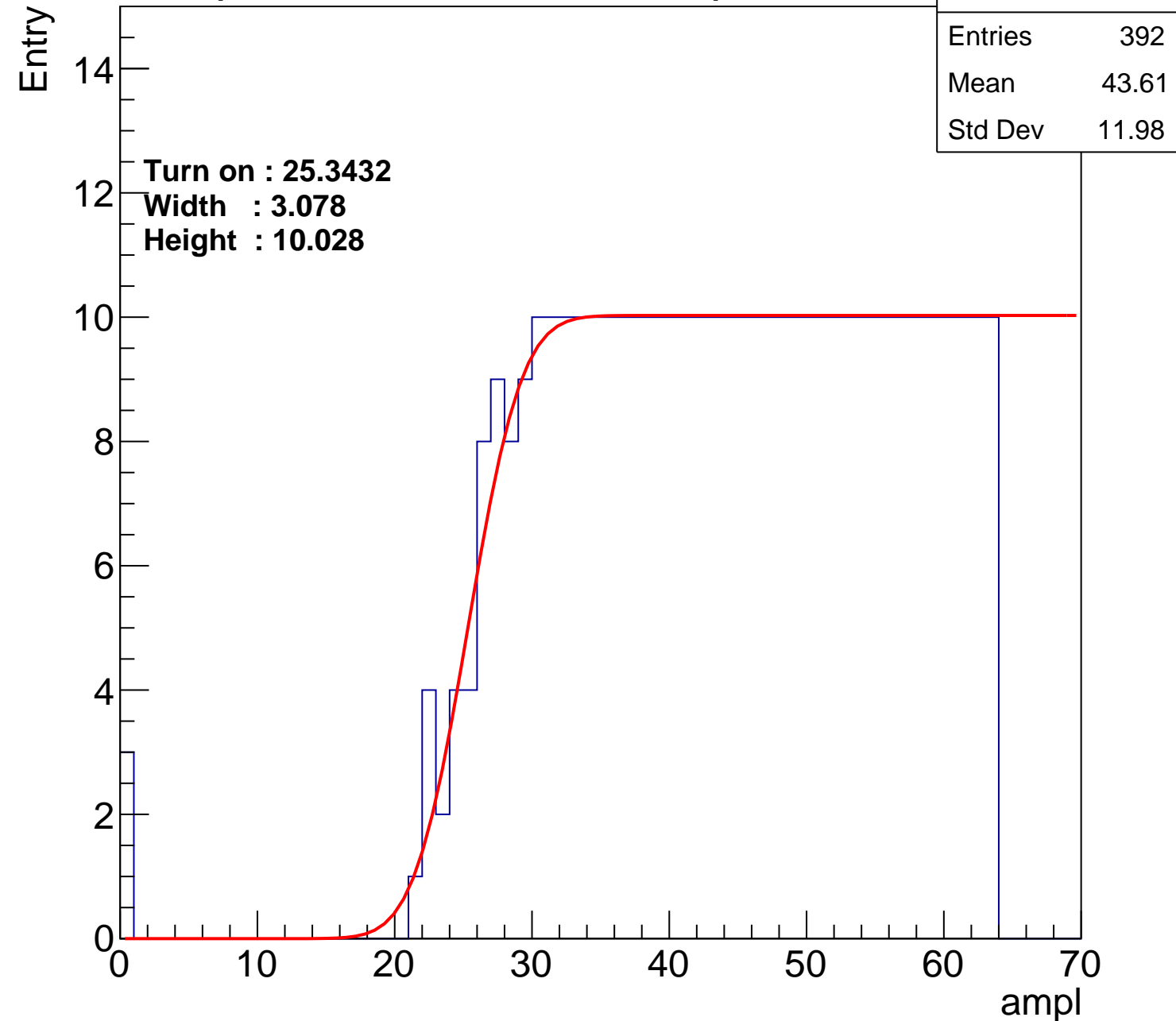
Width : 3.078

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.96
Std Dev	11.93

Turn on : 26.2129

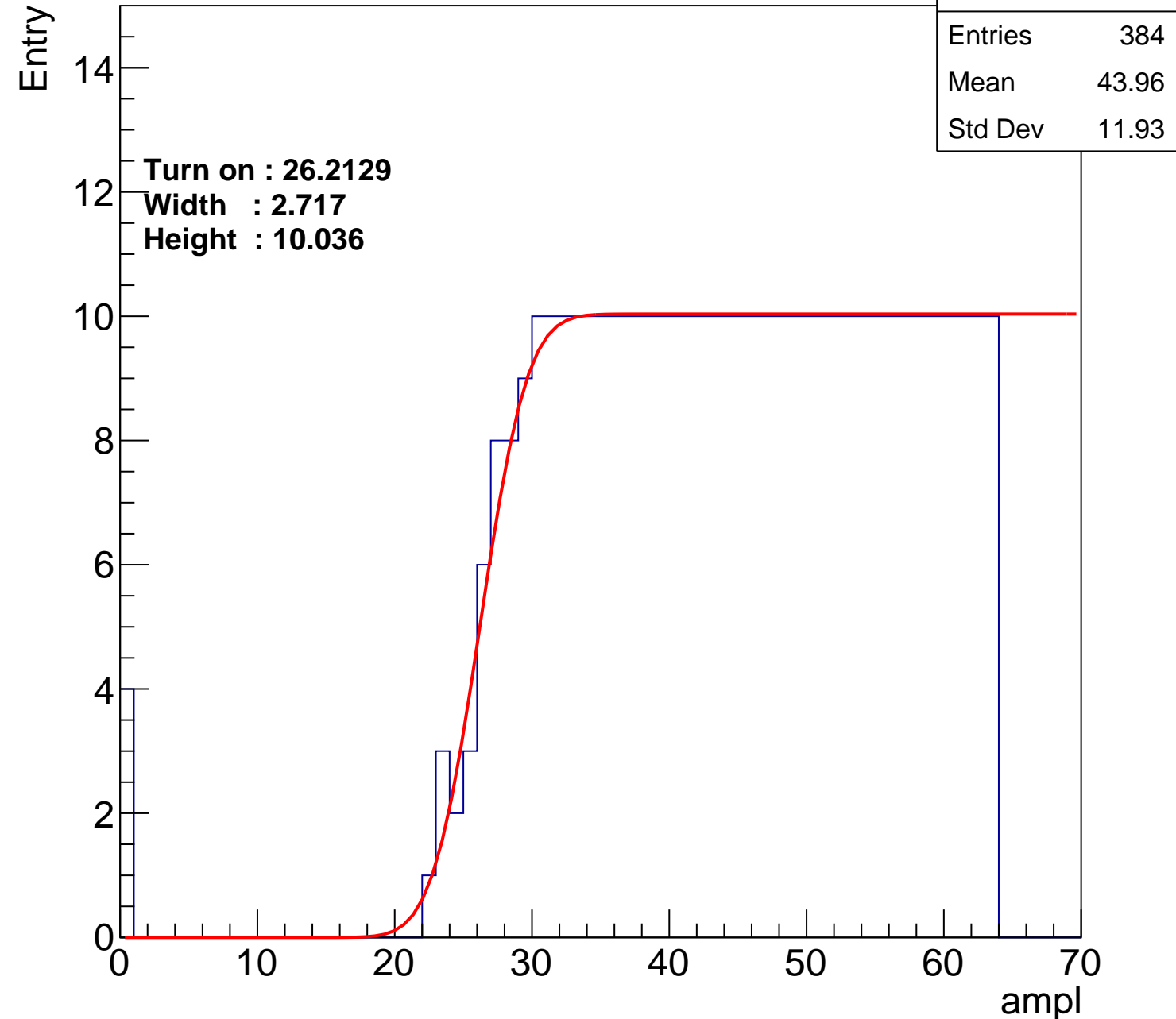
Width : 2.717

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.7
Std Dev	11.48

Turn on : 28.1405

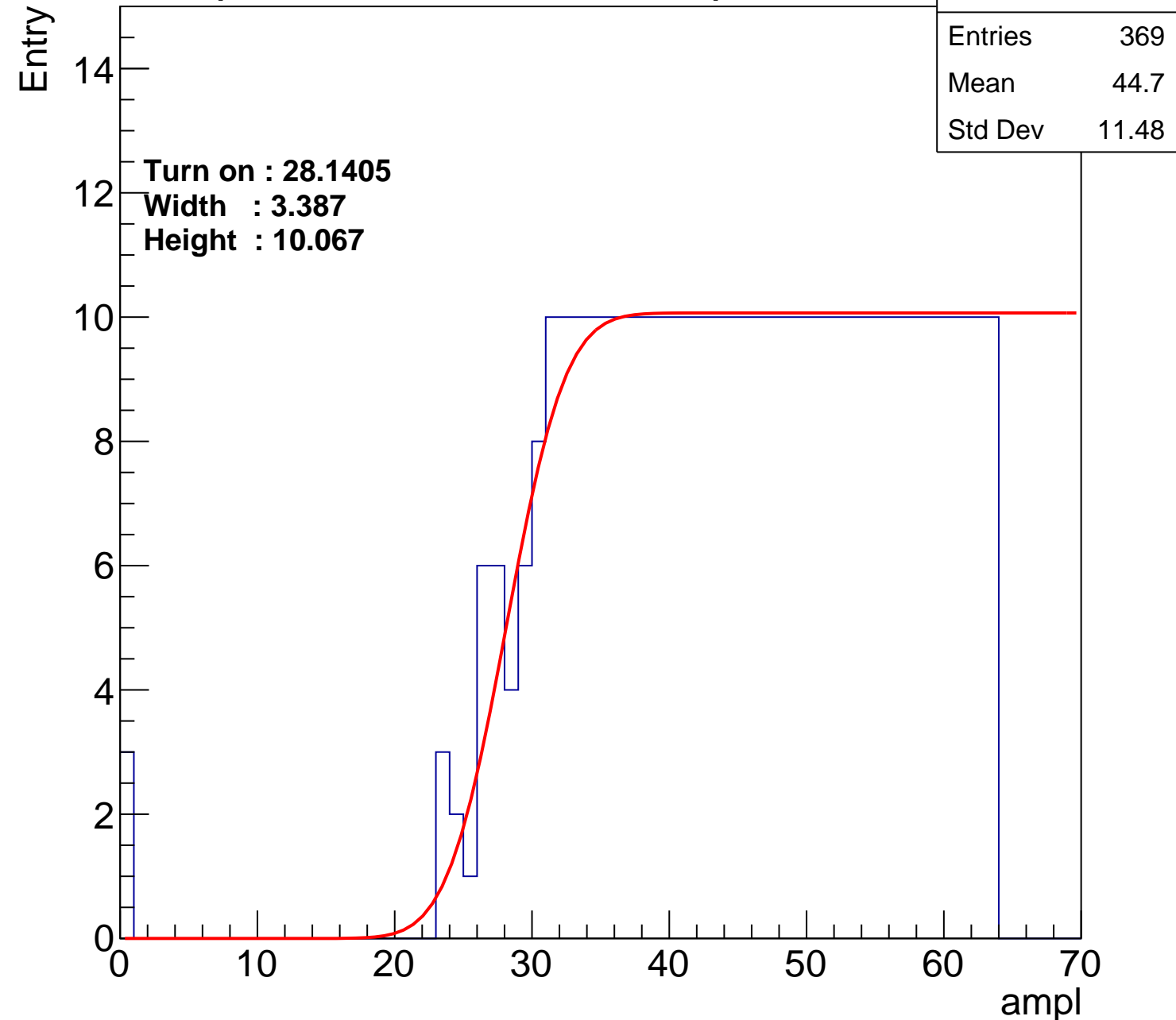
Width : 3.387

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.24
Std Dev	11.43

Turn on : 26.1253

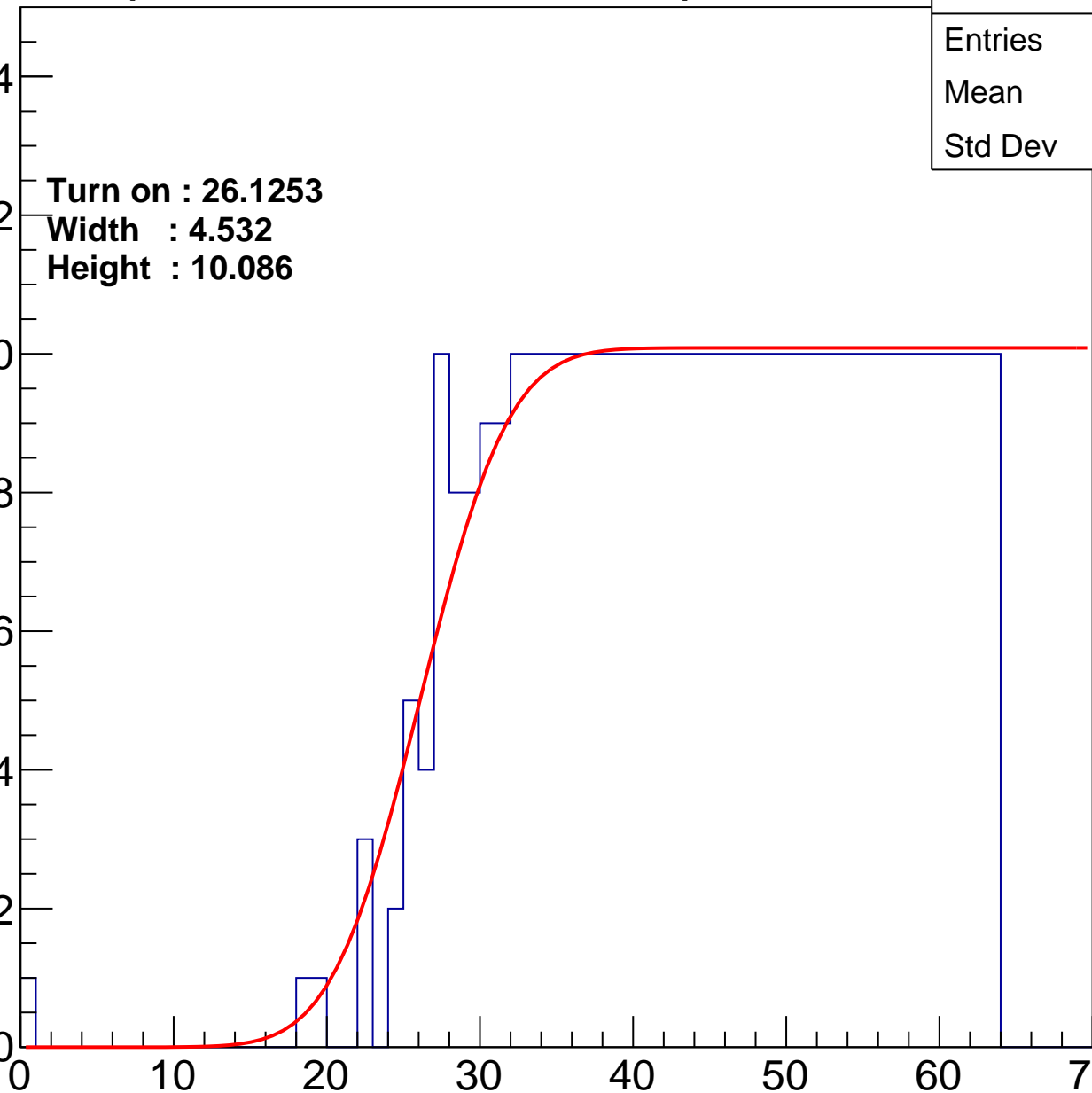
Width : 4.532

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.3894

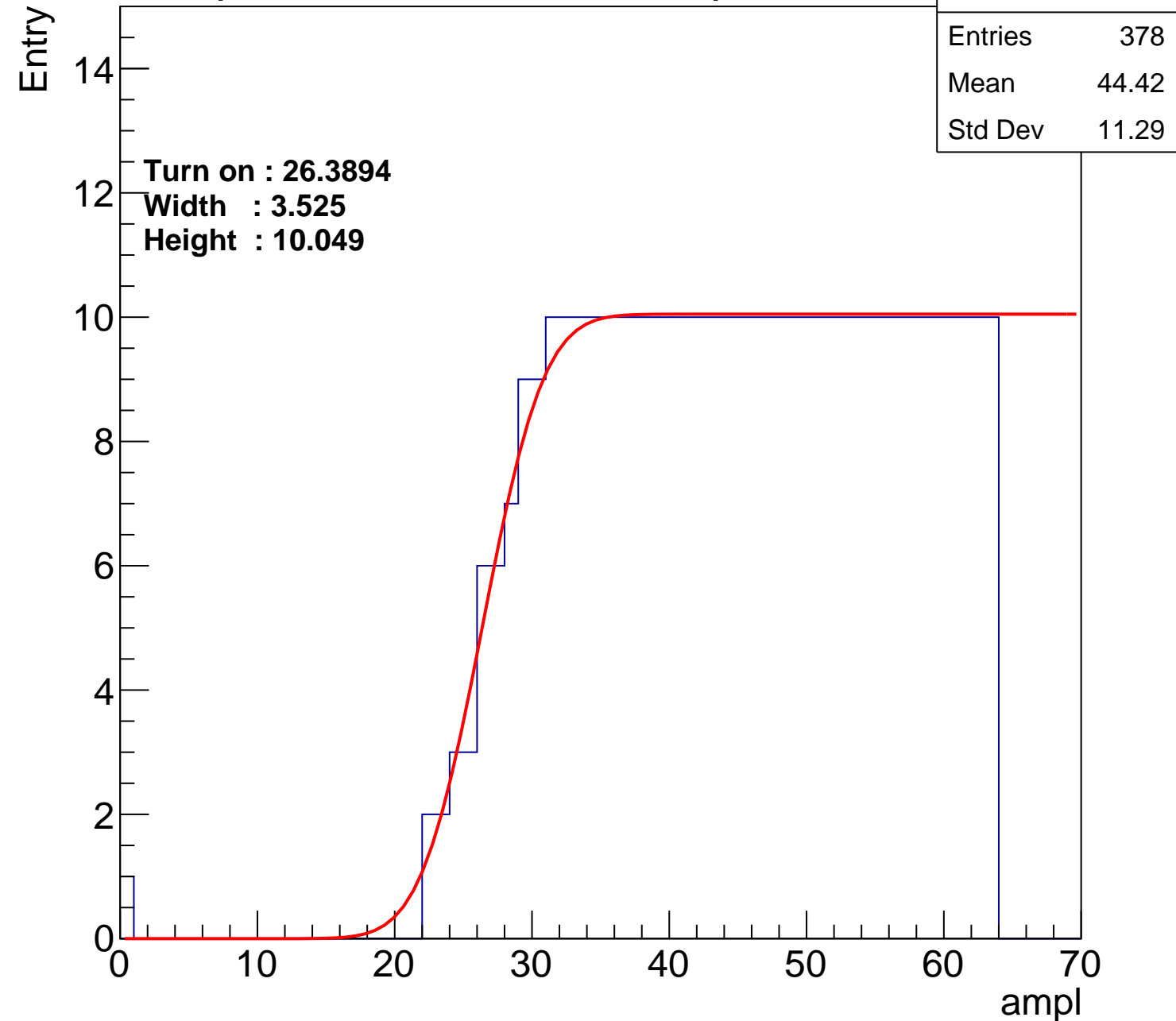
Width : 3.525

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.5283

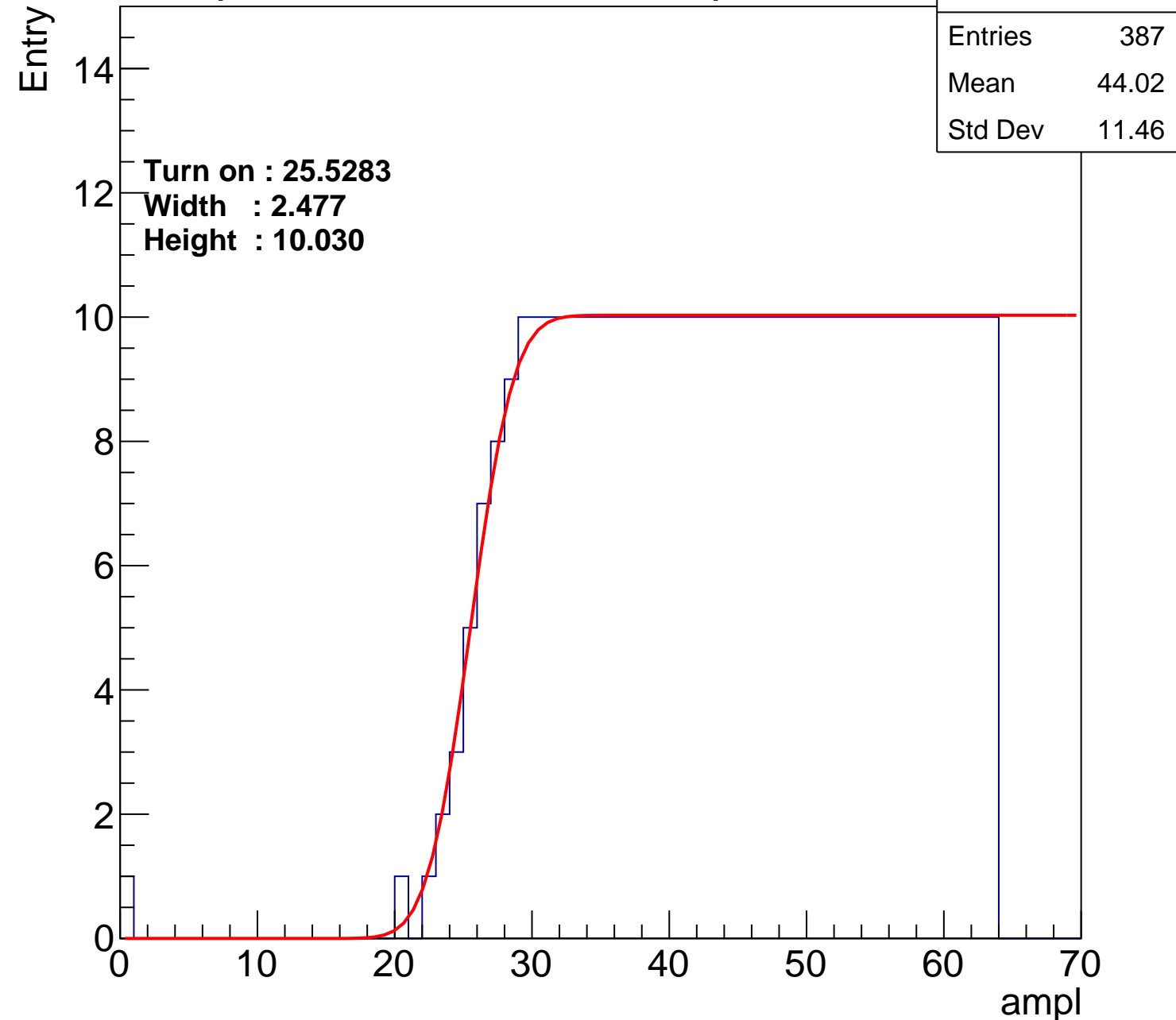
Width : 2.477

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.3939

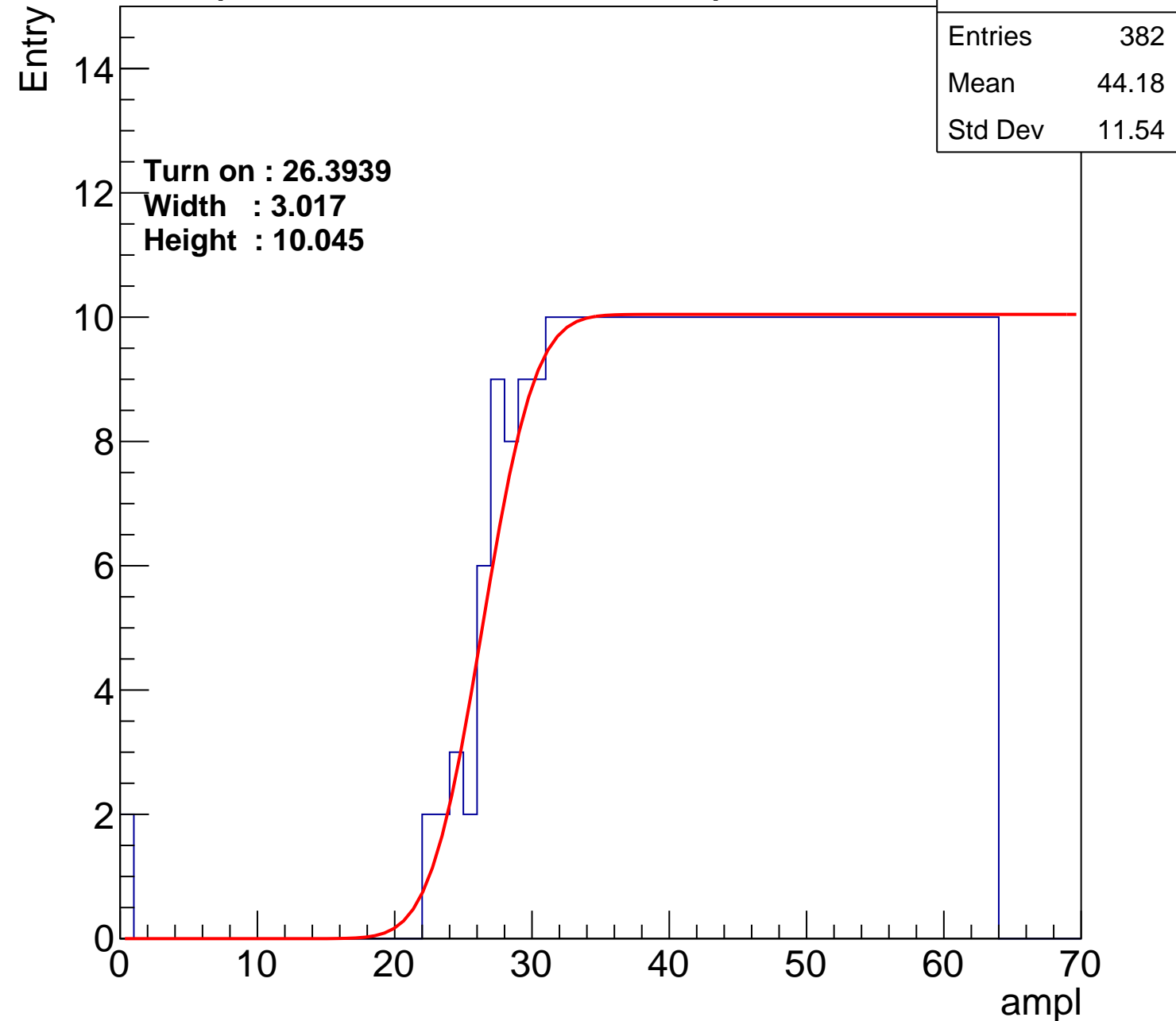
Width : 3.017

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.37
Std Dev	12.21

Turn on : 24.7197

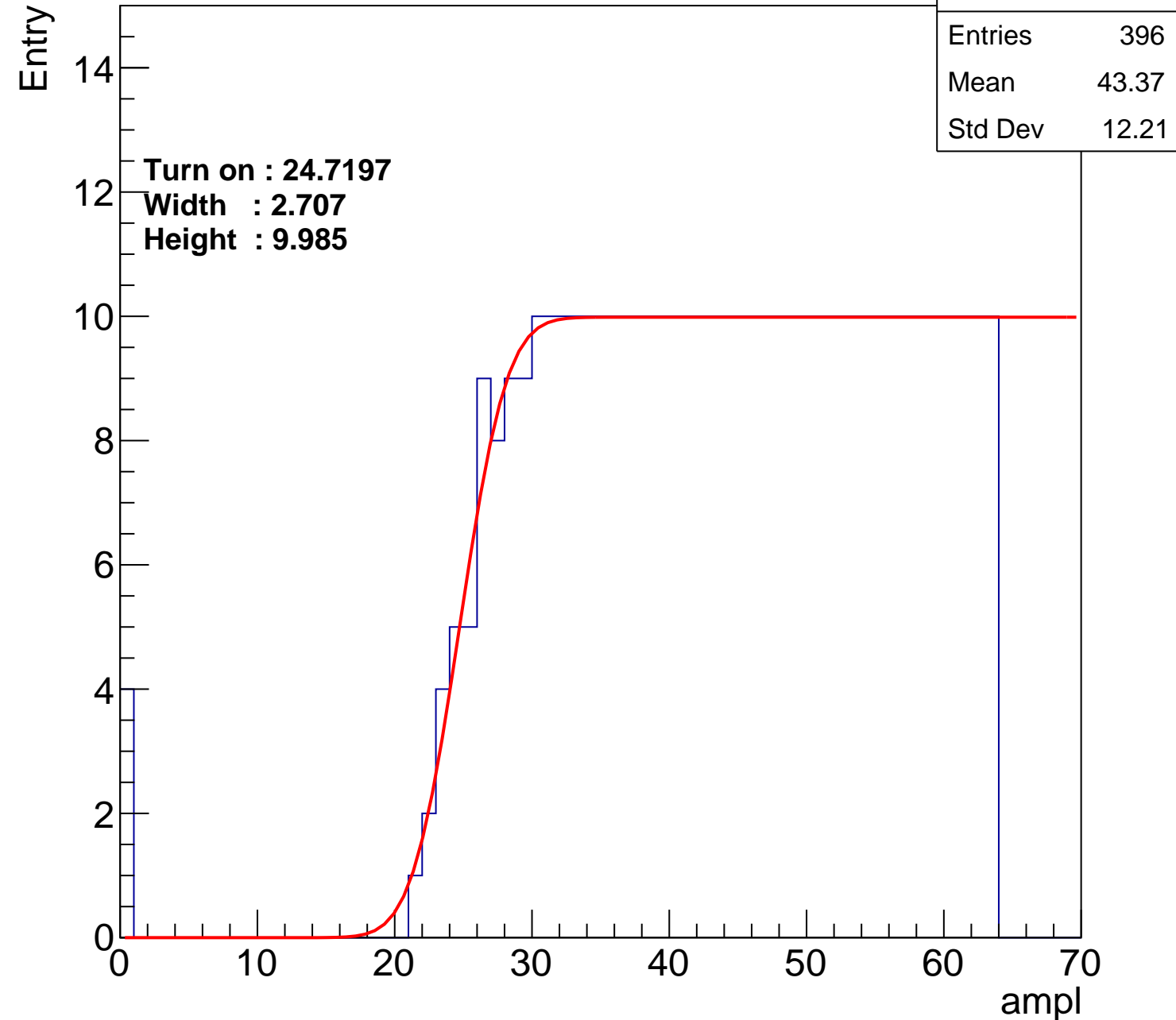
Width : 2.707

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.5323

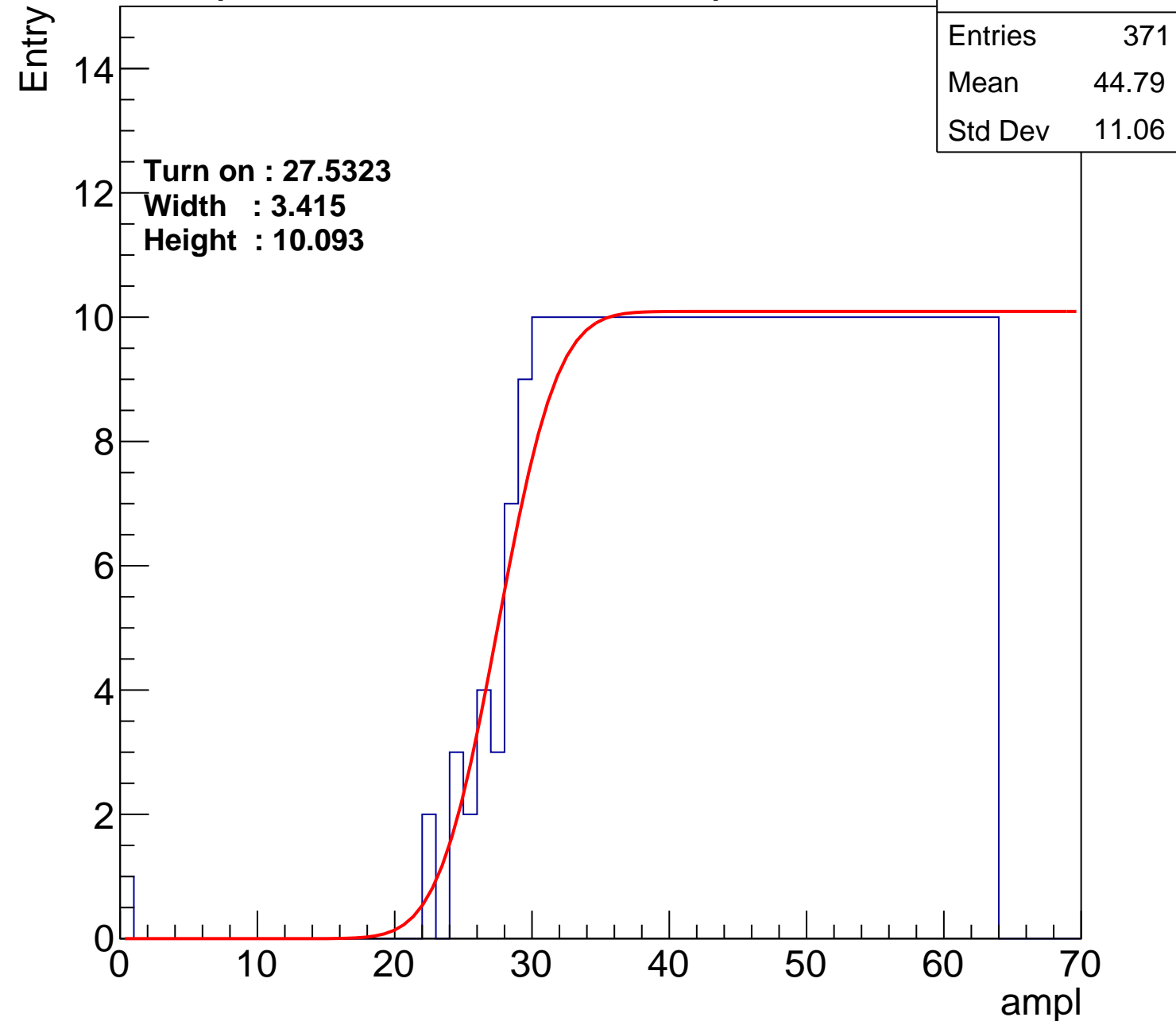
Width : 3.415

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	42.98
Std Dev	12.73

Turn on : 25.4770

Width : 4.350

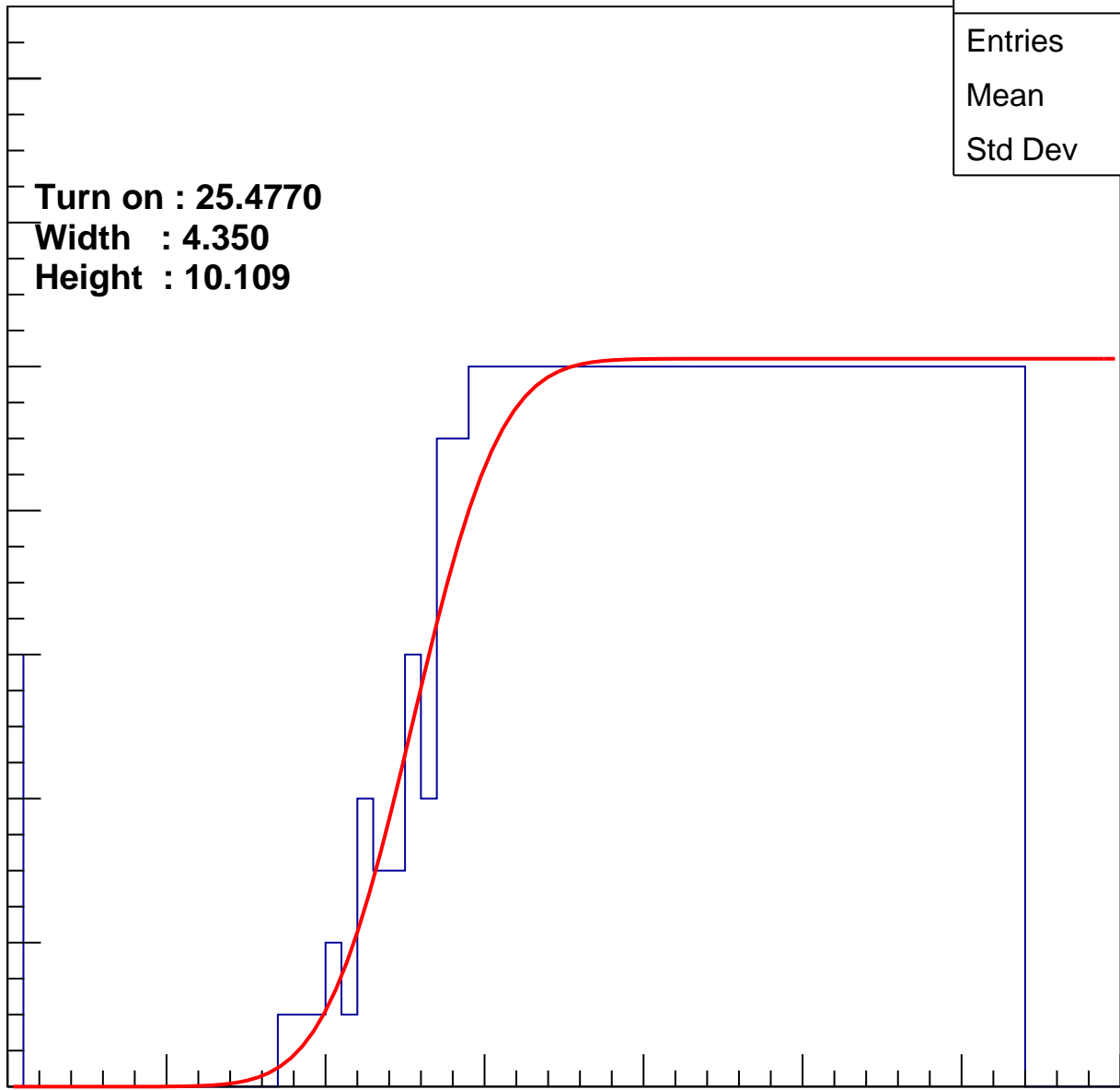
Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U17-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.02
Std Dev	11.1

Turn on : 27.9042

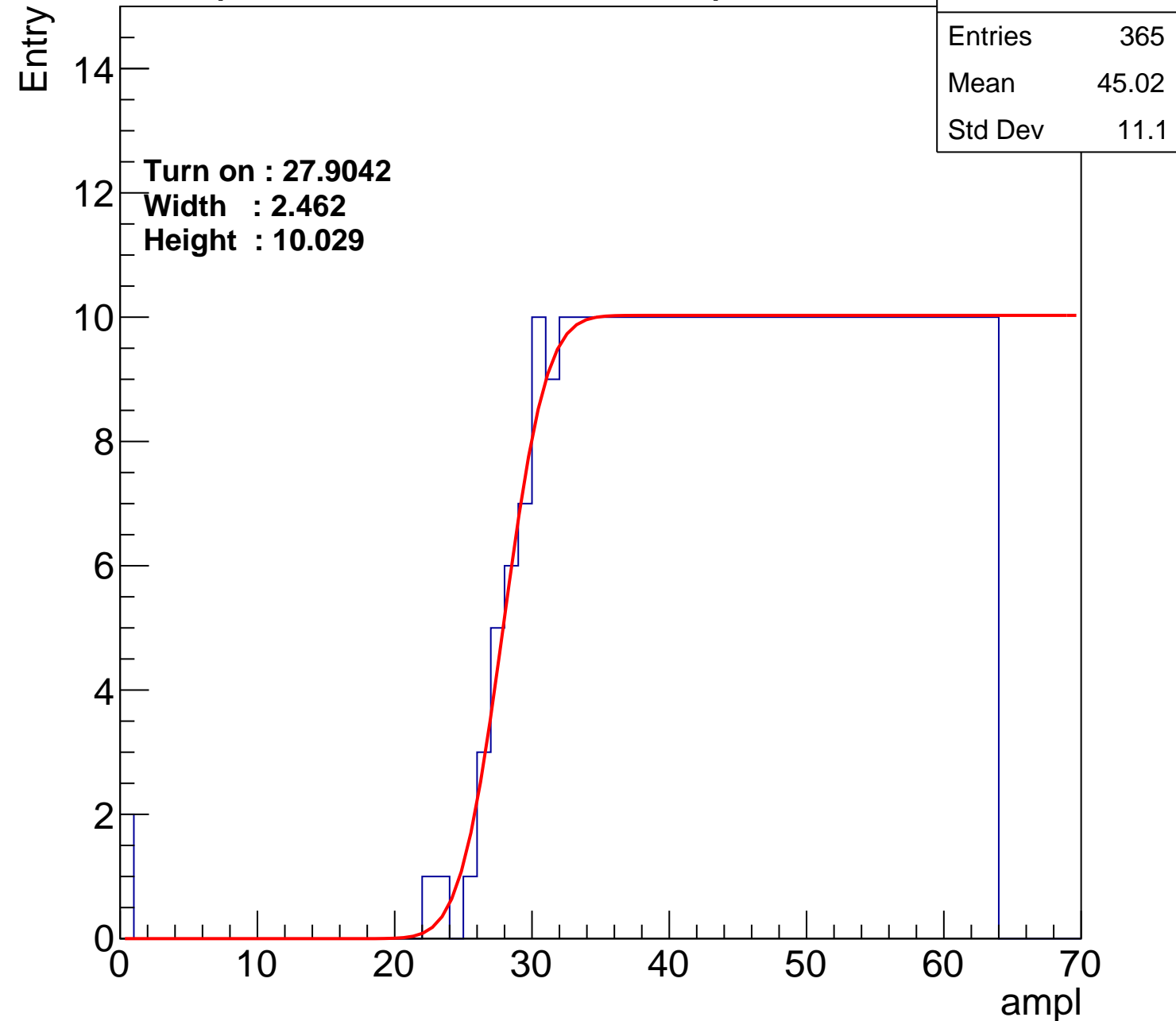
Width : 2.462

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.4663

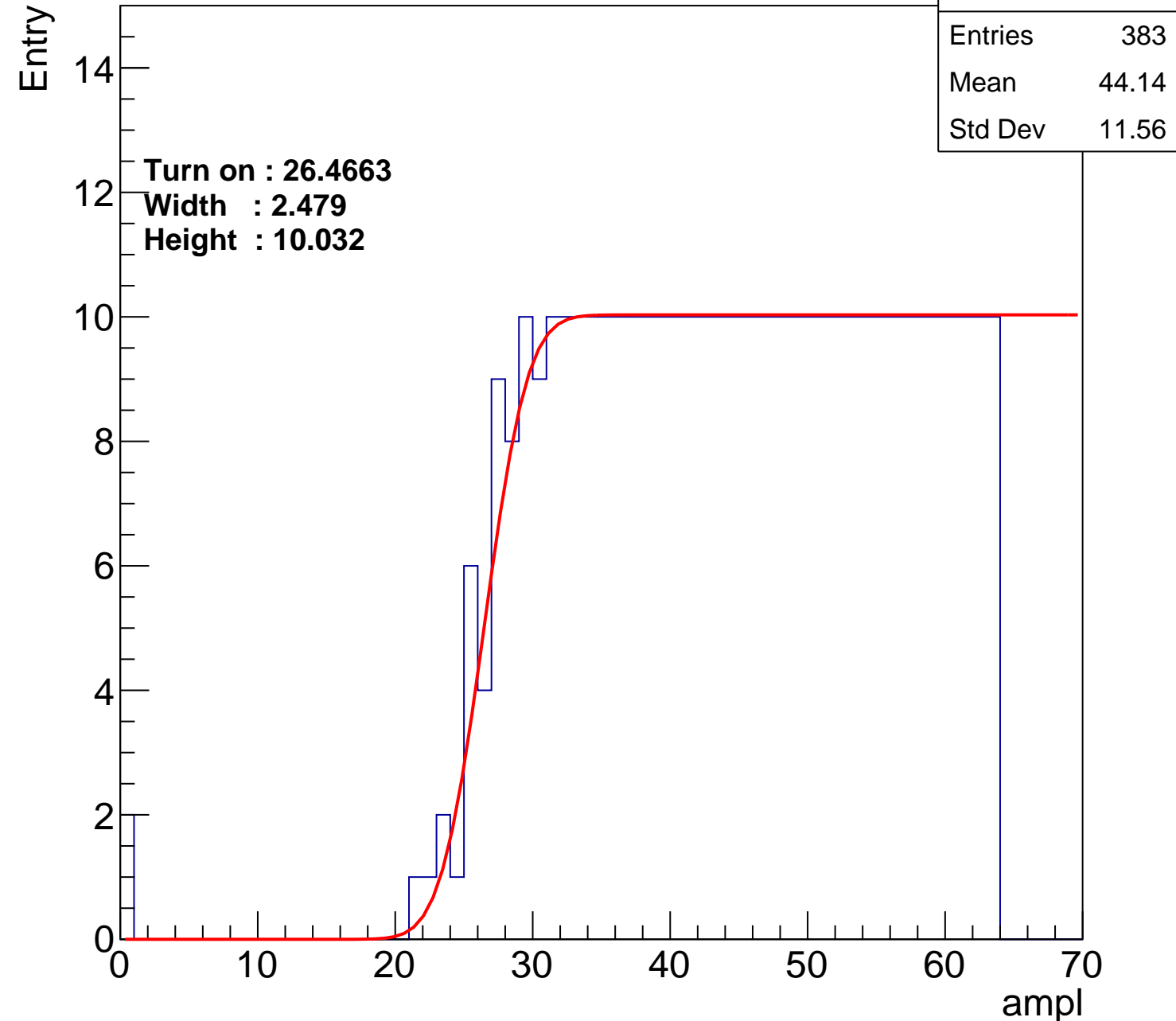
Width : 2.479

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.47
Std Dev	11.66

Turn on : 27.2542

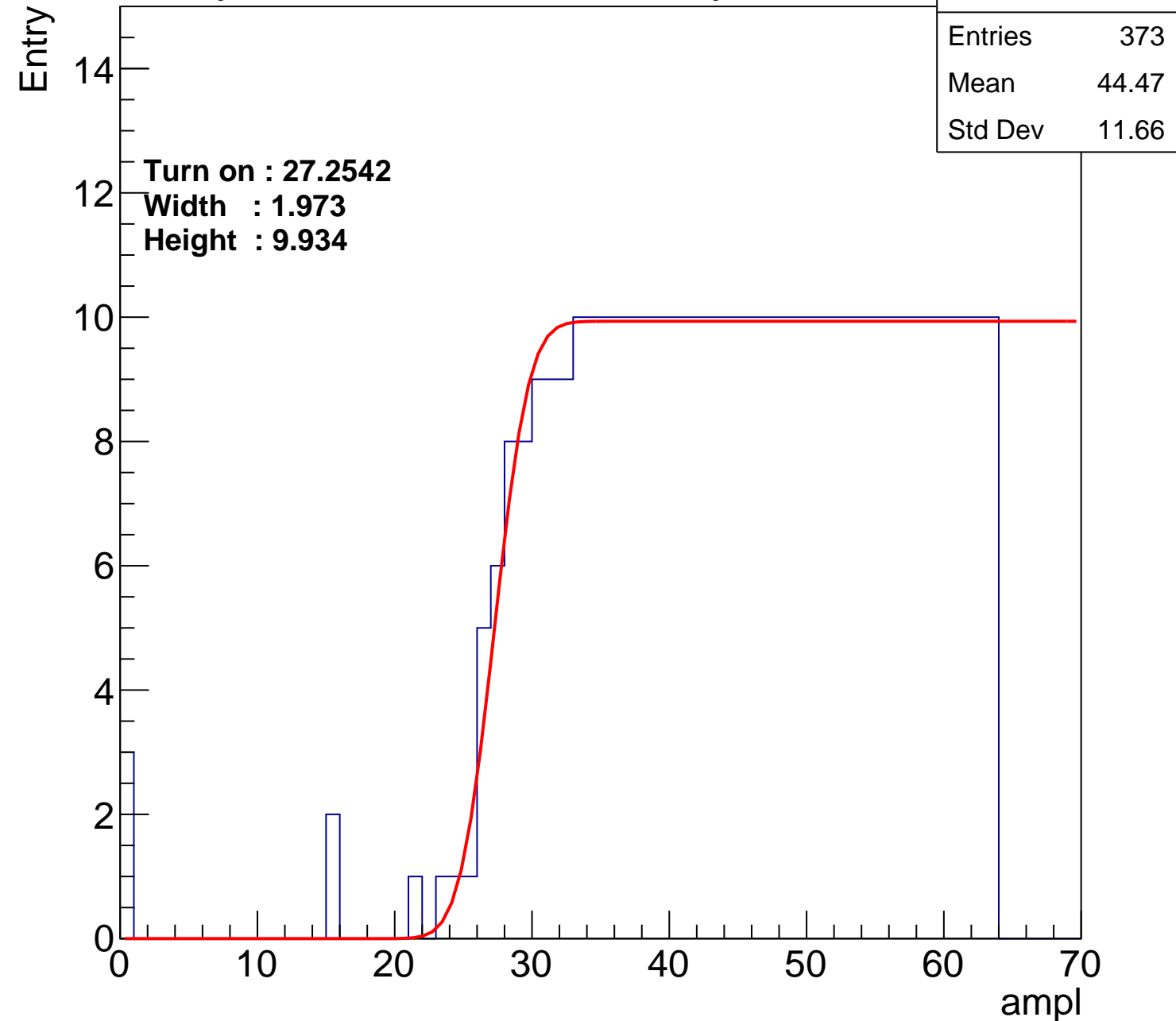
Width : 1.973

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.5812

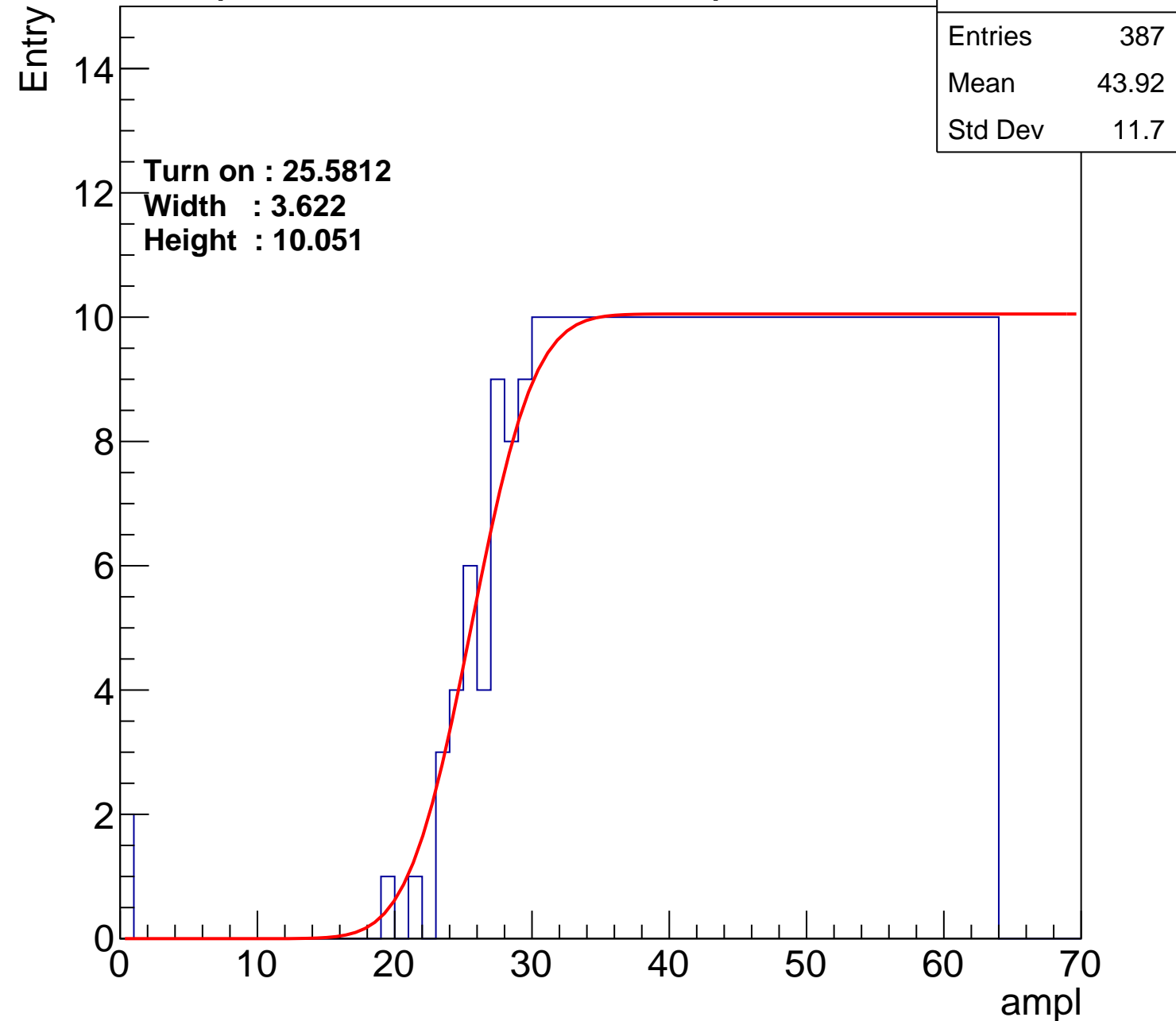
Width : 3.622

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.84
Std Dev	11.08

Turn on : 27.7550

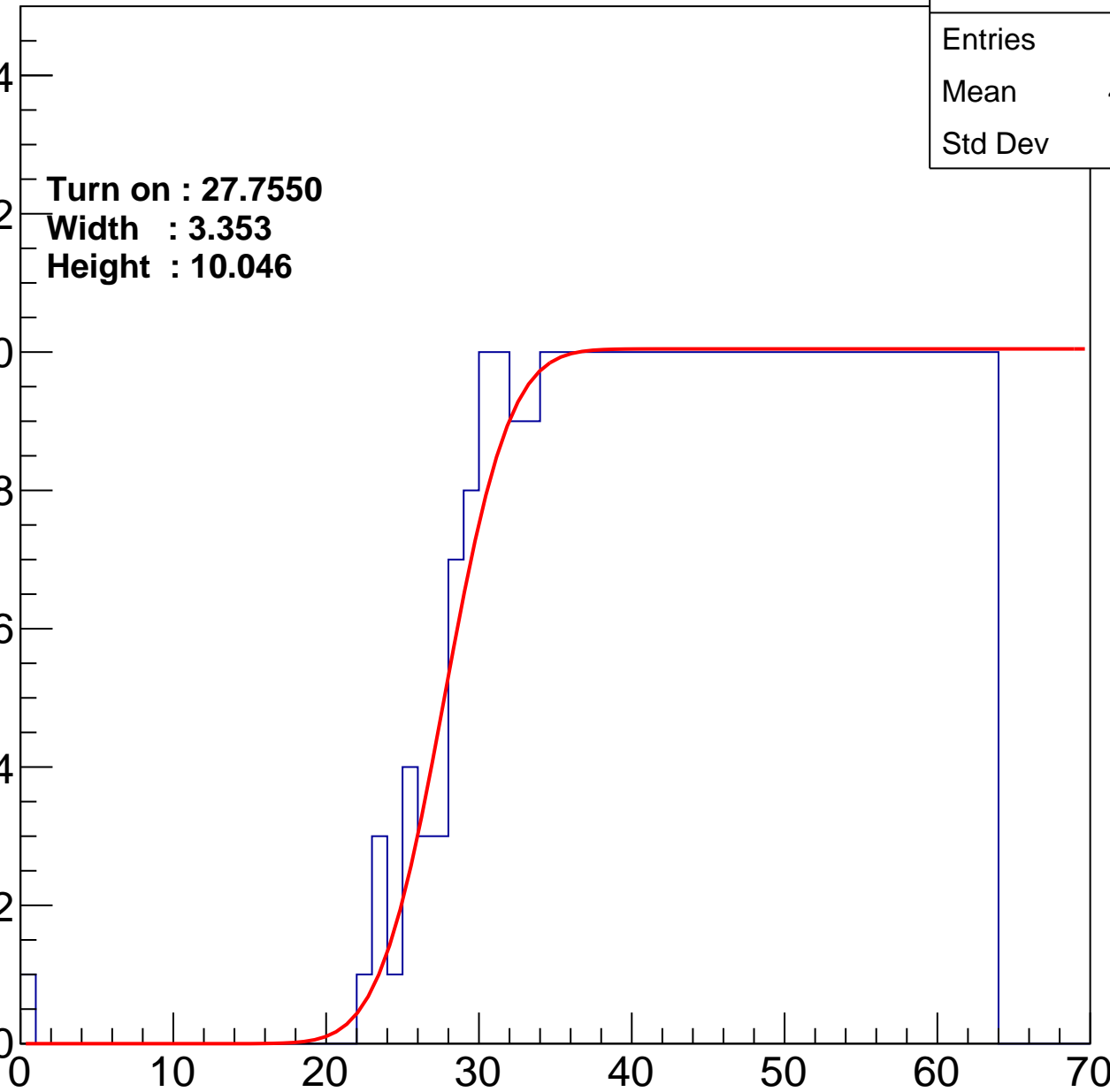
Width : 3.353

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch54

calib_packv5_042523_0143.root, FC#12, port B1

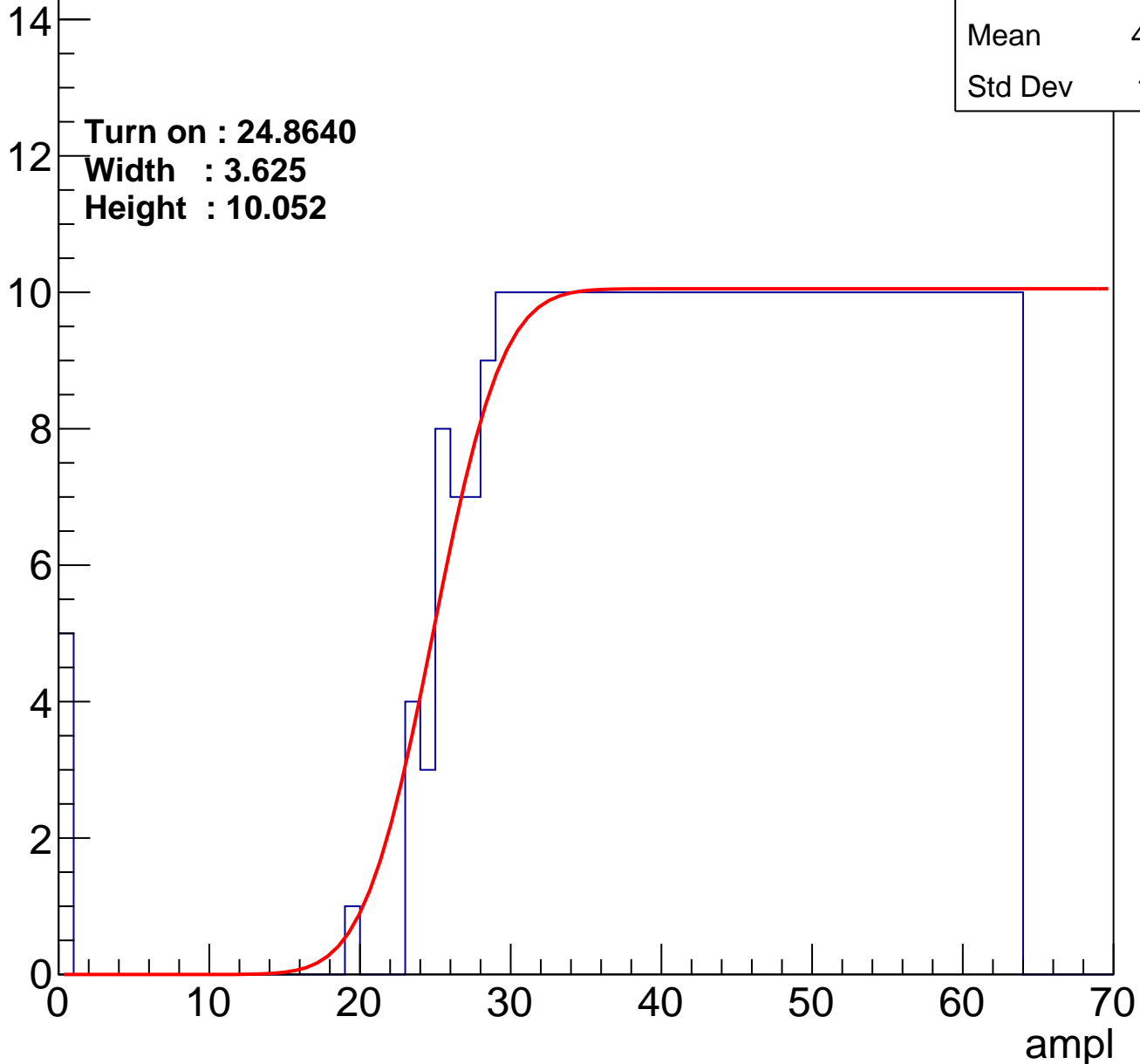
Entries	394
Mean	43.42
Std Dev	12.31

Turn on : 24.8640

Width : 3.625

Height : 10.052

Entry



B0L102S, U17-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.4
Std Dev	11.99

Turn on : 24.5625

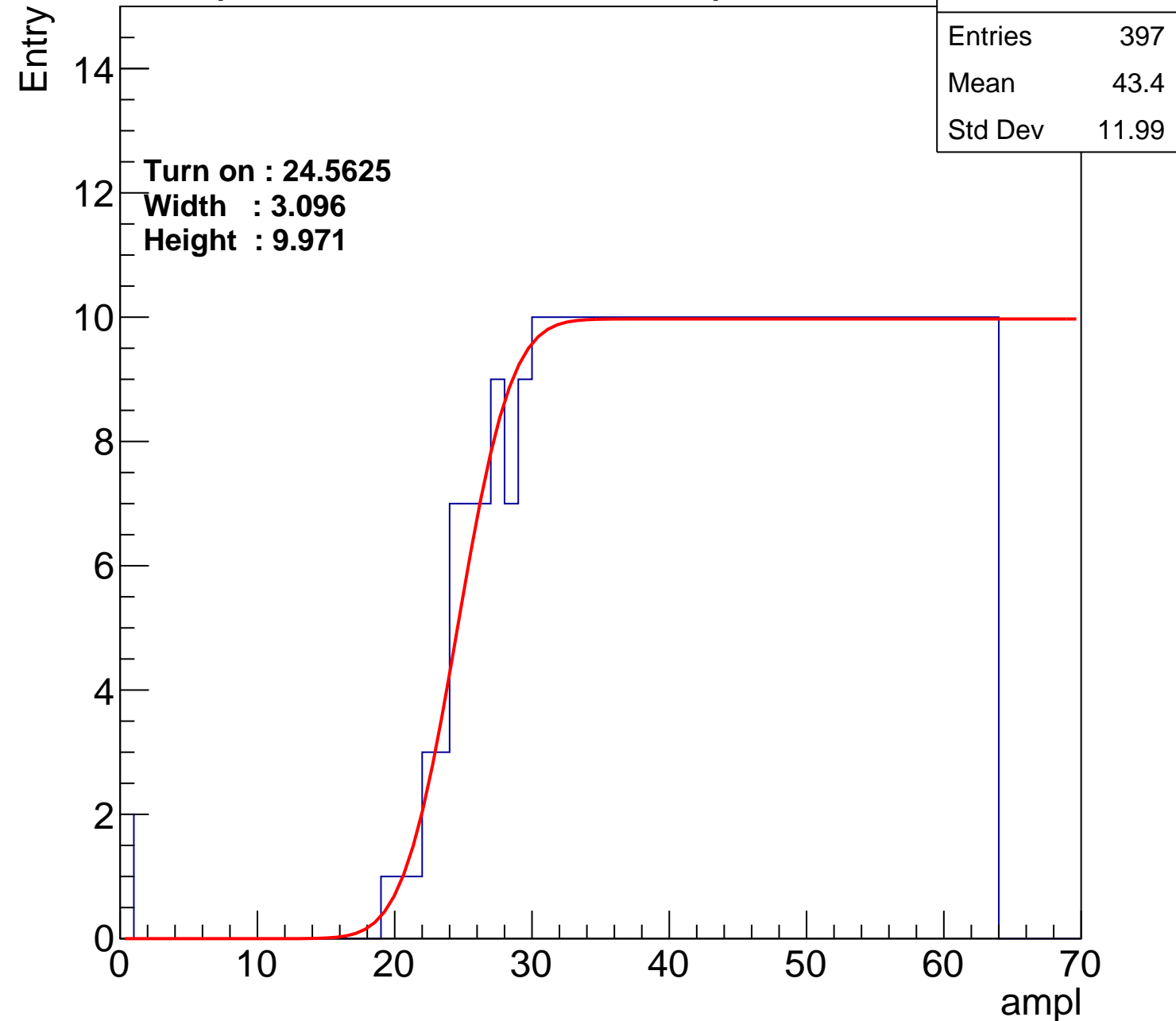
Width : 3.096

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.91
Std Dev	11.95

Turn on : 25.9917

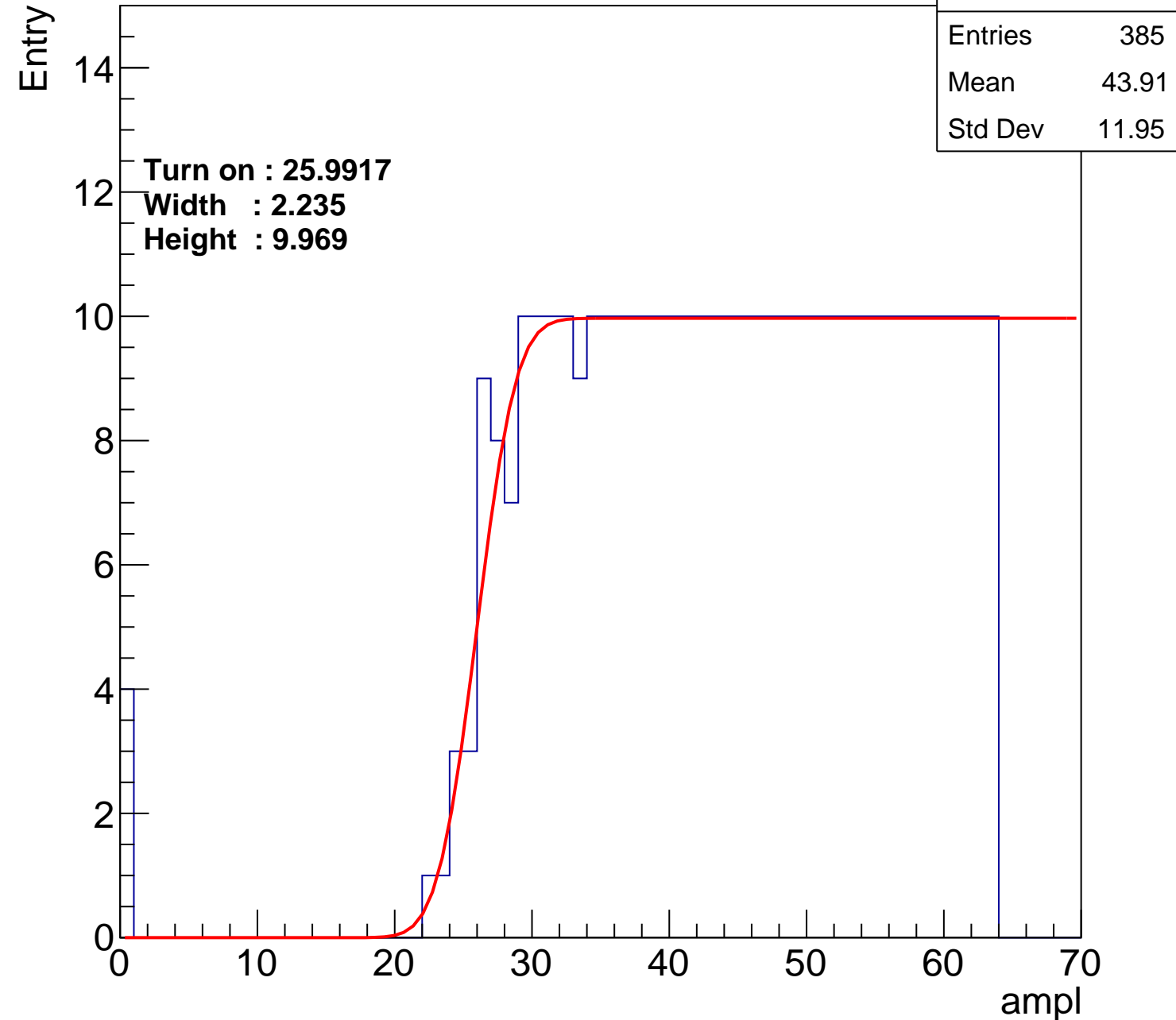
Width : 2.235

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.13
Std Dev	11.88

Turn on : 26.7602

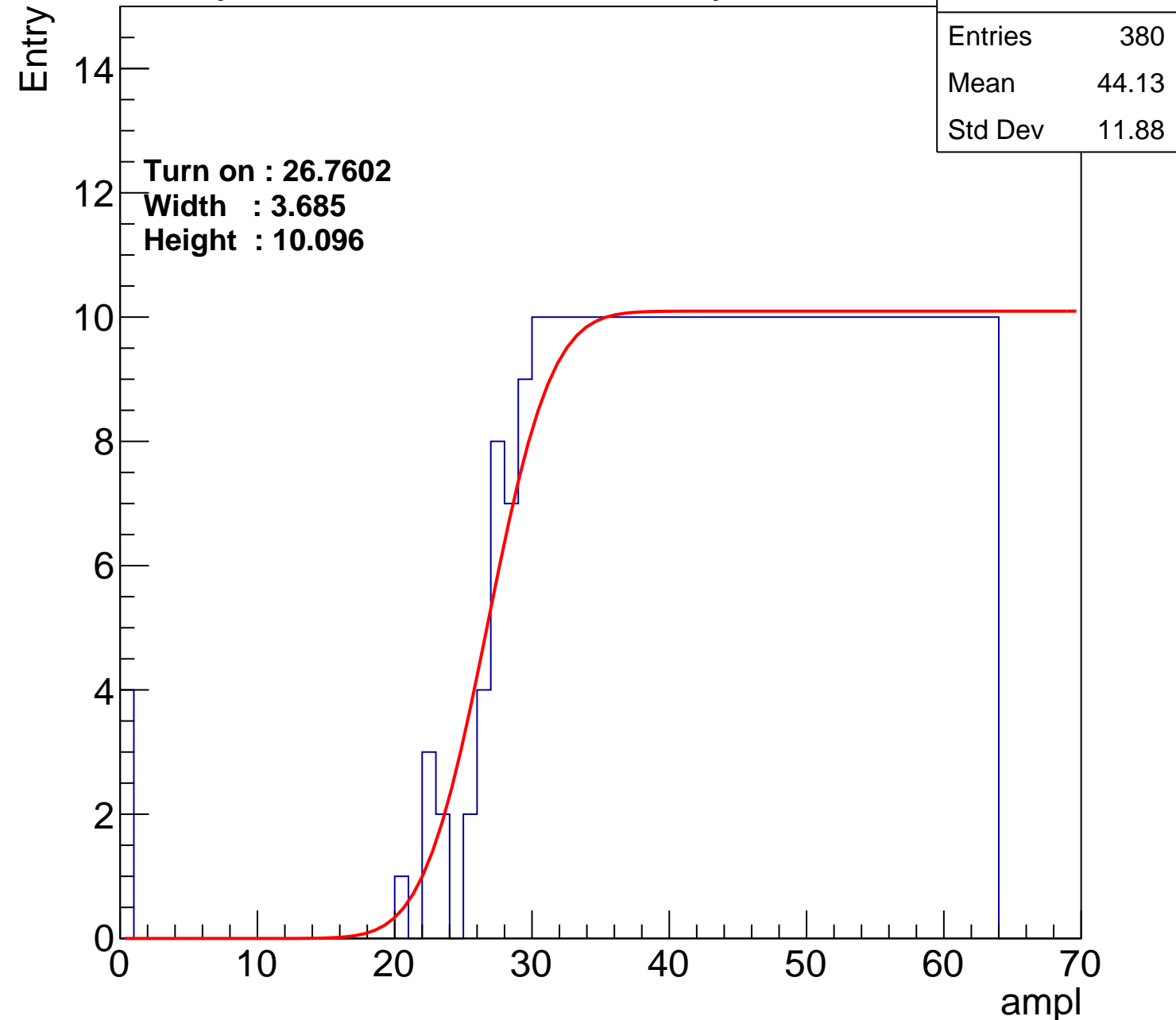
Width : 3.685

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.96
Std Dev	11.57

Turn on : 25.8603

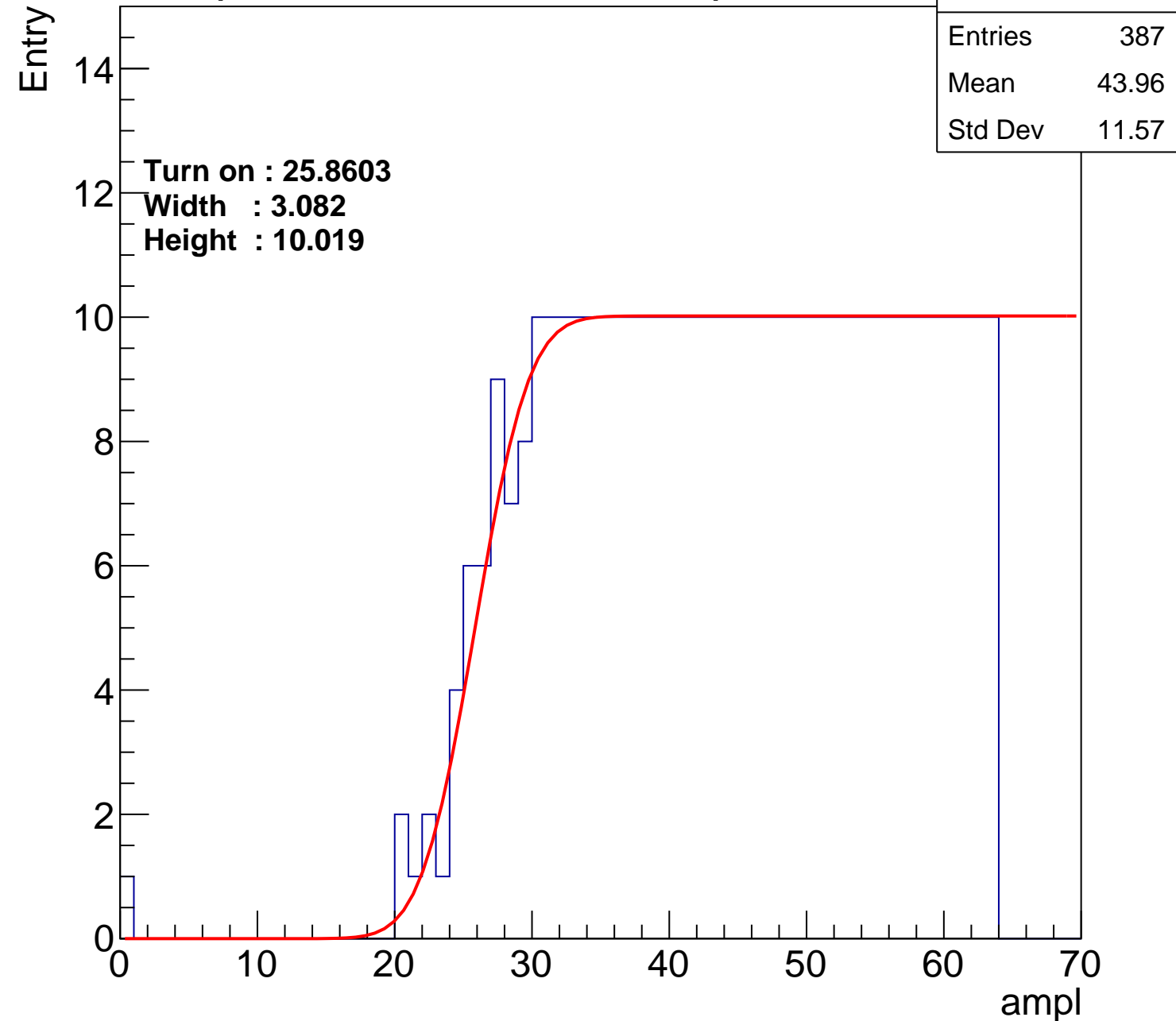
Width : 3.082

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.8545

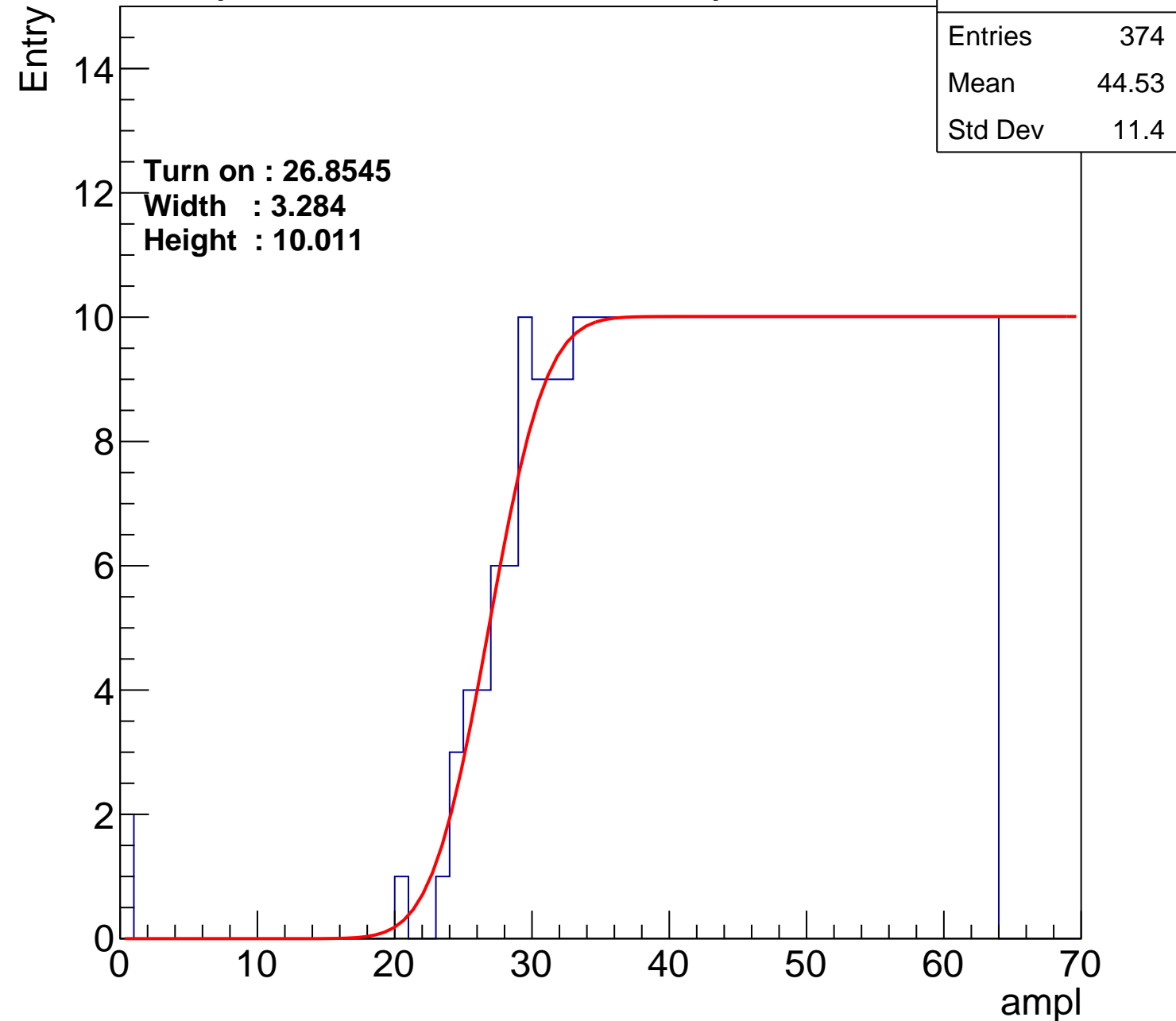
Width : 3.284

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch60

calib_packv5_042523_0143.root, FC#12, port B1

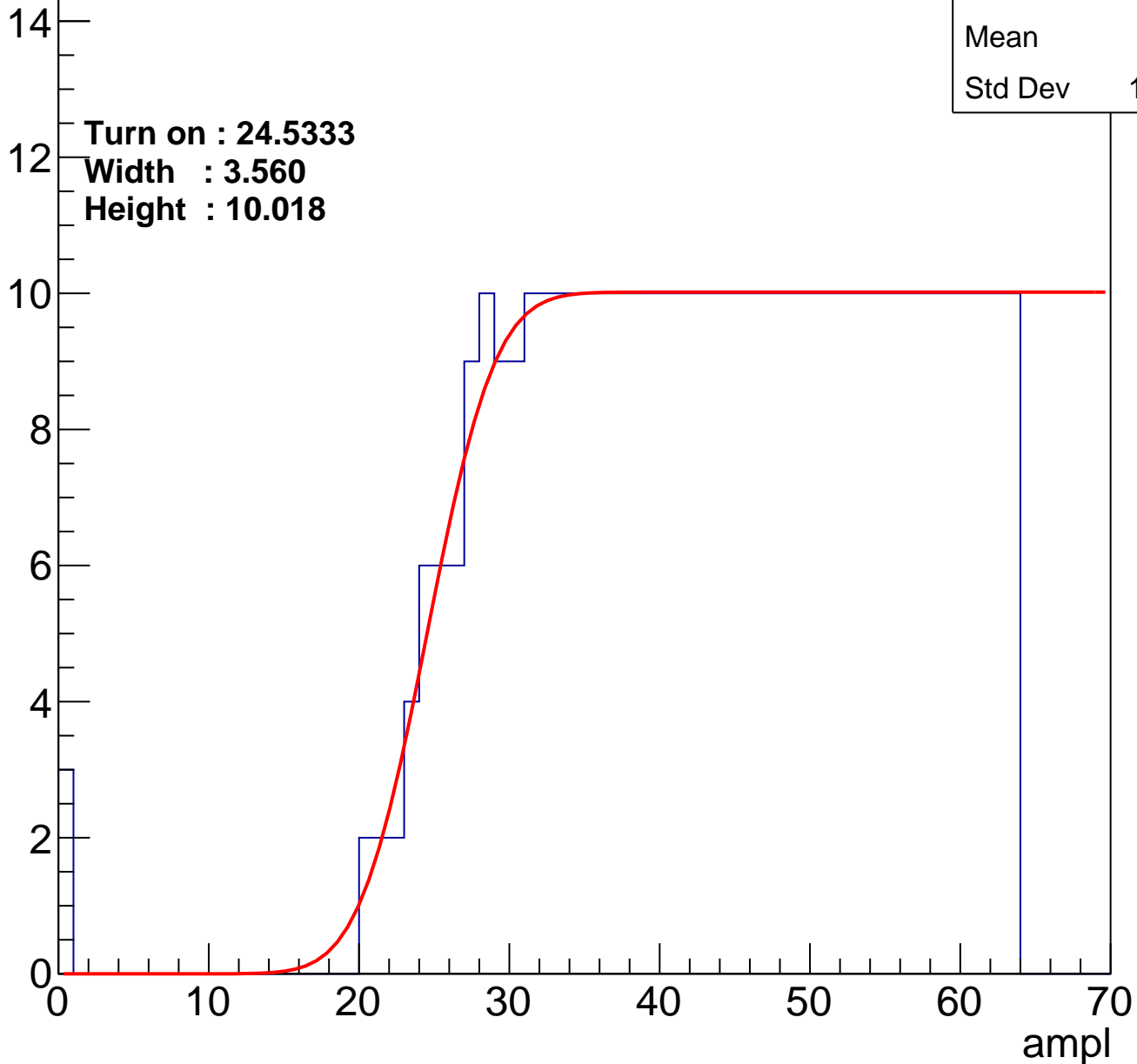
Entries	398
Mean	43.3
Std Dev	12.17

Turn on : 24.5333

Width : 3.560

Height : 10.018

Entry



B0L102S, U17-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.2
Std Dev	11.17

Turn on : 28.3447

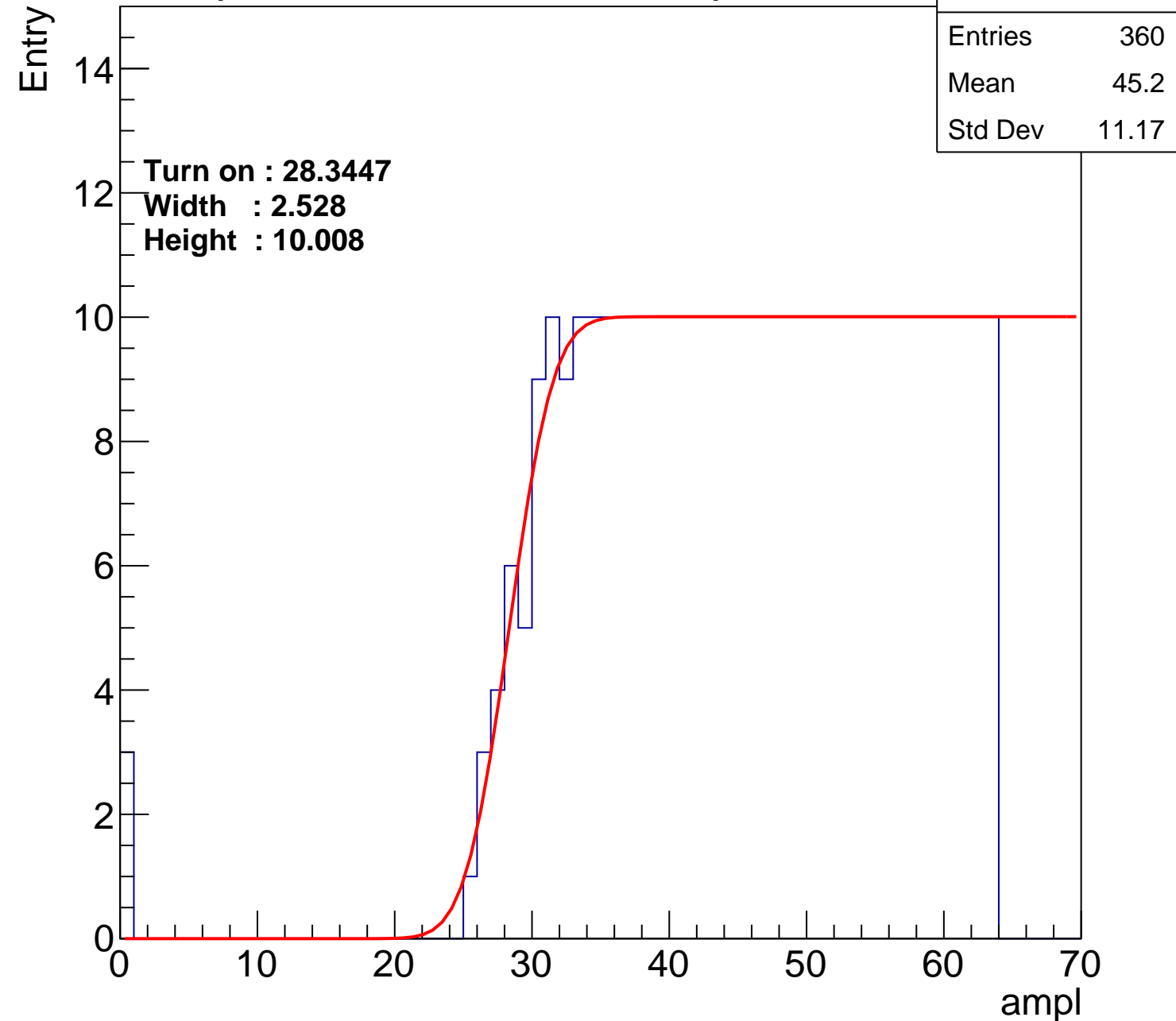
Width : 2.528

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.5
Std Dev	11.89

Turn on : 24.4426

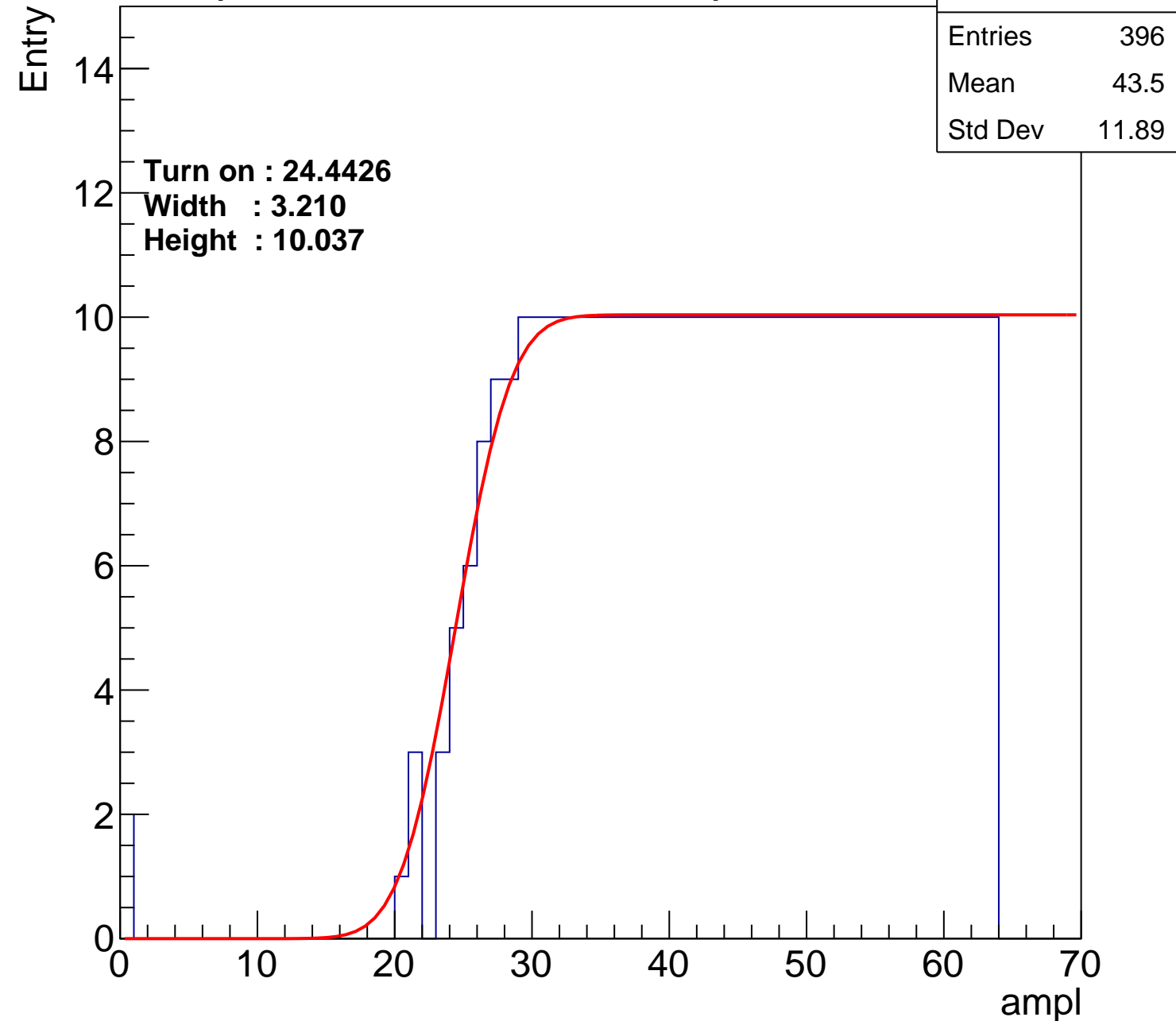
Width : 3.210

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch63

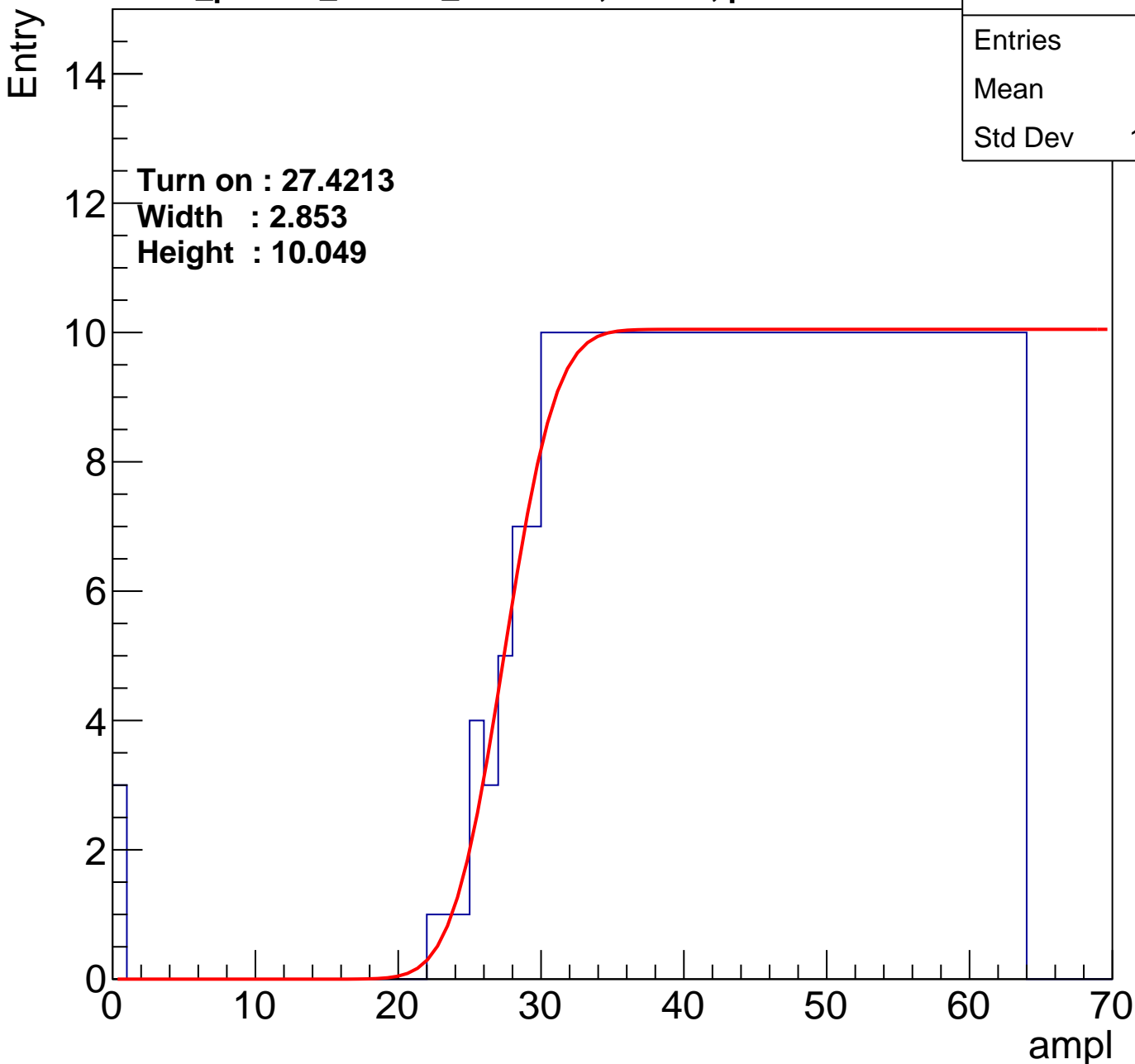
calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.6
Std Dev	11.49

Turn on : 27.4213

Width : 2.853

Height : 10.049



B0L102S, U17-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.94
Std Dev	11.71

Turn on : 25.8273

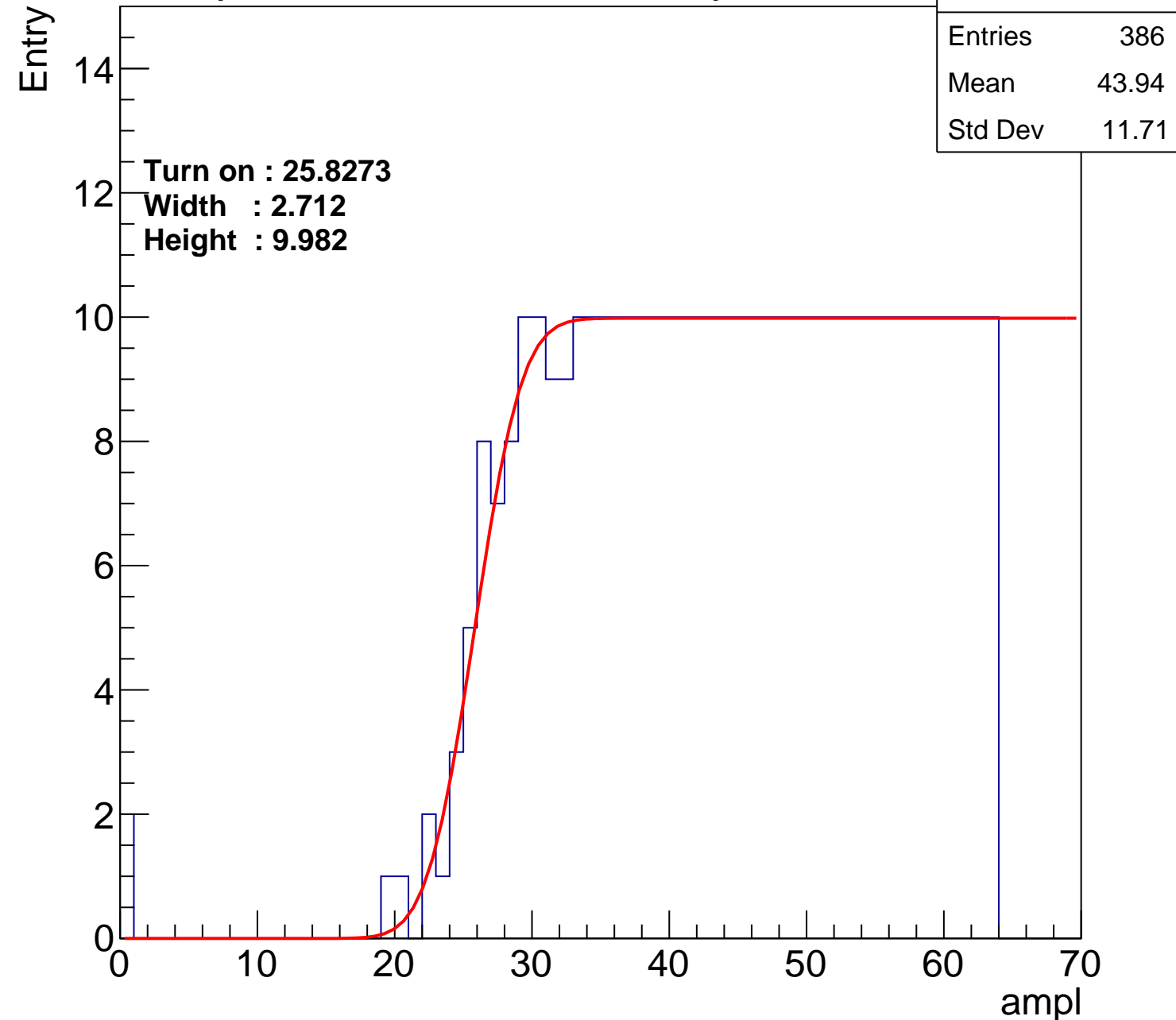
Width : 2.712

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch65

calib_packv5_042523_0143.root, FC#12, port B1

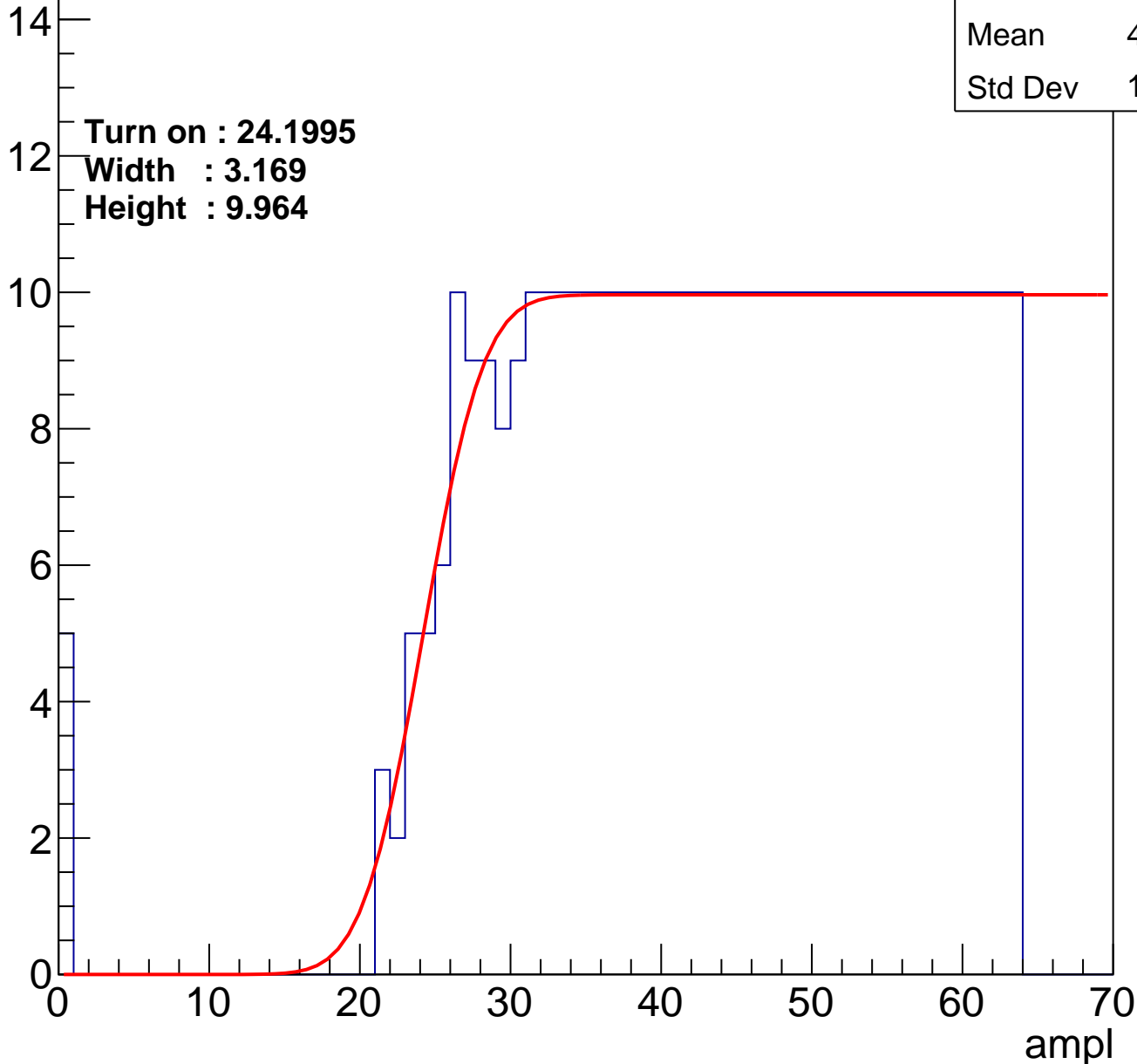
Entries	401
Mean	43.04
Std Dev	12.52

Turn on : 24.1995

Width : 3.169

Height : 9.964

Entry



B0L102S, U17-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 27.0321

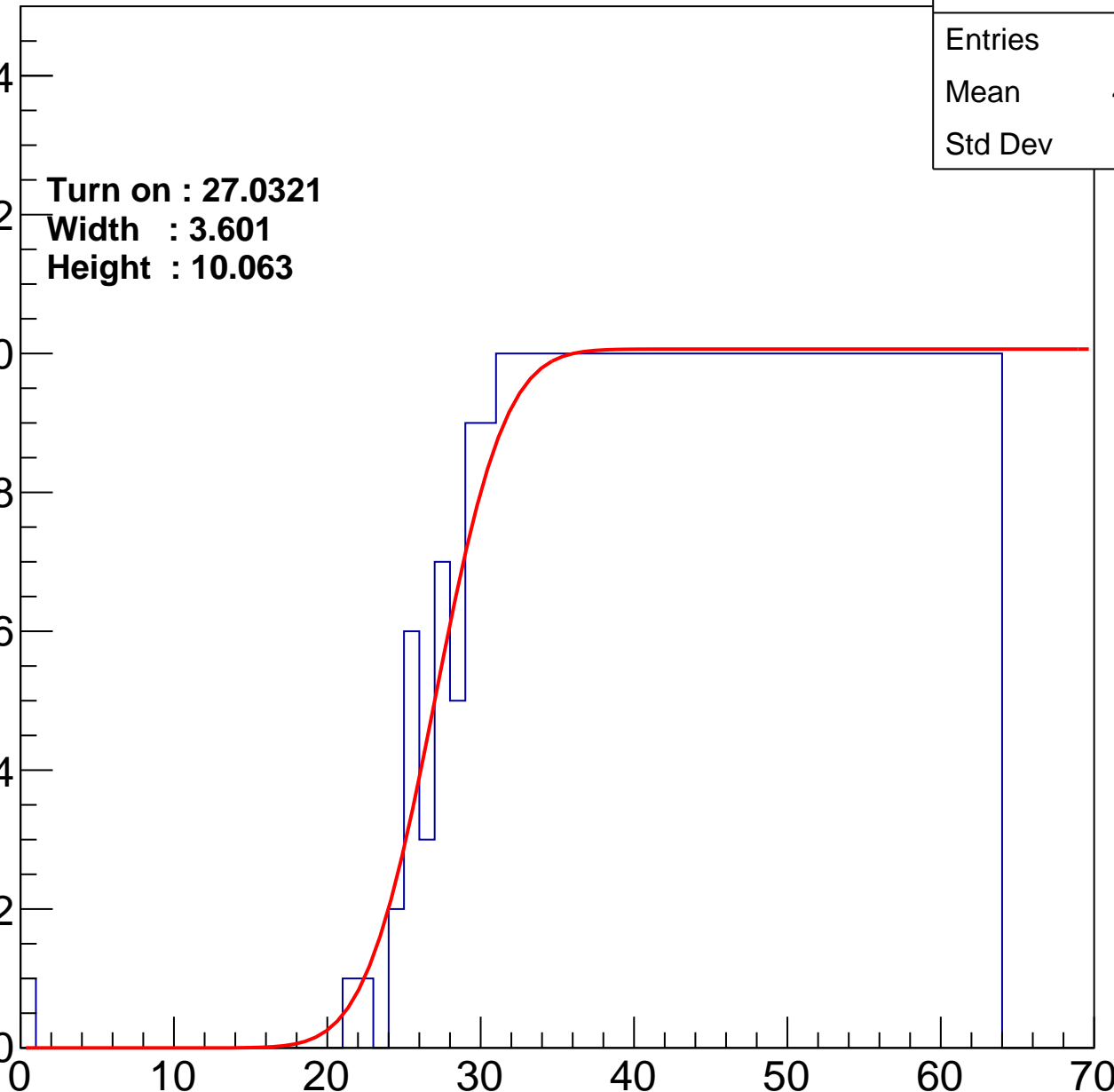
Width : 3.601

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch67

calib_packv5_042523_0143.root, FC#12, port B1

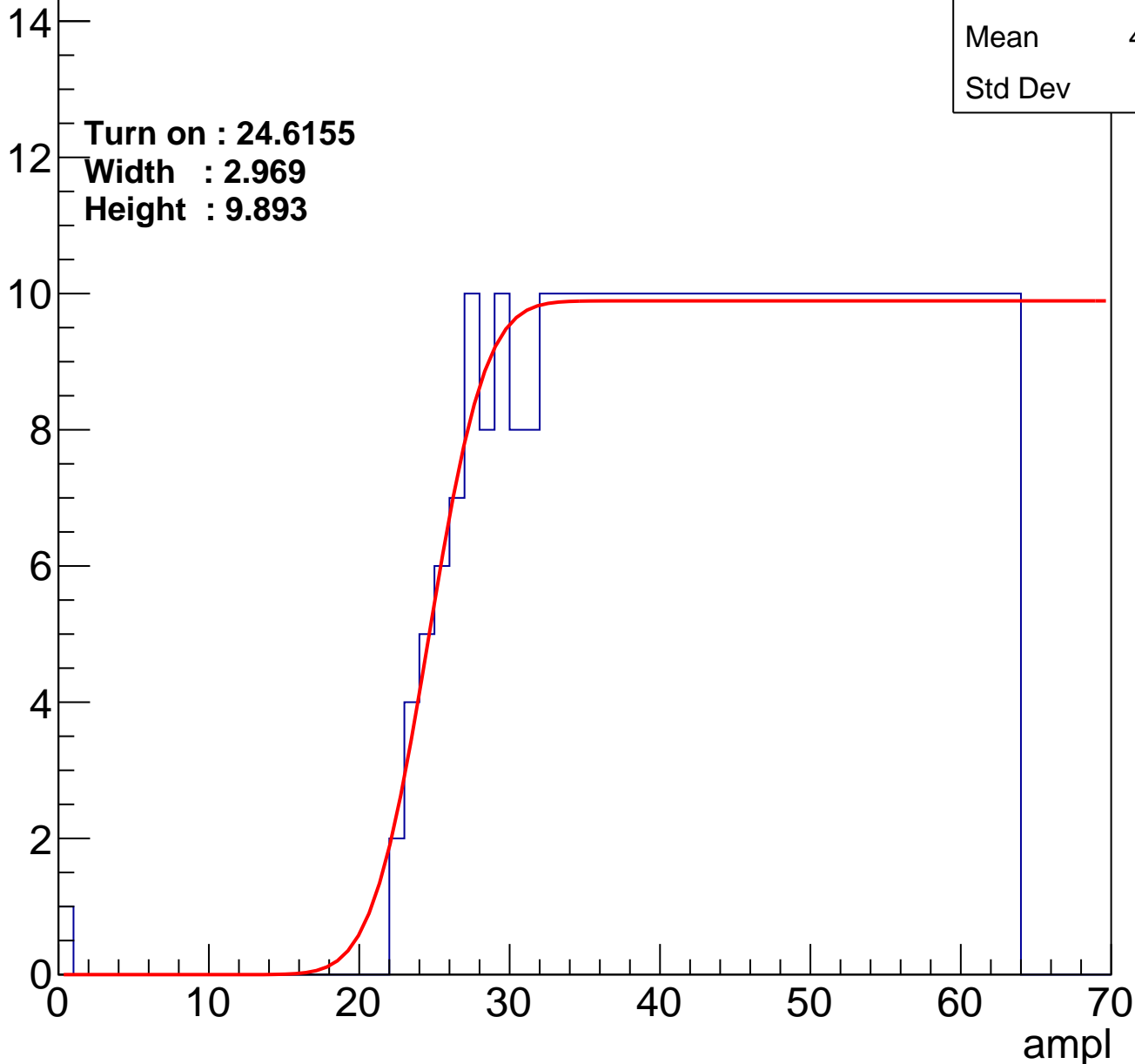
Entries	389
Mean	43.86
Std Dev	11.61

Turn on : 24.6155

Width : 2.969

Height : 9.893

Entry



B0L102S, U17-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.78
Std Dev	12

Turn on : 26.1540

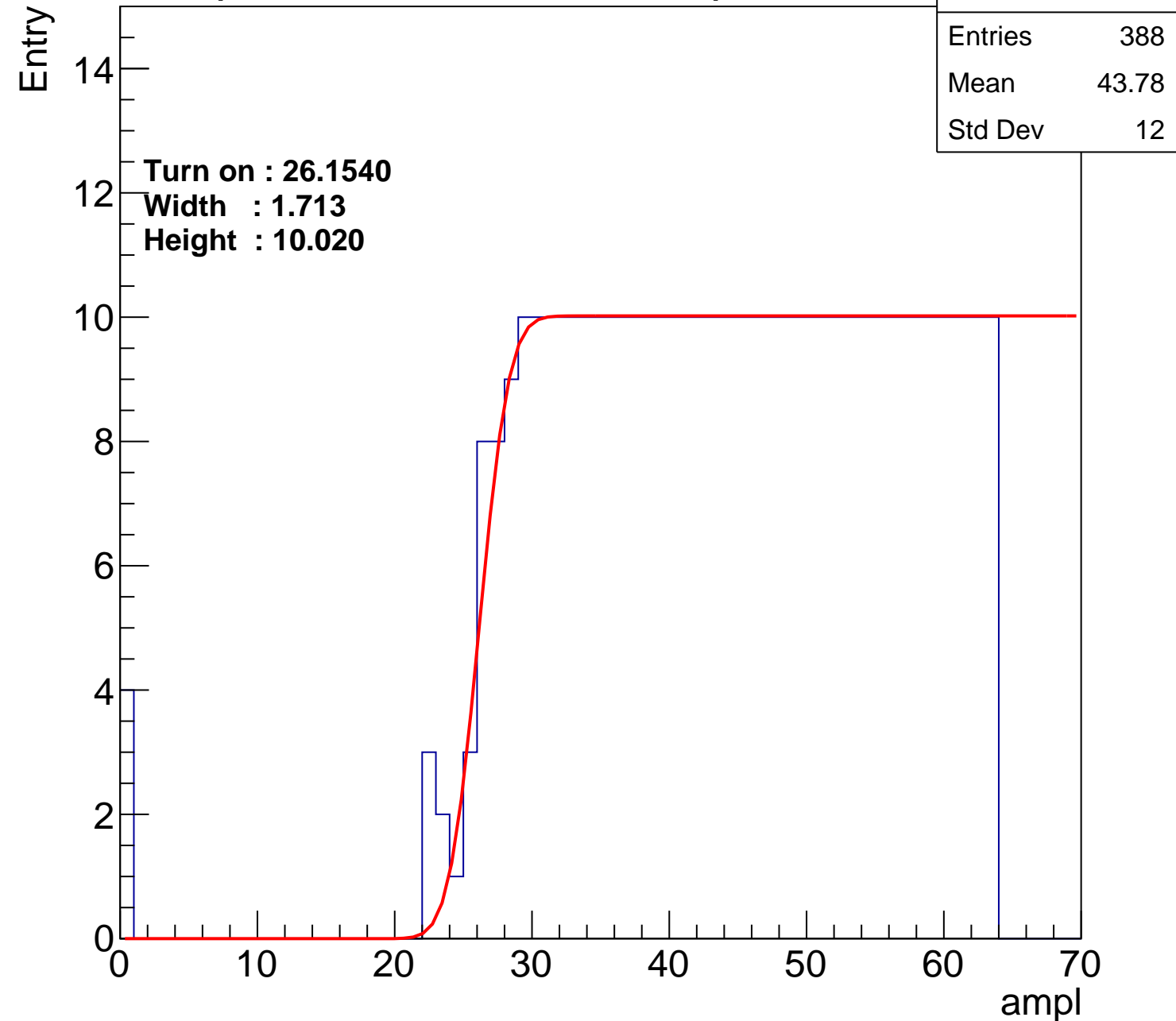
Width : 1.713

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch69

calib_packv5_042523_0143.root, FC#12, port B1

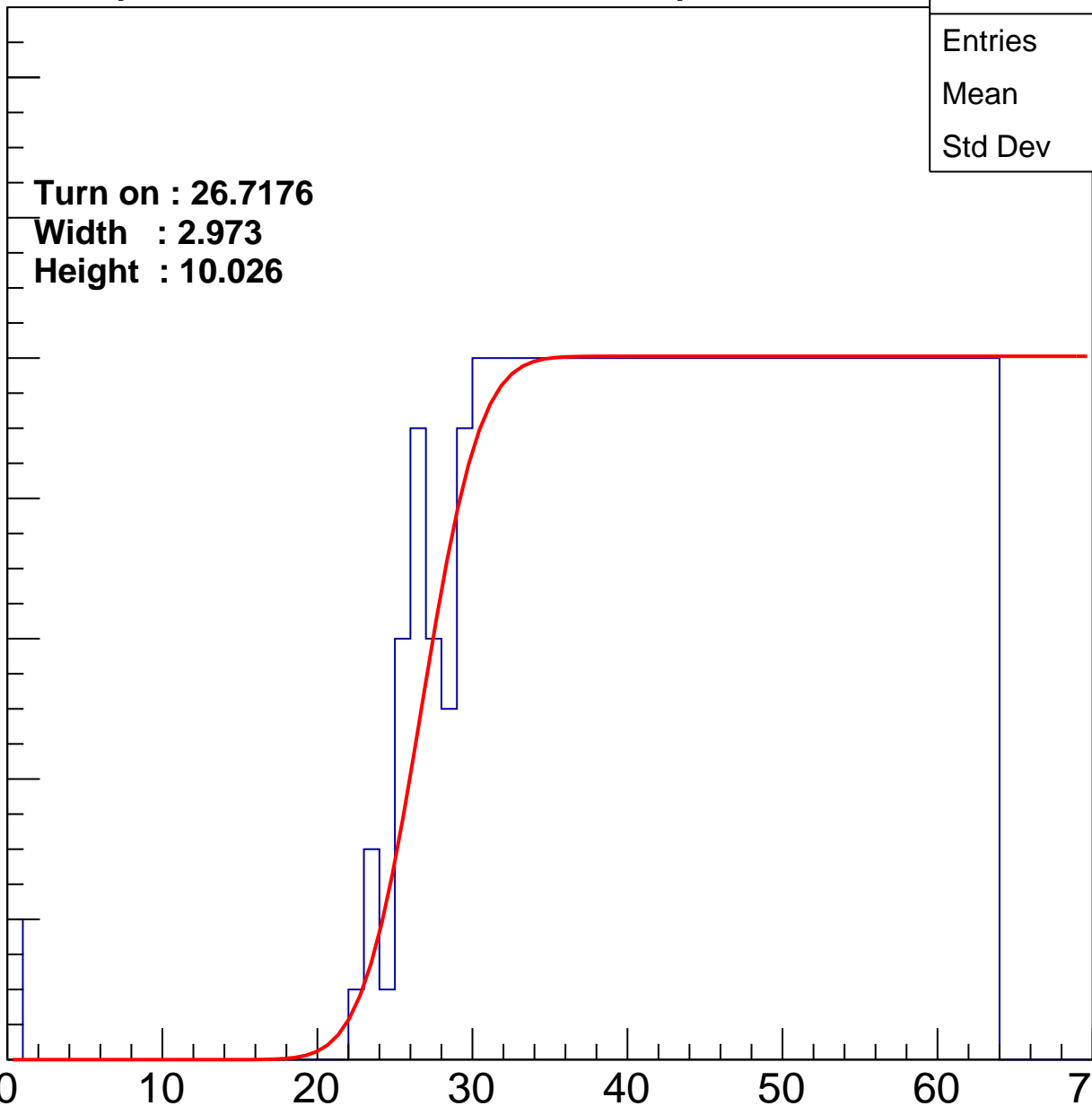
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7176
Width : 2.973
Height : 10.026

Entries	382
Mean	44.17
Std Dev	11.56

ampl



B0L102S, U17-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.51
Std Dev	11.76

Turn on : 24.7329

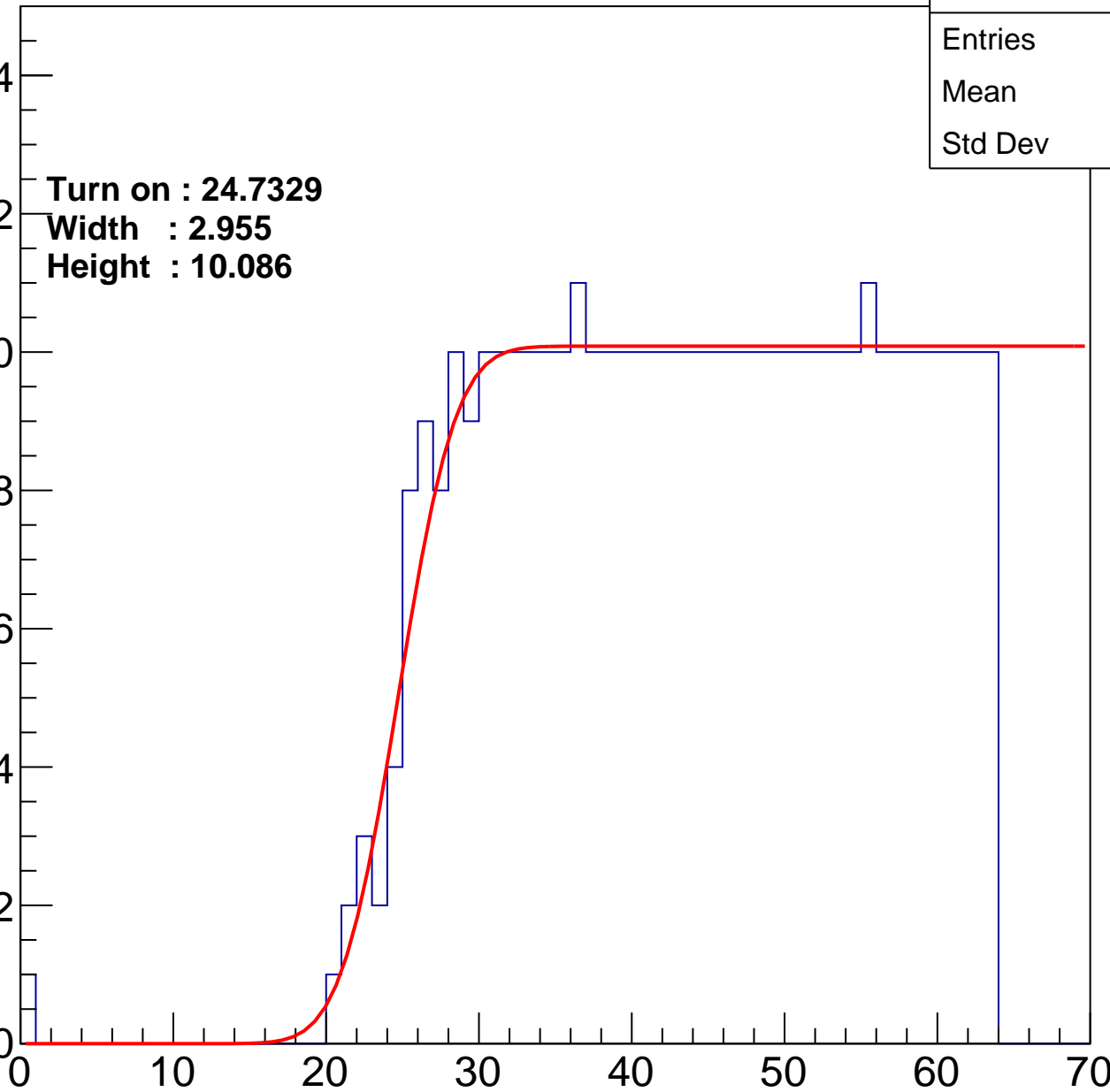
Width : 2.955

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.14
Std Dev	11.82

Turn on : 26.7730

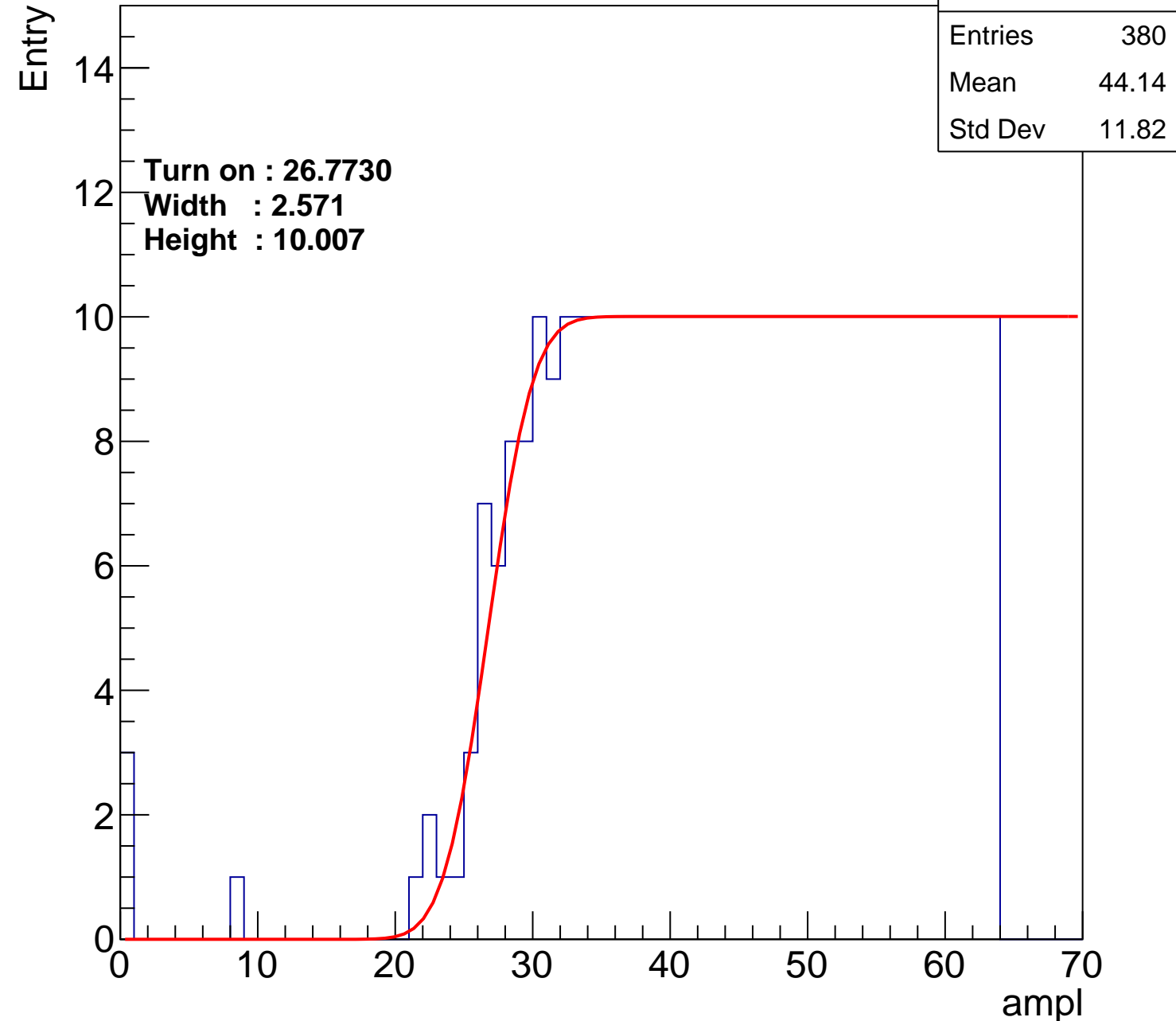
Width : 2.571

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.23
Std Dev	12.04

Turn on : 24.4178

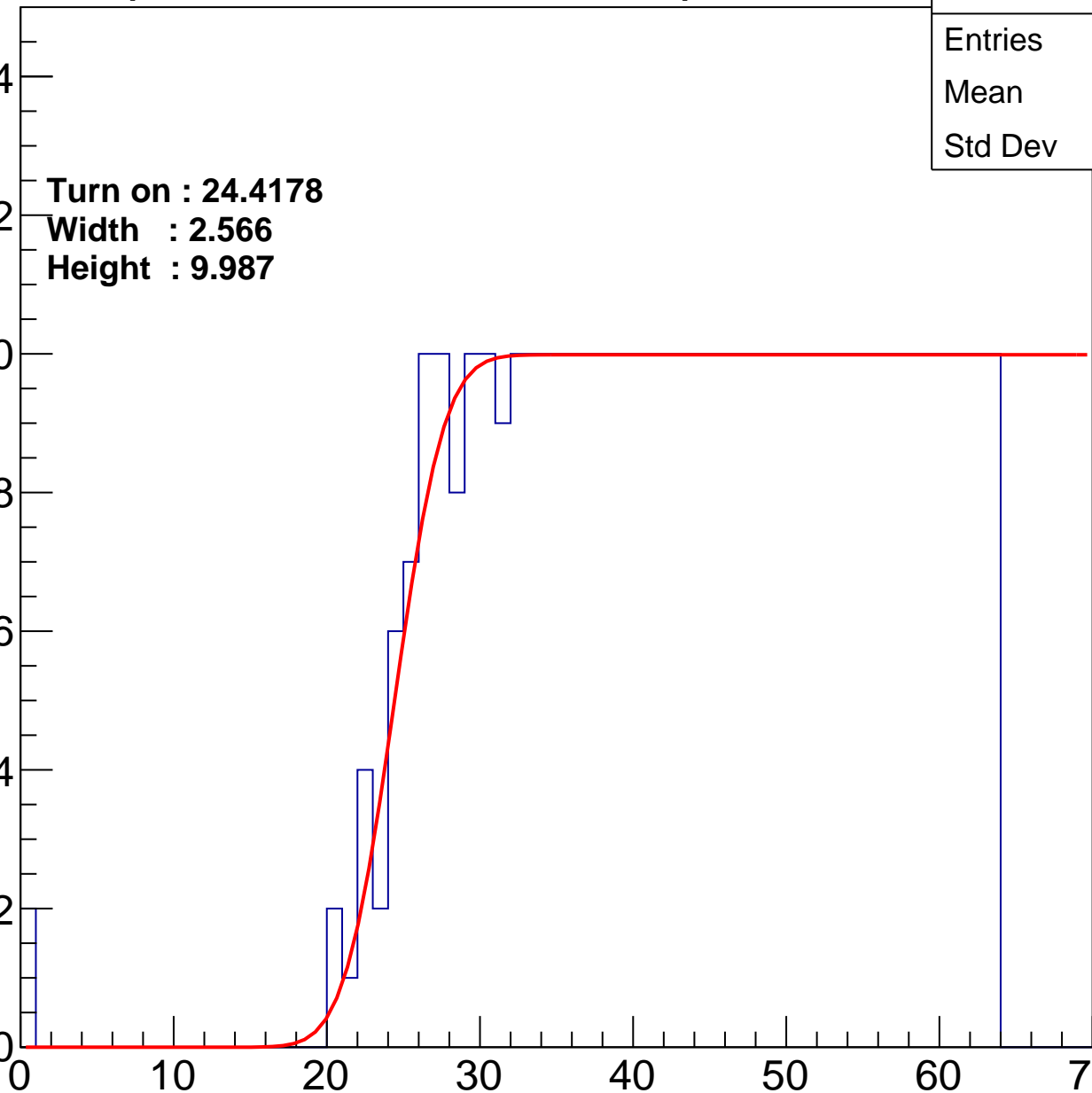
Width : 2.566

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.27
Std Dev	11.82

Turn on : 27.1483

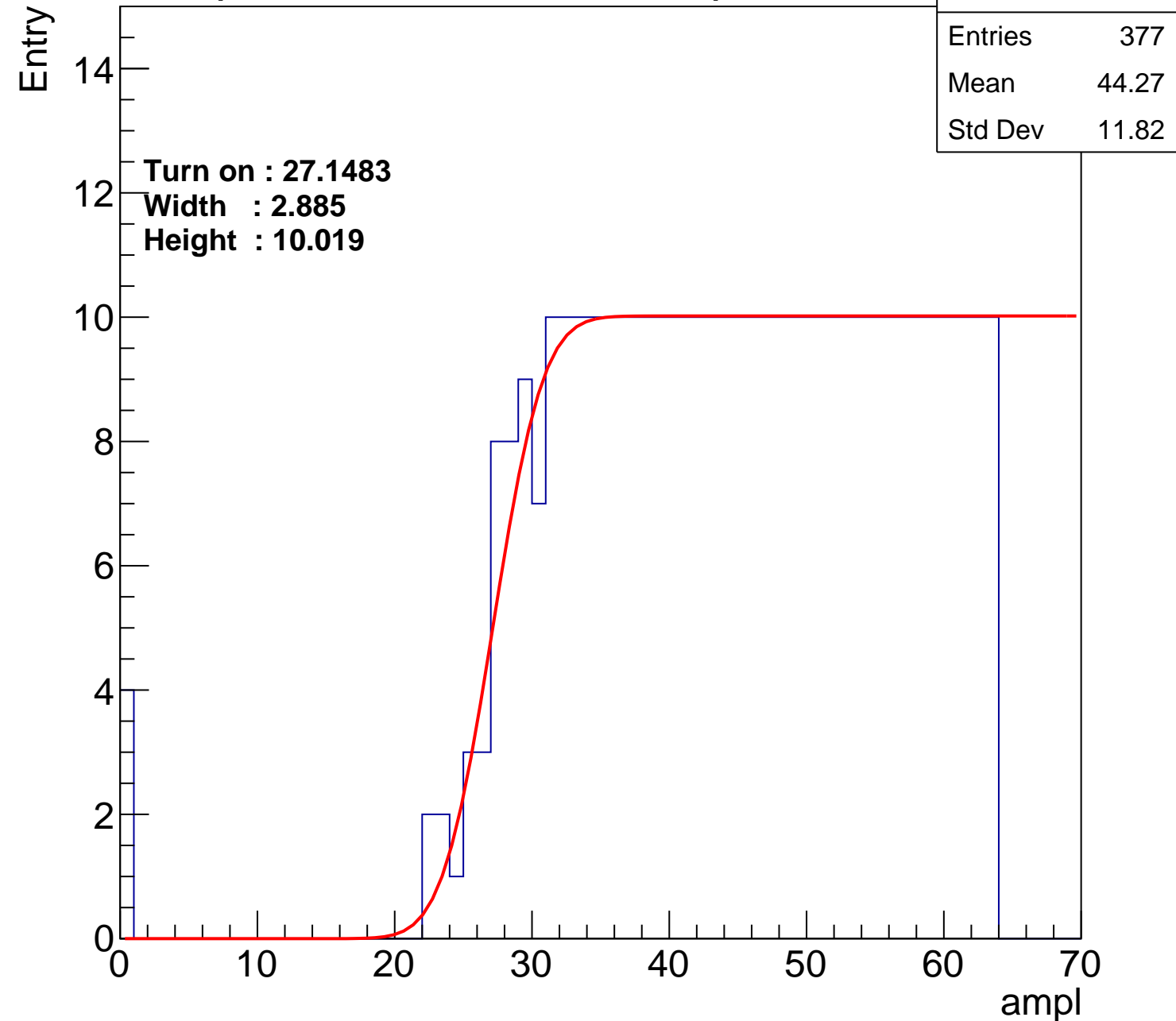
Width : 2.885

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.54
Std Dev	12.1

Turn on : 25.6823

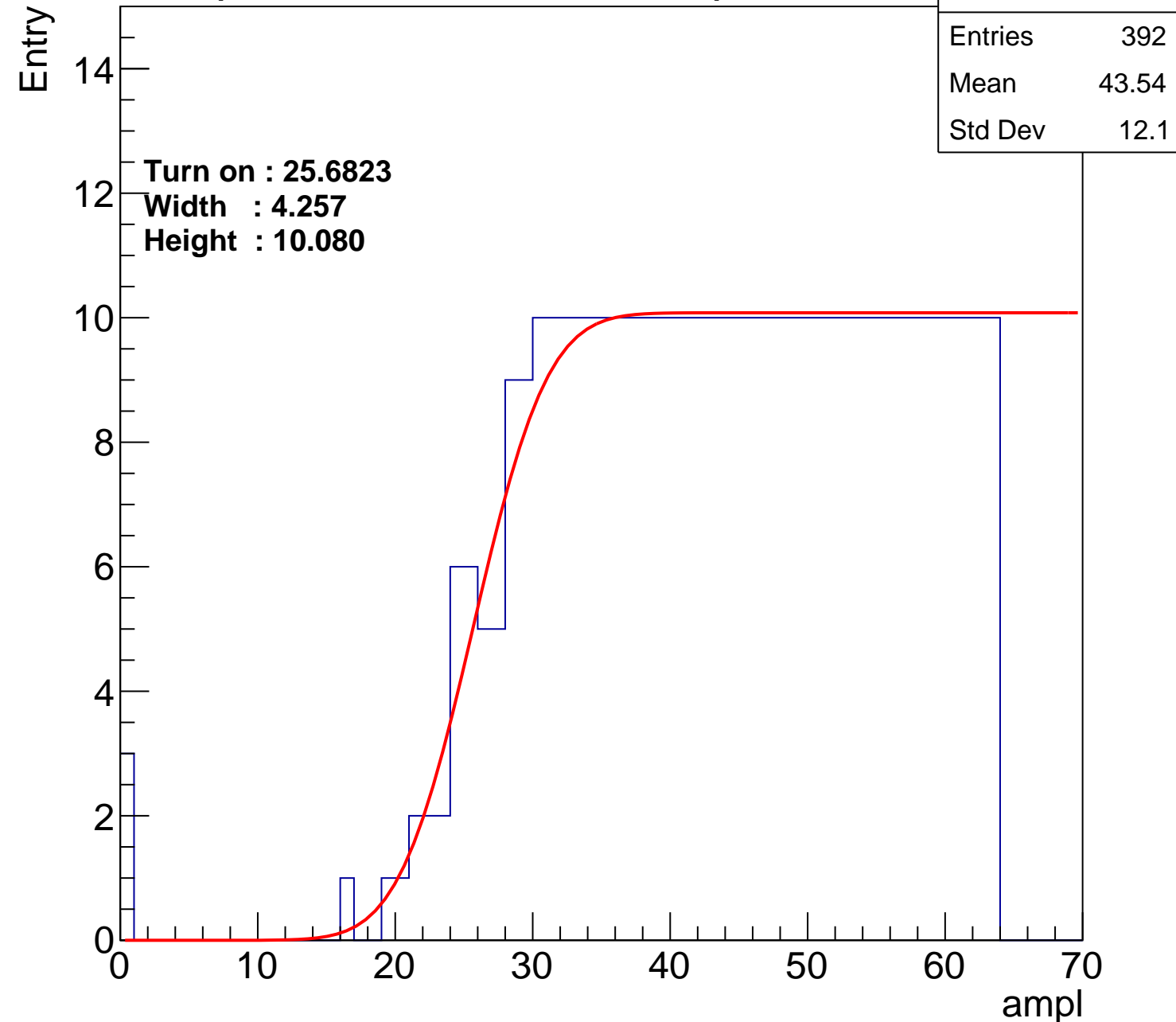
Width : 4.257

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch75

calib_packv5_042523_0143.root, FC#12, port B1

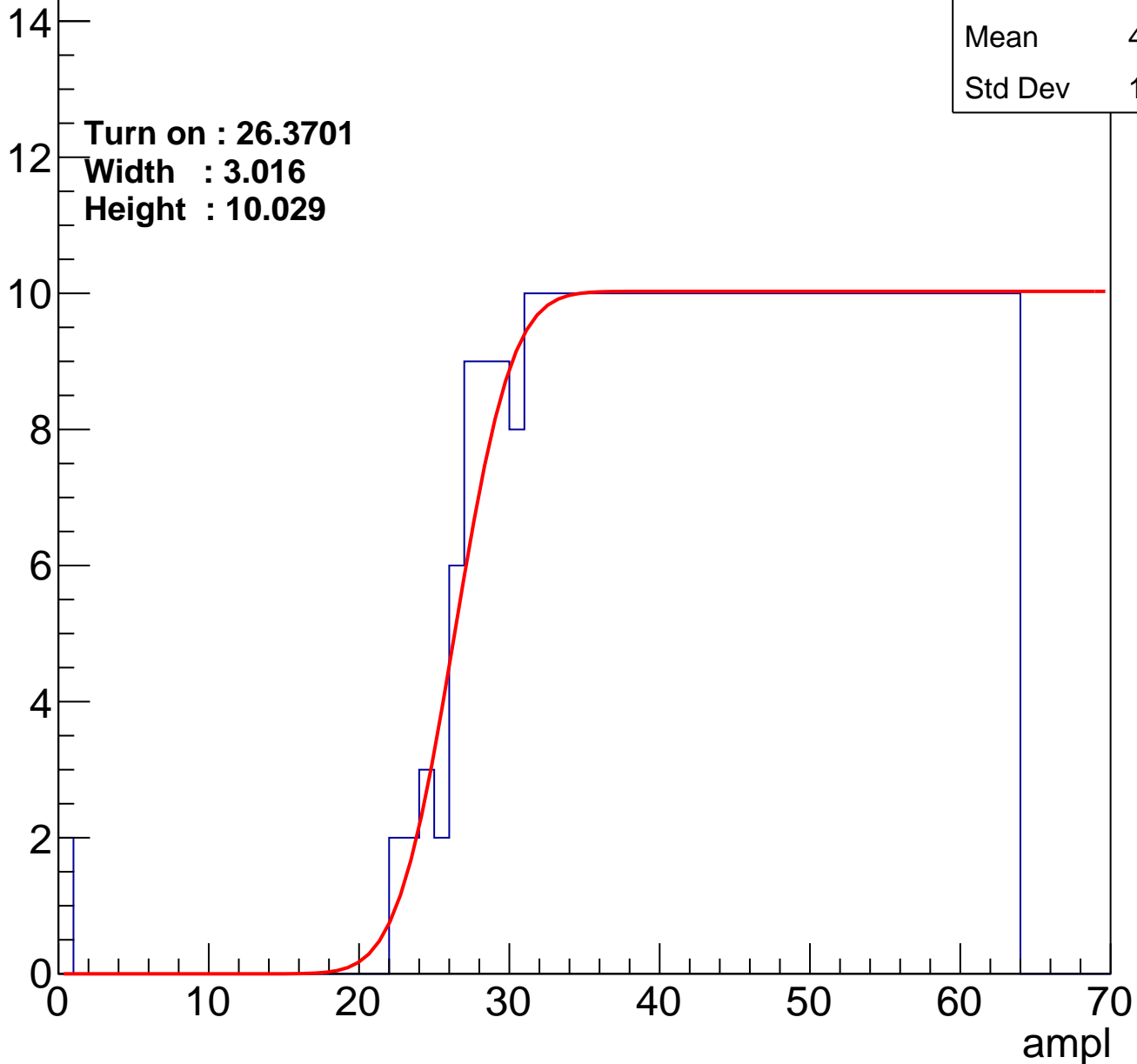
Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 26.3701

Width : 3.016

Height : 10.029

Entry



B0L102S, U17-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.83
Std Dev	12.75

Turn on : 24.6391

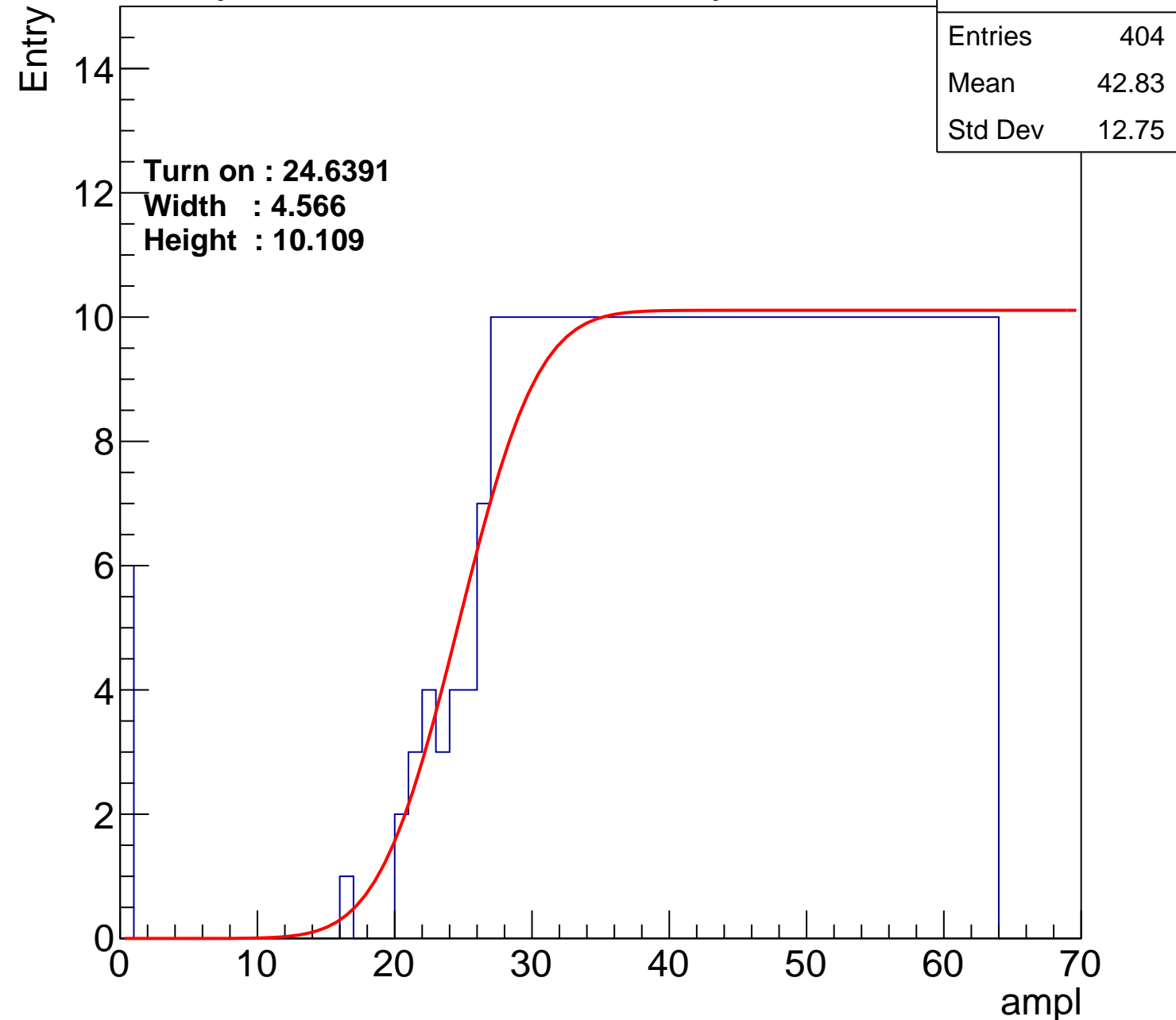
Width : 4.566

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.03
Std Dev	12.04

Turn on : 25.9703

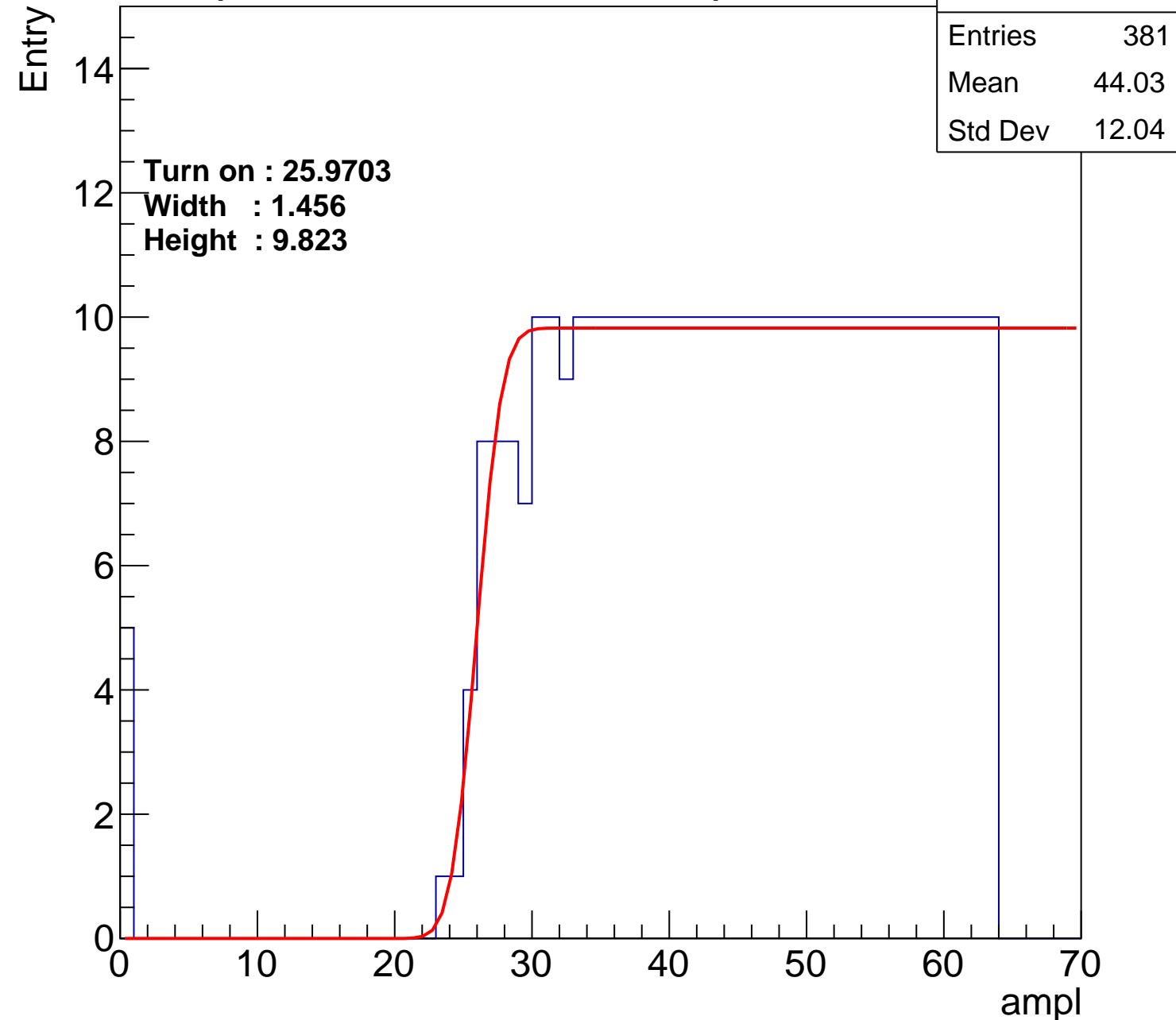
Width : 1.456

Height : 9.823

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch78

calib_packv5_042523_0143.root, FC#12, port B1

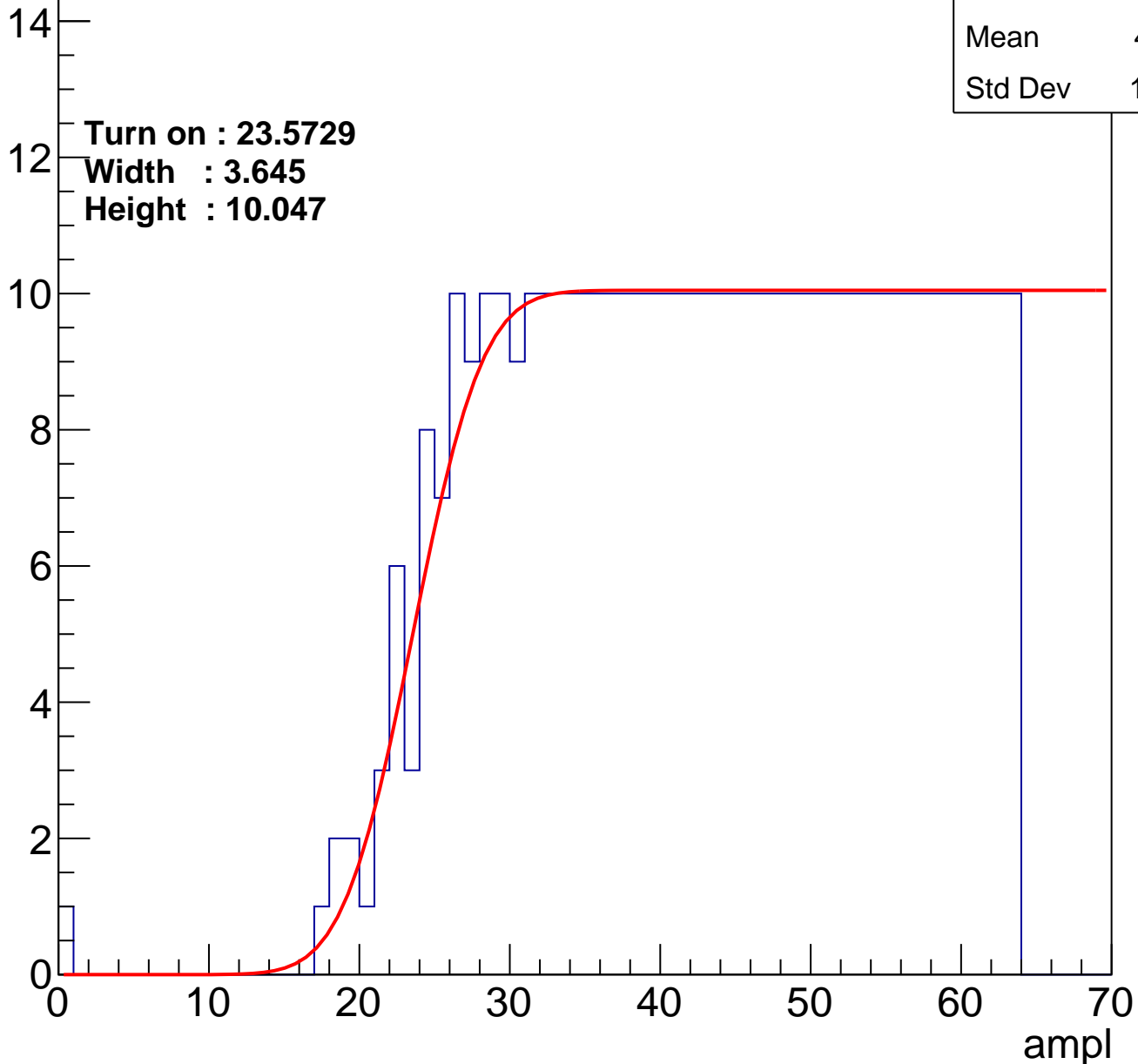
Entries	412
Mean	42.71
Std Dev	12.27

Turn on : 23.5729

Width : 3.645

Height : 10.047

Entry



B0L102S, U17-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.6502

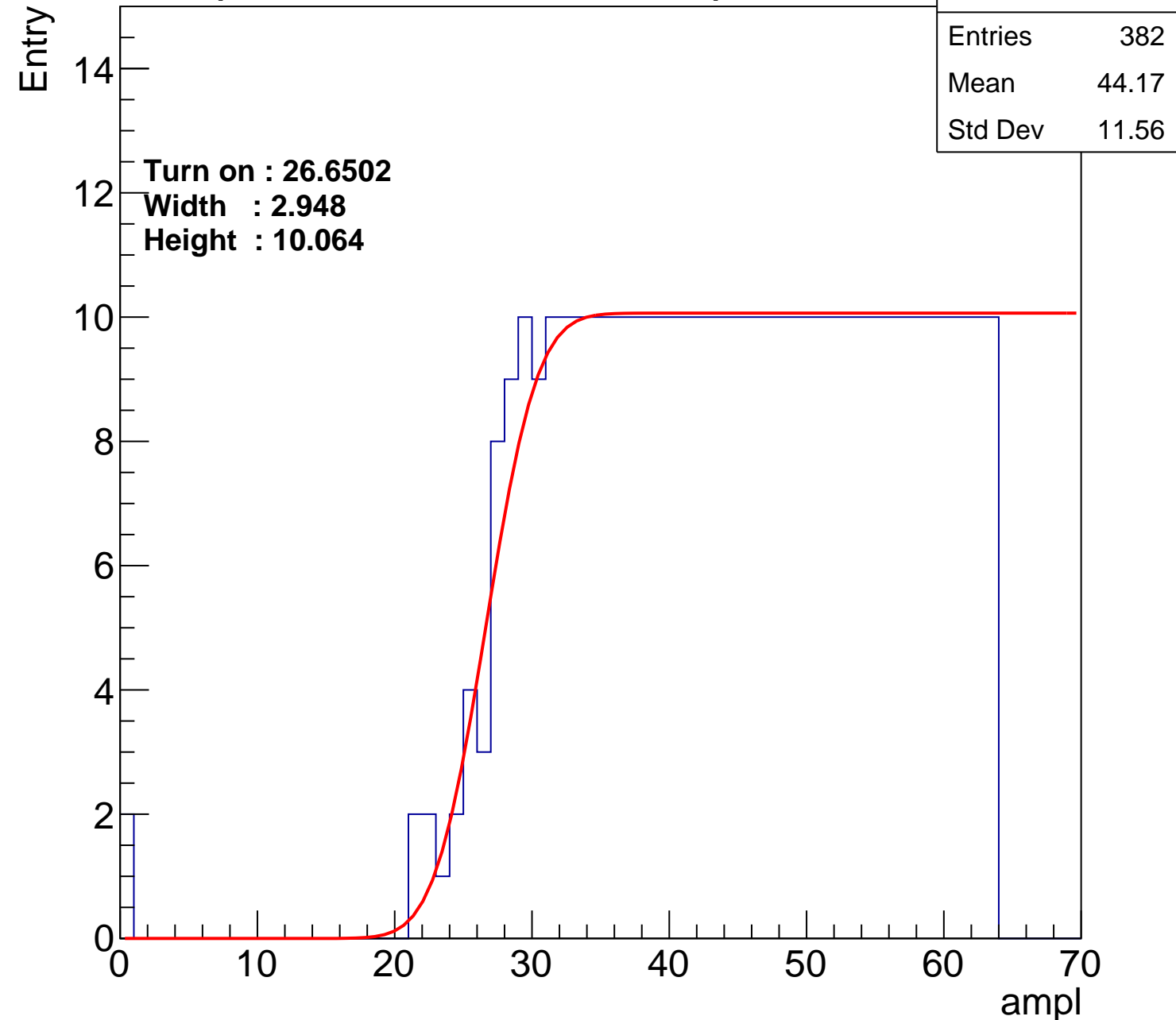
Width : 2.948

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch80

calib_packv5_042523_0143.root, FC#12, port B1

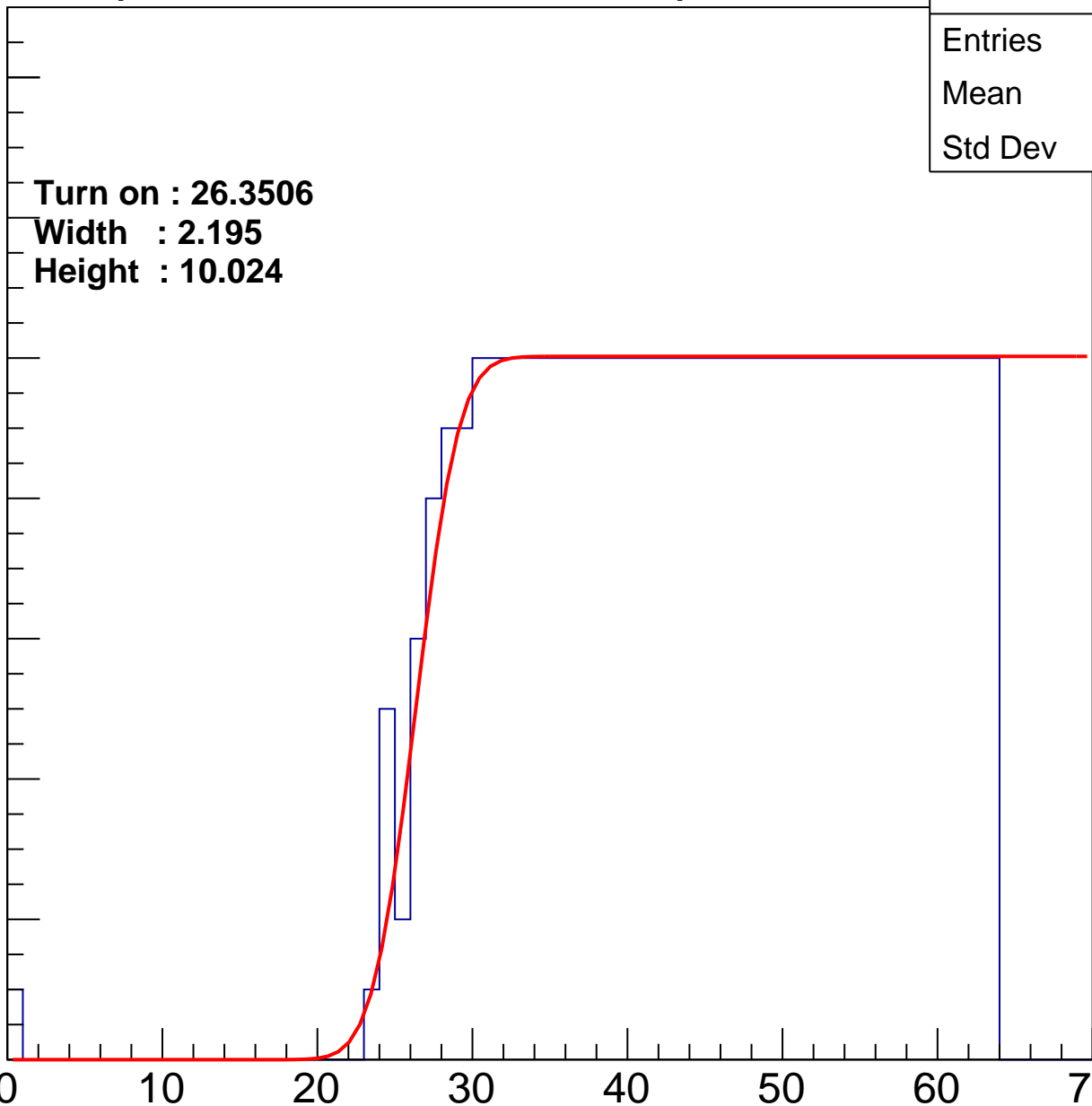
Entry

14
12
10
8
6
4
2
0

Turn on : 26.3506
Width : 2.195
Height : 10.024

Entries	381
Mean	44.33
Std Dev	11.28

ampl



B0L102S, U17-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.24
Std Dev	11.56

Turn on : 26.6925

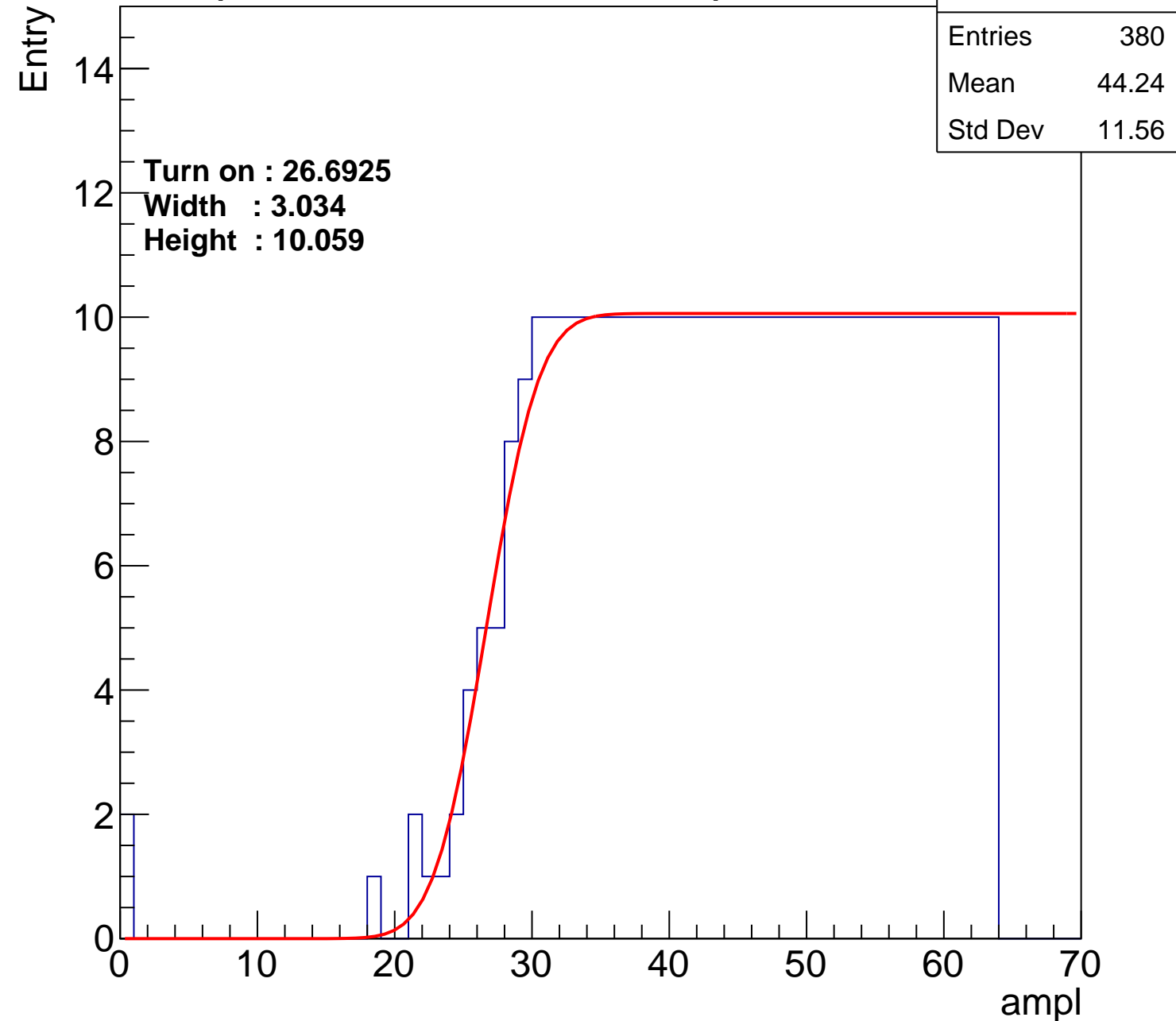
Width : 3.034

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.87
Std Dev	12.14

Turn on : 26.4000

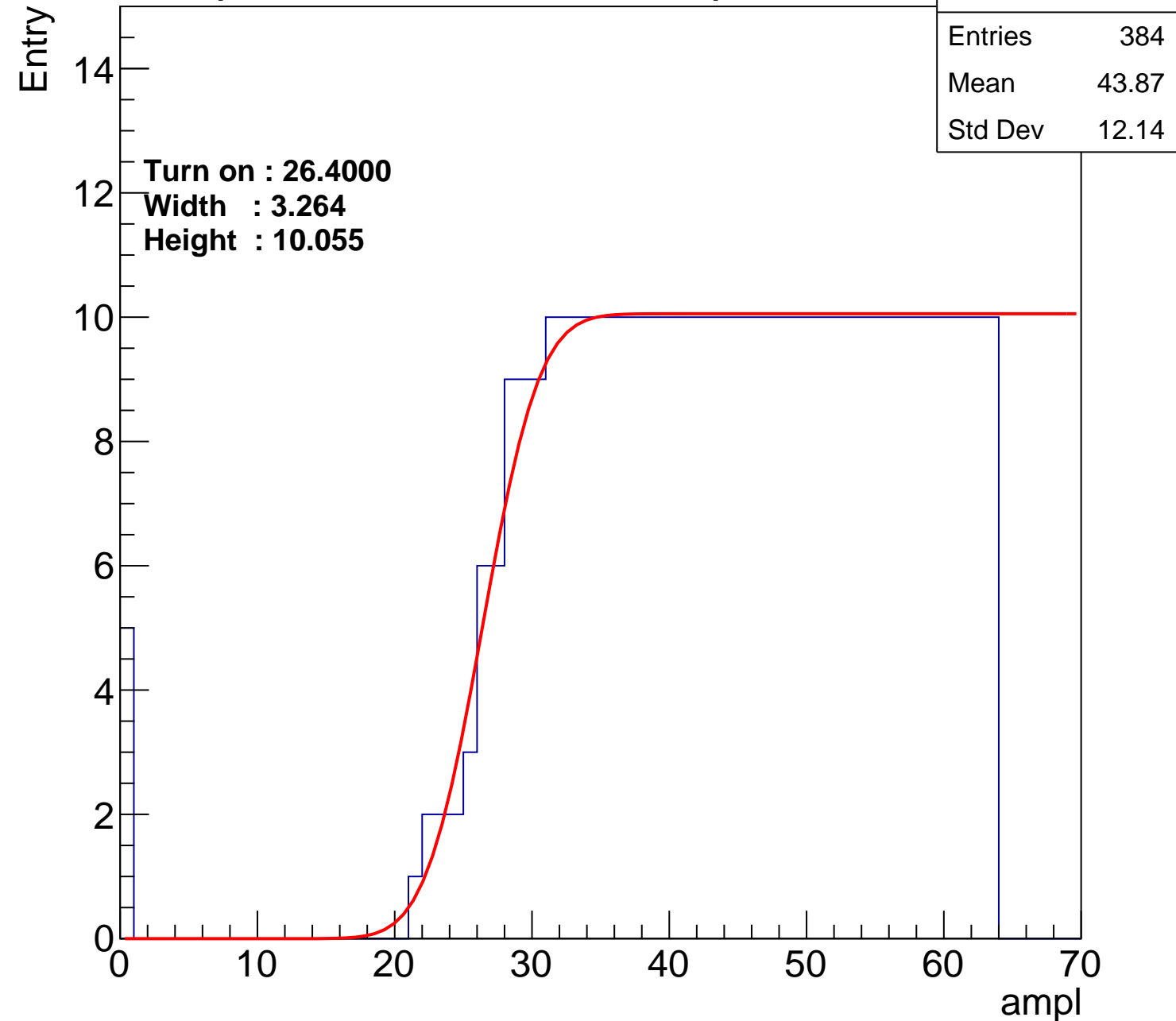
Width : 3.264

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch83

calib_packv5_042523_0143.root, FC#12, port B1

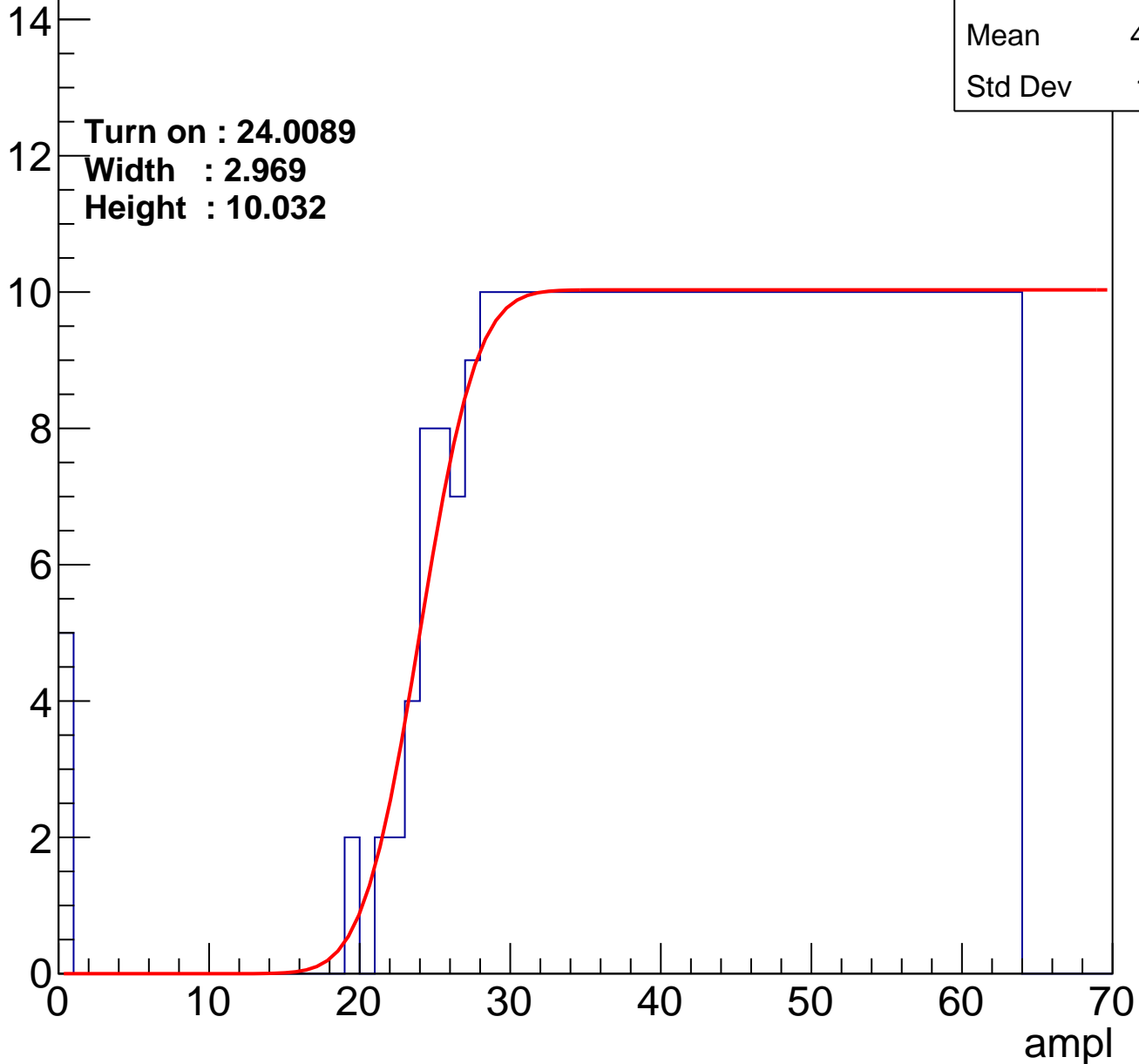
Entries	407
Mean	42.78
Std Dev	12.61

Turn on : 24.0089

Width : 2.969

Height : 10.032

Entry



B0L102S, U17-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.1
Std Dev	12.42

Turn on : 24.6955

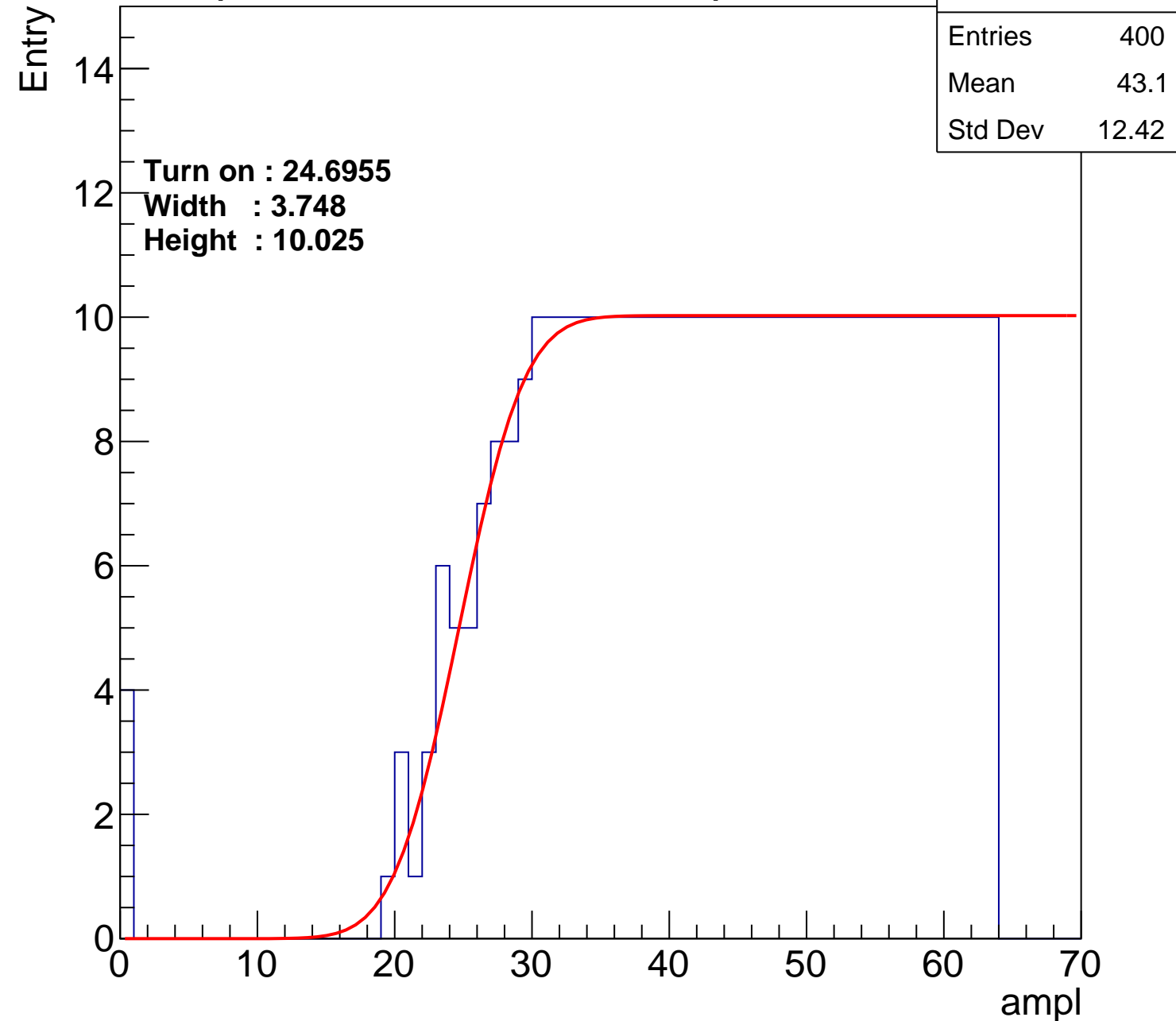
Width : 3.748

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.55
Std Dev	12.31

Turn on : 25.5802

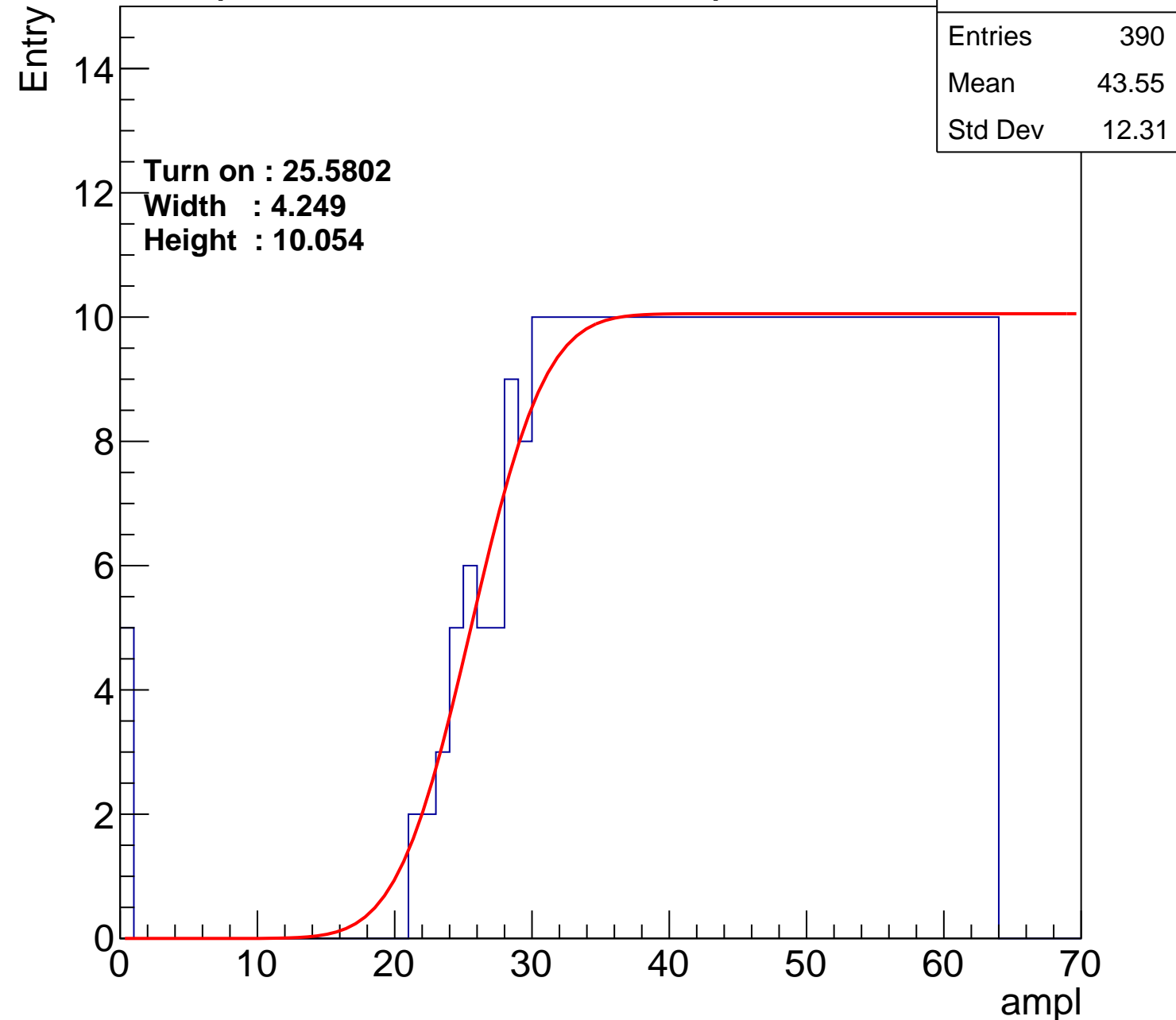
Width : 4.249

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.0622

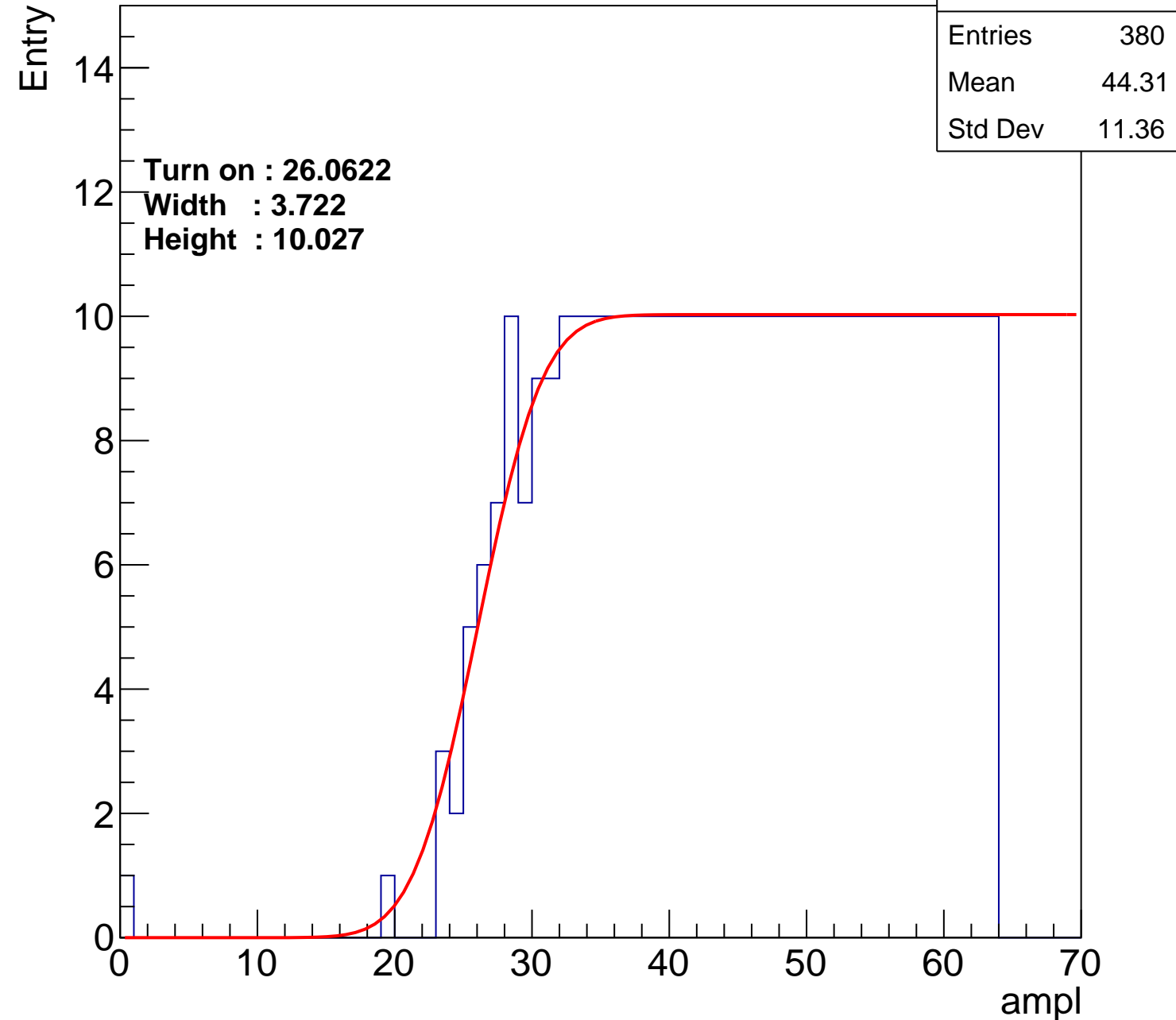
Width : 3.722

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	43.87
Std Dev	12.41

Turn on : 26.5225

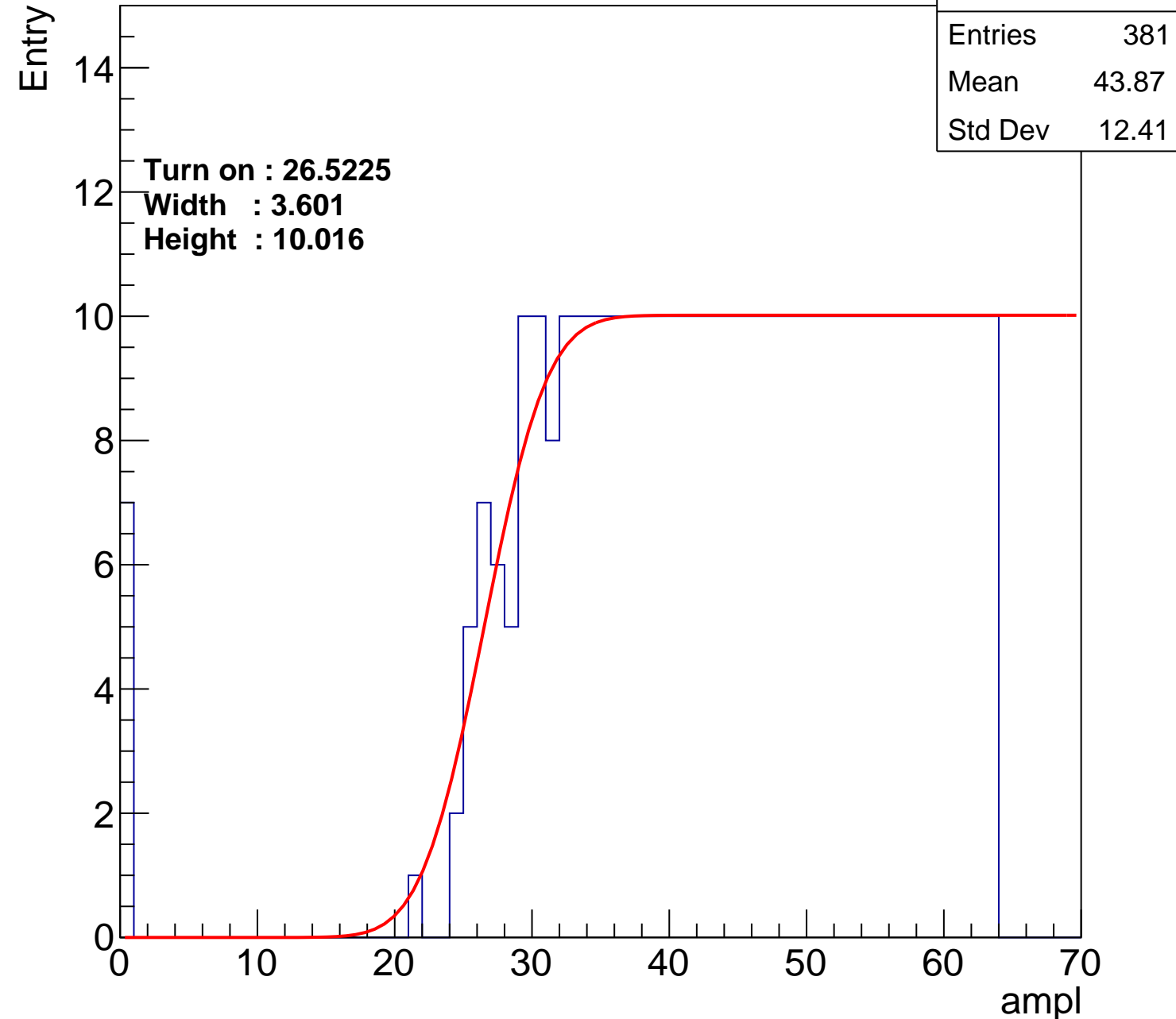
Width : 3.601

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.55
Std Dev	12.04

Turn on : 24.8445

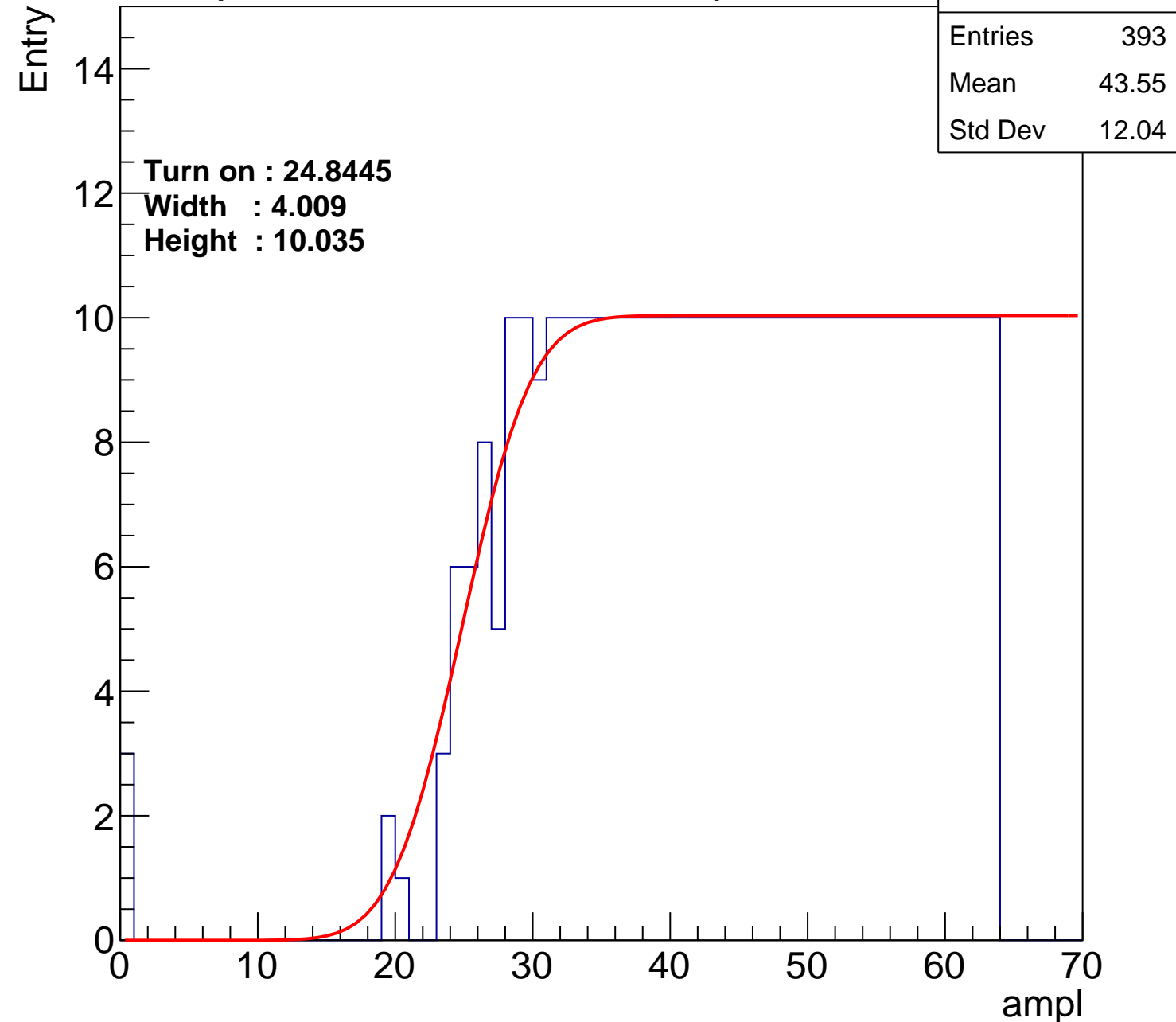
Width : 4.009

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.99
Std Dev	11.98

Turn on : 26.4550

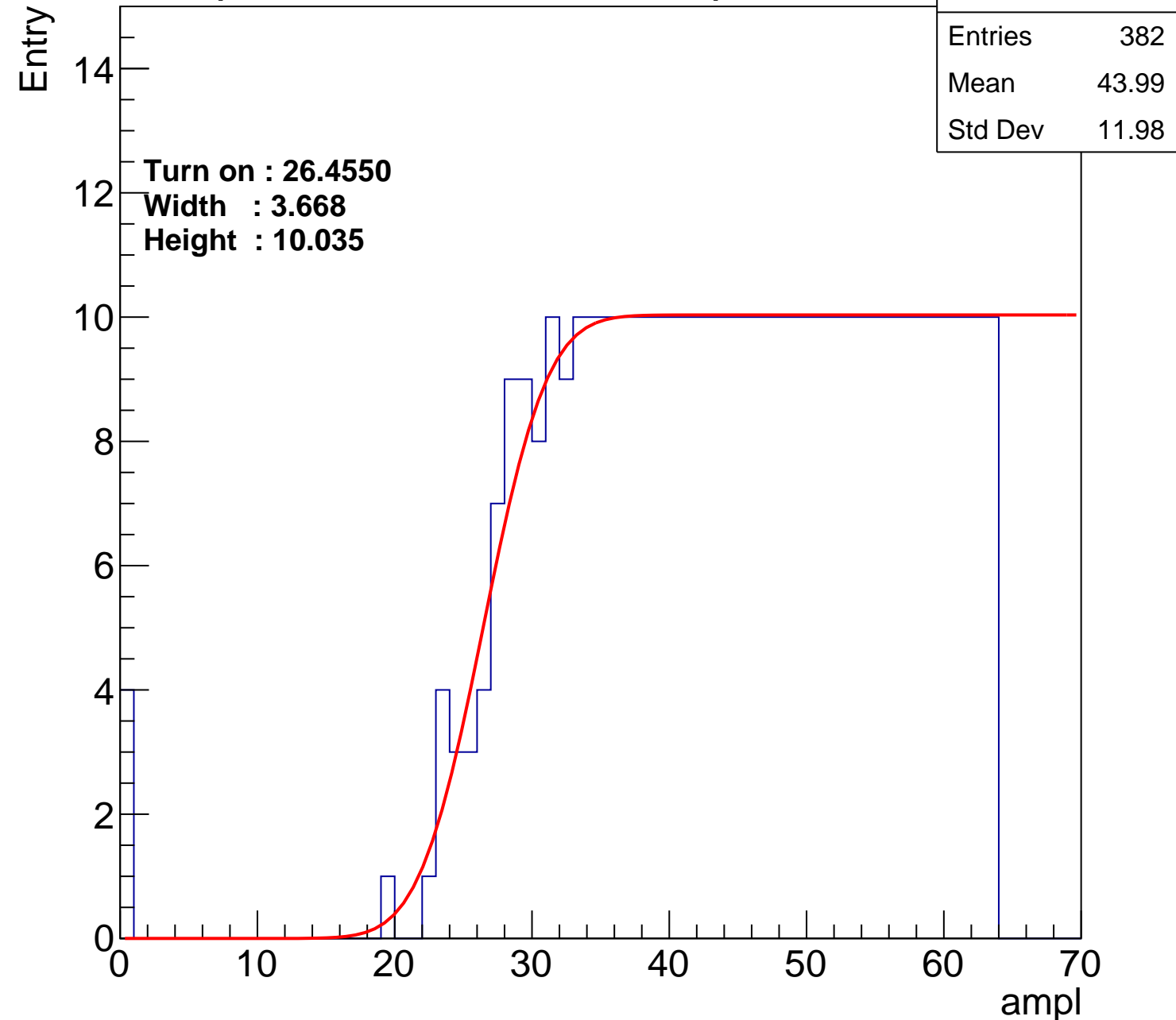
Width : 3.668

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.43
Std Dev	12.01

Turn on : 24.8591

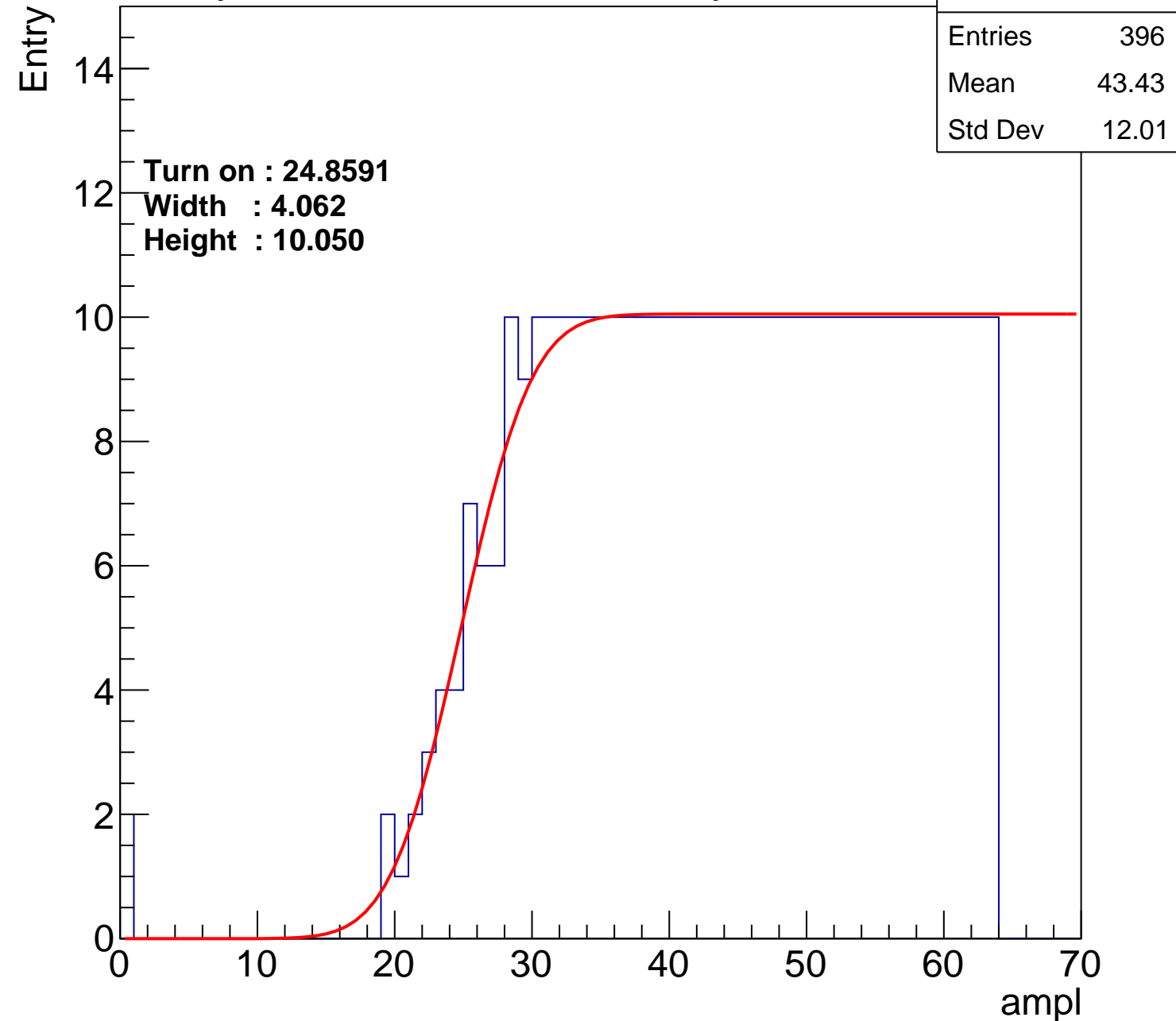
Width : 4.062

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.4405

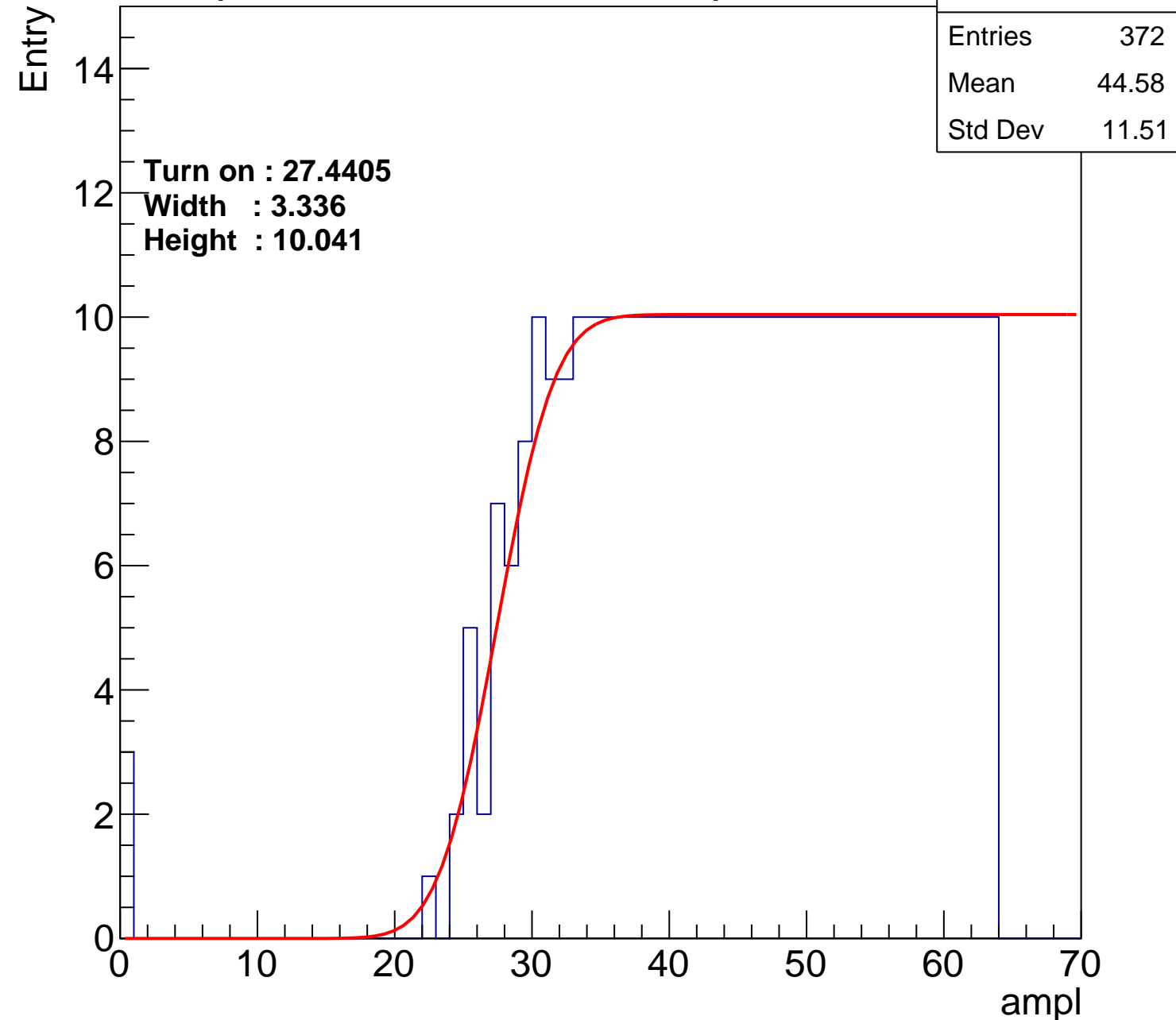
Width : 3.336

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.17
Std Dev	12.15

Turn on : 24.0060

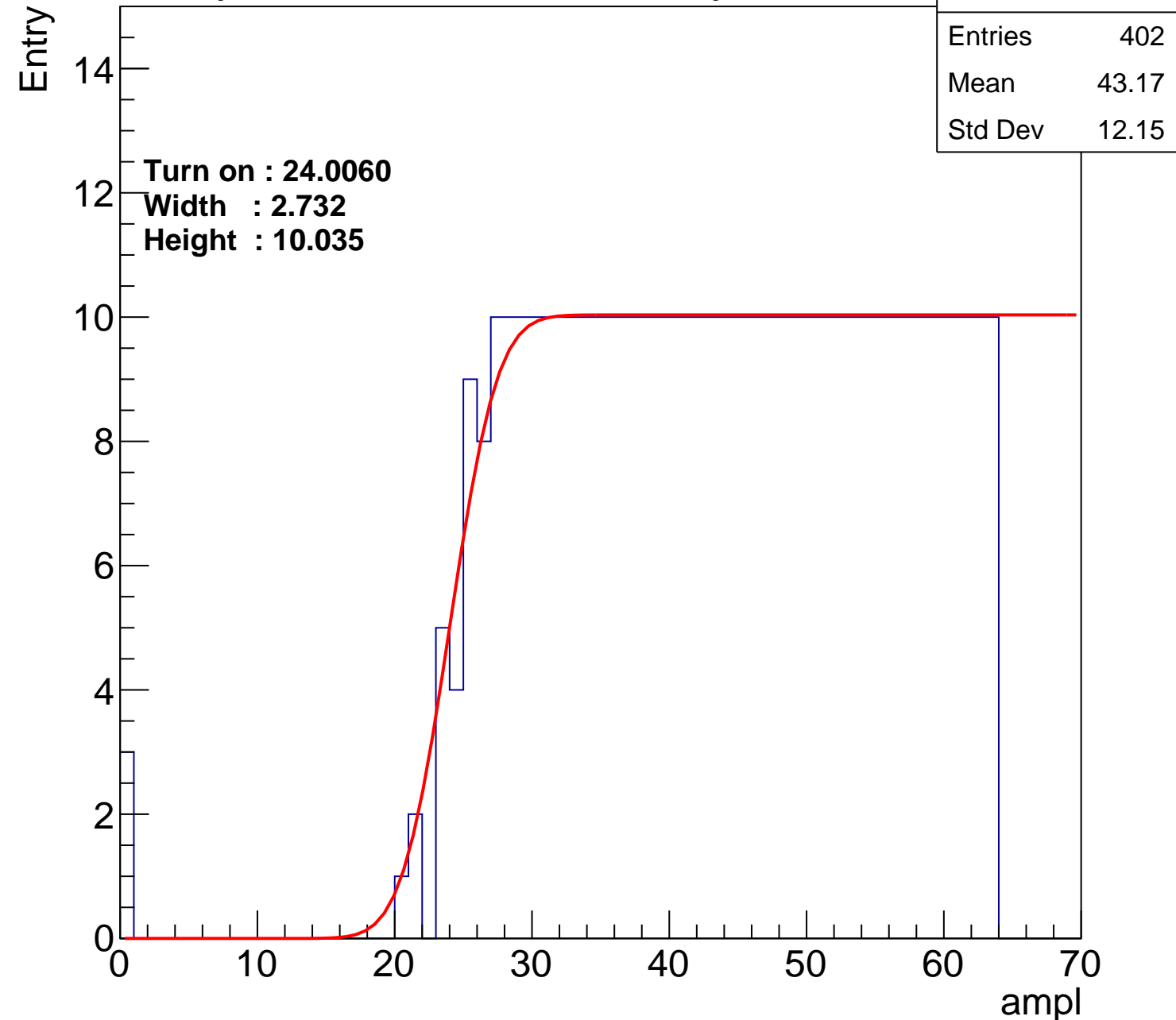
Width : 2.732

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.14
Std Dev	11.83

Turn on : 26.6419

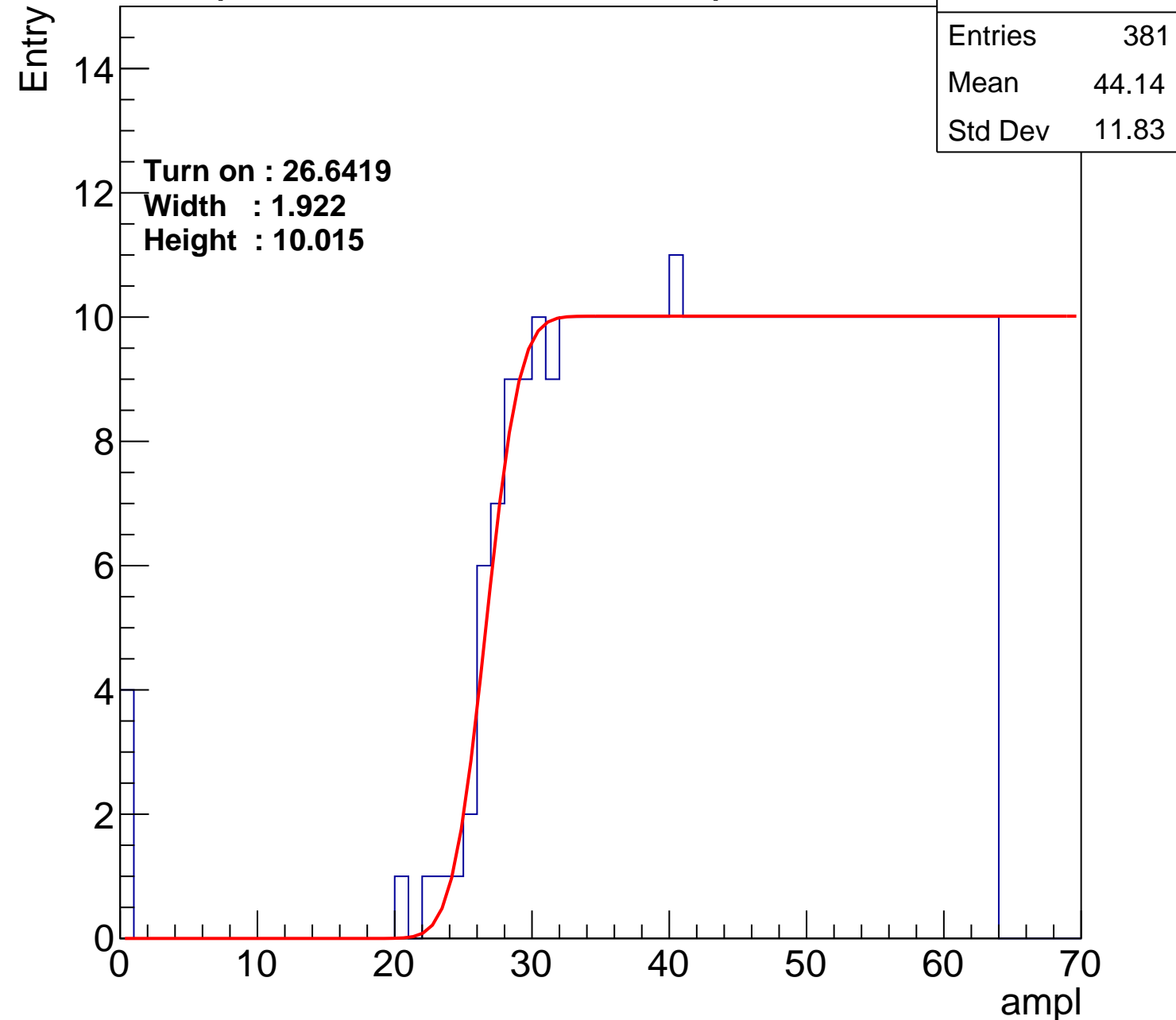
Width : 1.922

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch94

calib_packv5_042523_0143.root, FC#12, port B1

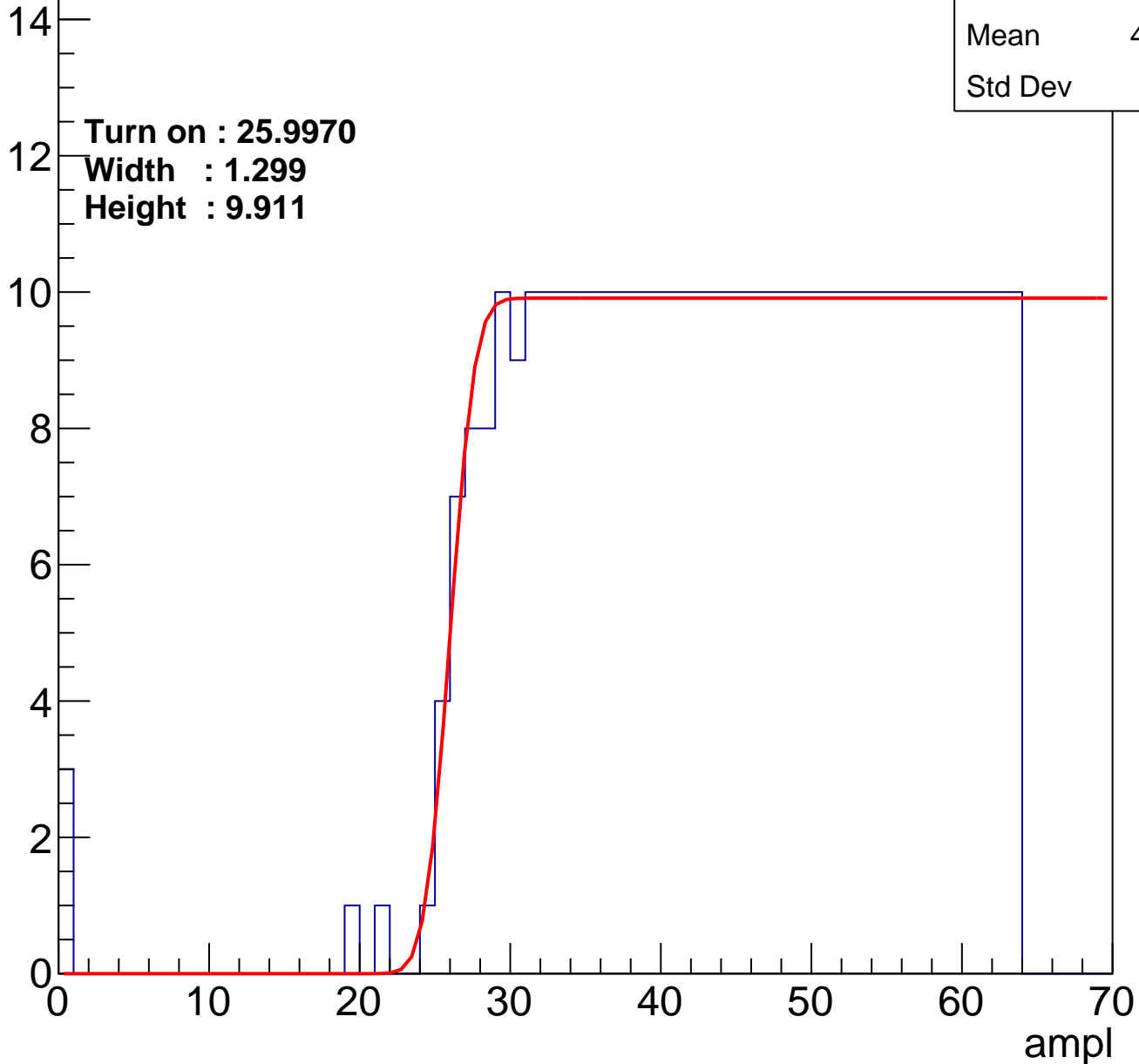
Entries	382
Mean	44.13
Std Dev	11.7

Turn on : 25.9970

Width : 1.299

Height : 9.911

Entry



B0L102S, U17-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.94
Std Dev	12.53

Turn on : 24.5958

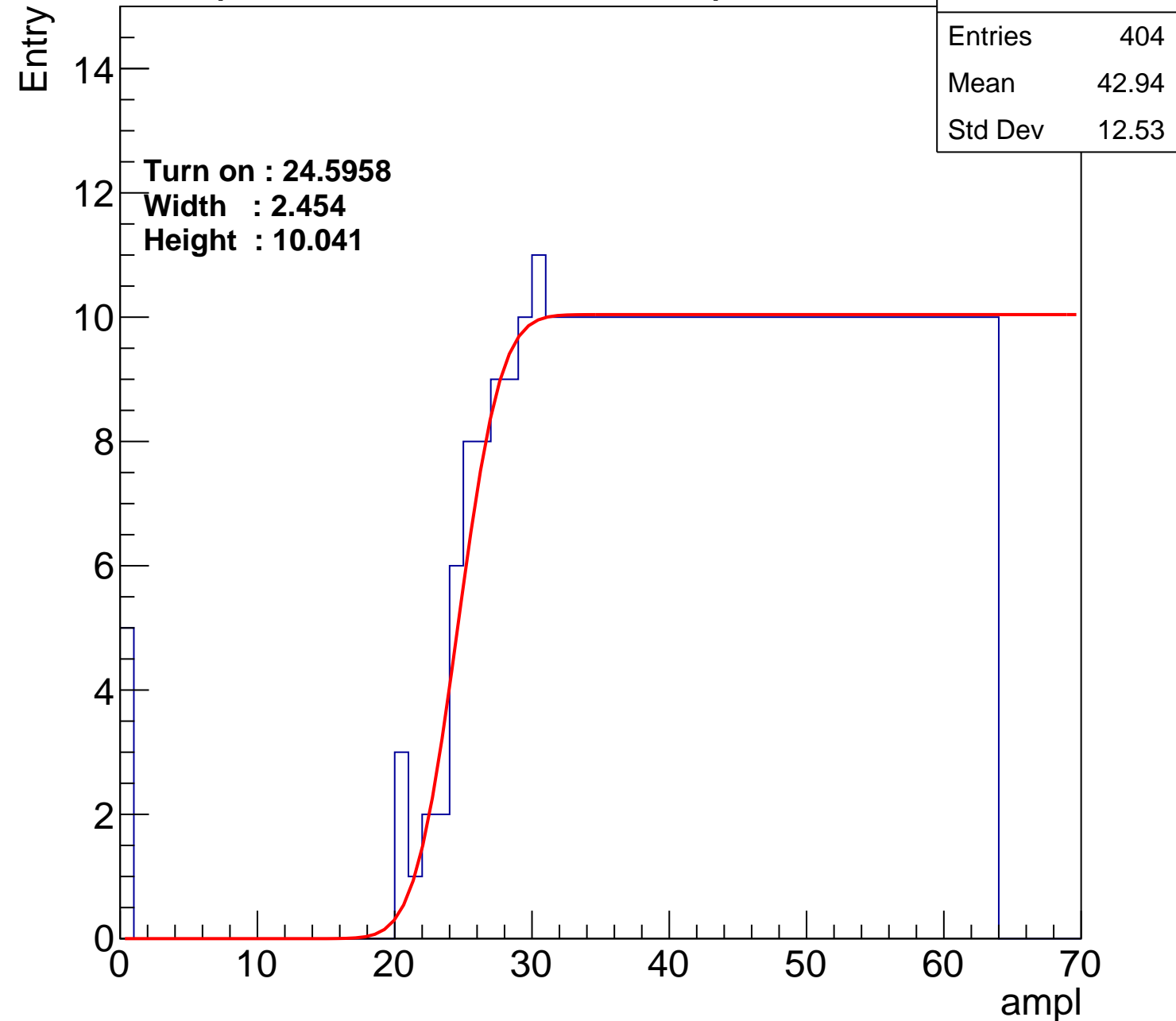
Width : 2.454

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.46
Std Dev	12.24

Turn on : 25.3847

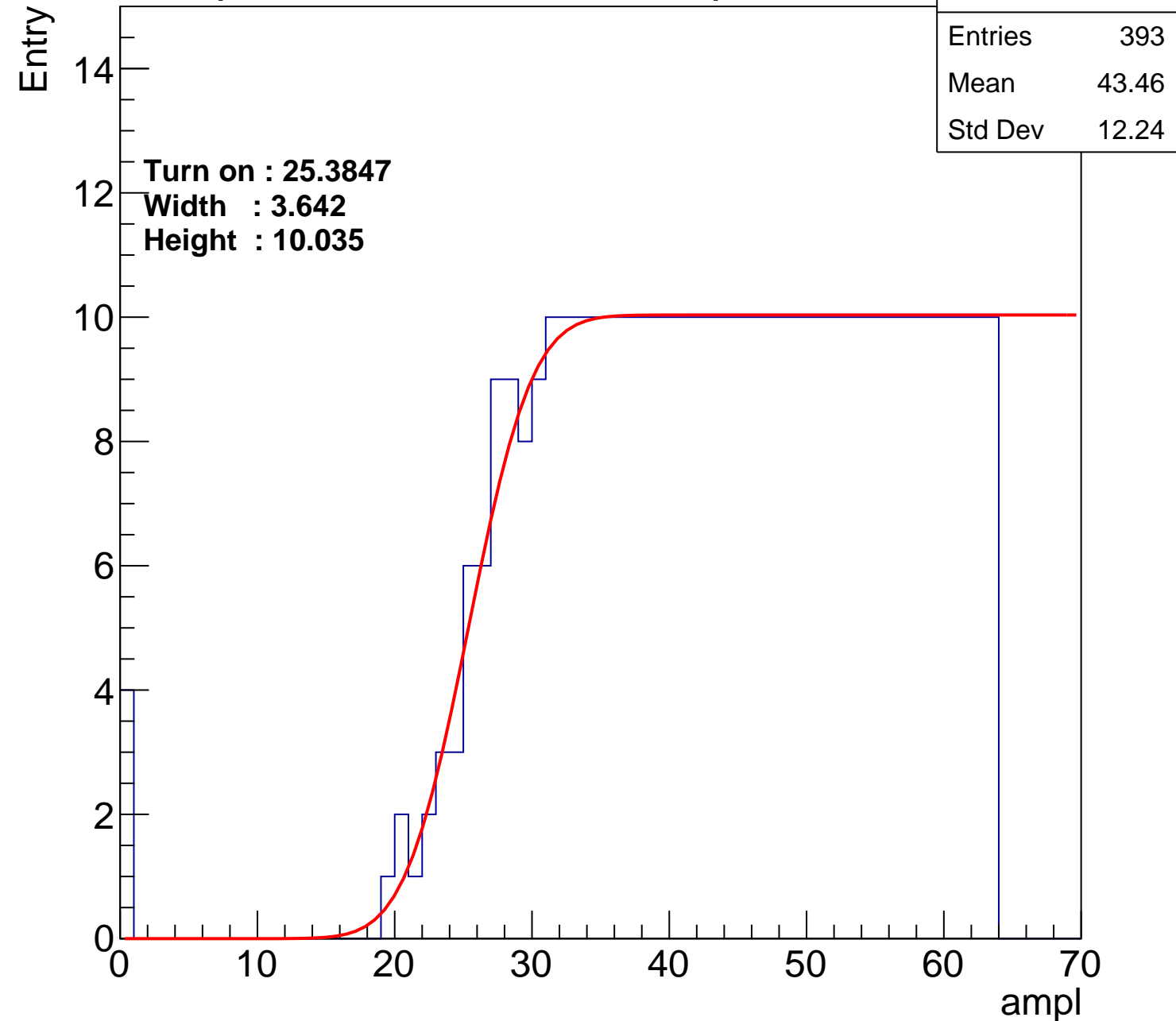
Width : 3.642

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.06
Std Dev	11.3

Turn on : 28.2974

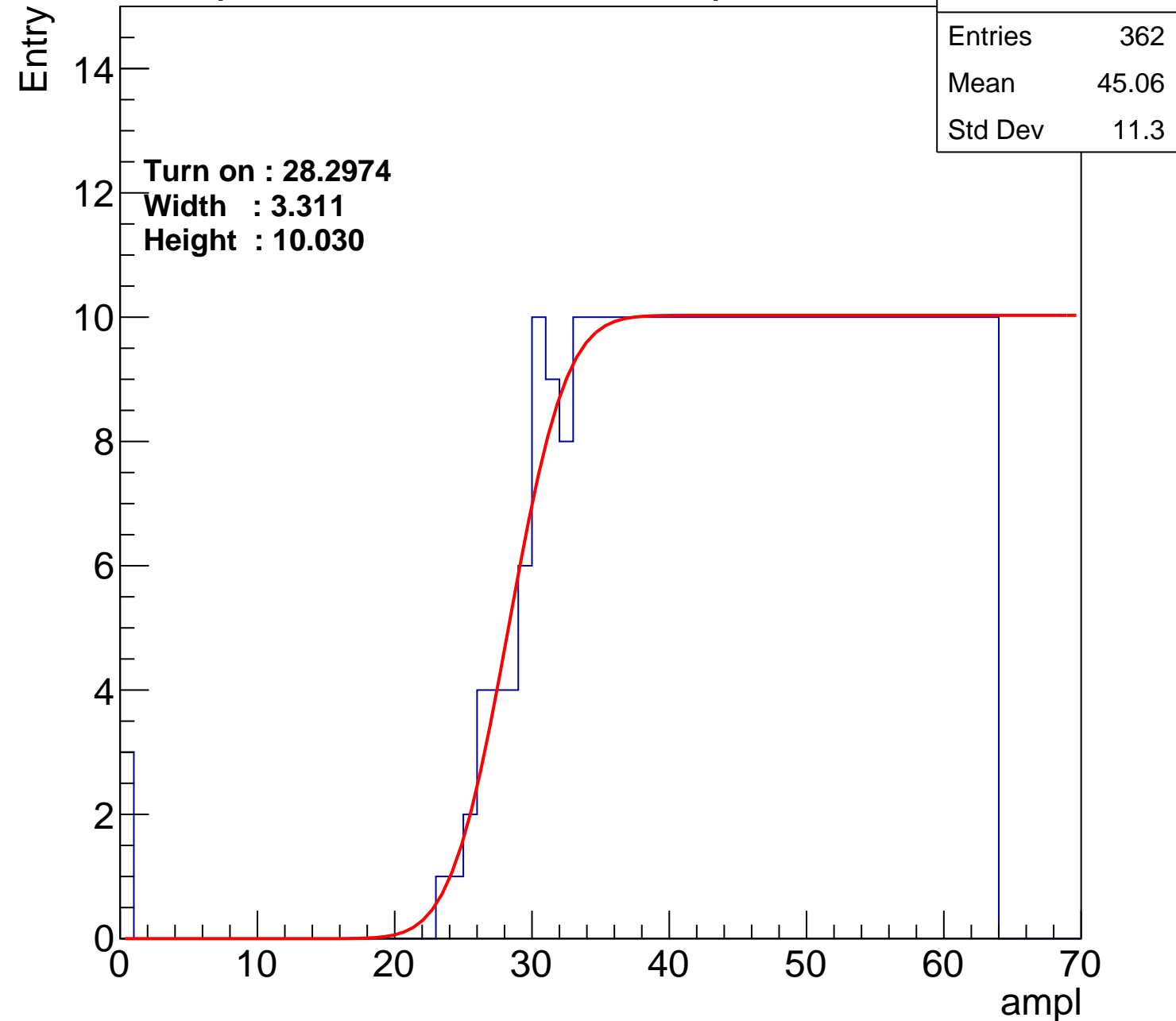
Width : 3.311

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.07
Std Dev	12.47

Turn on : 24.6047

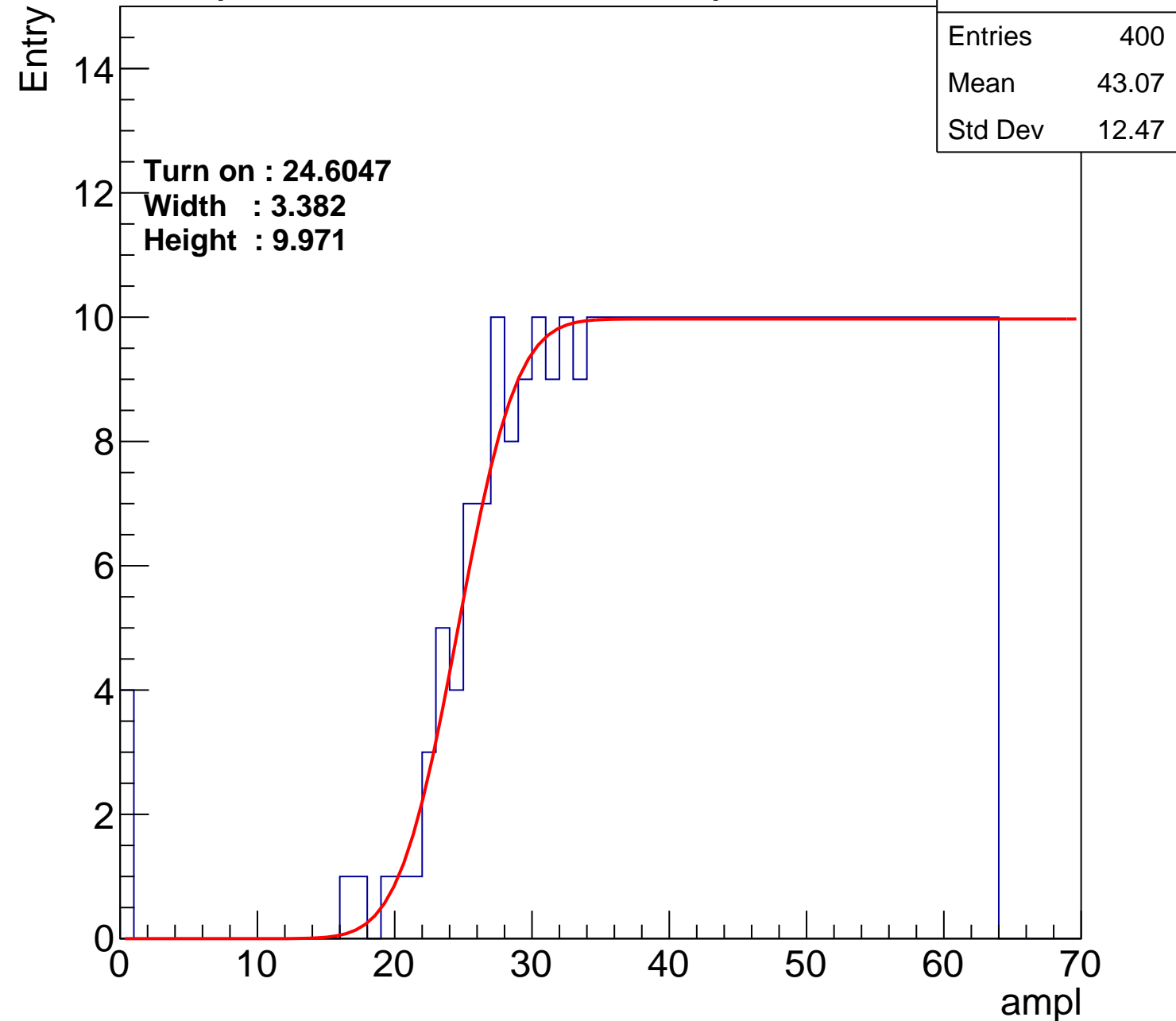
Width : 3.382

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.77
Std Dev	11.95

Turn on : 26.5267

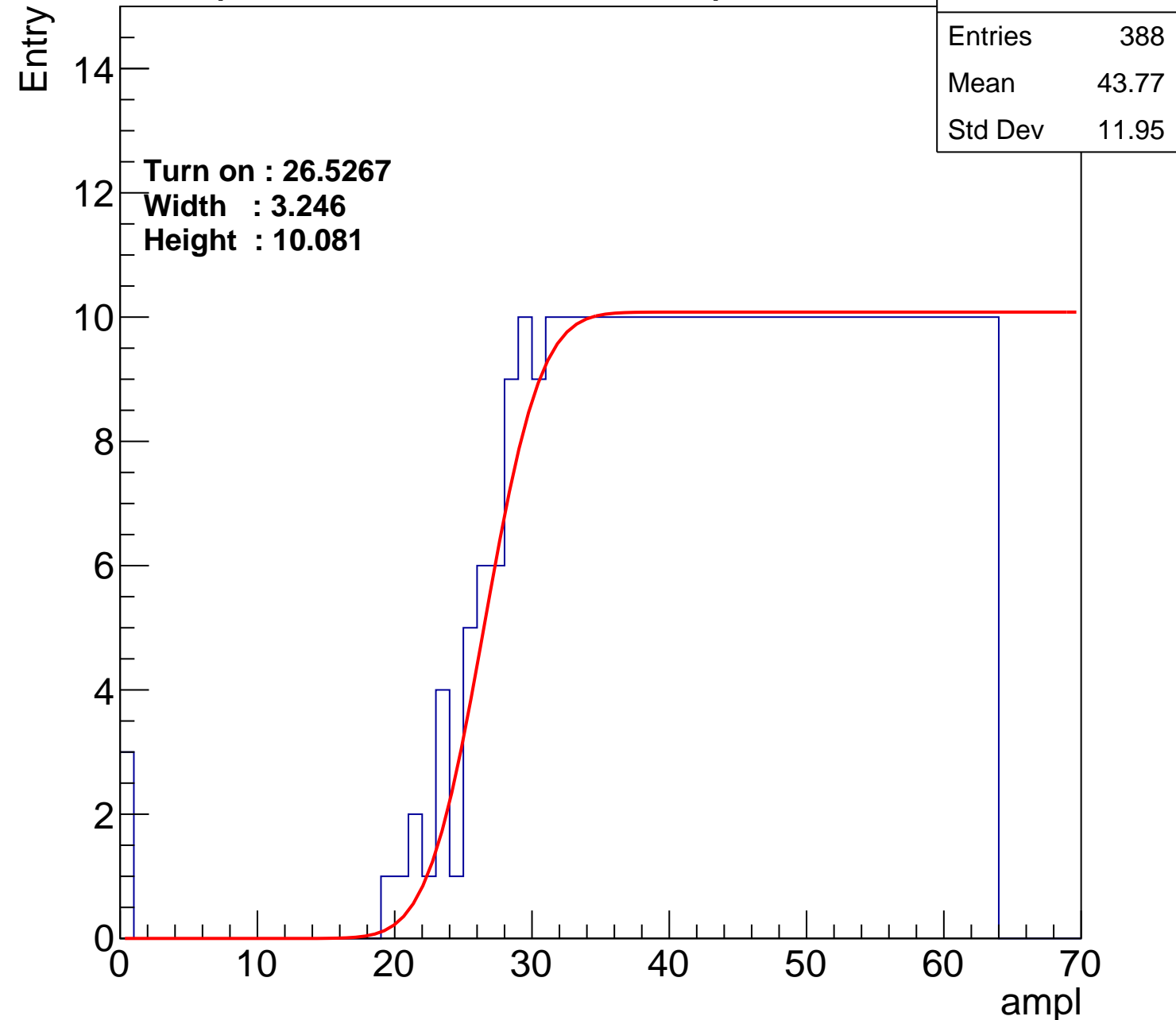
Width : 3.246

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.91
Std Dev	12.13

Turn on : 26.4715

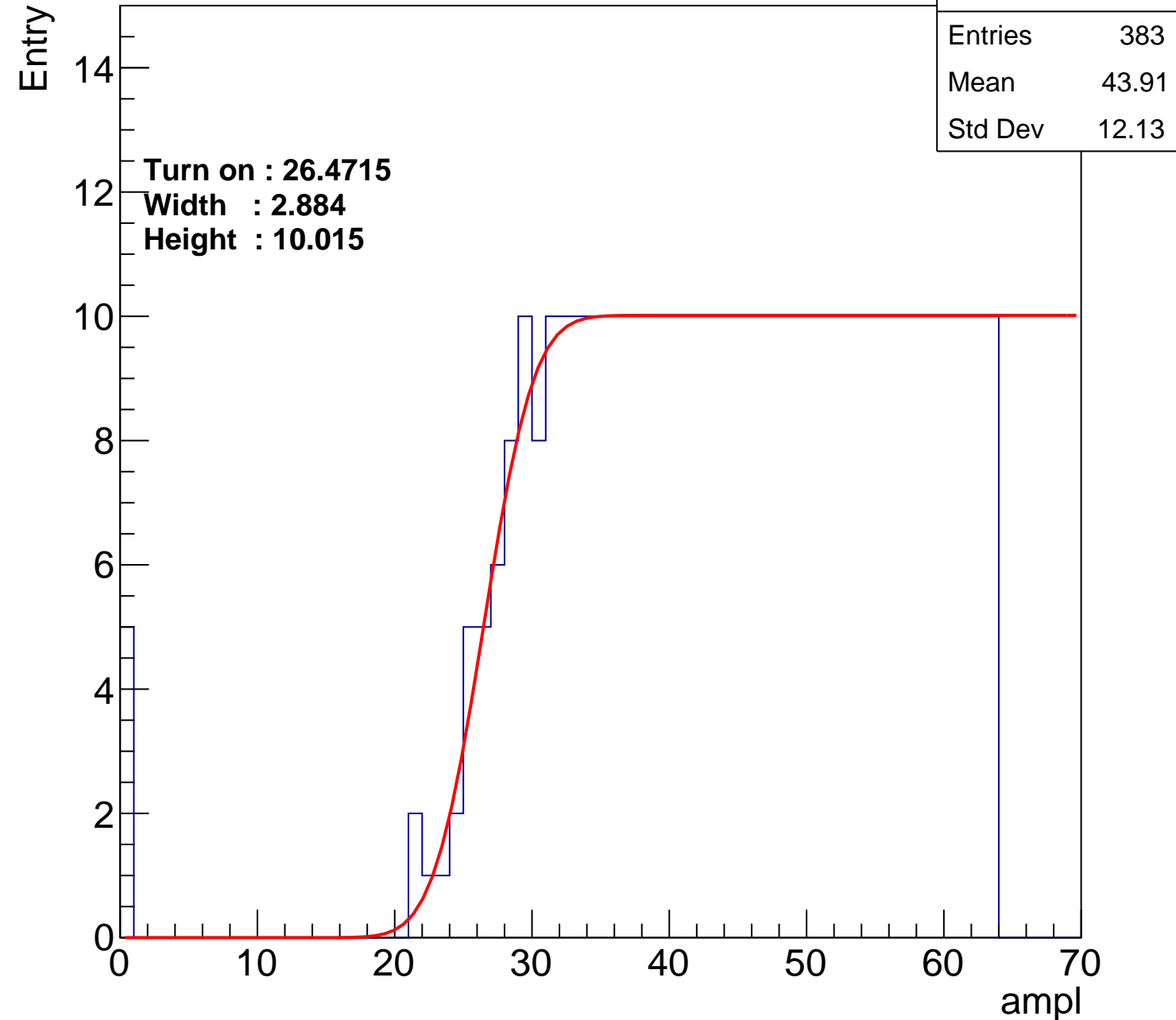
Width : 2.884

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.89
Std Dev	12.38

Turn on : 23.4383

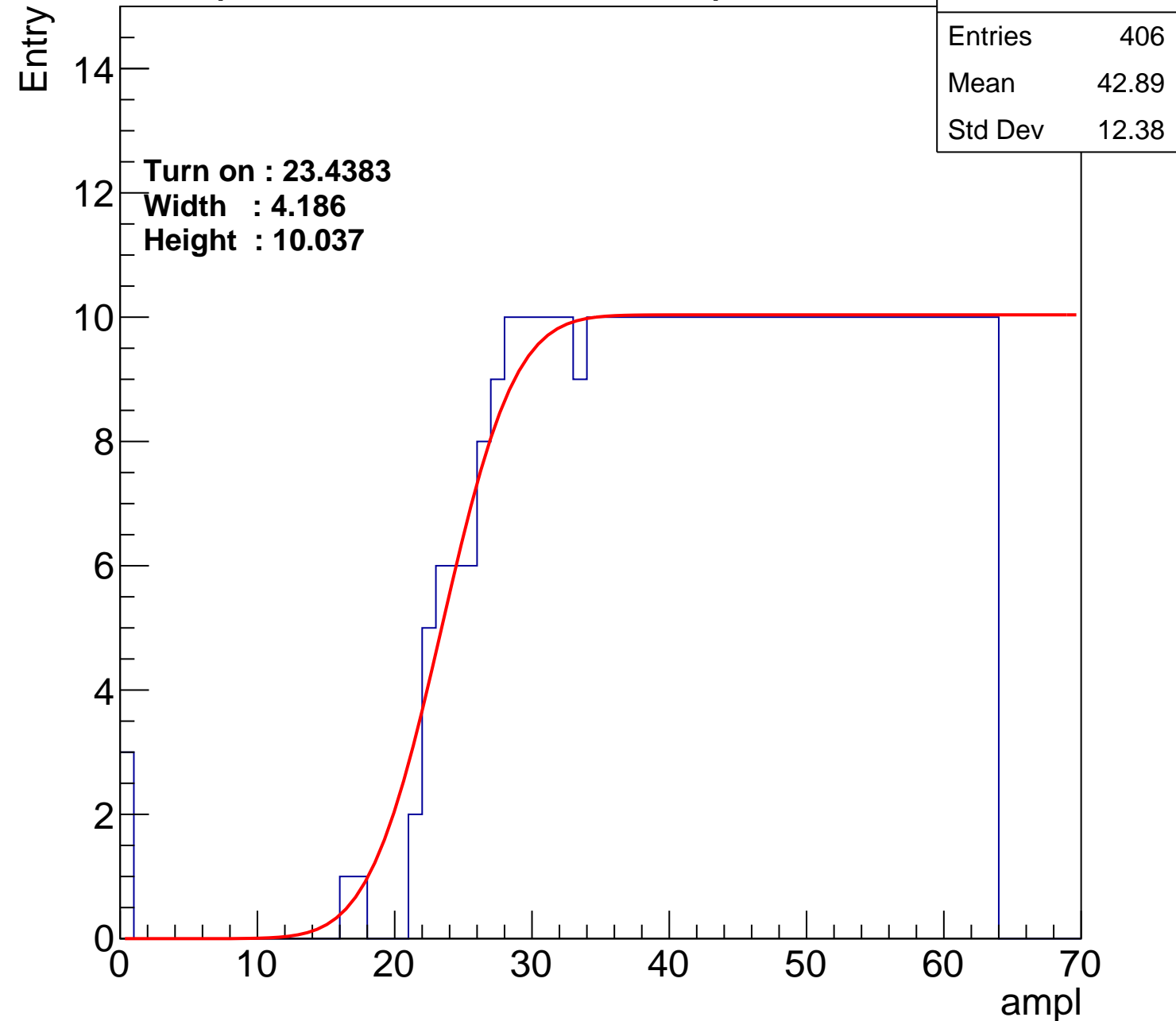
Width : 4.186

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.61
Std Dev	12.57

Turn on : 23.6995

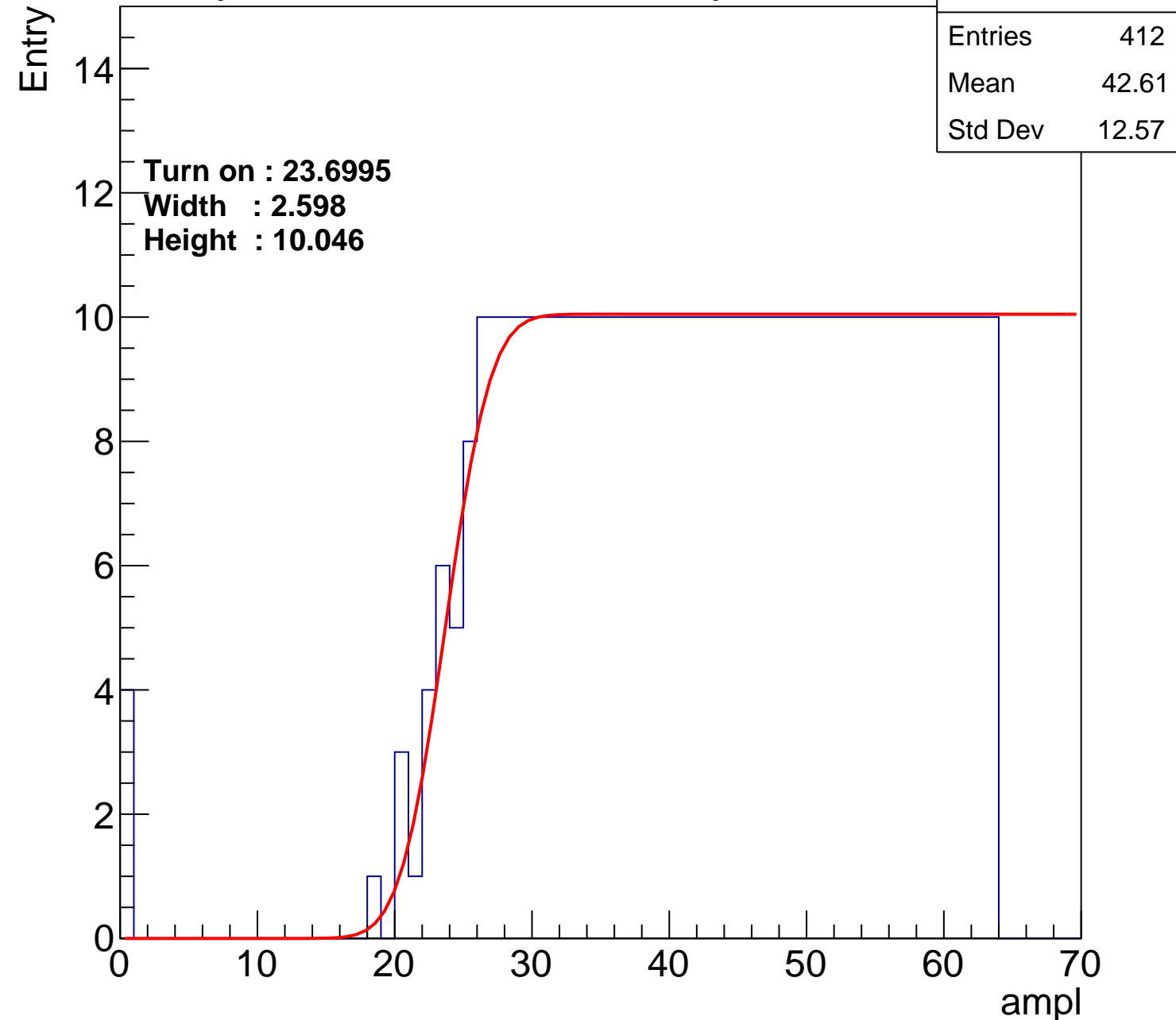
Width : 2.598

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.45
Std Dev	11.81

Turn on : 24.2866

Width : 2.578

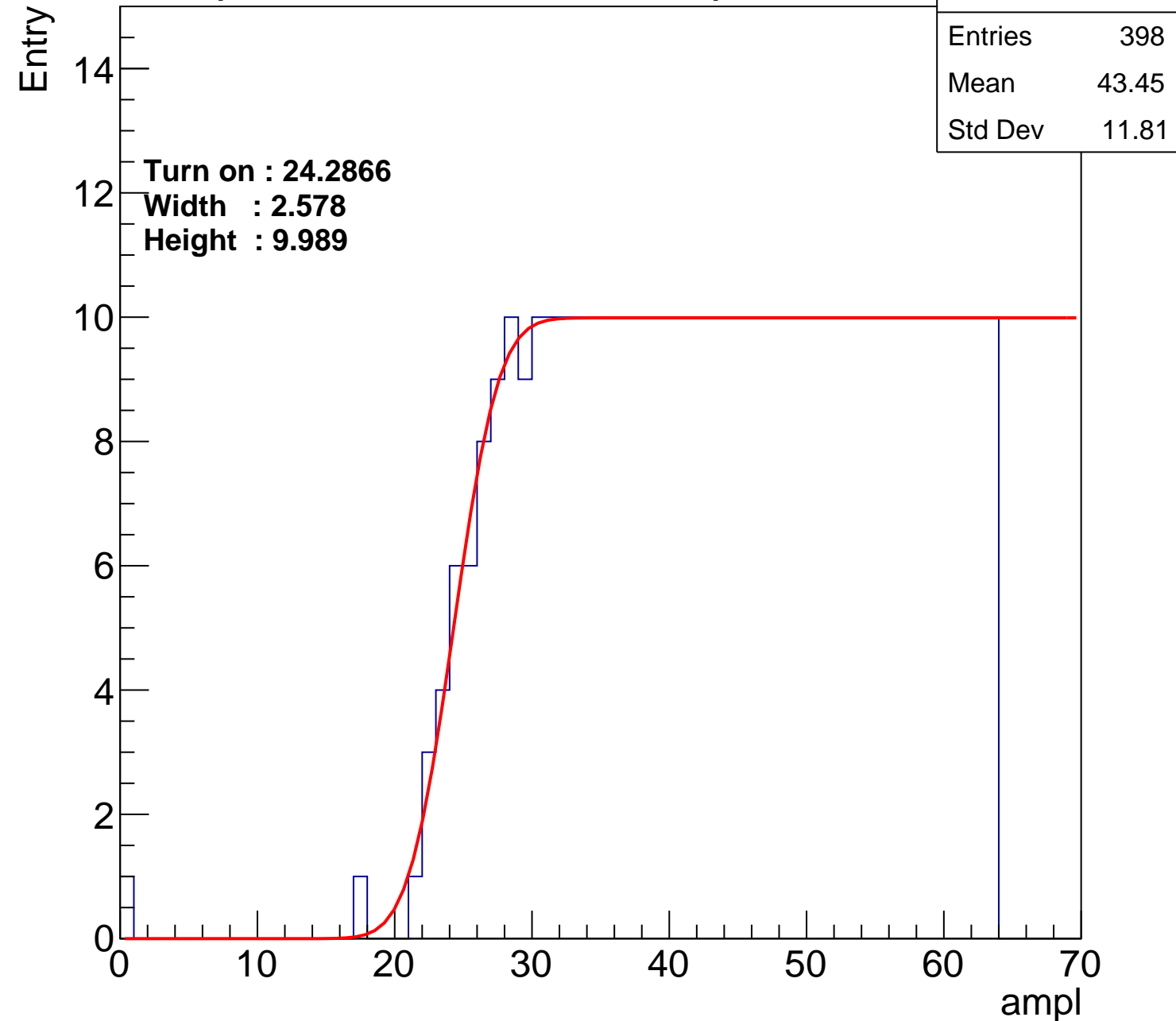
Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U17-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.76
Std Dev	12.69

Turn on : 24.1480

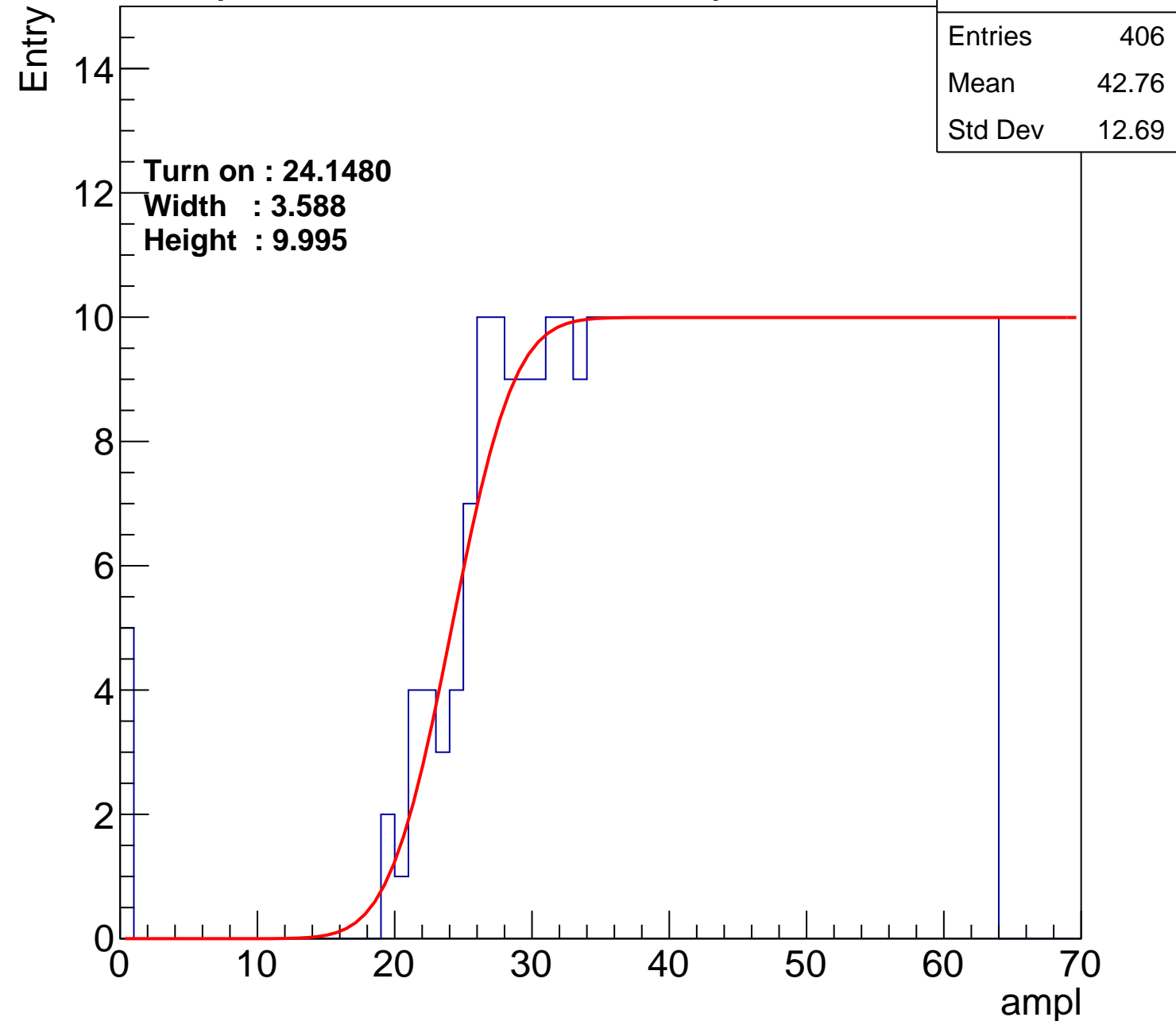
Width : 3.588

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.99
Std Dev	12.33

Turn on : 25.3535

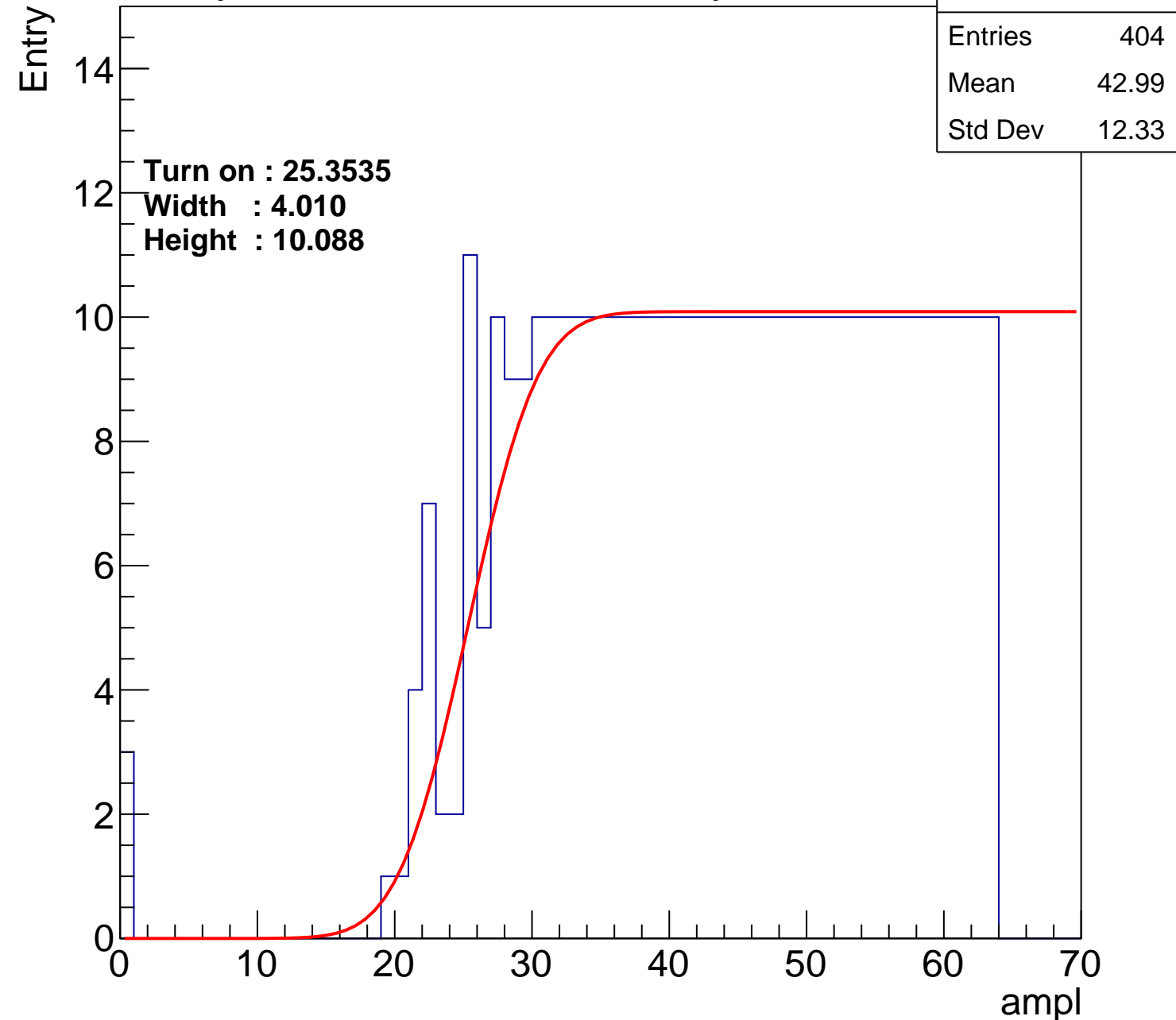
Width : 4.010

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.65
Std Dev	11.75

Turn on : 25.3092

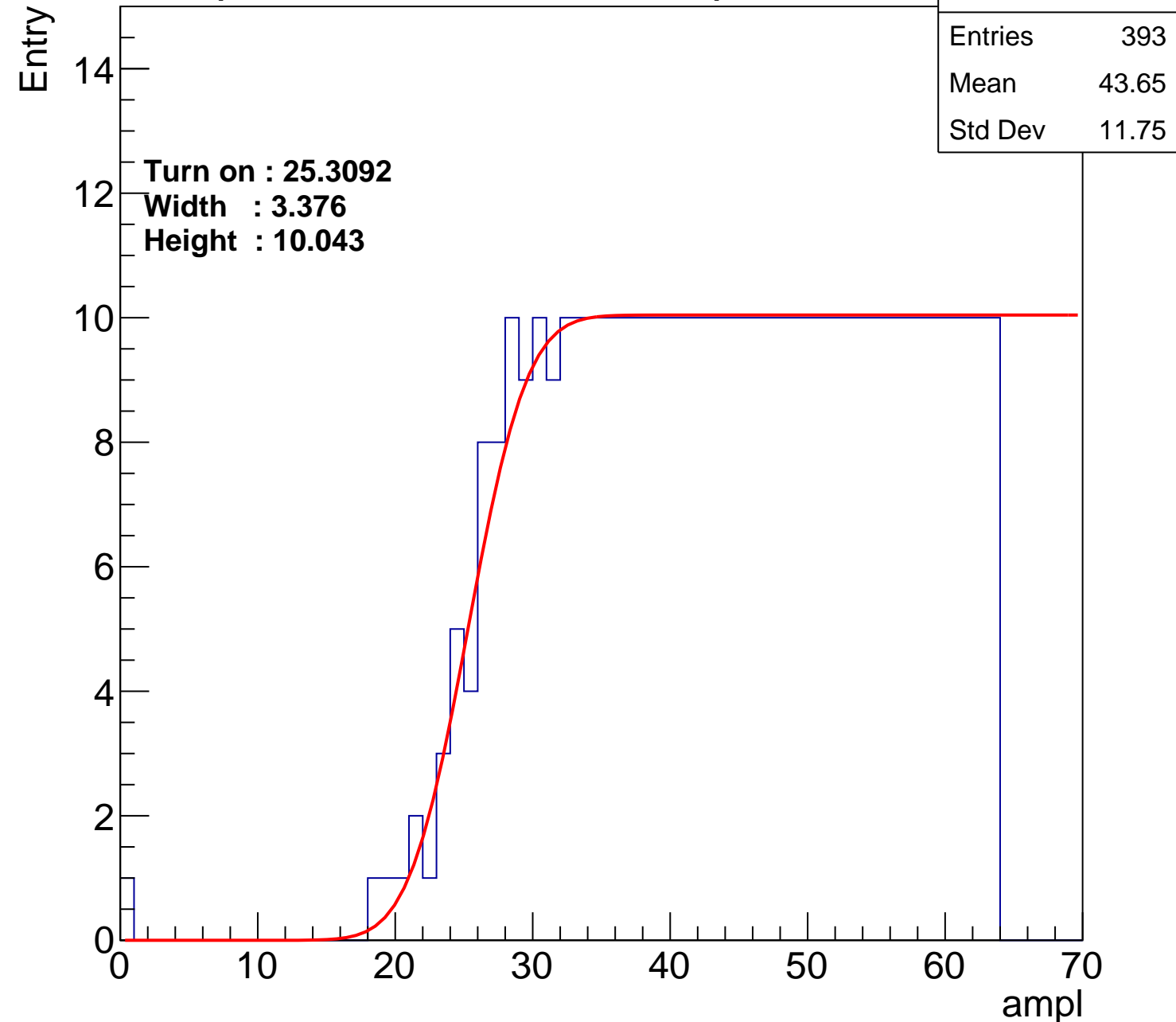
Width : 3.376

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.76
Std Dev	11.12

Turn on : 27.4102

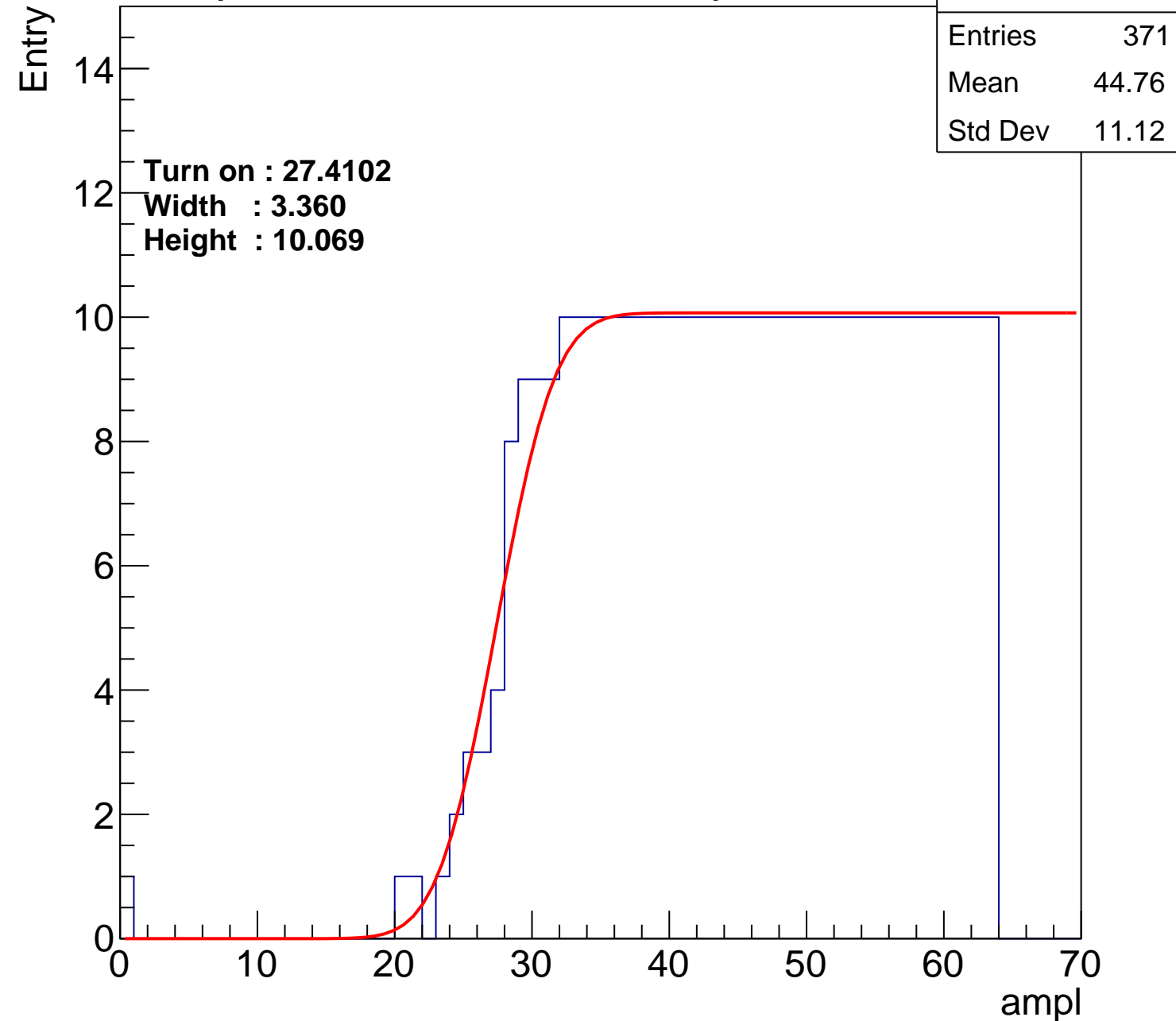
Width : 3.360

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.6989

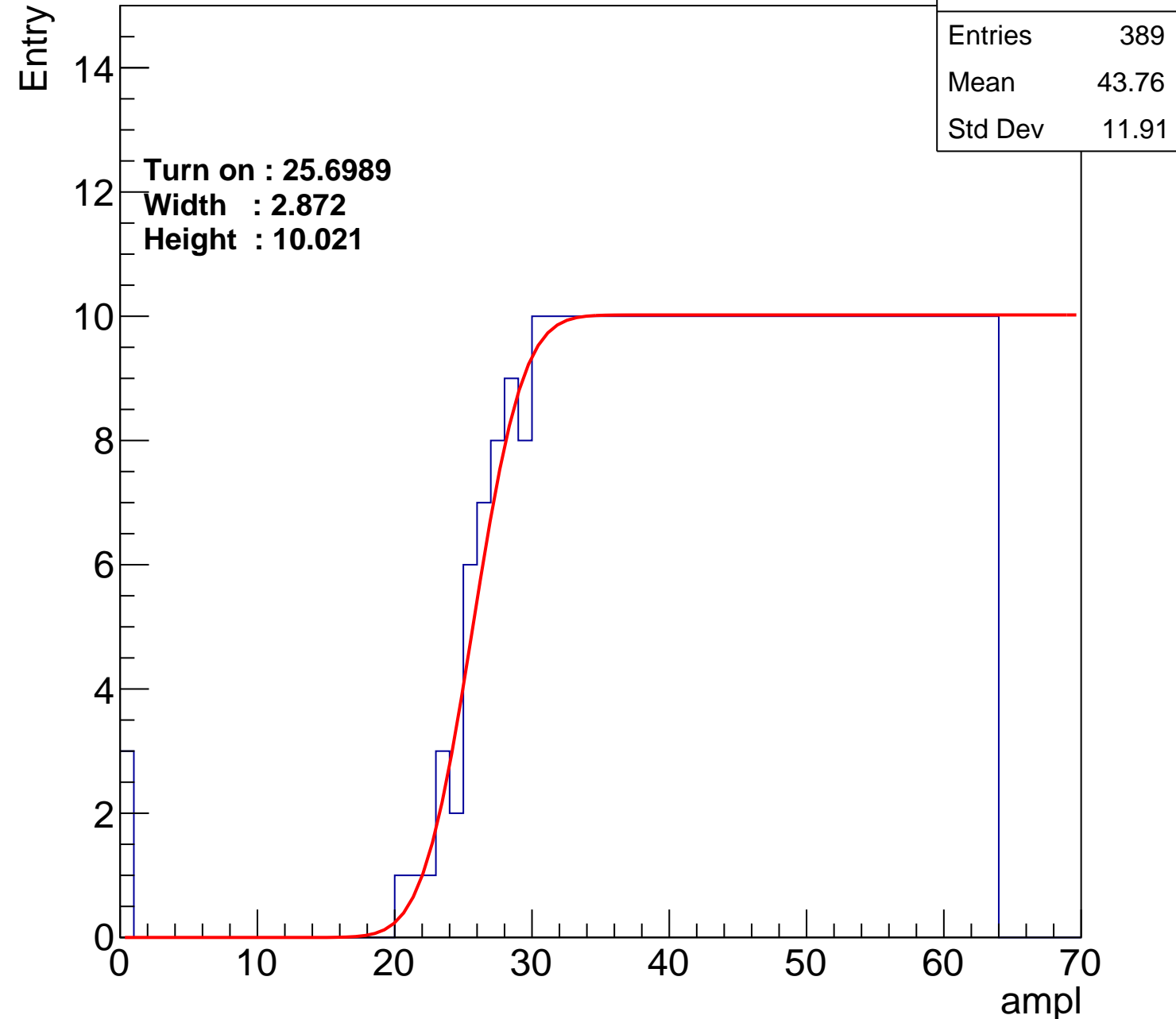
Width : 2.872

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.87
Std Dev	11.61

Turn on : 25.1516

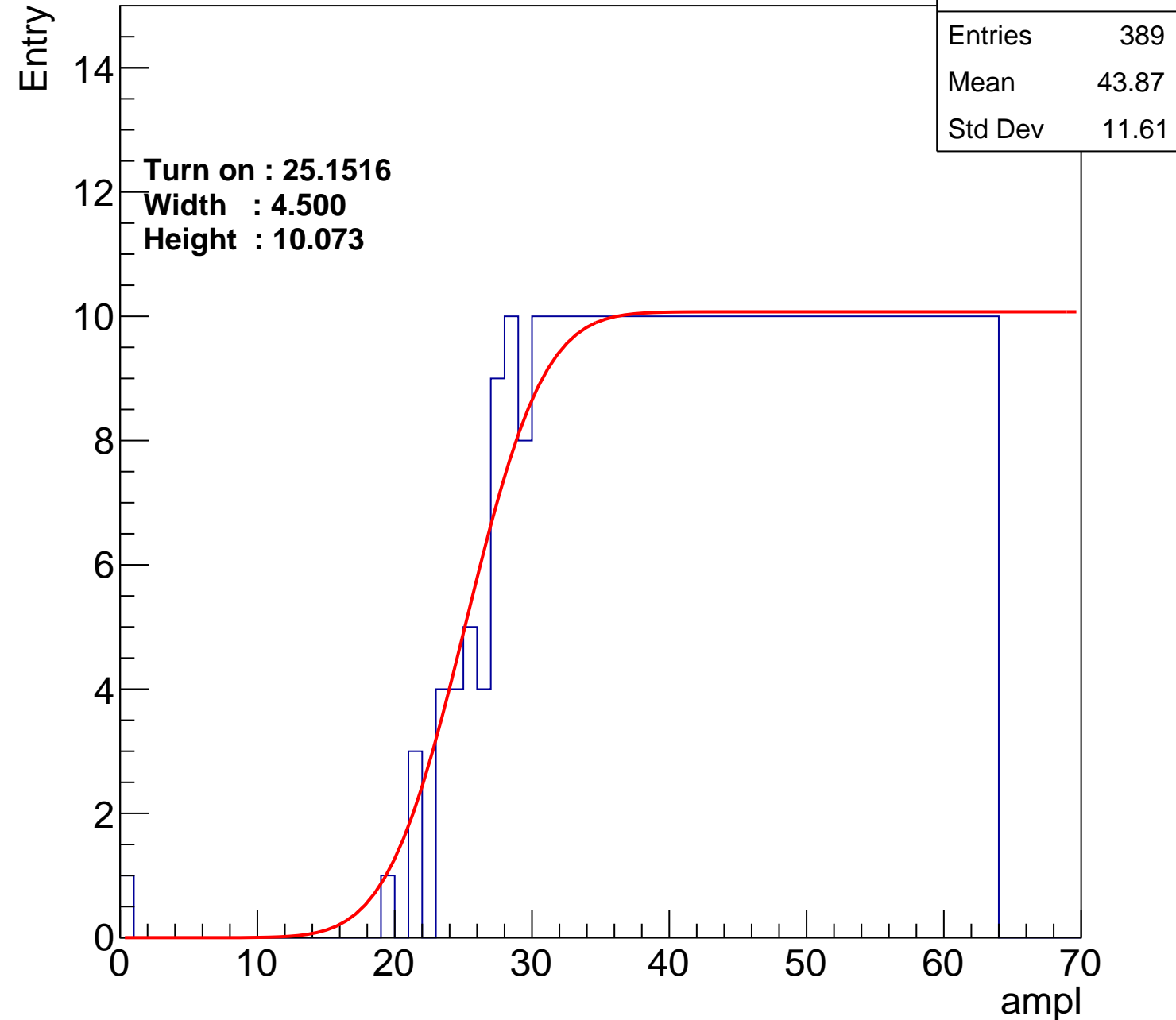
Width : 4.500

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch110

calib_packv5_042523_0143.root, FC#12, port B1

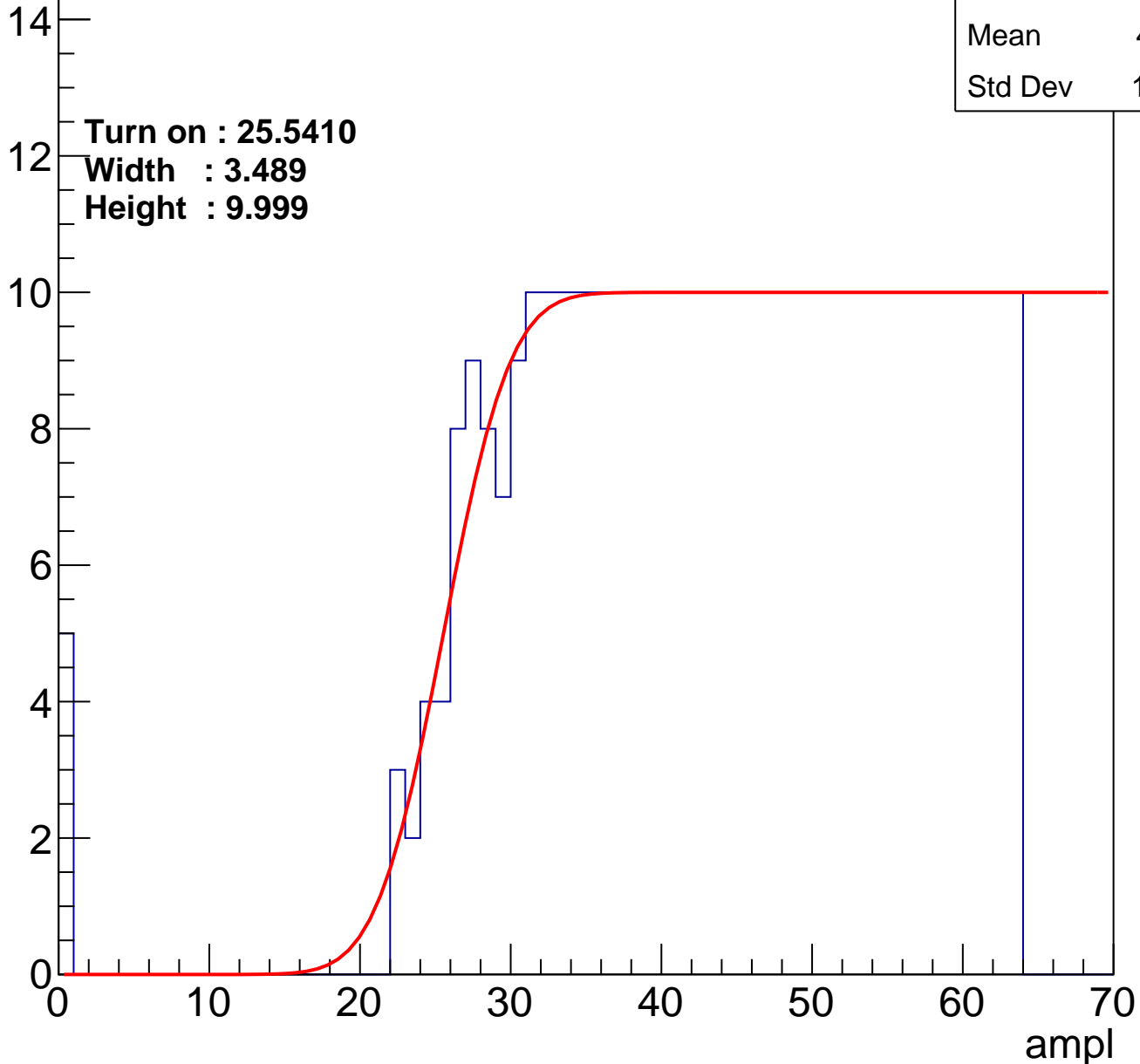
Entries	389
Mean	43.61
Std Dev	12.26

Turn on : 25.5410

Width : 3.489

Height : 9.999

Entry



B0L102S, U17-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 26.9621

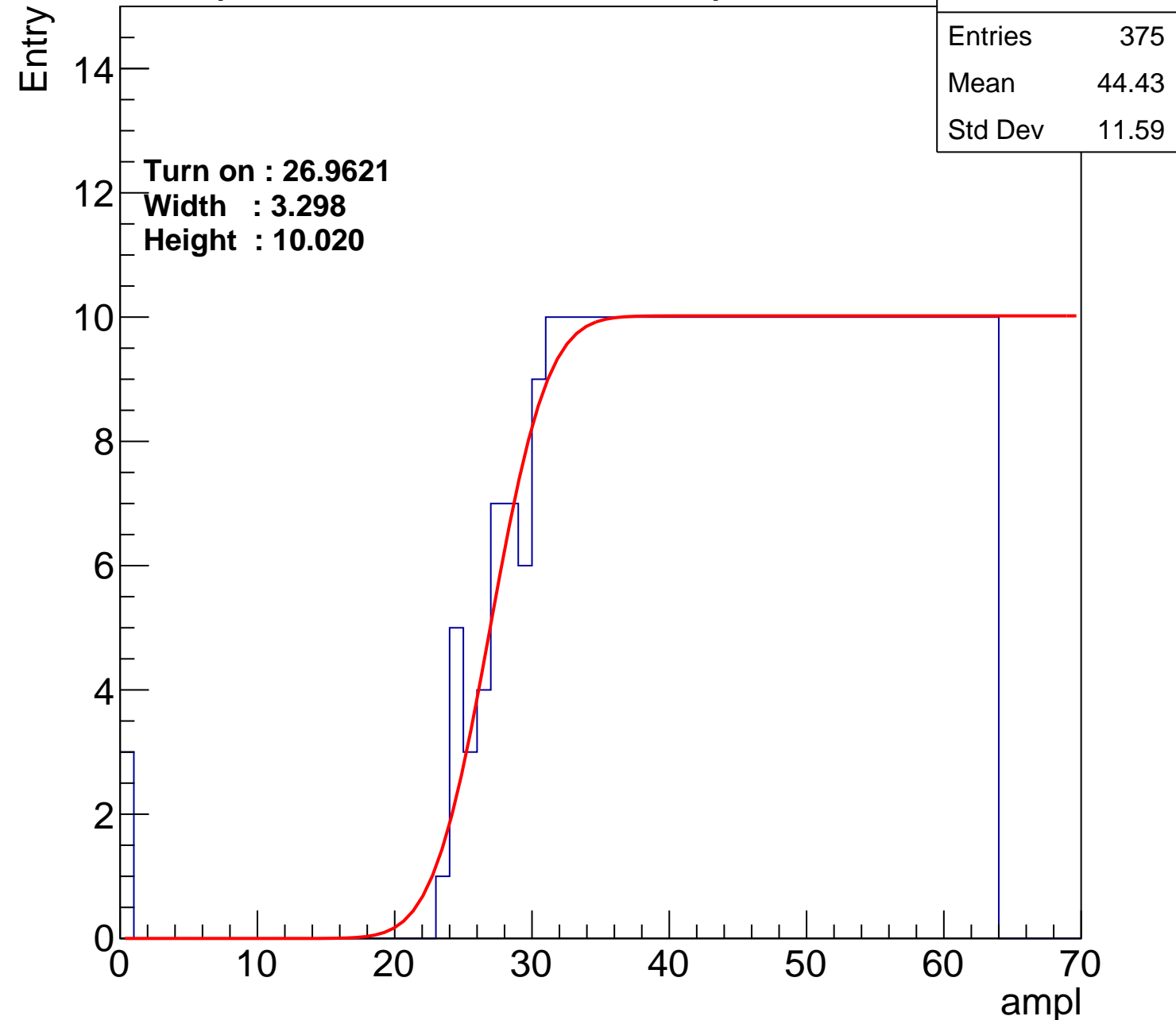
Width : 3.298

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.92
Std Dev	12.34

Turn on : 23.8936

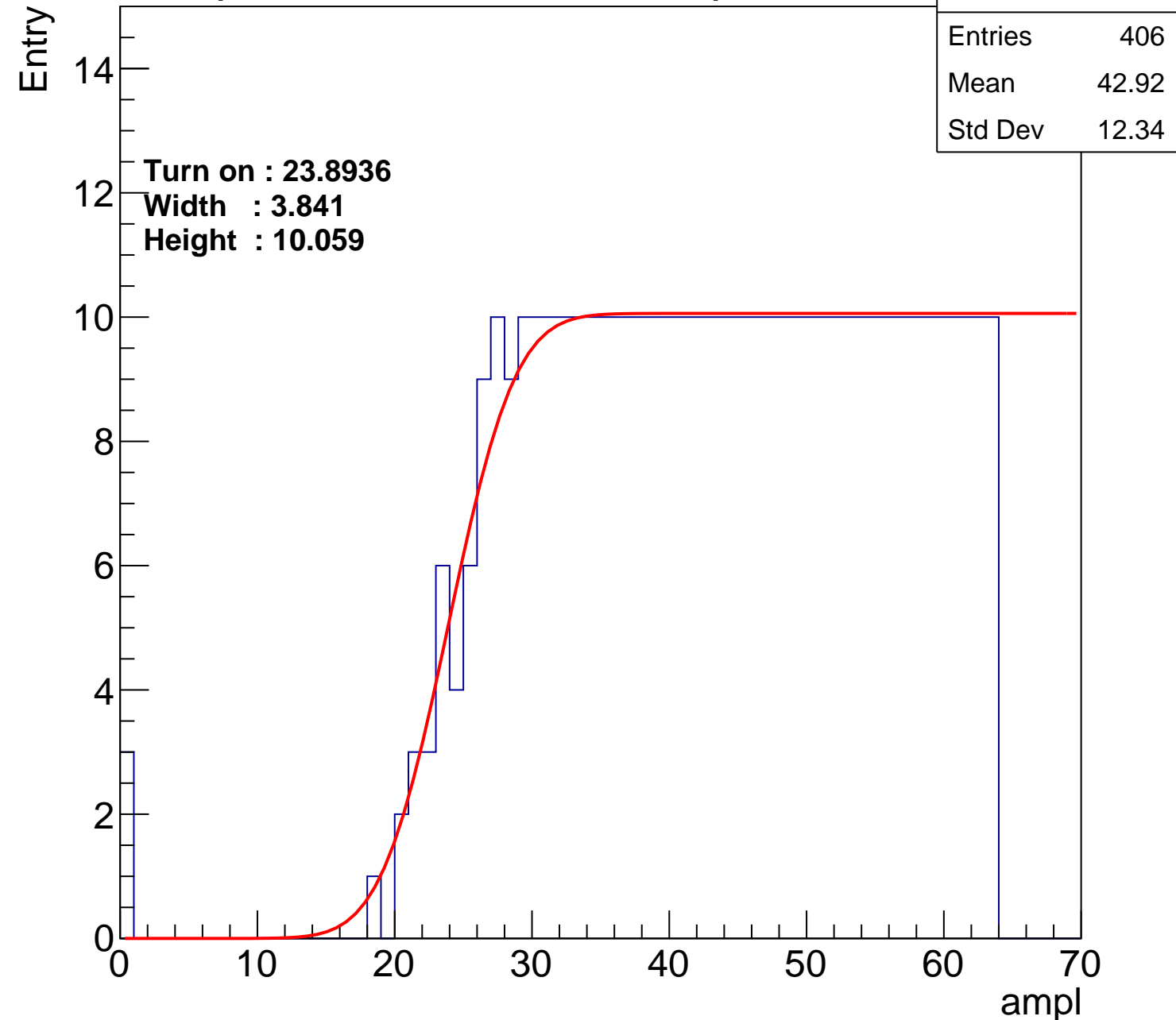
Width : 3.841

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.8
Std Dev	11.66

Turn on : 25.3290

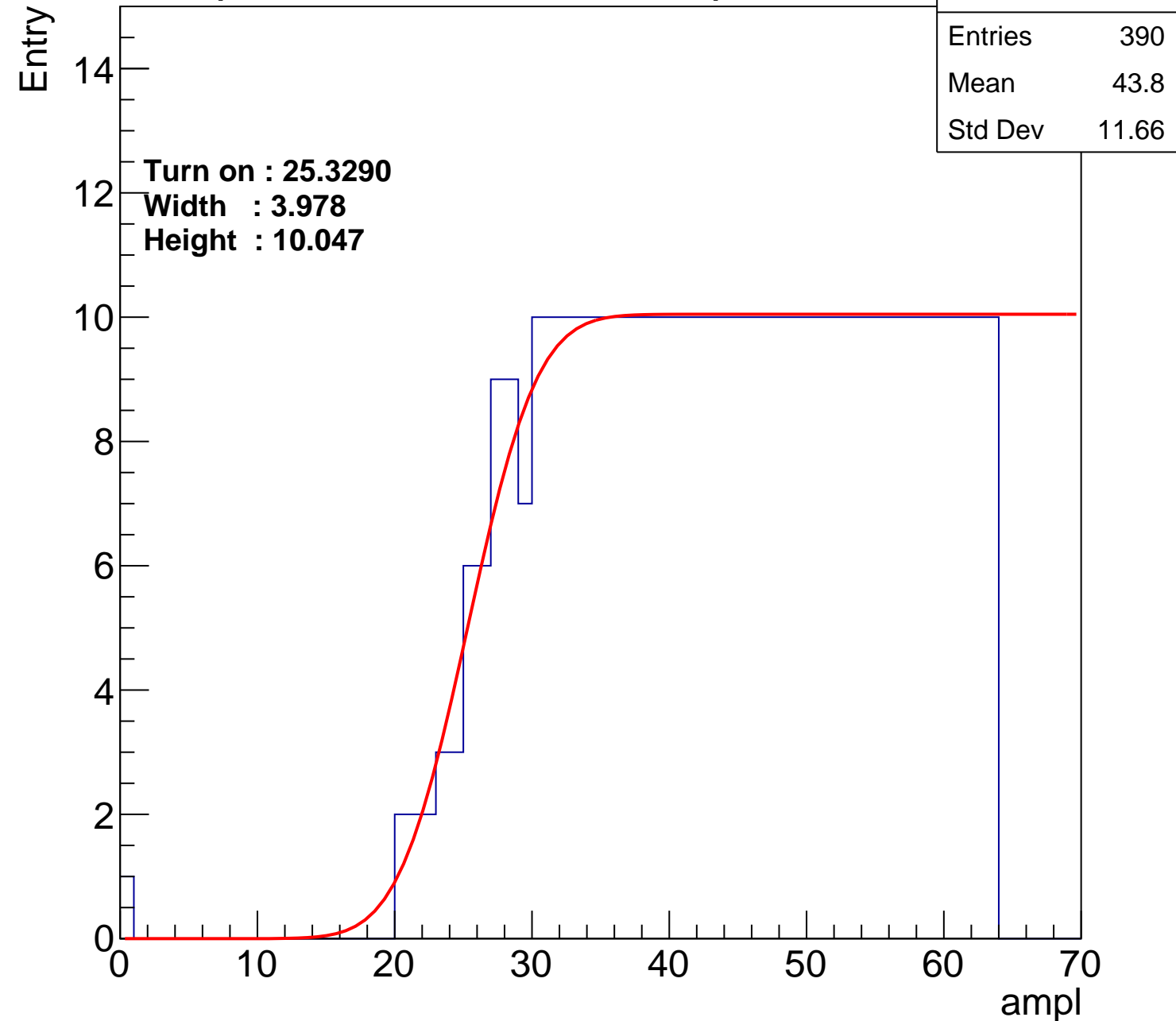
Width : 3.978

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.1
Std Dev	12.22

Turn on : 24.0615

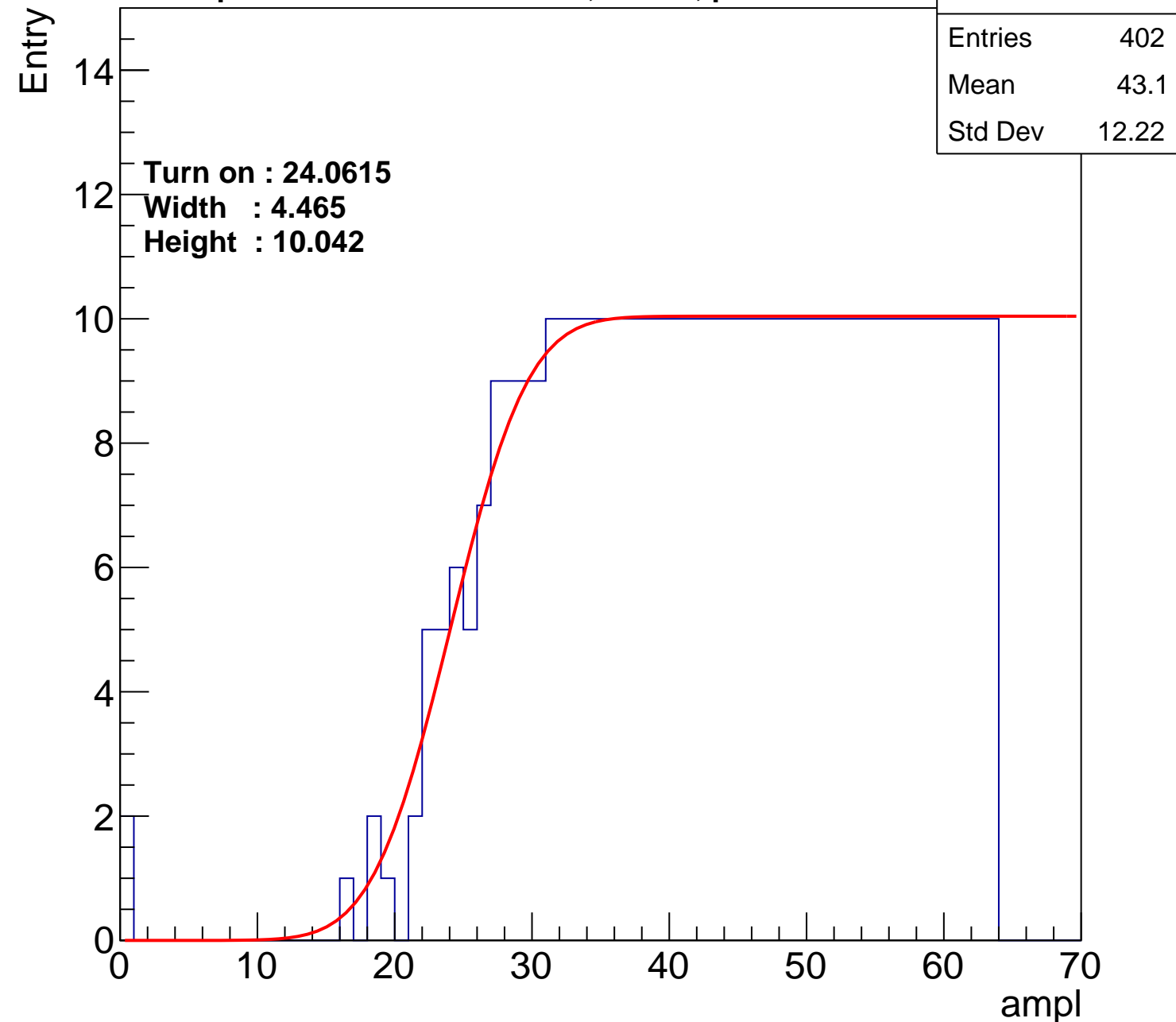
Width : 4.465

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.92
Std Dev	12.46

Turn on : 24.3144

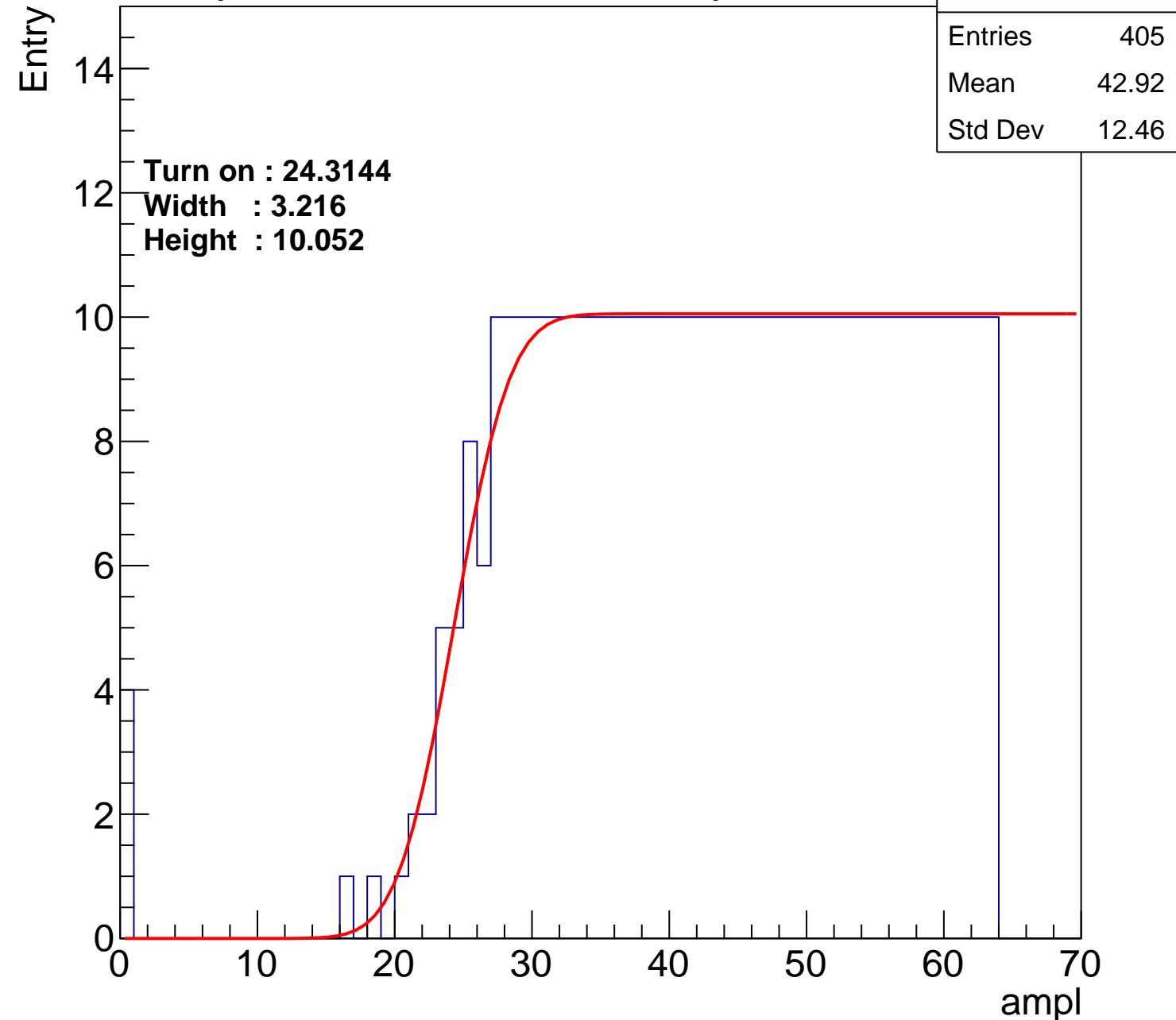
Width : 3.216

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.87
Std Dev	12.23

Turn on : 26.7575

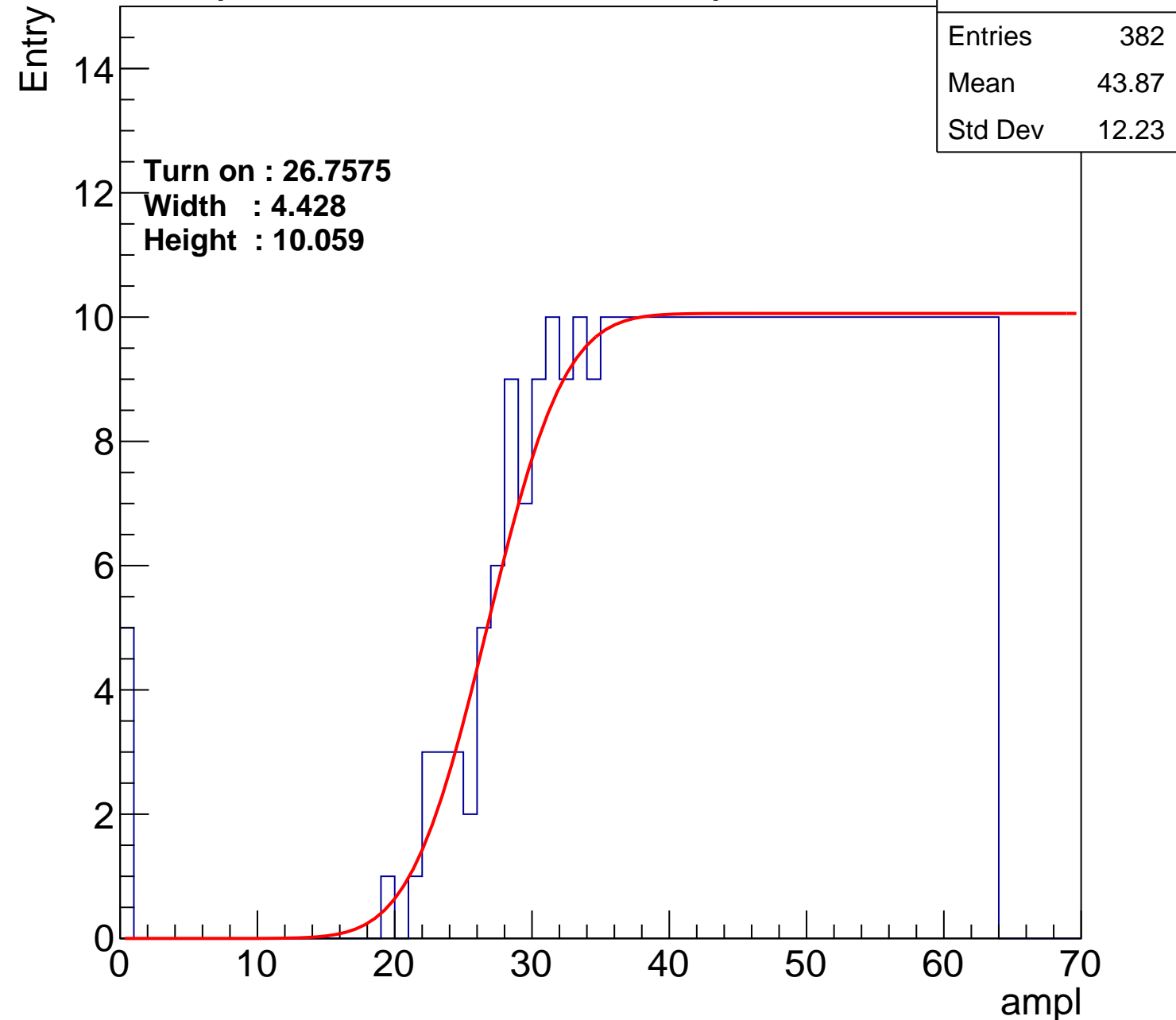
Width : 4.428

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch117

calib_packv5_042523_0143.root, FC#12, port B1

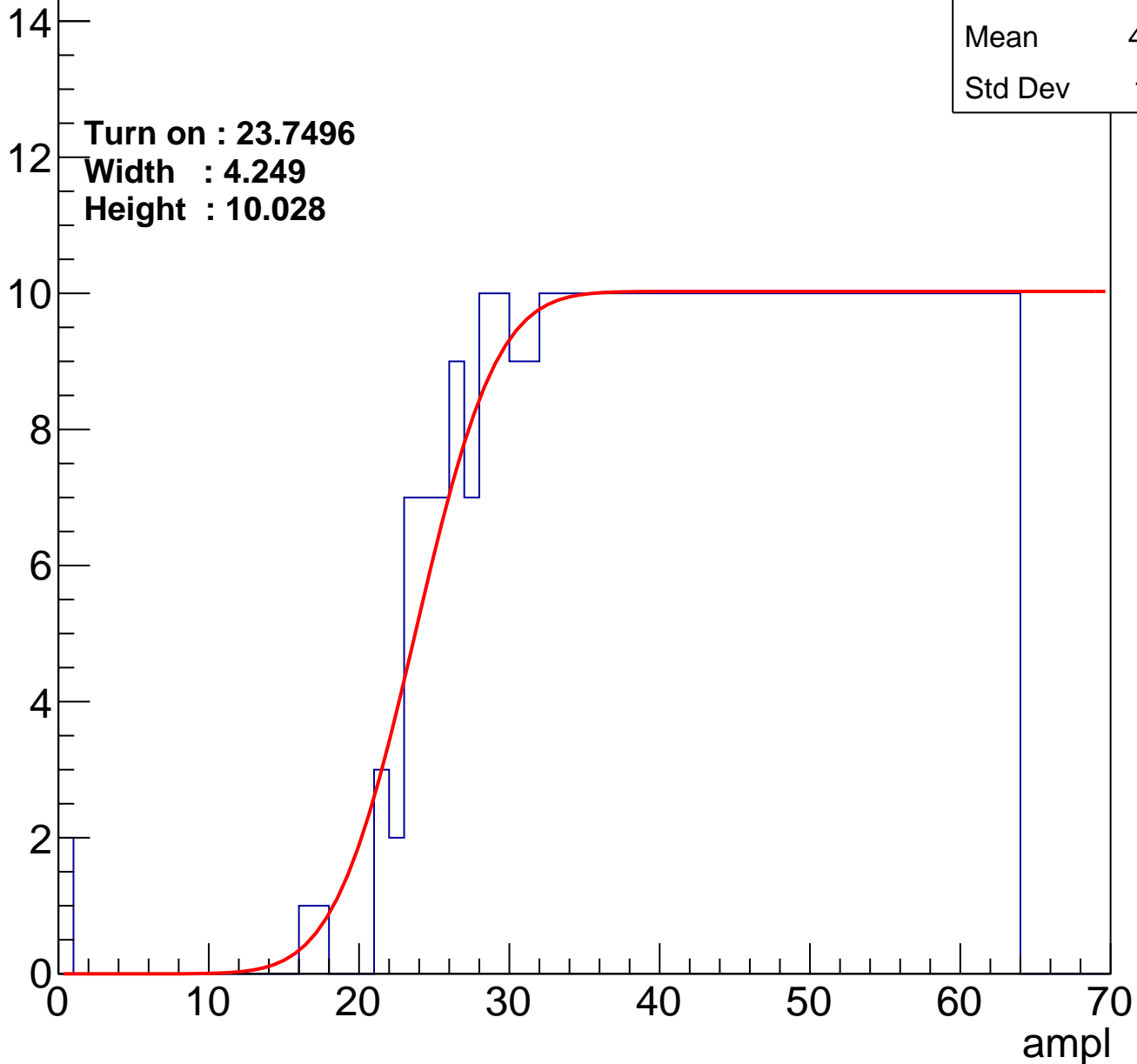
Entries	404
Mean	43.03
Std Dev	12.21

Turn on : 23.7496

Width : 4.249

Height : 10.028

Entry



B0L102S, U17-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 27.3641

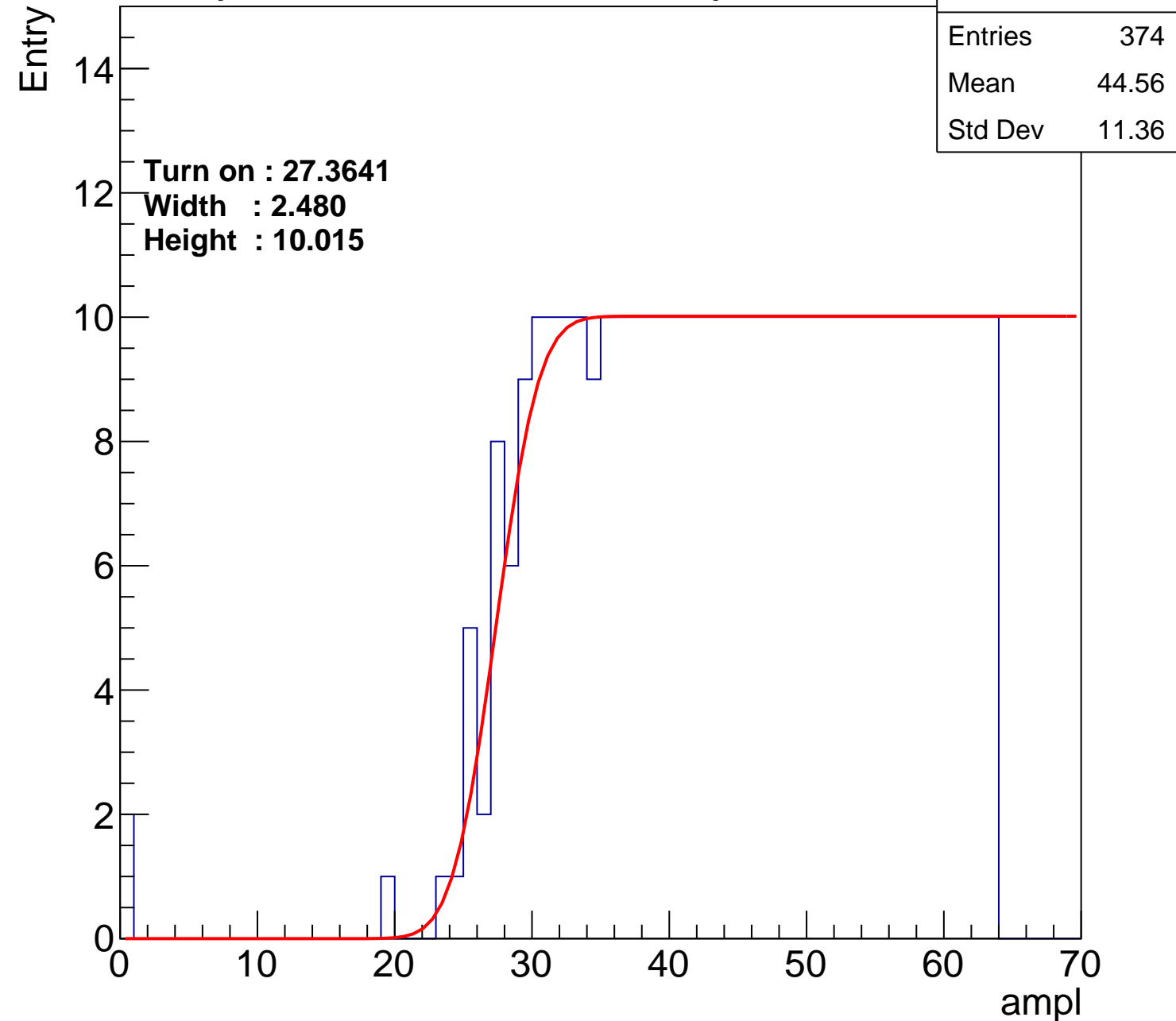
Width : 2.480

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.53
Std Dev	11.99

Turn on : 24.5762

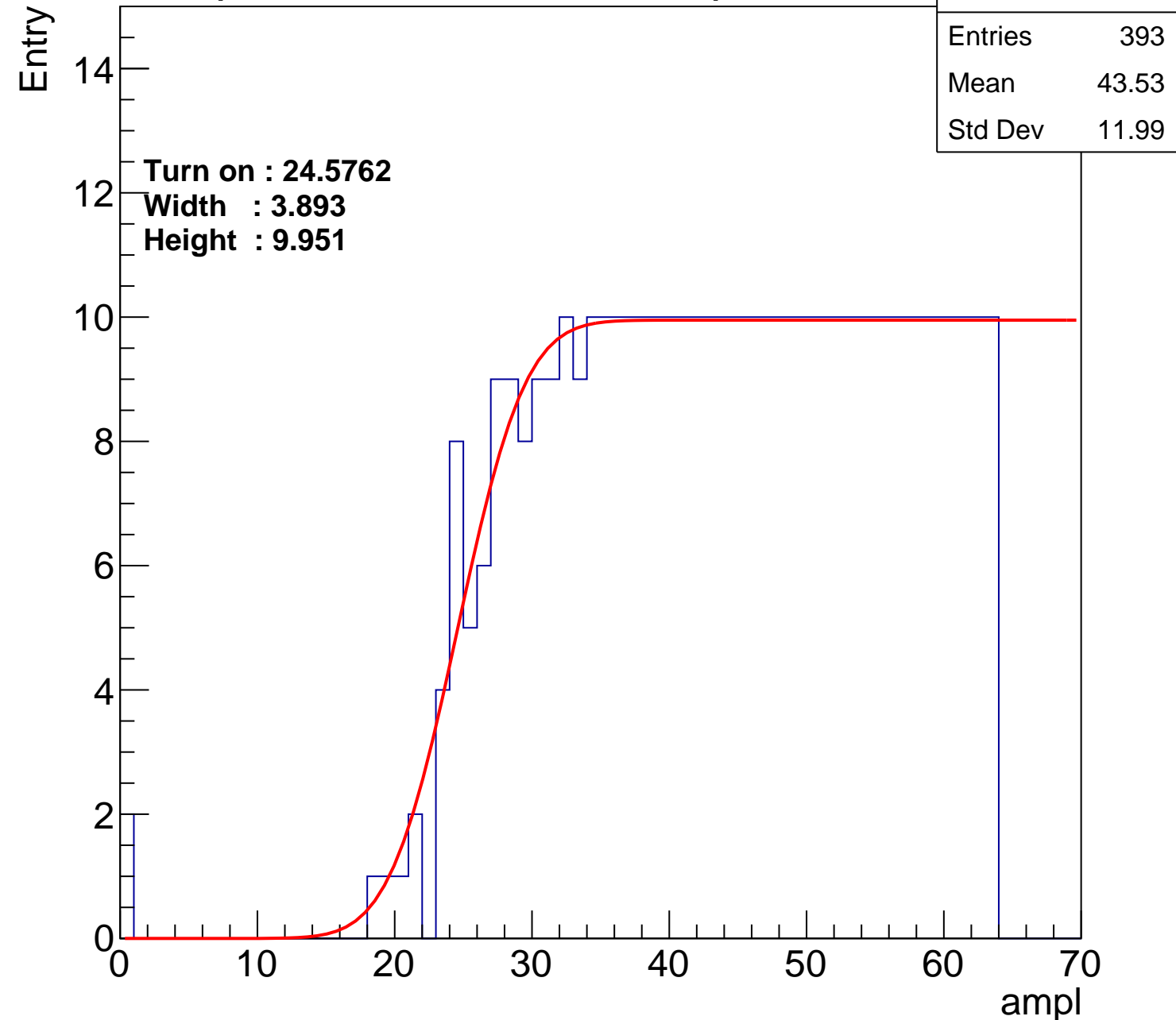
Width : 3.893

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 26.0088

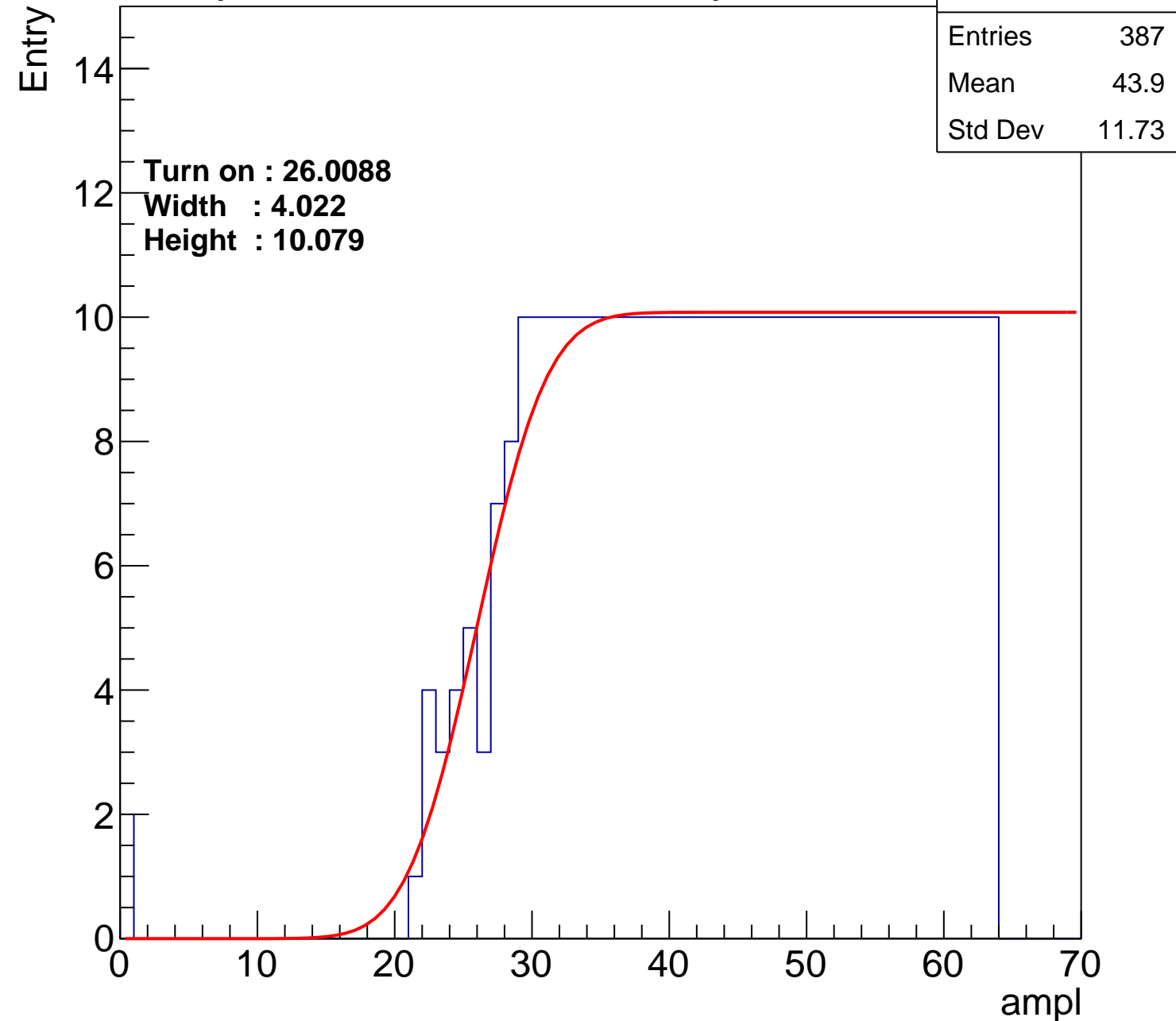
Width : 4.022

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.13
Std Dev	11.92

Turn on : 26.8289

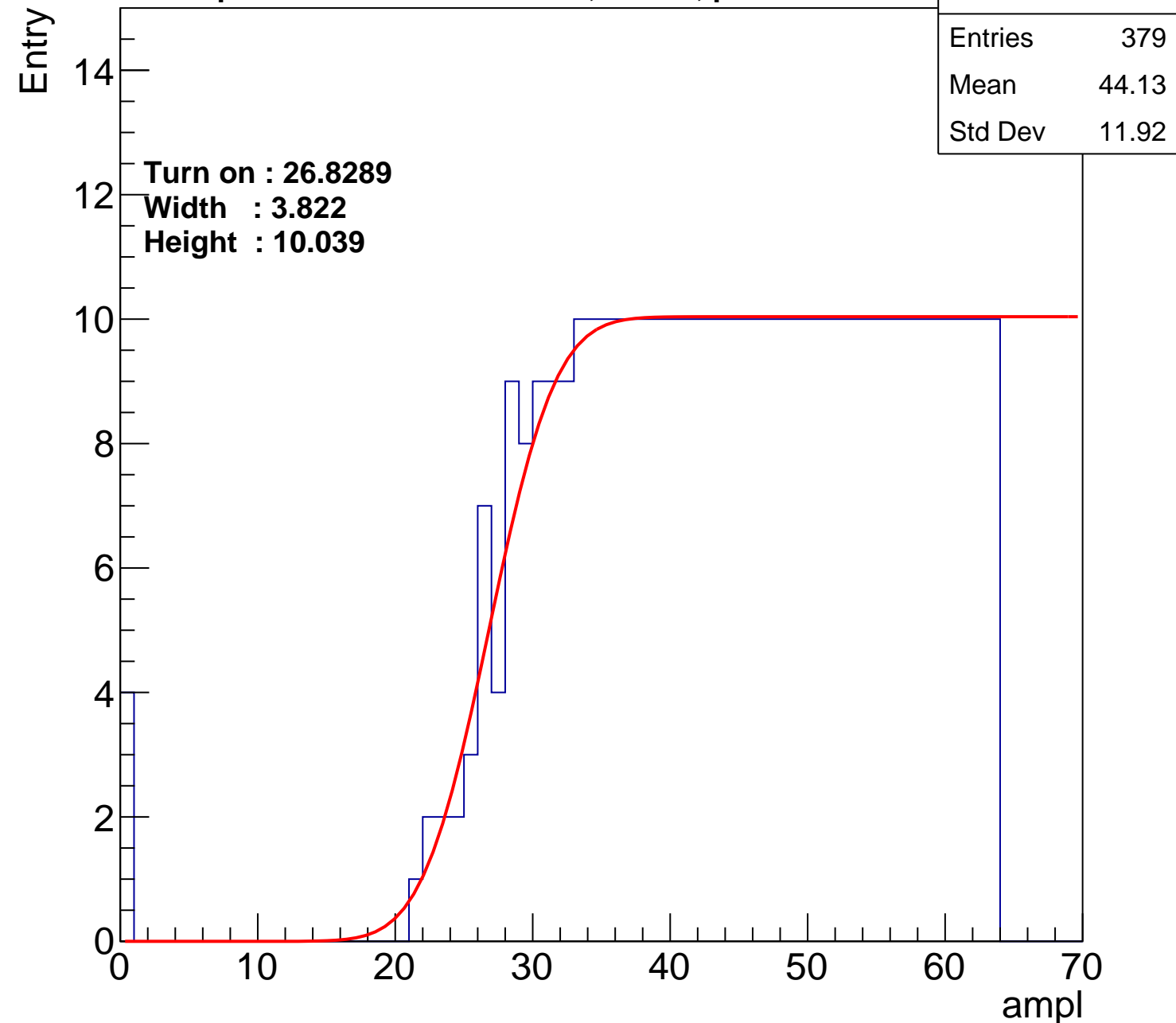
Width : 3.822

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.12
Std Dev	12.51

Turn on : 24.8798

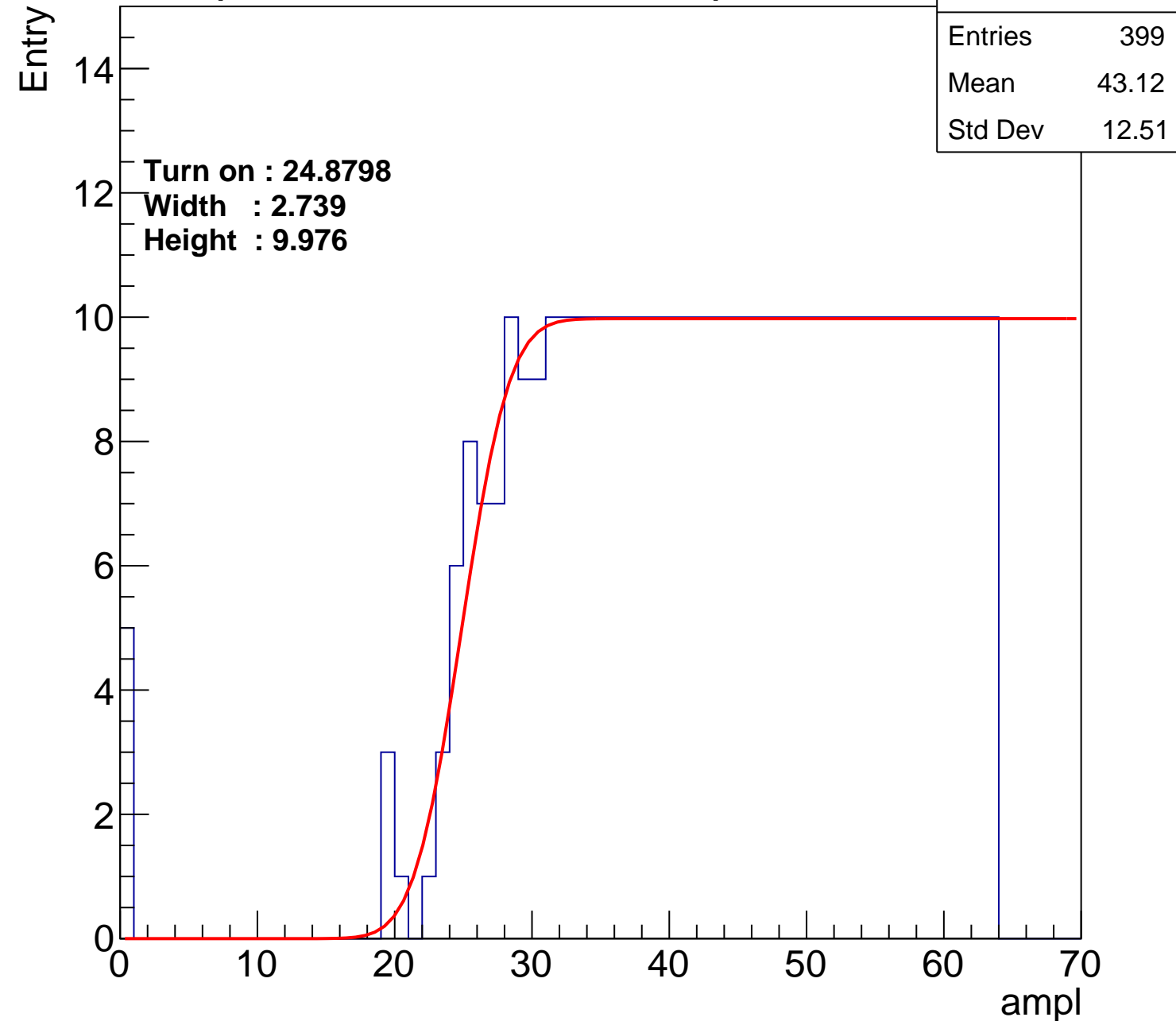
Width : 2.739

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.03
Std Dev	11.55

Turn on : 25.7457

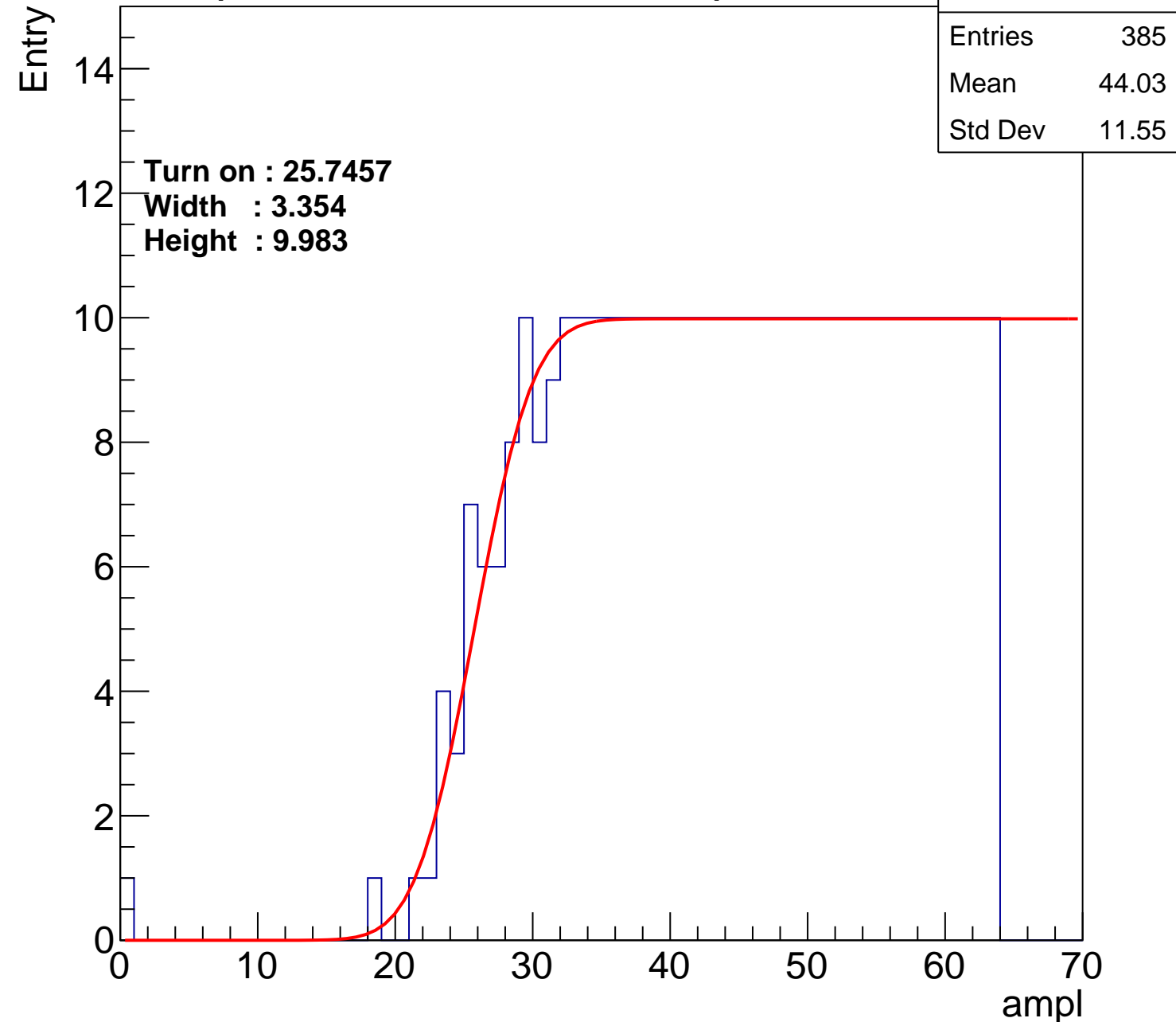
Width : 3.354

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.31
Std Dev	12.15

Turn on : 24.4437

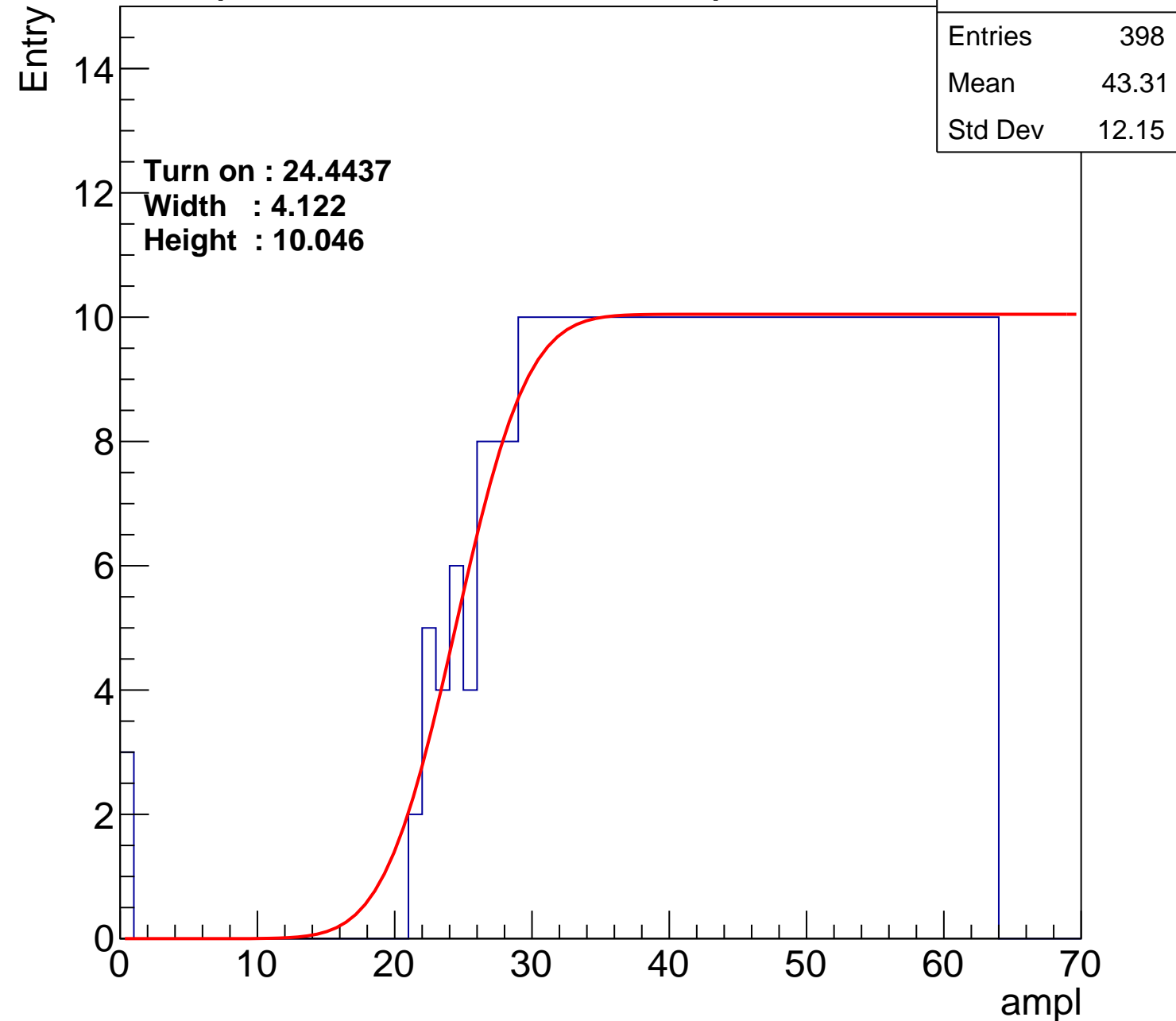
Width : 4.122

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U17-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.28
Std Dev	12.3

Turn on : 25.1200

Width : 3.514

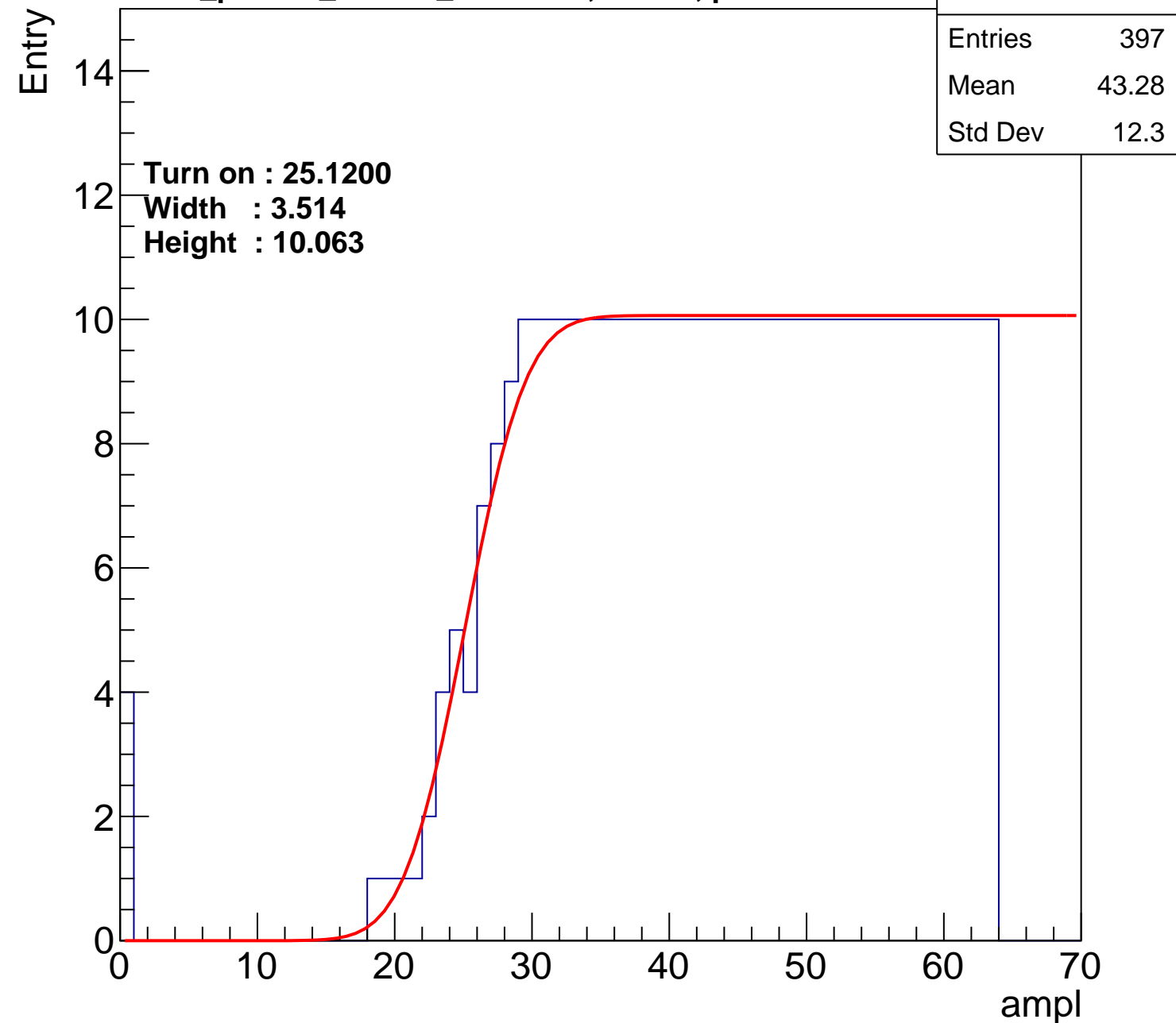
Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U17-ch126

calib_packv5_042523_0143.root, FC#12, port B1

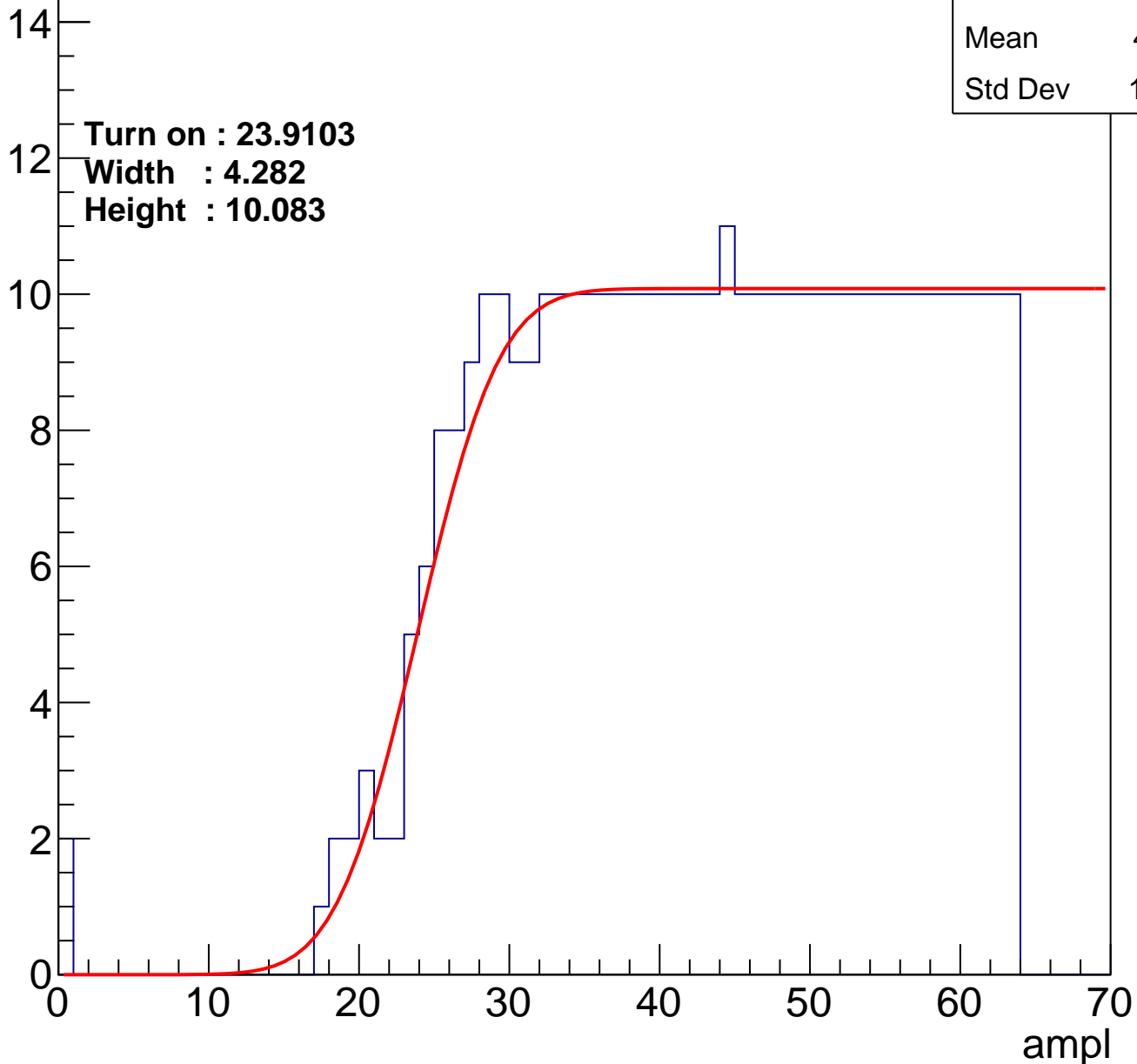
Entries	409
Mean	42.81
Std Dev	12.35

Turn on : 23.9103

Width : 4.282

Height : 10.083

Entry



B0L102S, U17-ch127

calib_packv5_042523_0143.root, FC#12, port B1

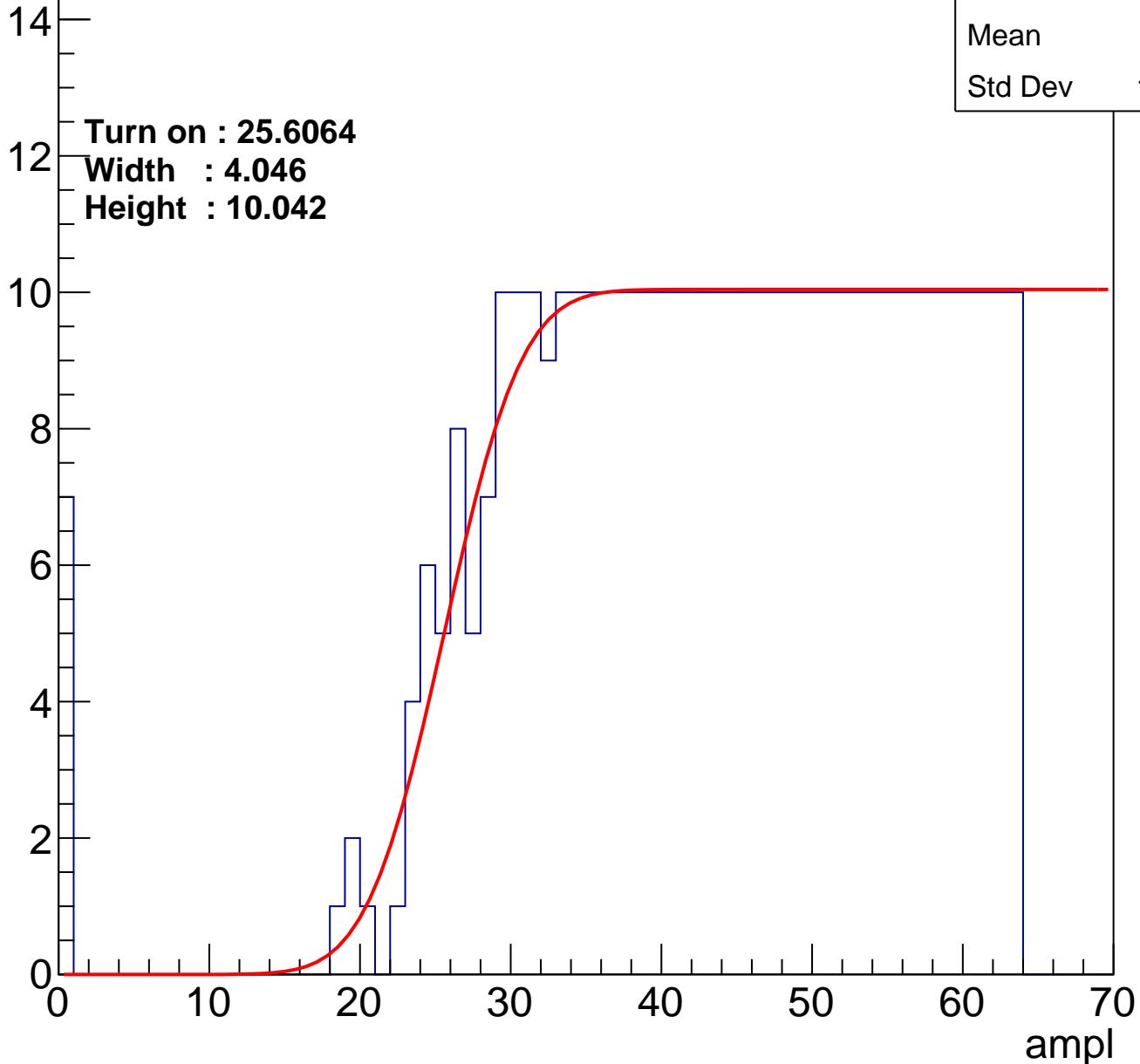
Entries	396
Mean	43.1
Std Dev	12.81

Turn on : 25.6064

Width : 4.046

Height : 10.042

Entry



B0L102S, U17-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.1
Std Dev	12.81

Turn on : 25.6064

Width : 4.046

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

