



# B1L002S, U8-ch0, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	26.93
Std Dev	3.036

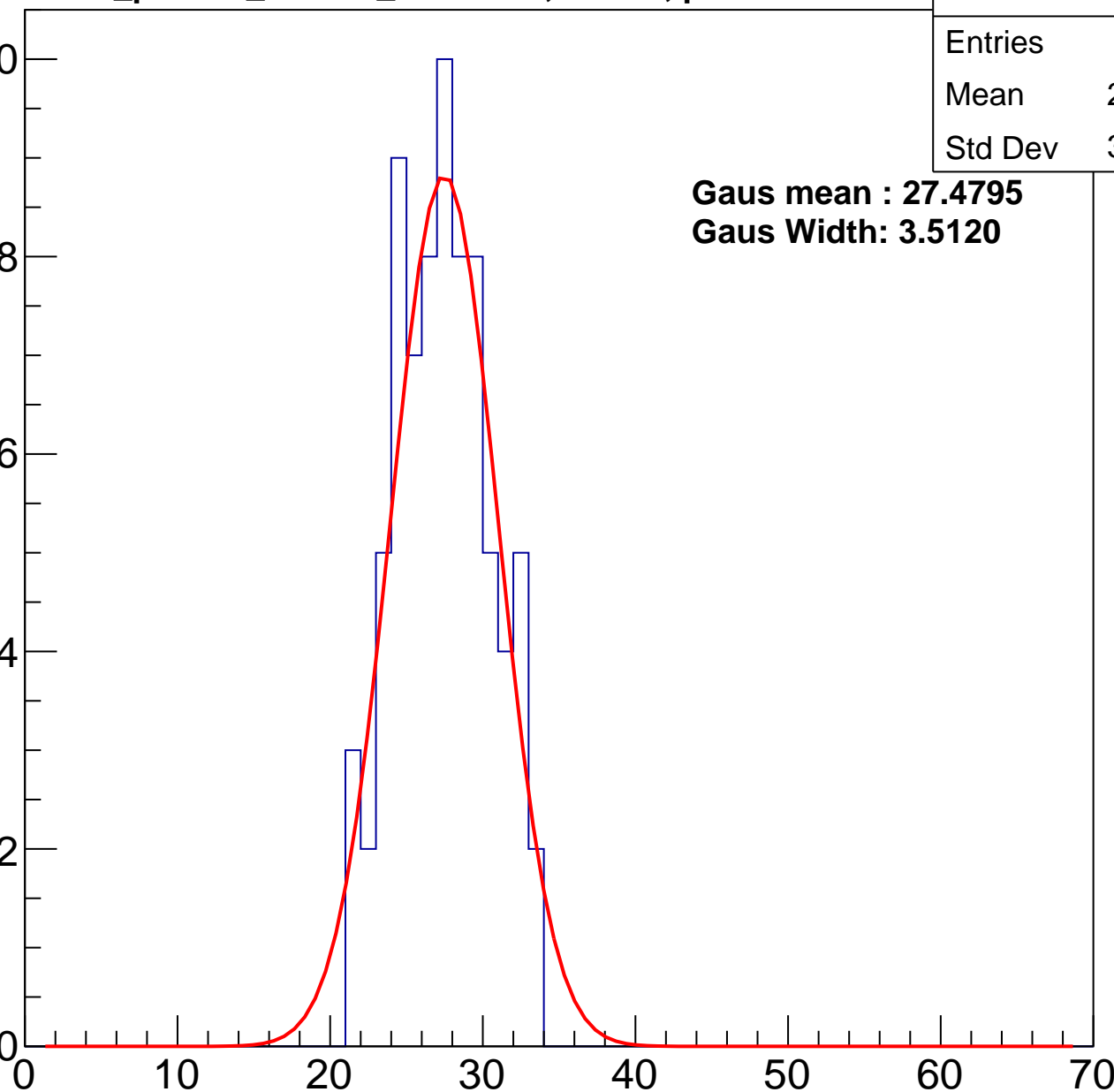
**Gaus mean : 27.4795**

**Gaus Width: 3.5120**

Entry

10  
8  
6  
4  
2  
0

ampl



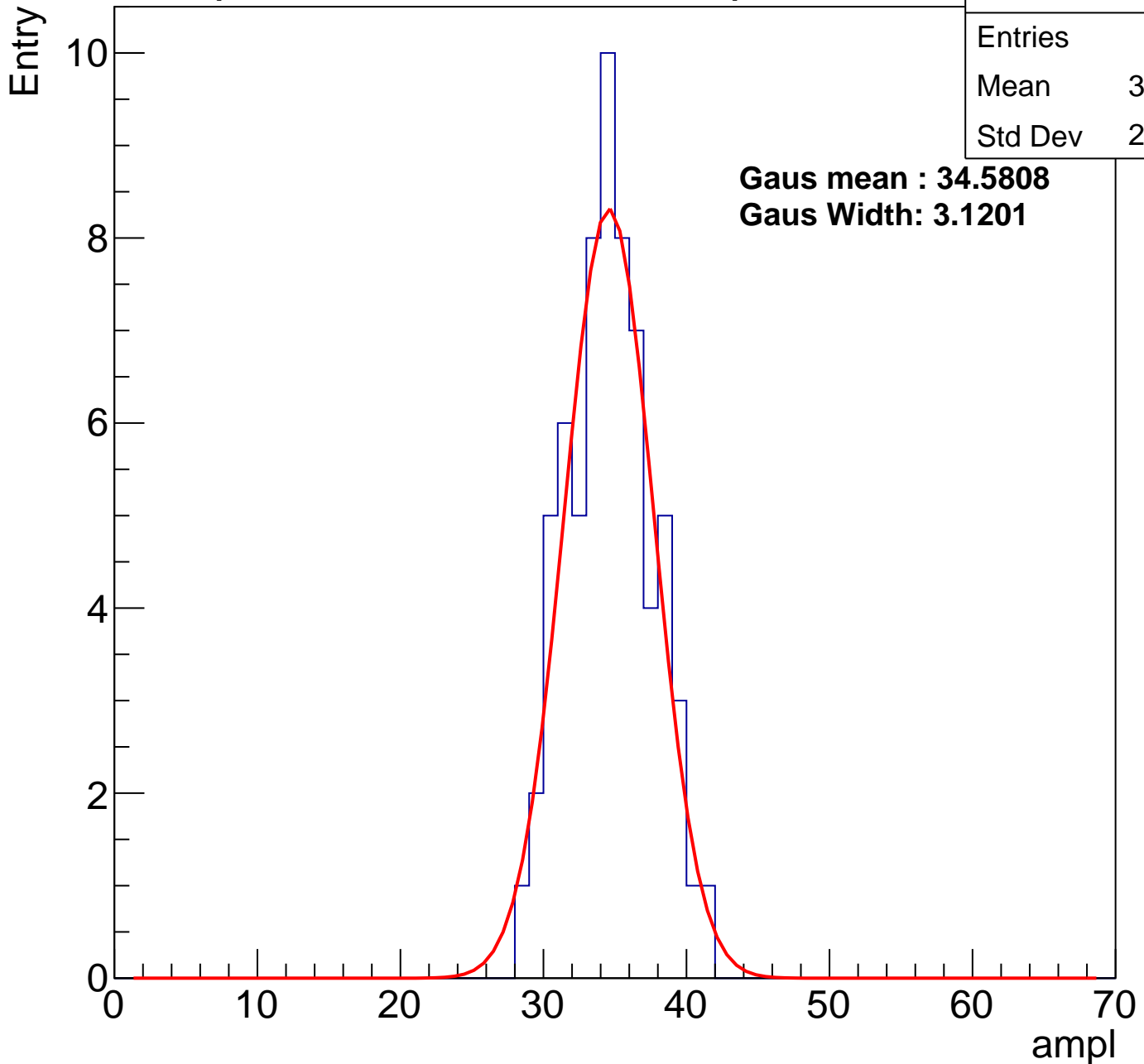
# B1L002S, U8-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	34.15
Std Dev	2.909

**Gaus mean : 34.5808**

**Gaus Width: 3.1201**



# B1L002S, U8-ch0, adc2

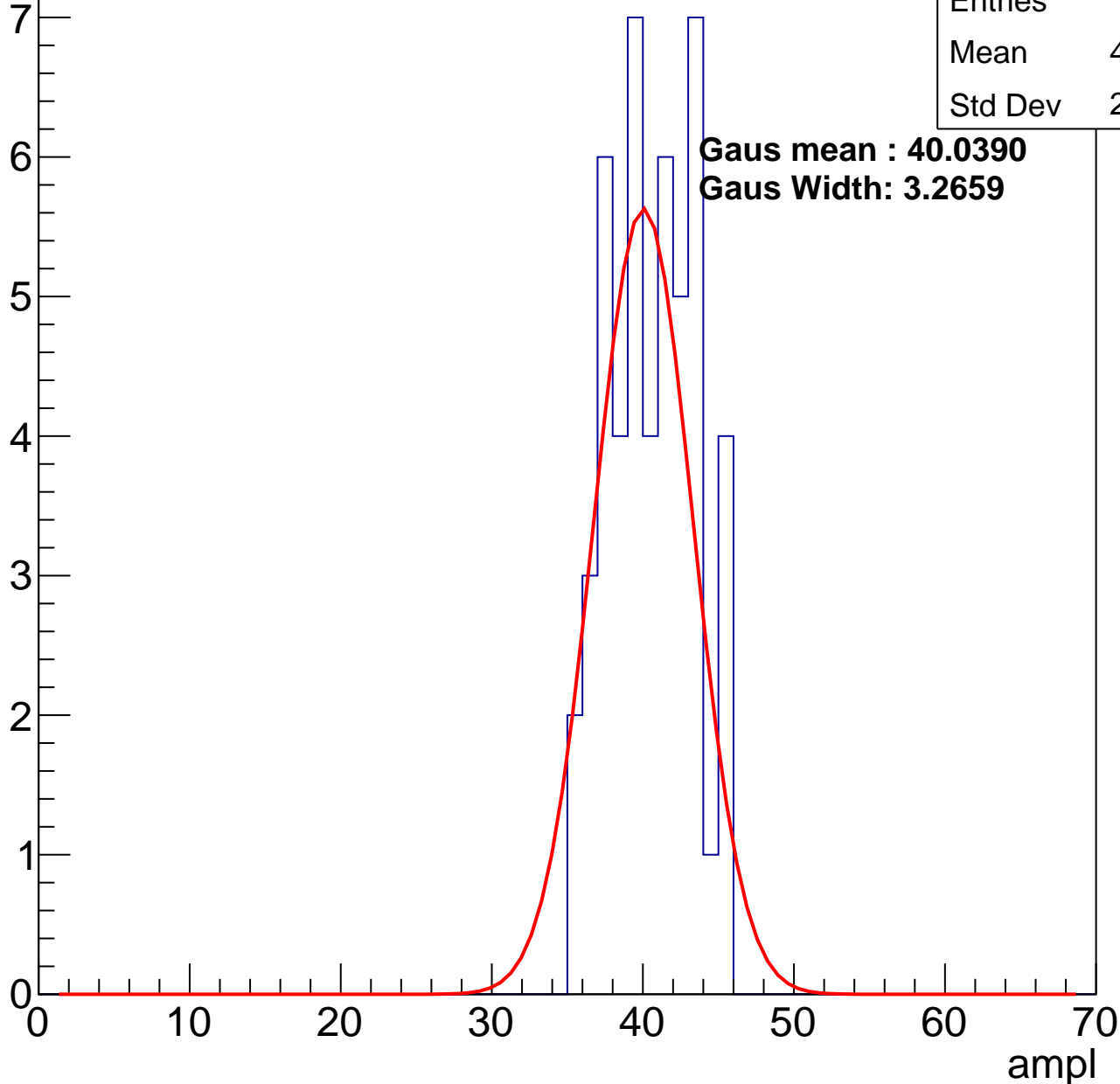
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.12
Std Dev	2.782

**Gaus mean : 40.0390**

**Gaus Width: 3.2659**

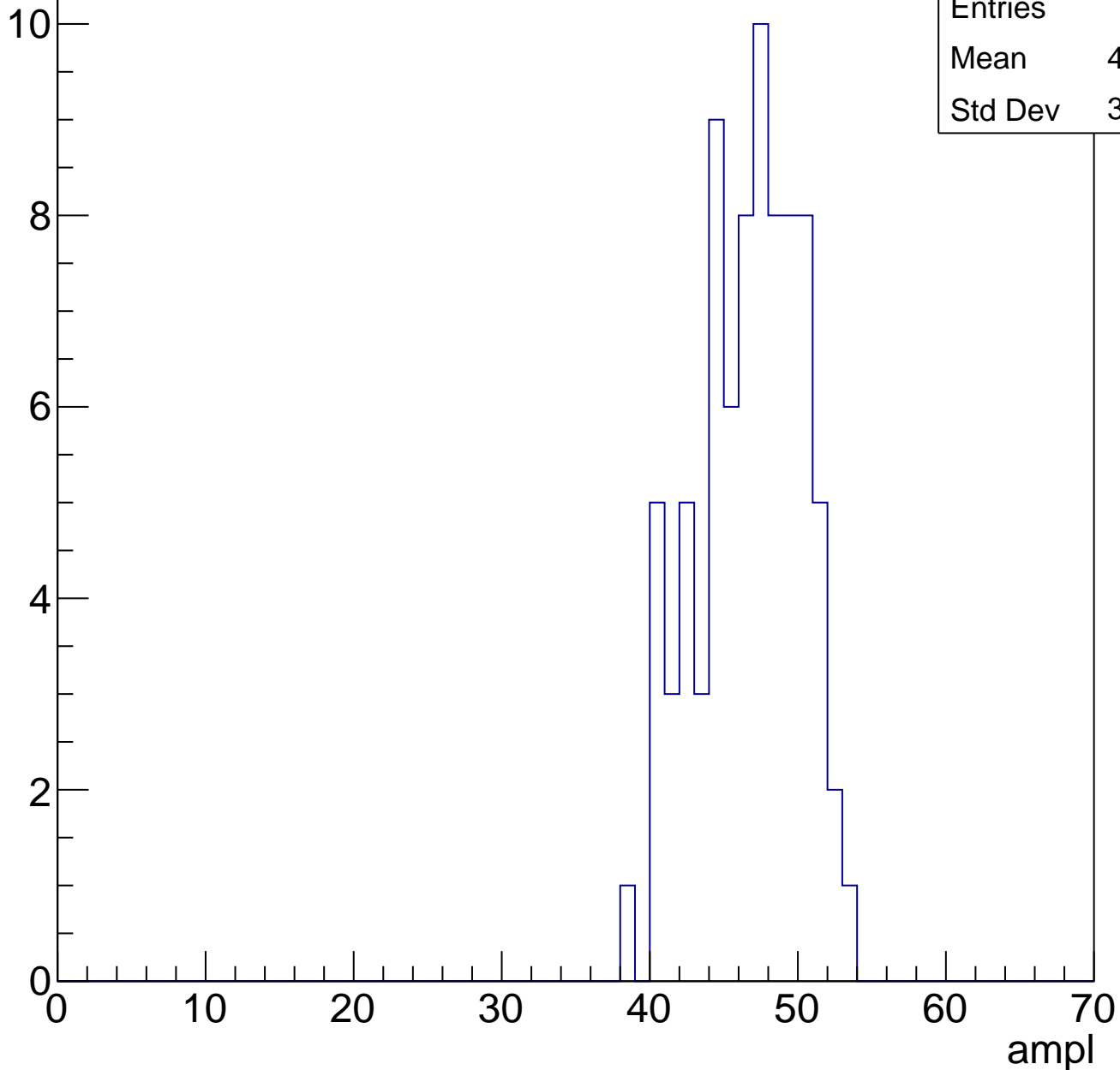


# B1L002S, U8-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	46.24
Std Dev	3.406

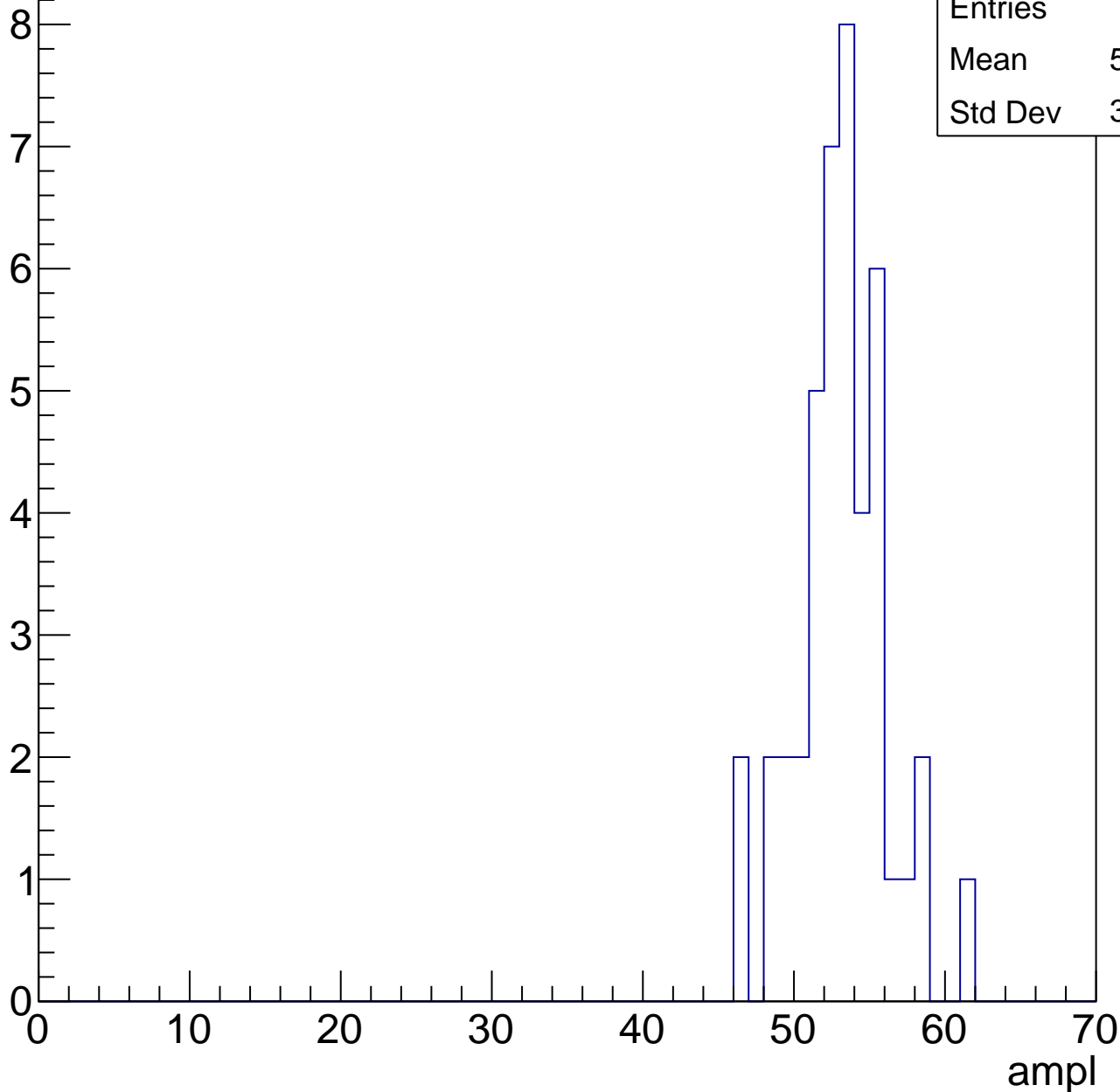
Entry



# B1L002S, U8-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

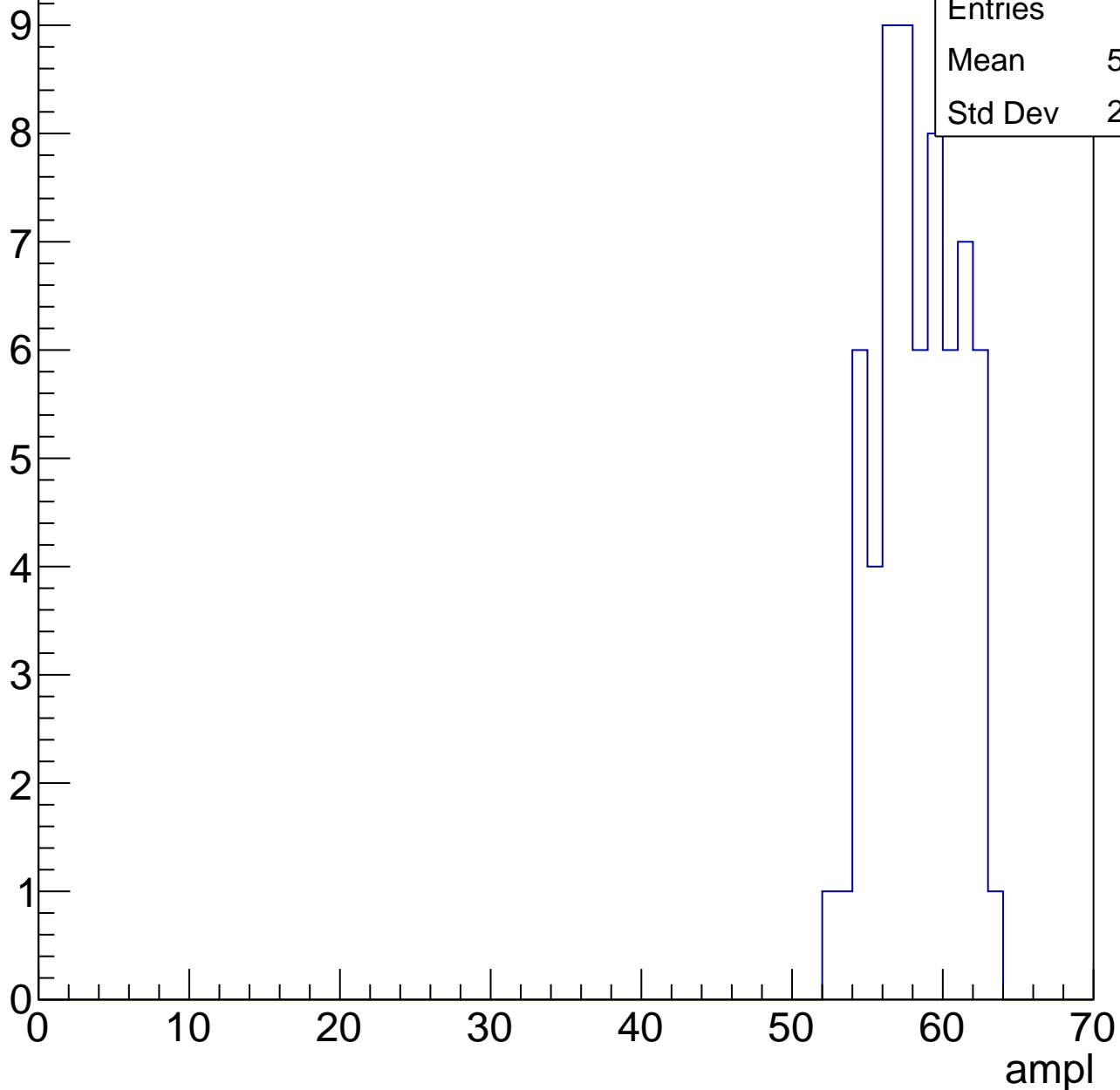
Entry



# B1L002S, U8-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



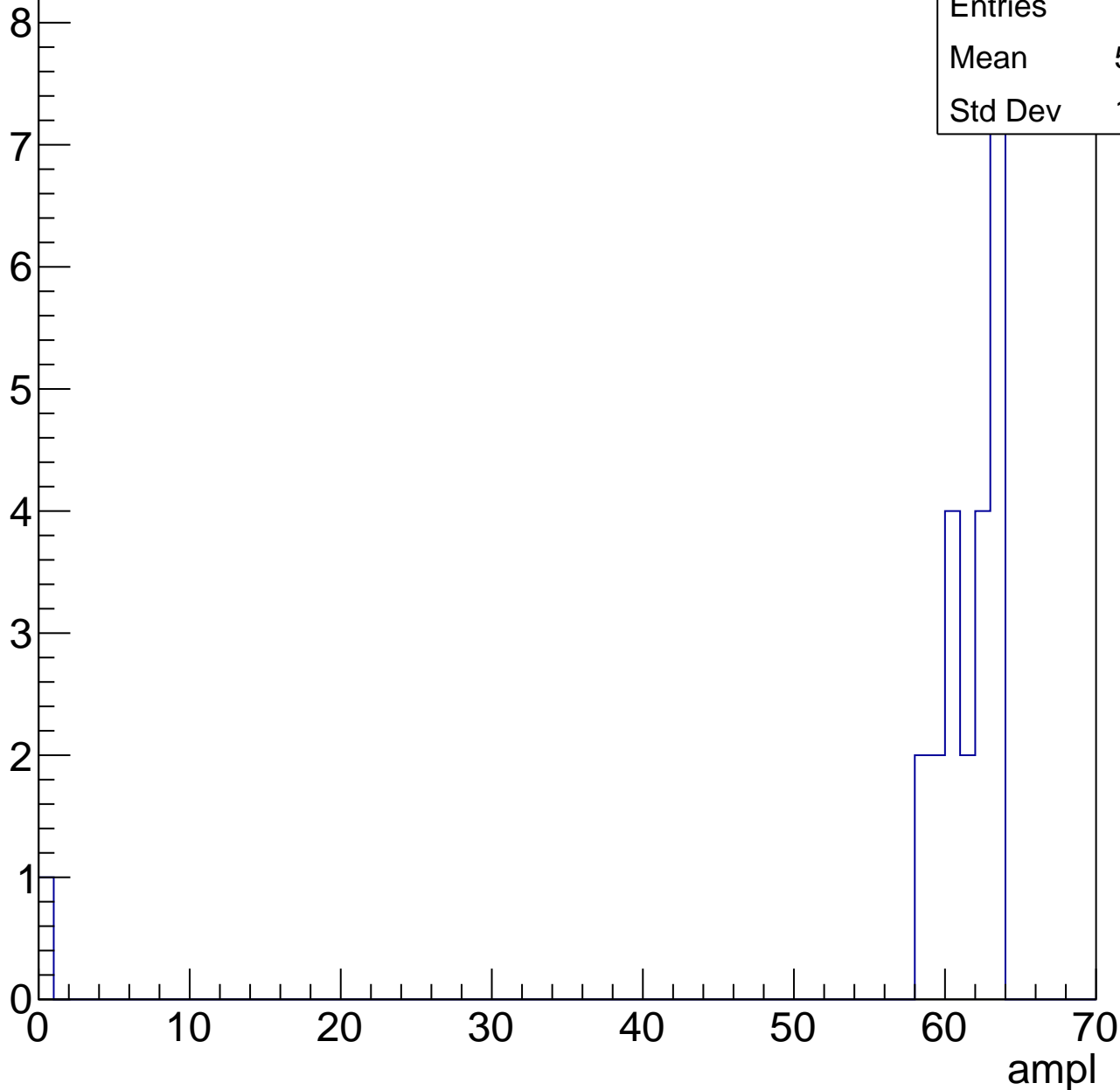
Entries	64
Mean	57.94
Std Dev	2.663

# B1L002S, U8-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.61
Std Dev	12.61





# B1L002S, U8-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch1, adc0

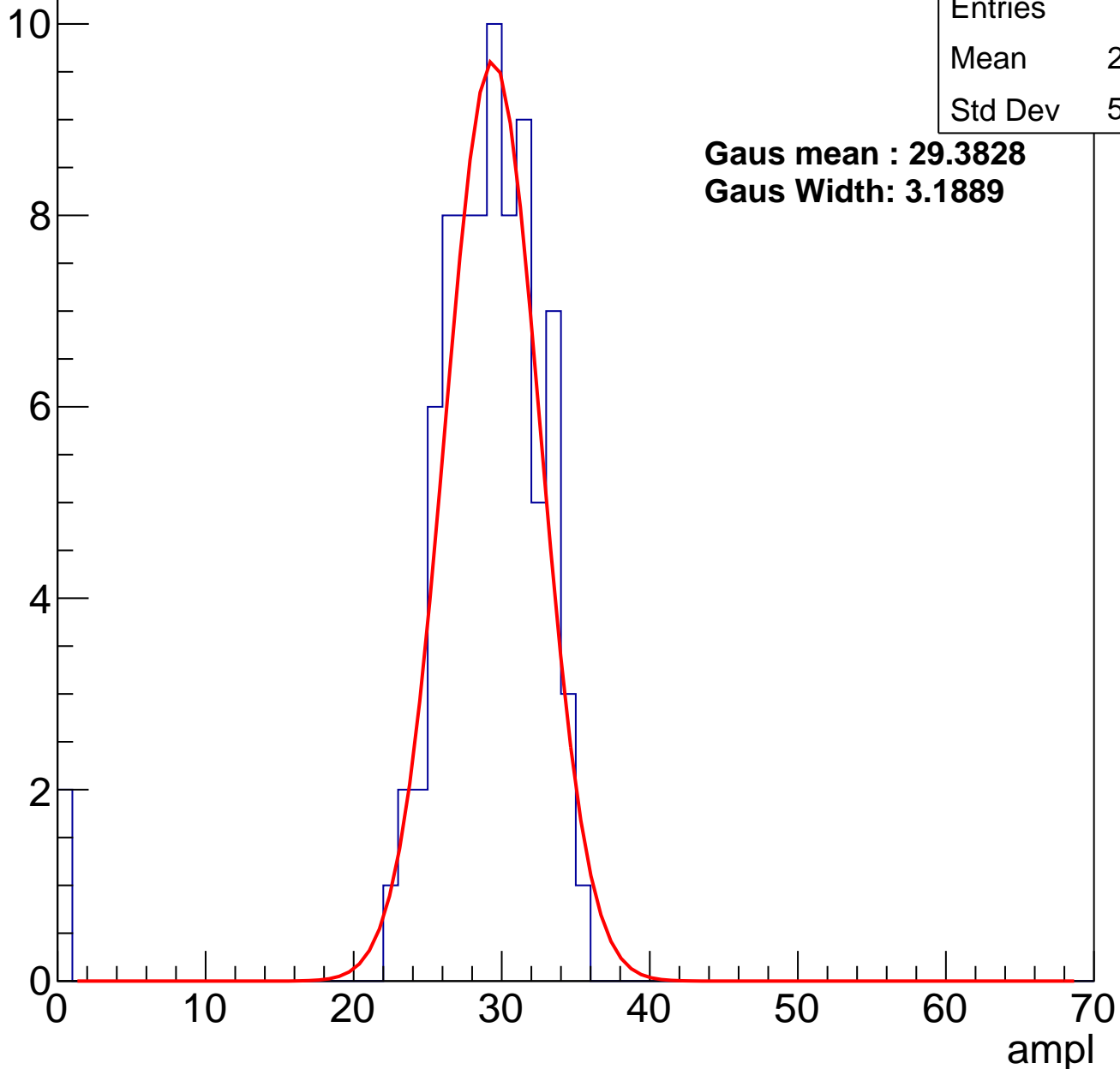
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	80
Mean	28.14
Std Dev	5.378

**Gaus mean : 29.3828**

**Gaus Width: 3.1889**

Entry



# B1L002S, U8-ch1, adc1

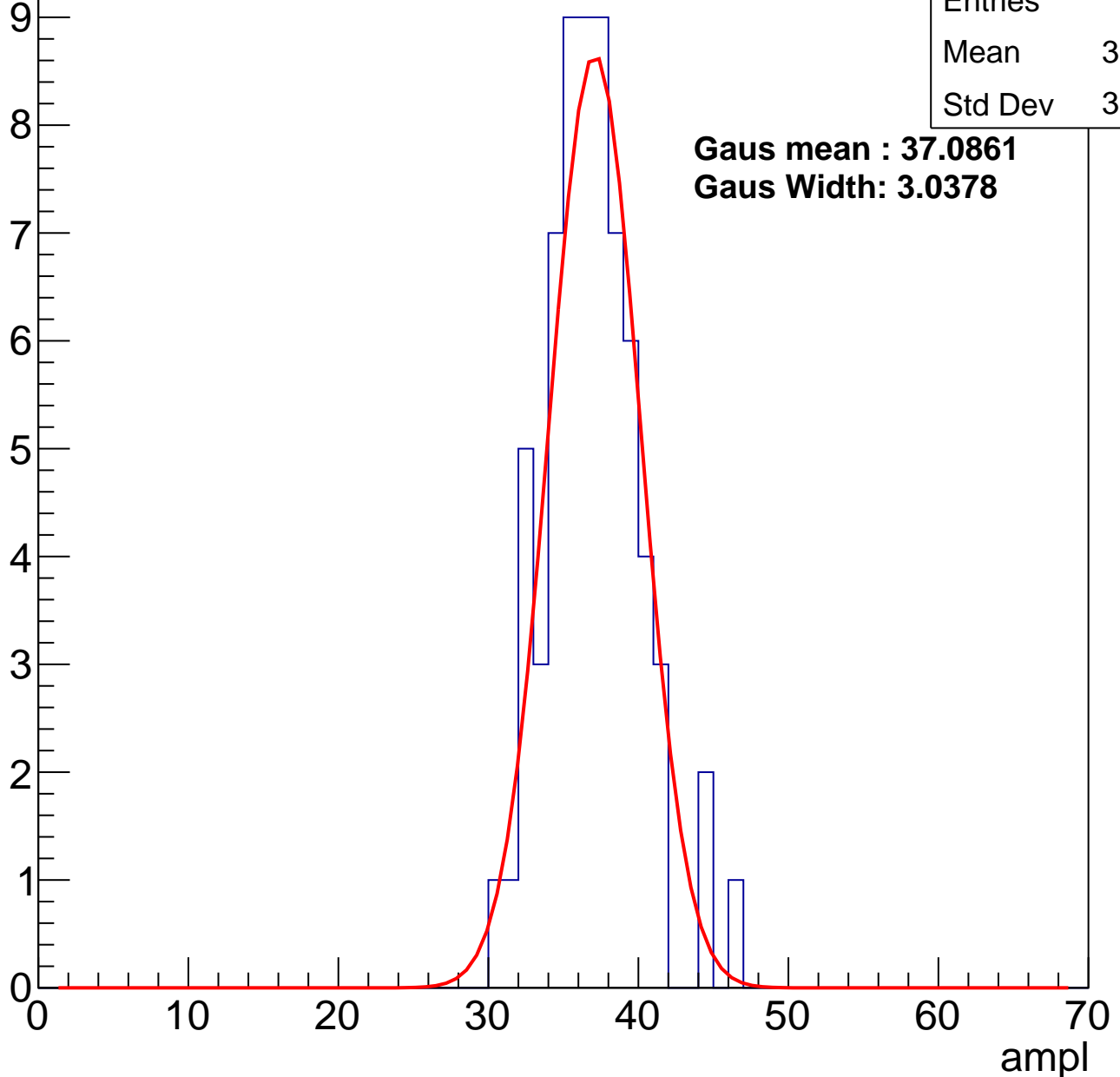
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	36.52
Std Dev	3.102

**Gaus mean : 37.0861**

**Gaus Width: 3.0378**



# B1L002S, U8-ch1, adc2

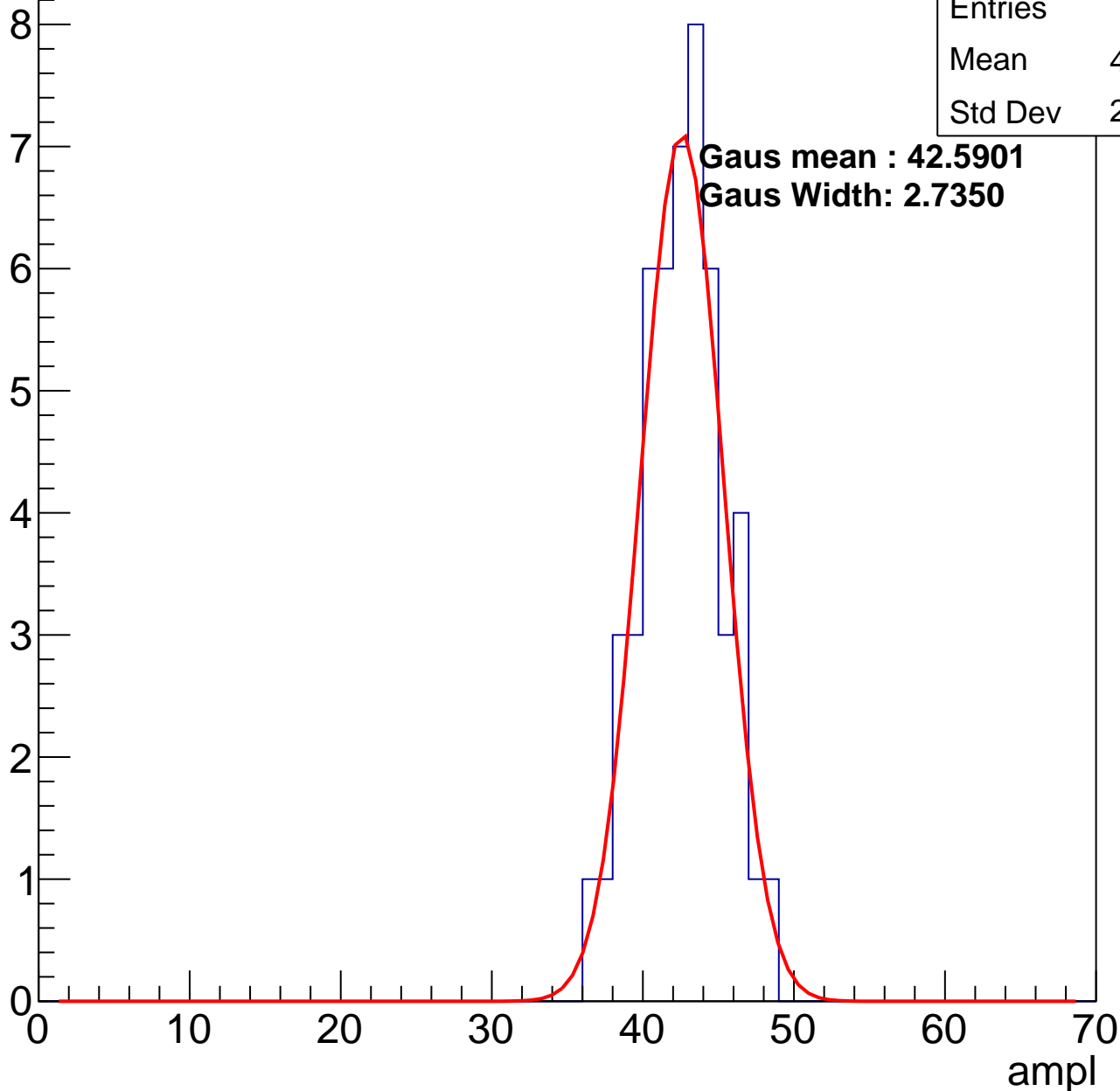
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	42.12
Std Dev	2.643

**Gaus mean : 42.5901**

**Gaus Width: 2.7350**

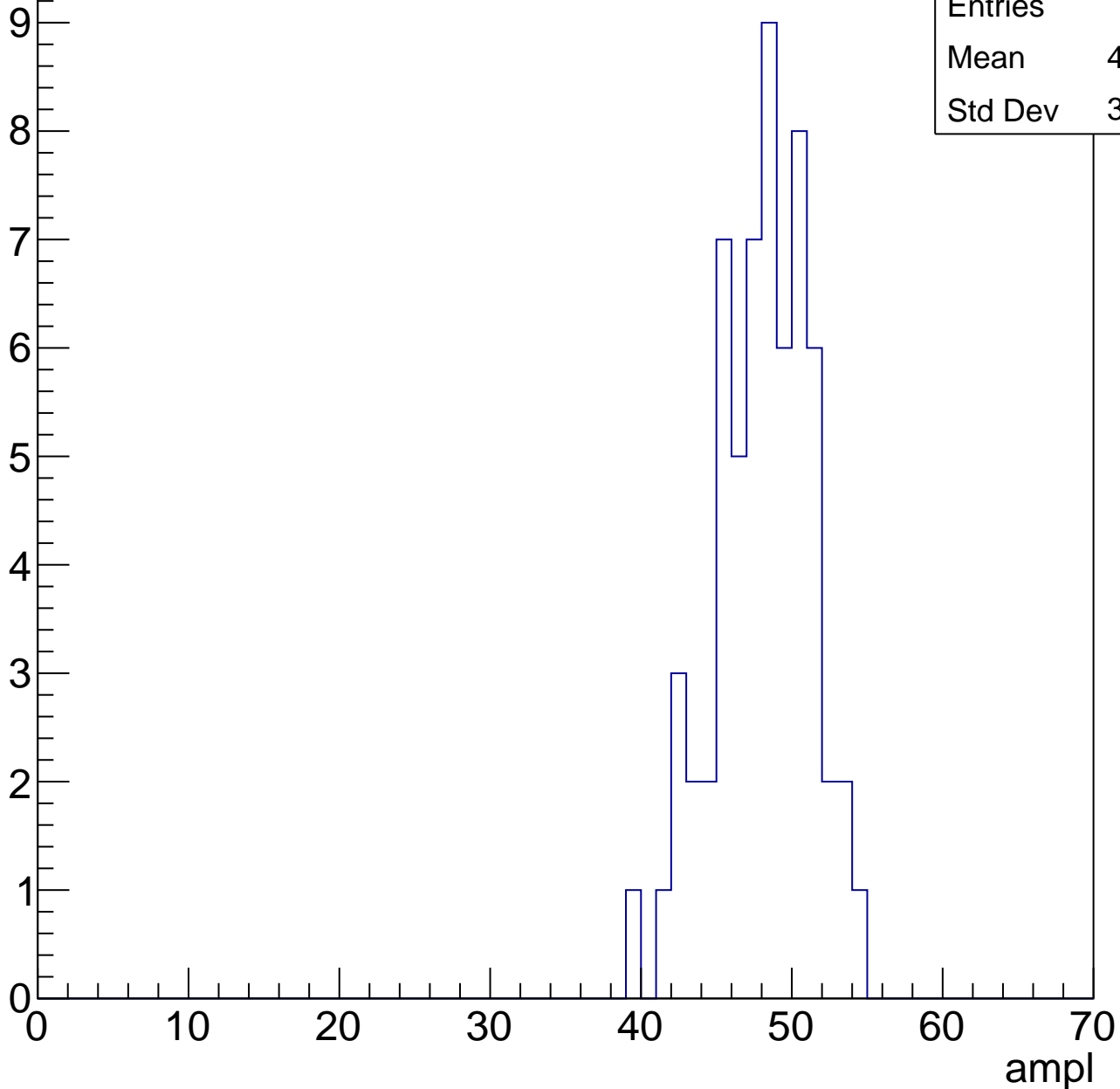


# B1L002S, U8-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

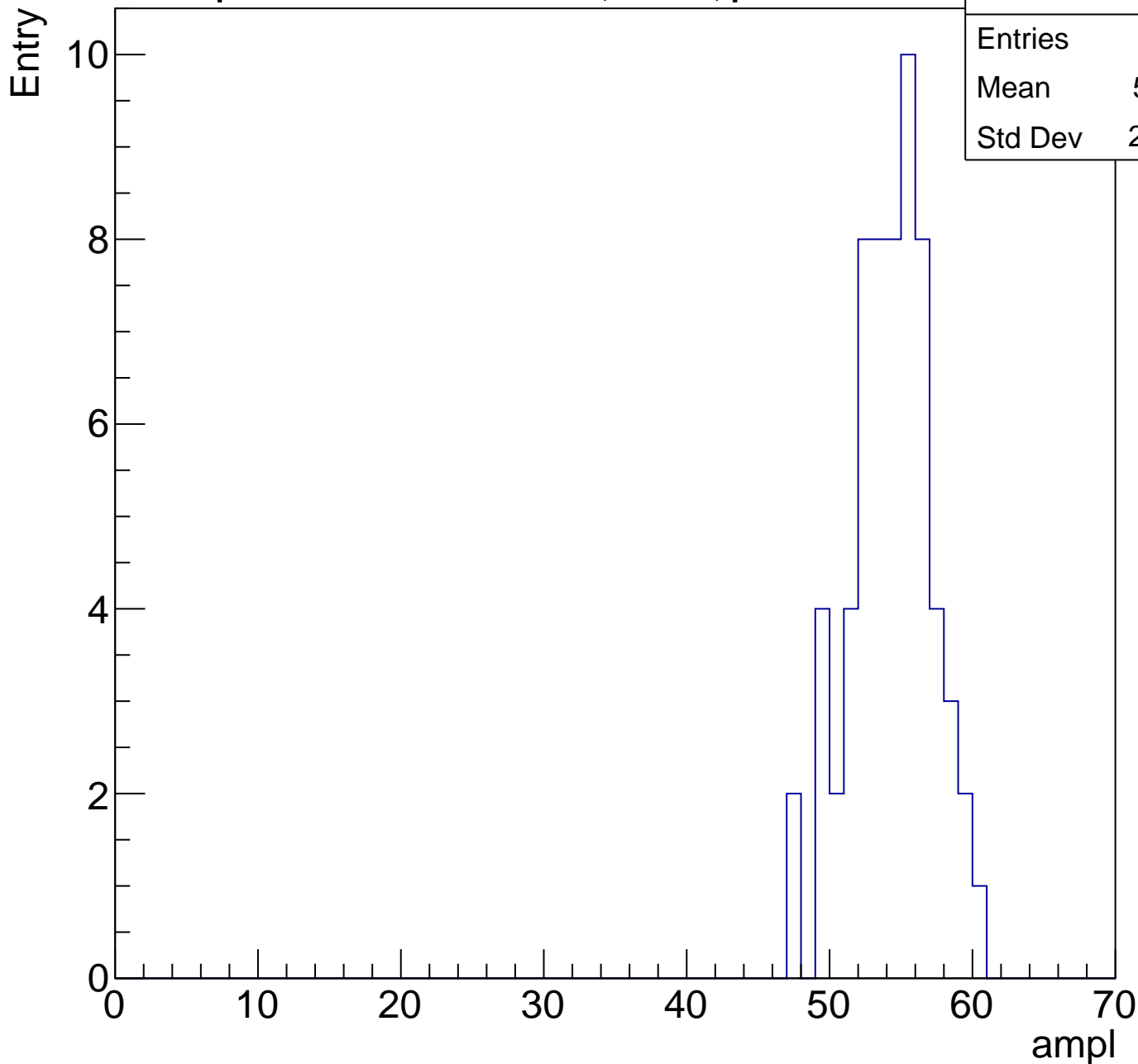
Entries	62
Mean	47.58
Std Dev	3.134



# B1L002S, U8-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	53.81
Std Dev	2.839

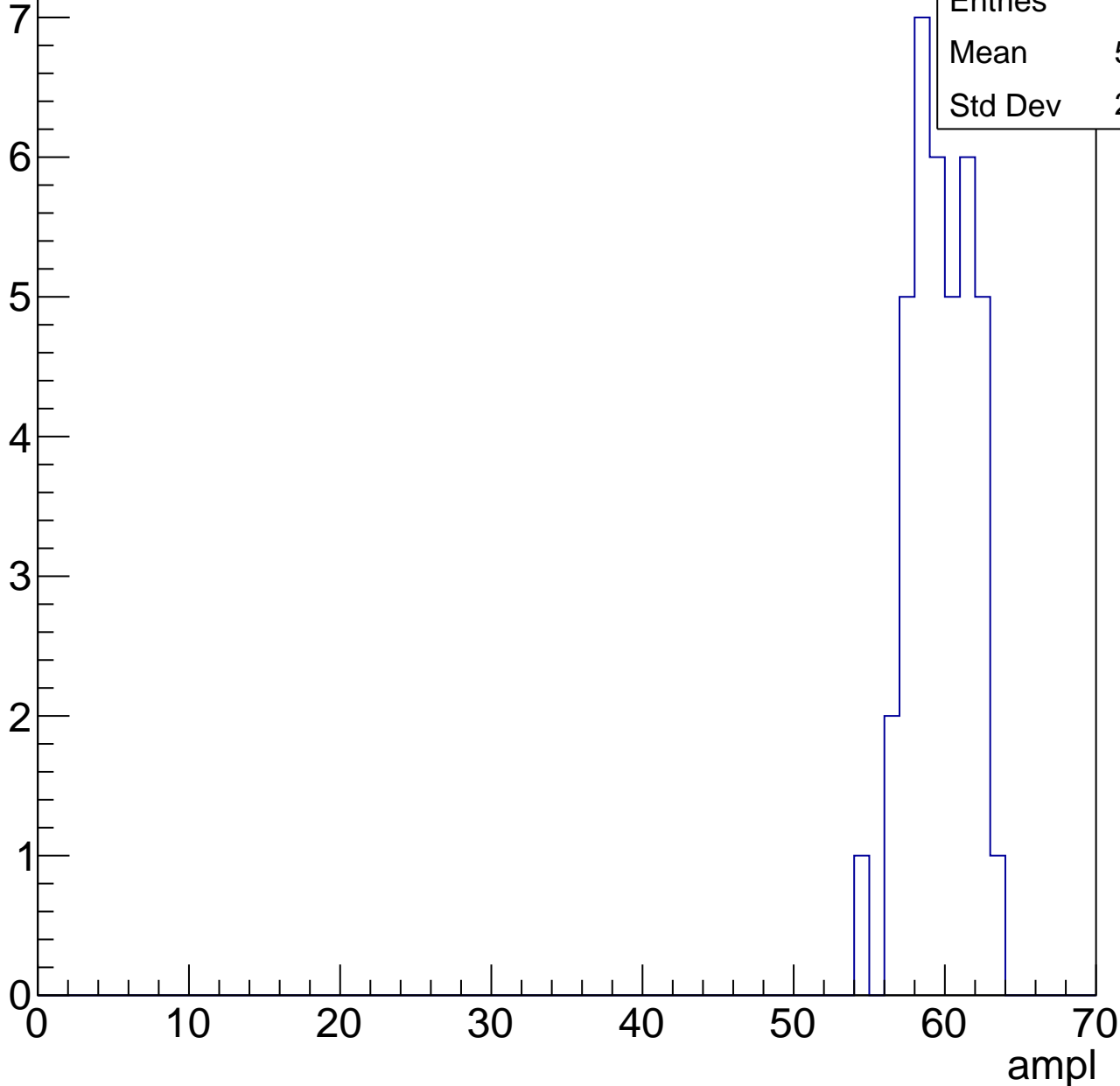


# B1L002S, U8-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	59.21
Std Dev	2.041

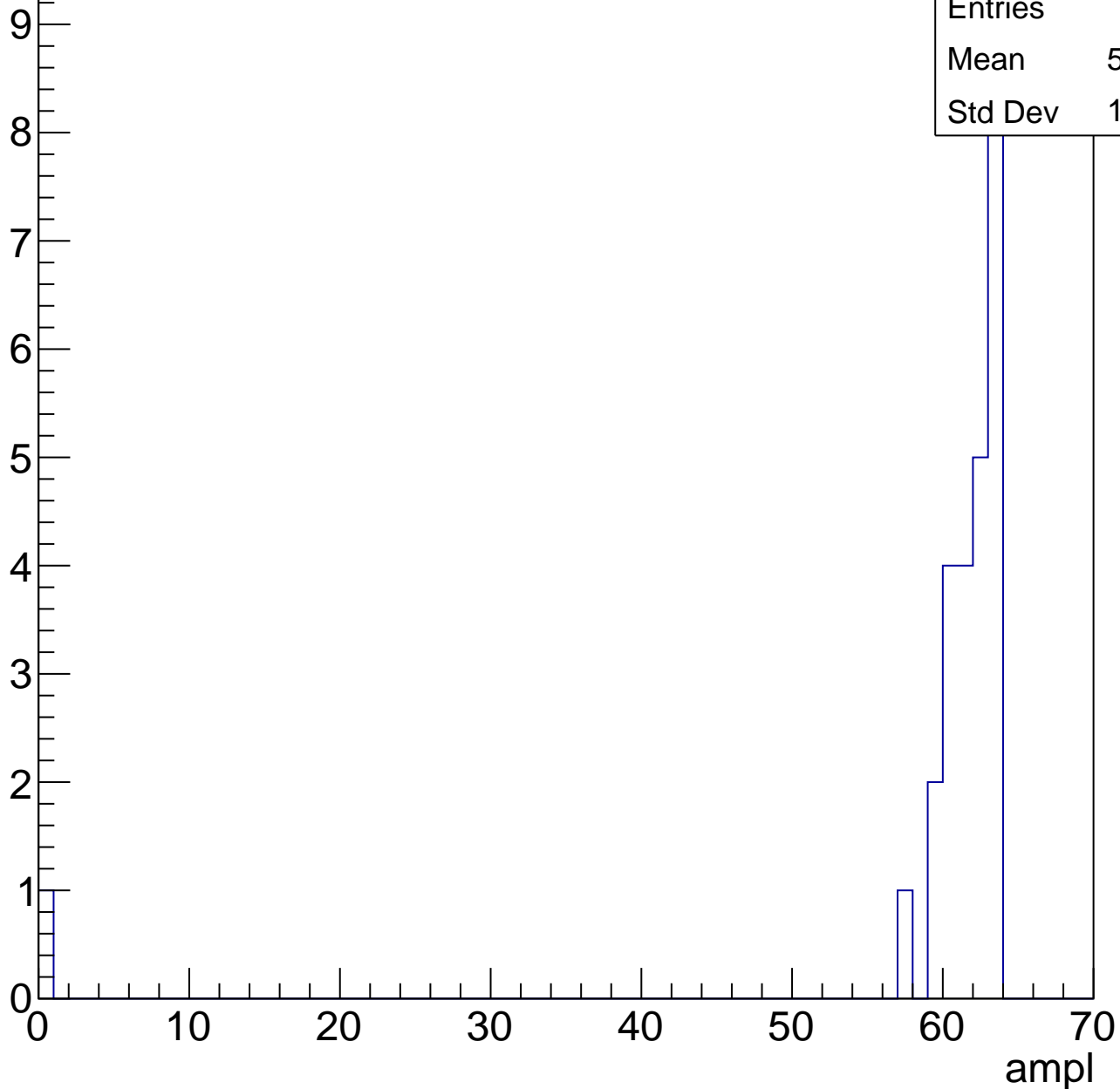


# B1L002S, U8-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59.08
Std Dev	11.92

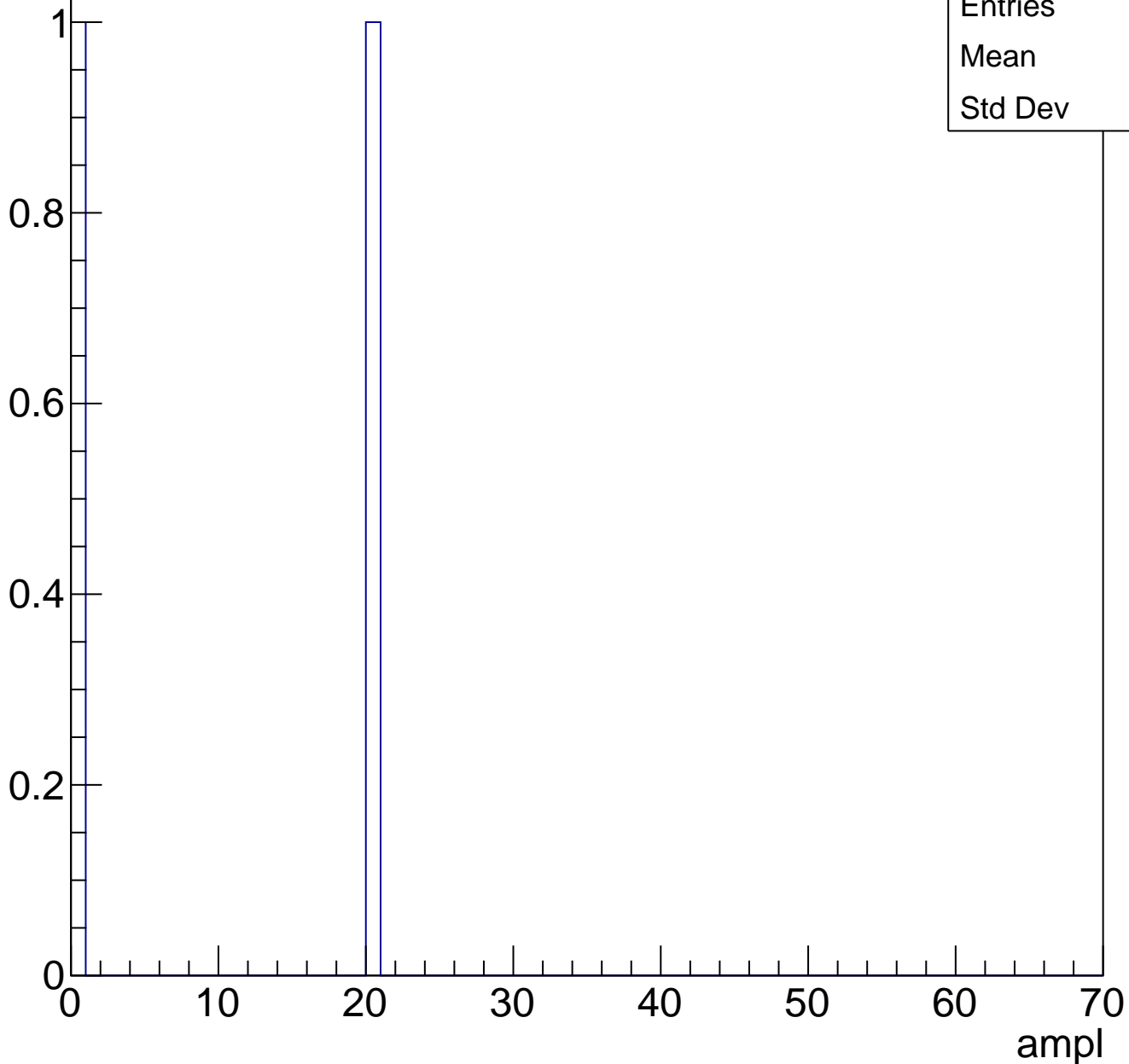




# B1L002S, U8-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch2, adc0

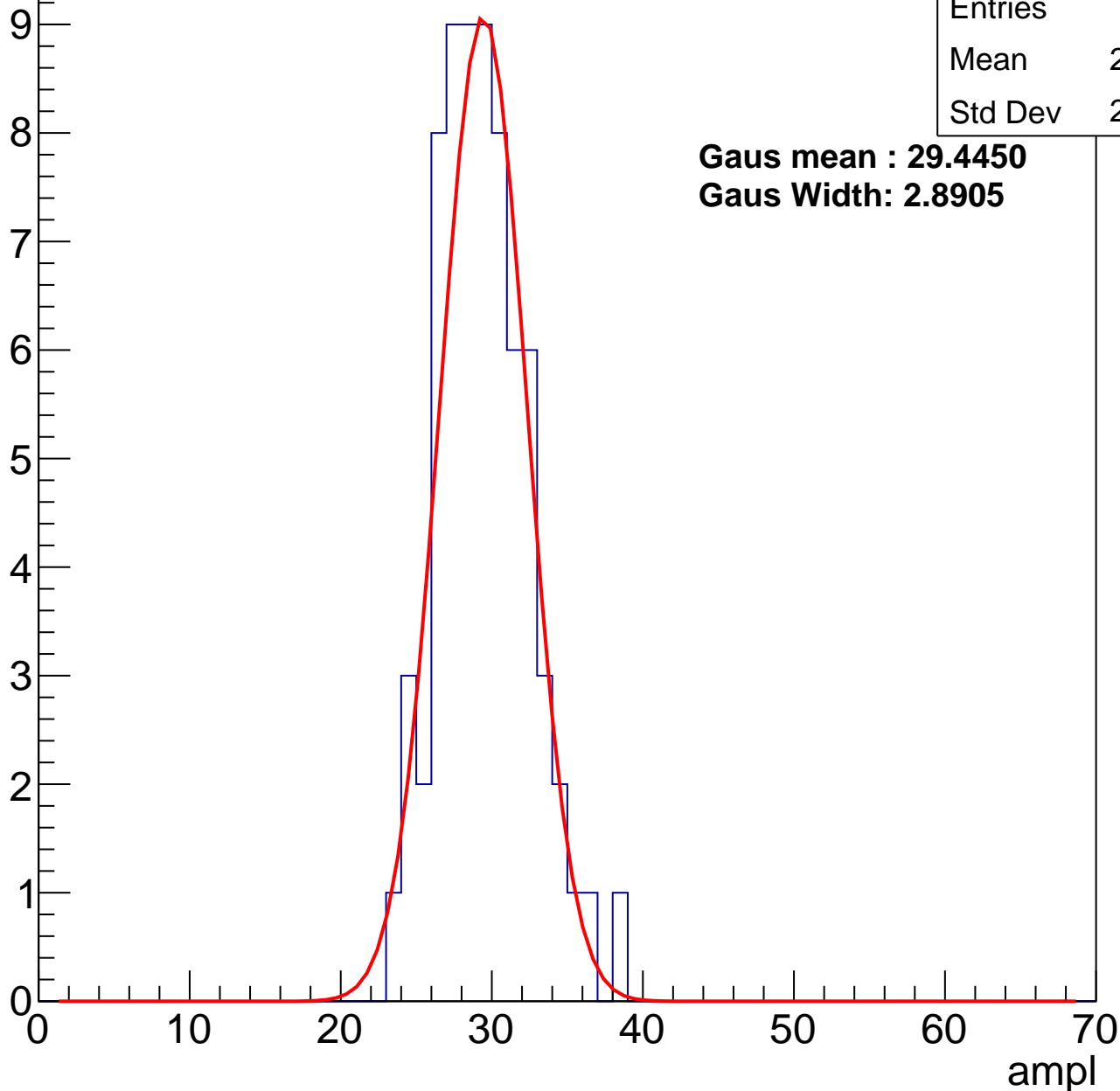
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	29.03
Std Dev	2.973

**Gaus mean : 29.4450**

**Gaus Width: 2.8905**



# B1L002S, U8-ch2, adc1

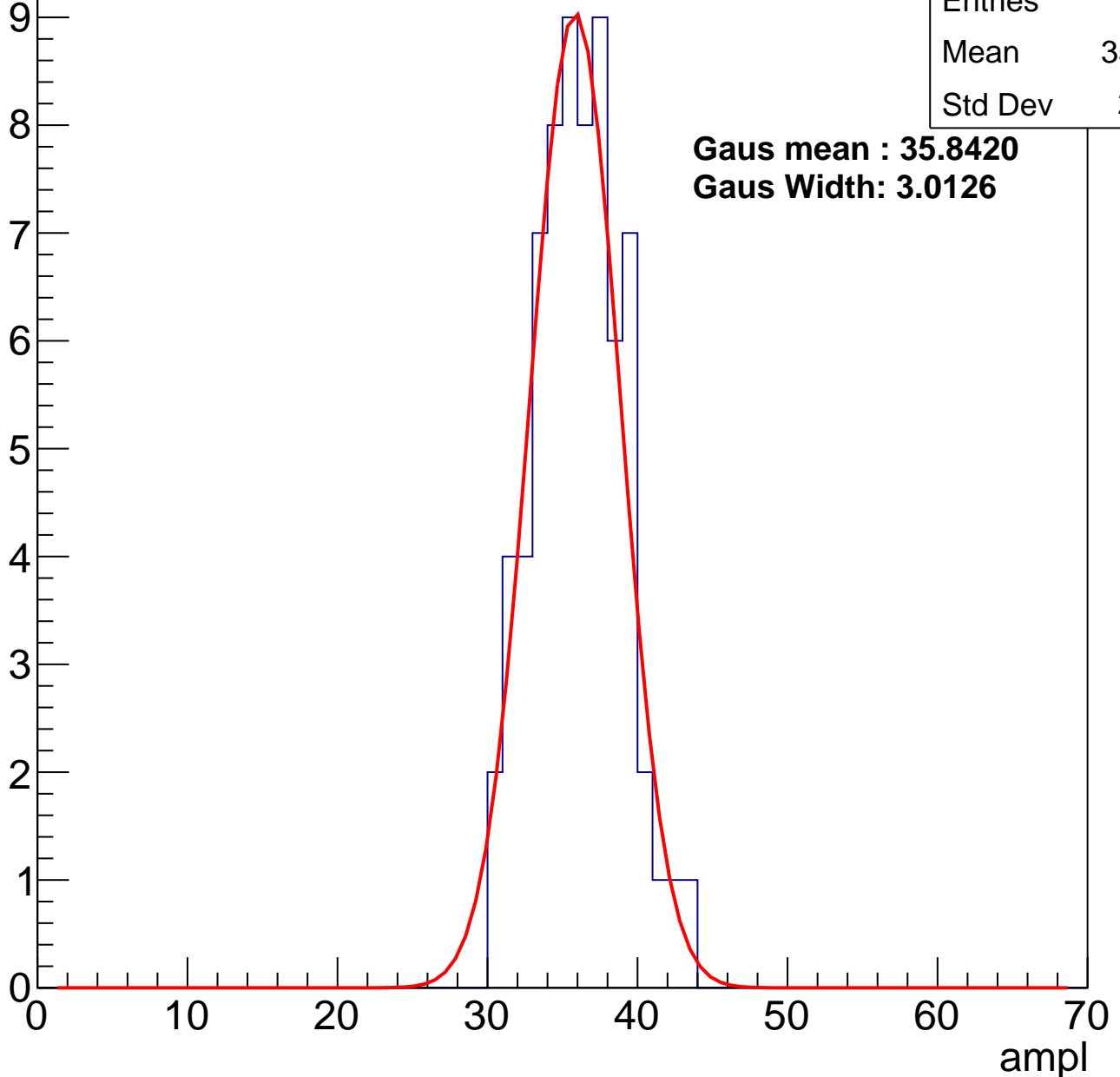
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	35.62
Std Dev	2.87

**Gaus mean : 35.8420**

**Gaus Width: 3.0126**



# B1L002S, U8-ch2, adc2

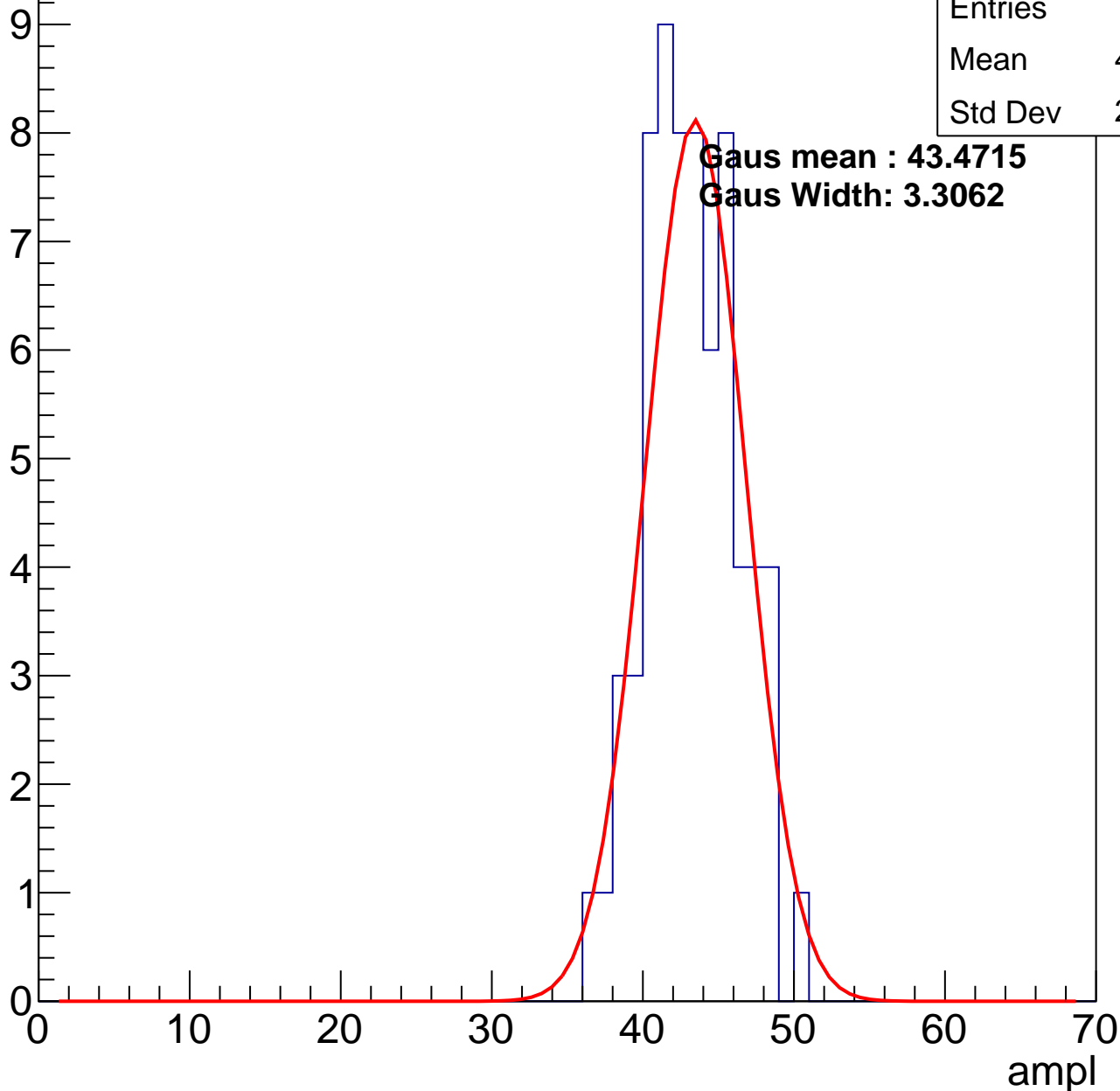
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	42.81
Std Dev	2.991

**Gaus mean : 43.4715**

**Gaus Width: 3.3062**



# B1L002S, U8-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

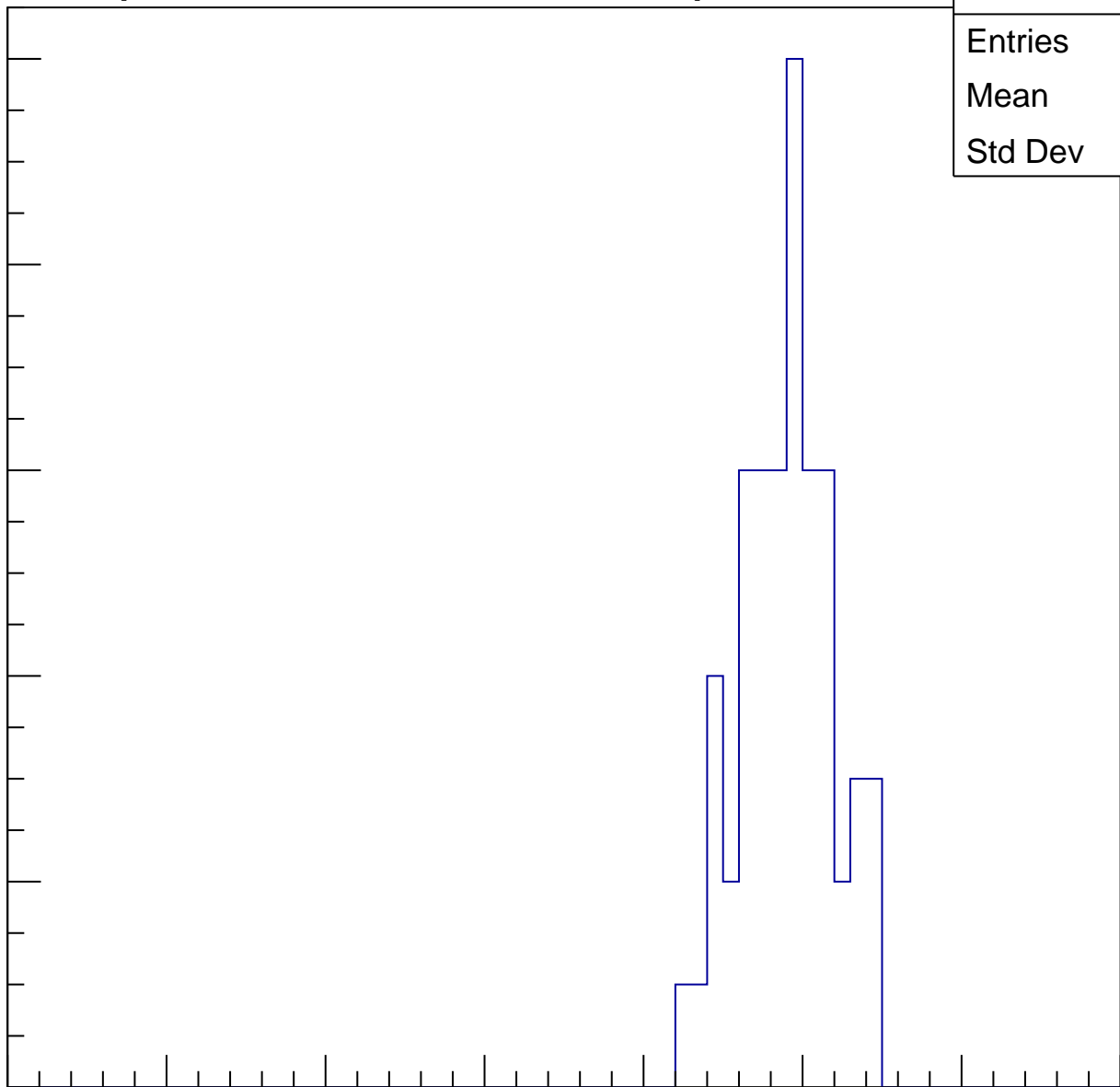
Entries	56
Mean	48.54
Std Dev	2.866

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

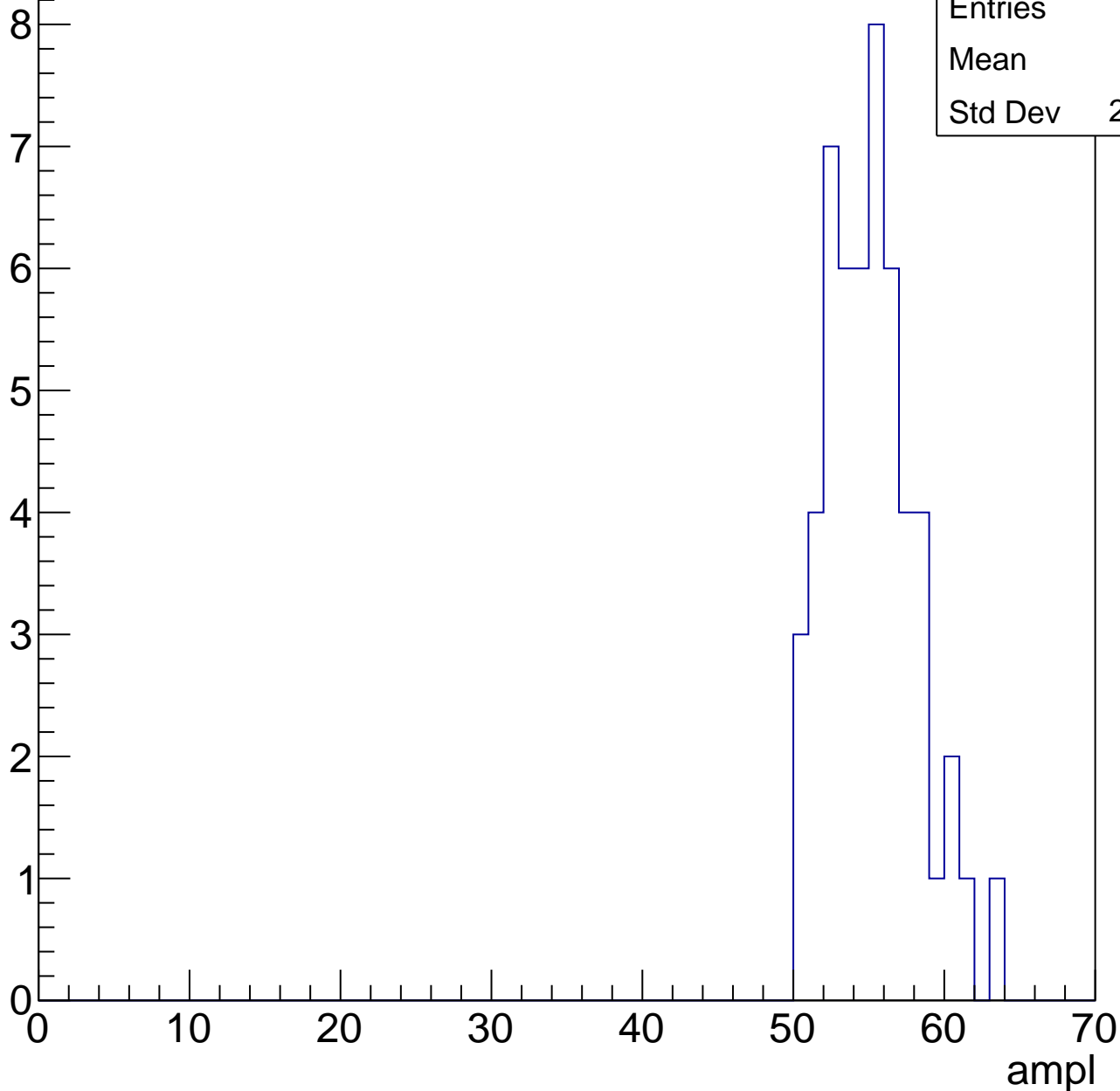


# B1L002S, U8-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	54.7
Std Dev	2.918

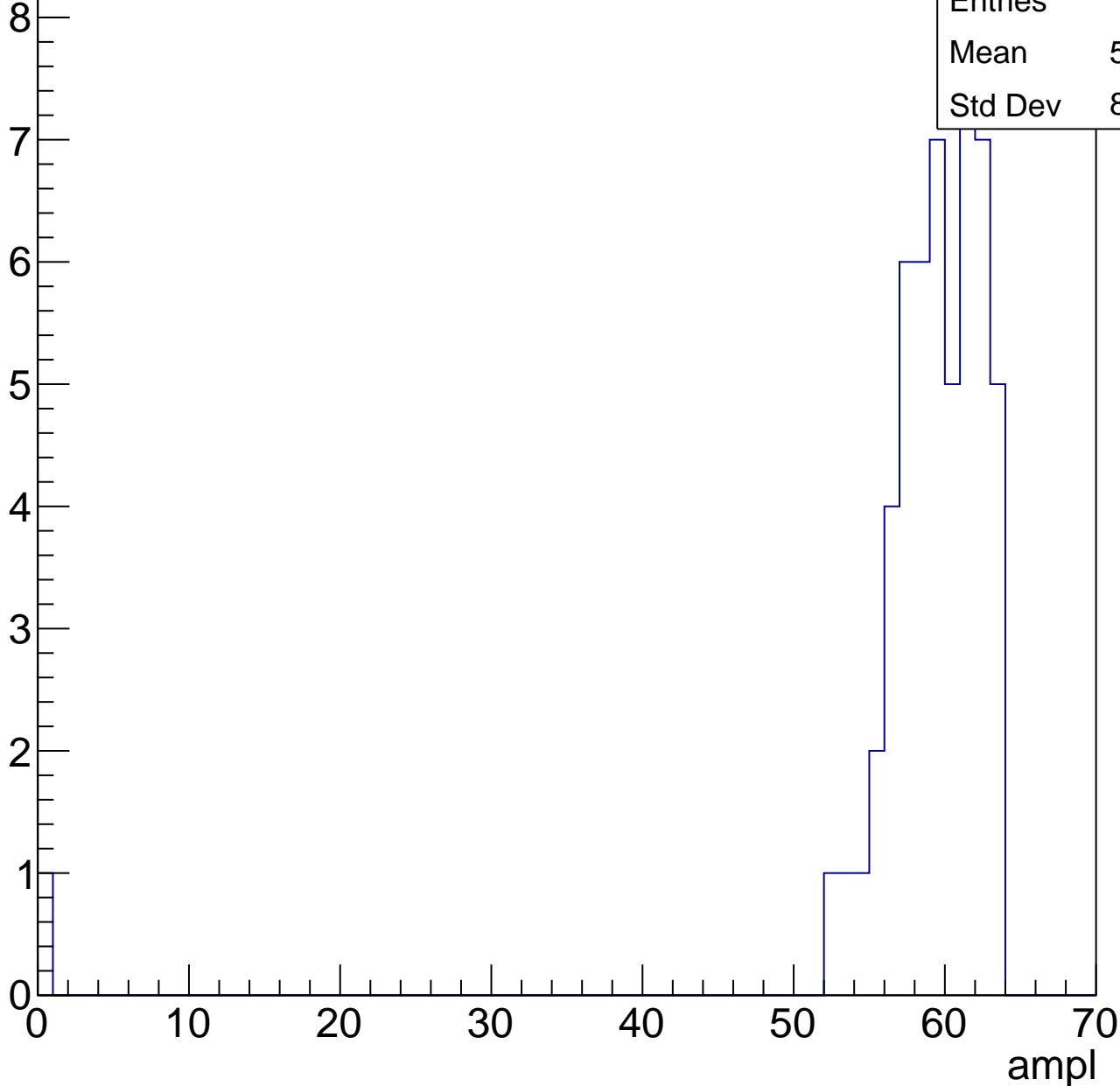


# B1L002S, U8-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

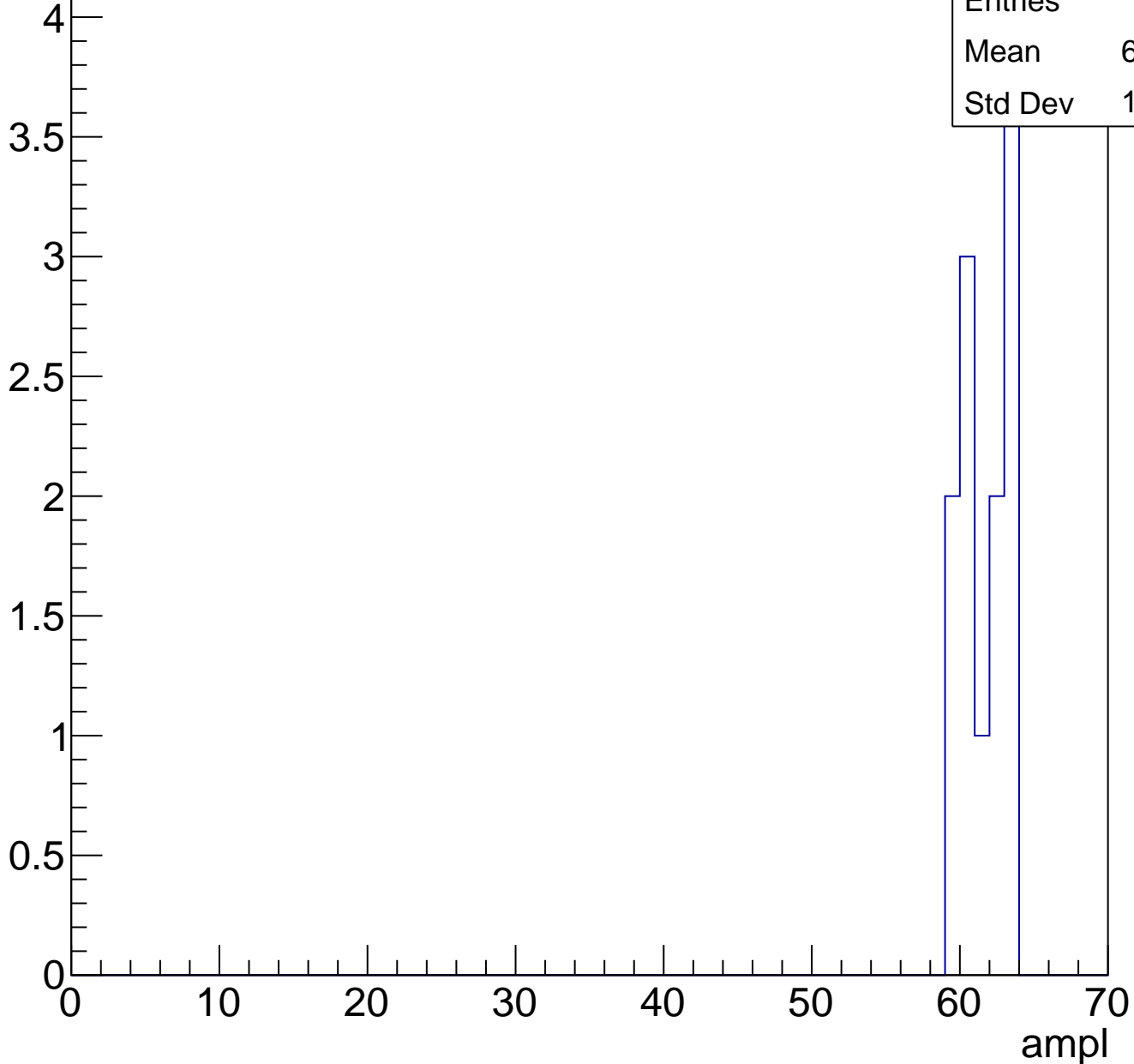
Entries	54
Mean	58.02
Std Dev	8.407



# B1L002S, U8-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	12
Mean	61.25
Std Dev	1.534



# B1L002S, U8-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch3, adc0

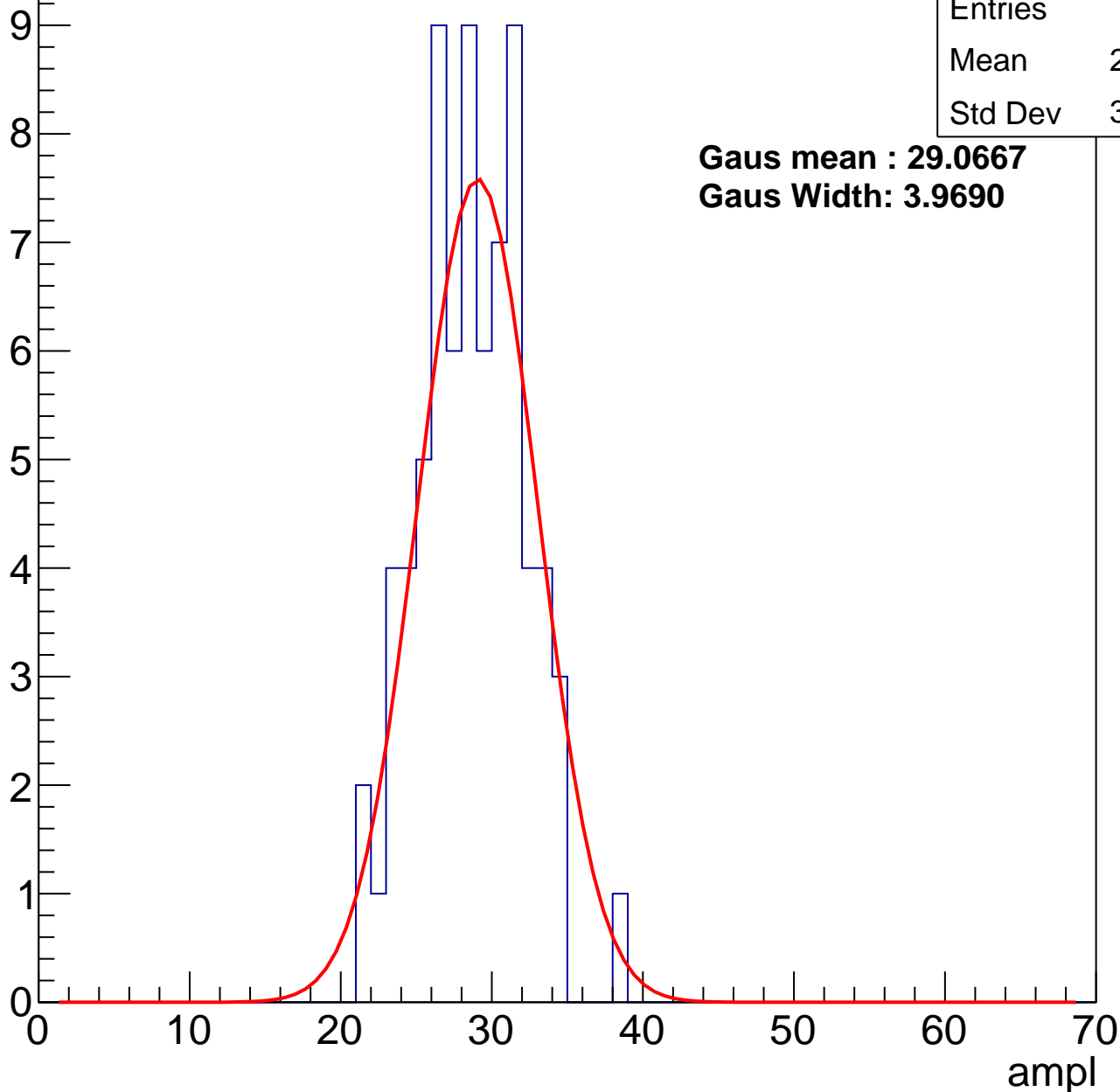
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.22
Std Dev	3.426

**Gaus mean : 29.0667**

**Gaus Width: 3.9690**



# B1L002S, U8-ch3, adc1

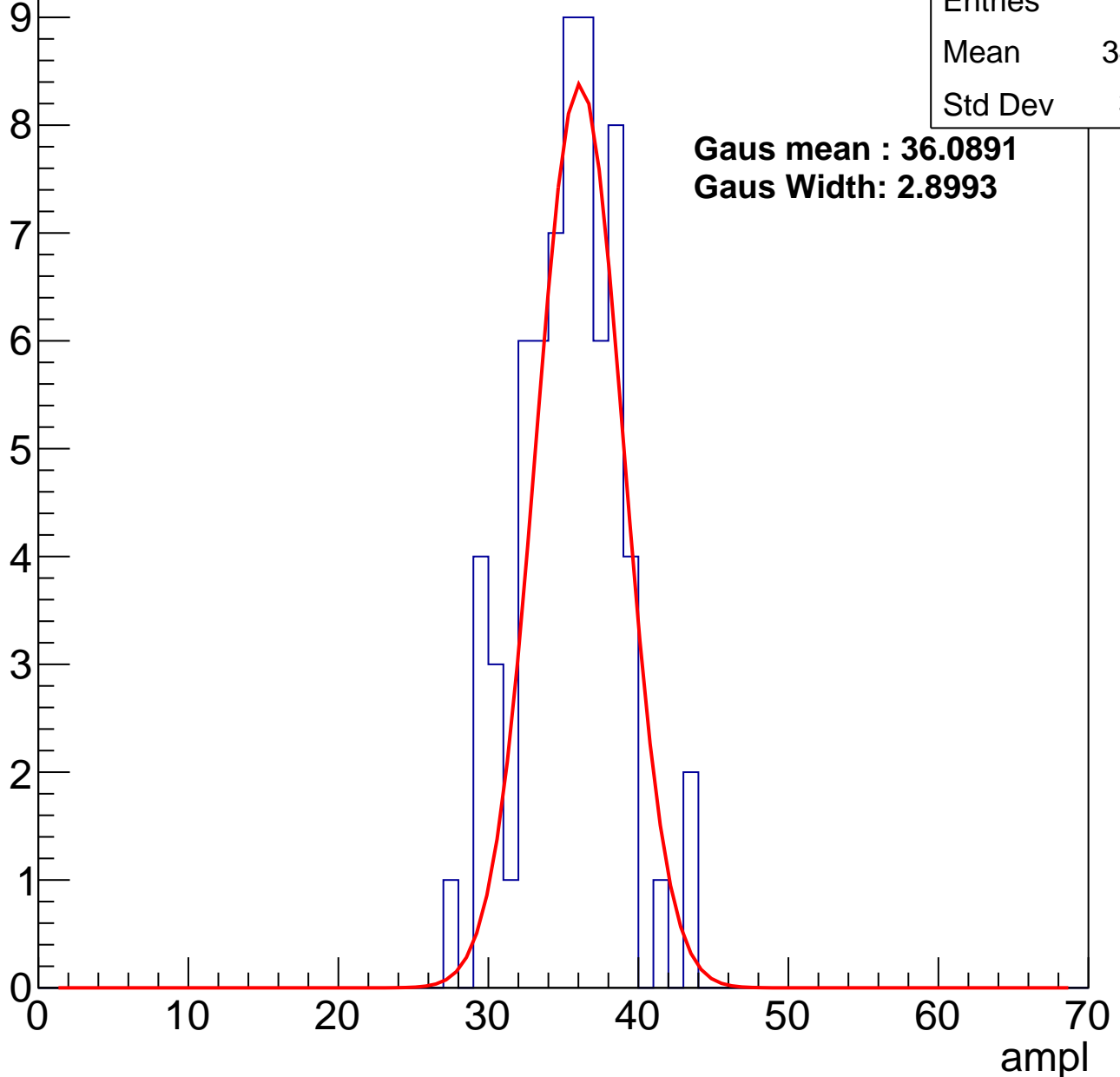
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.93
Std Dev	3.27

**Gaus mean : 36.0891**

**Gaus Width: 2.8993**



# B1L002S, U8-ch3, adc2

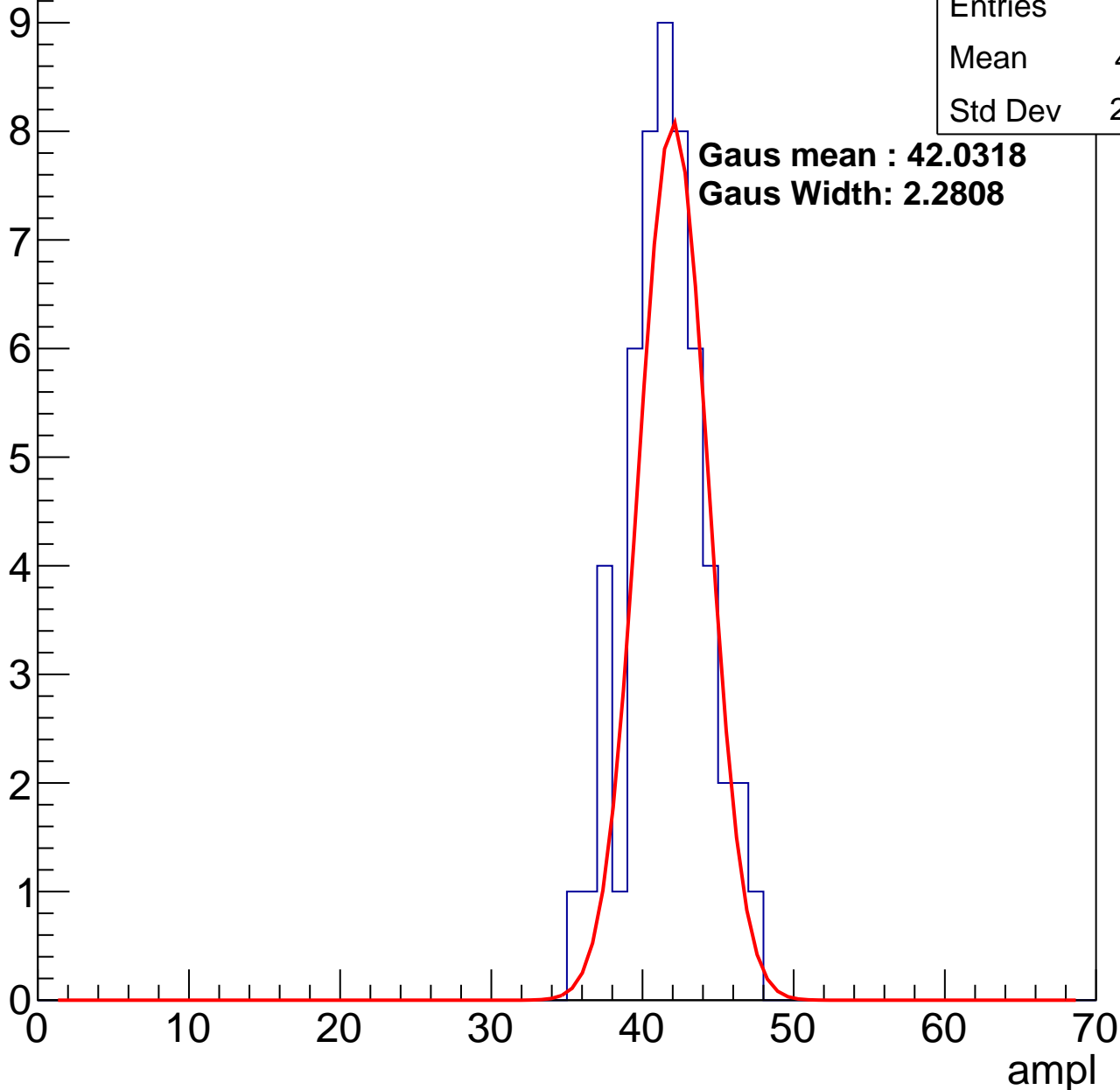
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	41.11
Std Dev	2.575

**Gaus mean : 42.0318**

**Gaus Width: 2.2808**

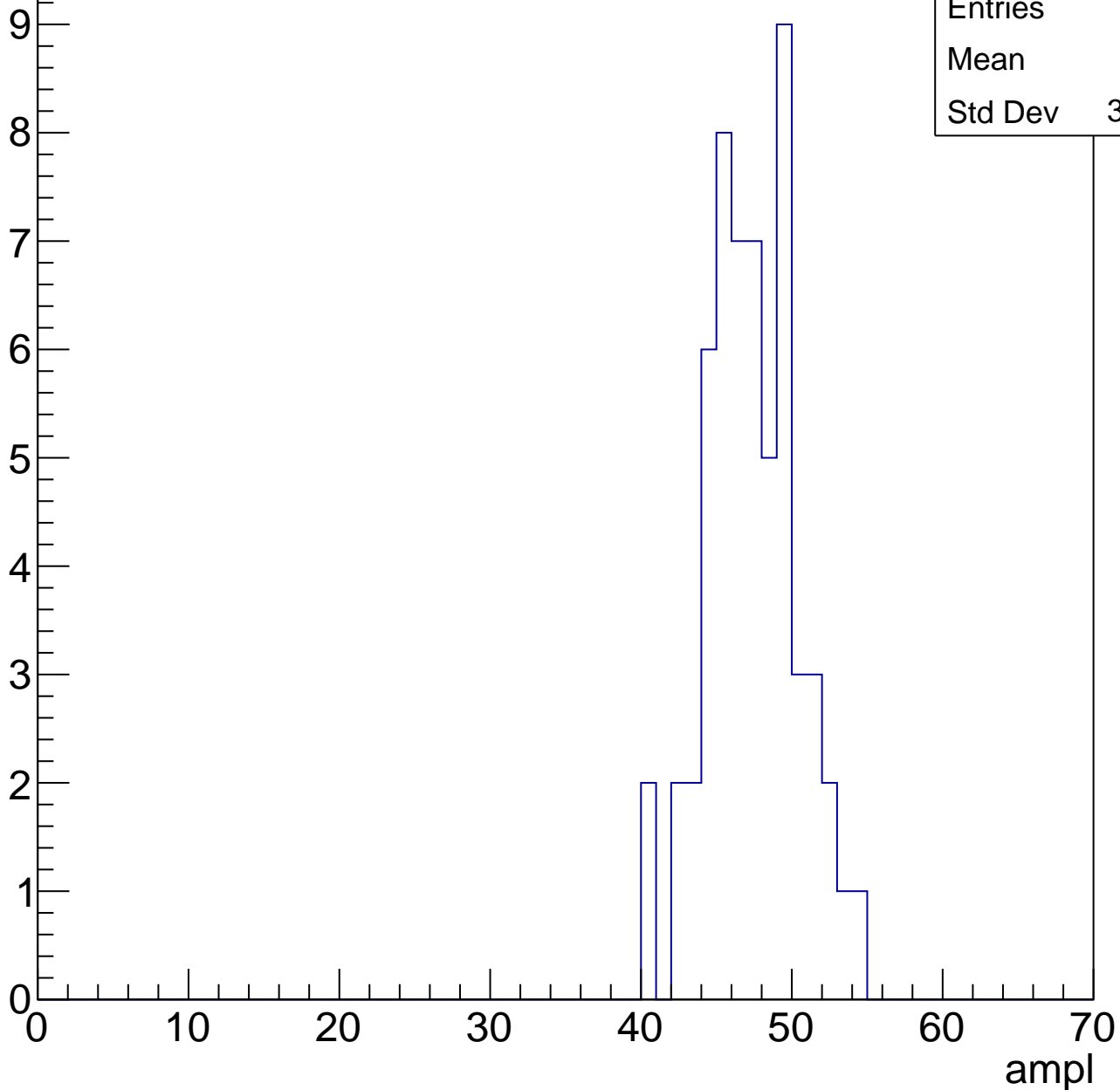


# B1L002S, U8-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

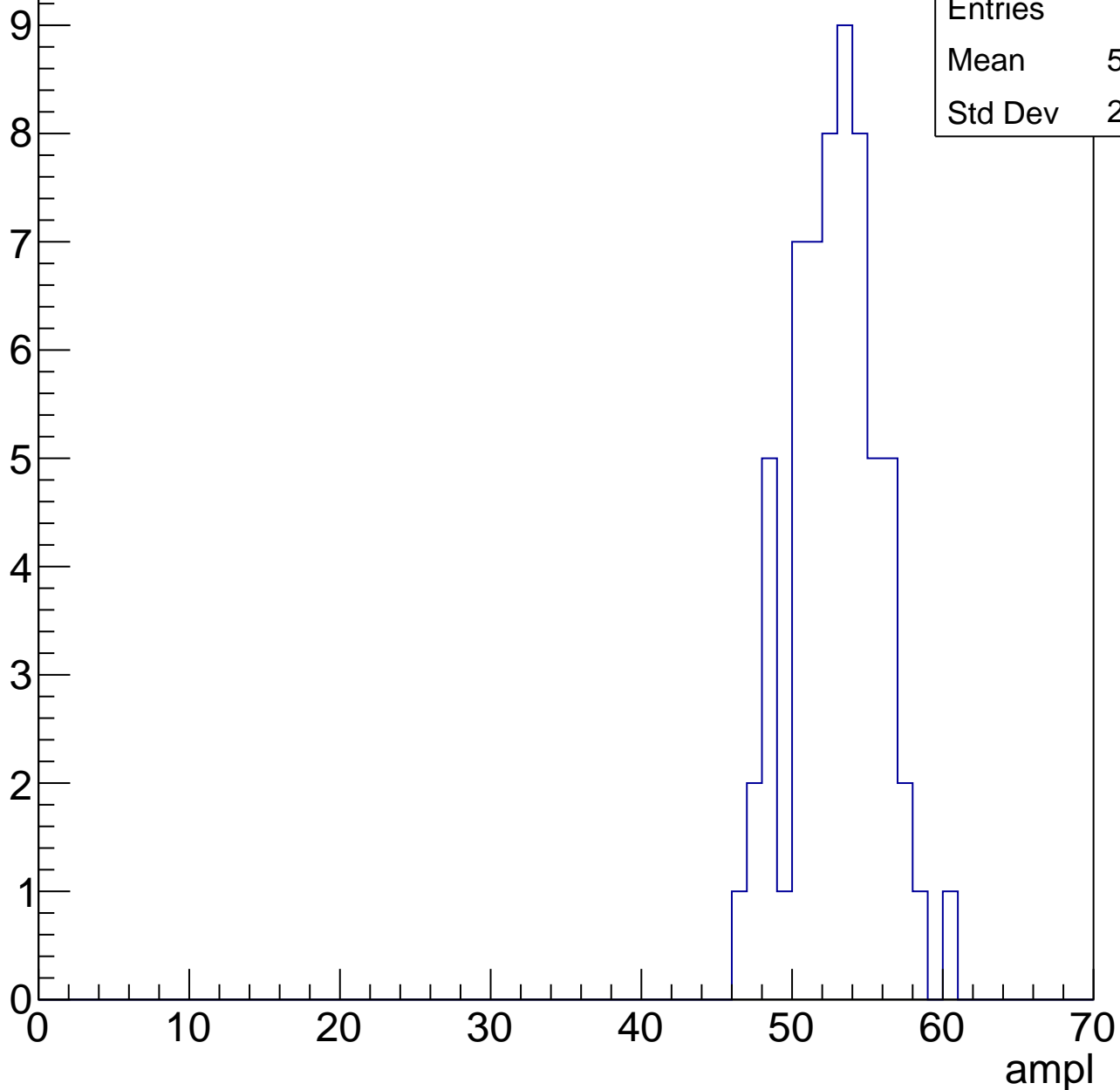
Entries	58
Mean	46.9
Std Dev	3.004



# B1L002S, U8-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

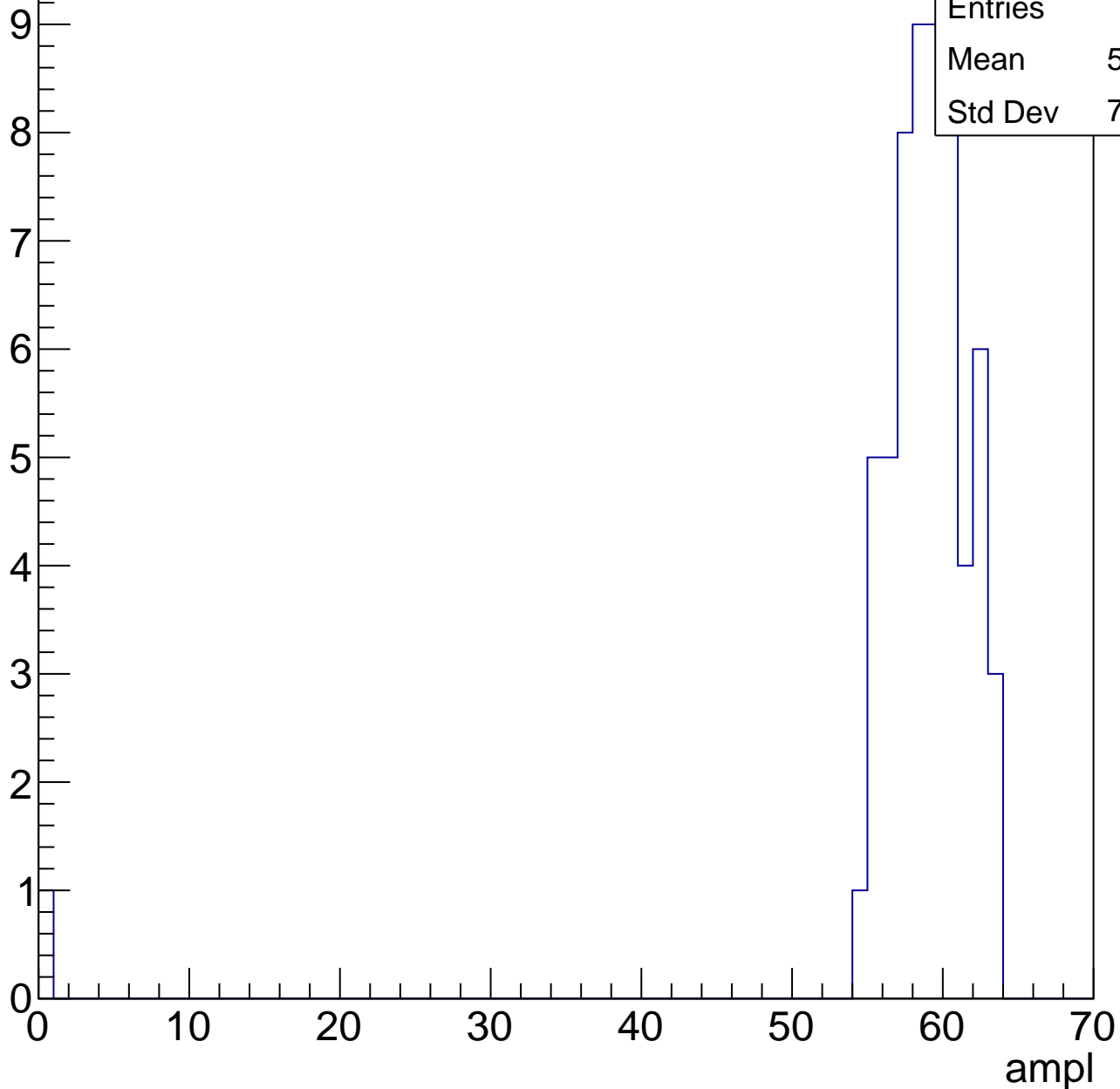


Entries	62
Mean	52.39
Std Dev	2.887

# B1L002S, U8-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

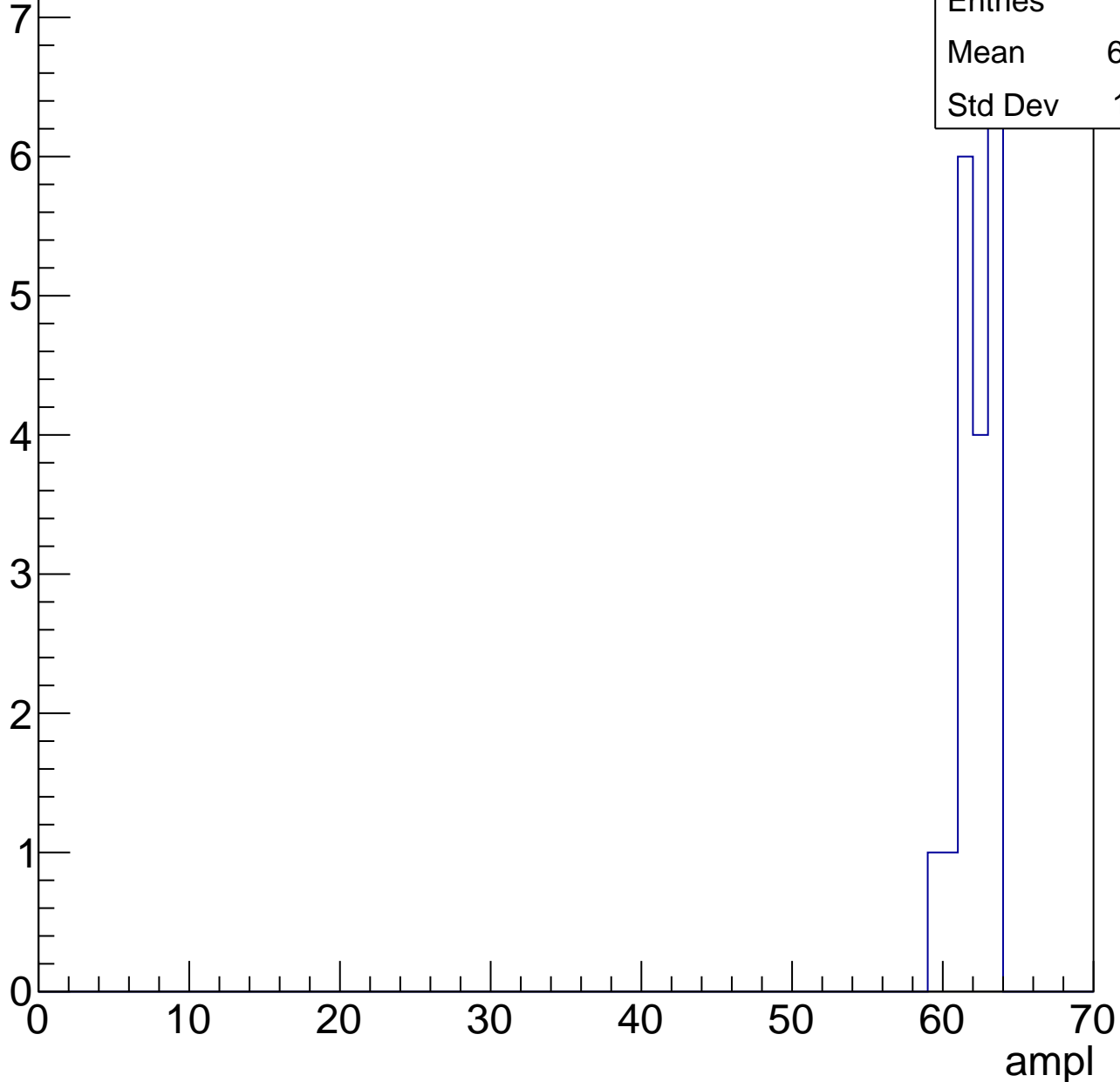


Entries	59
Mean	57.68
Std Dev	7.914

# B1L002S, U8-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L002S, U8-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	26.71
Std Dev	2.525

**Gaus mean : 27.5553**

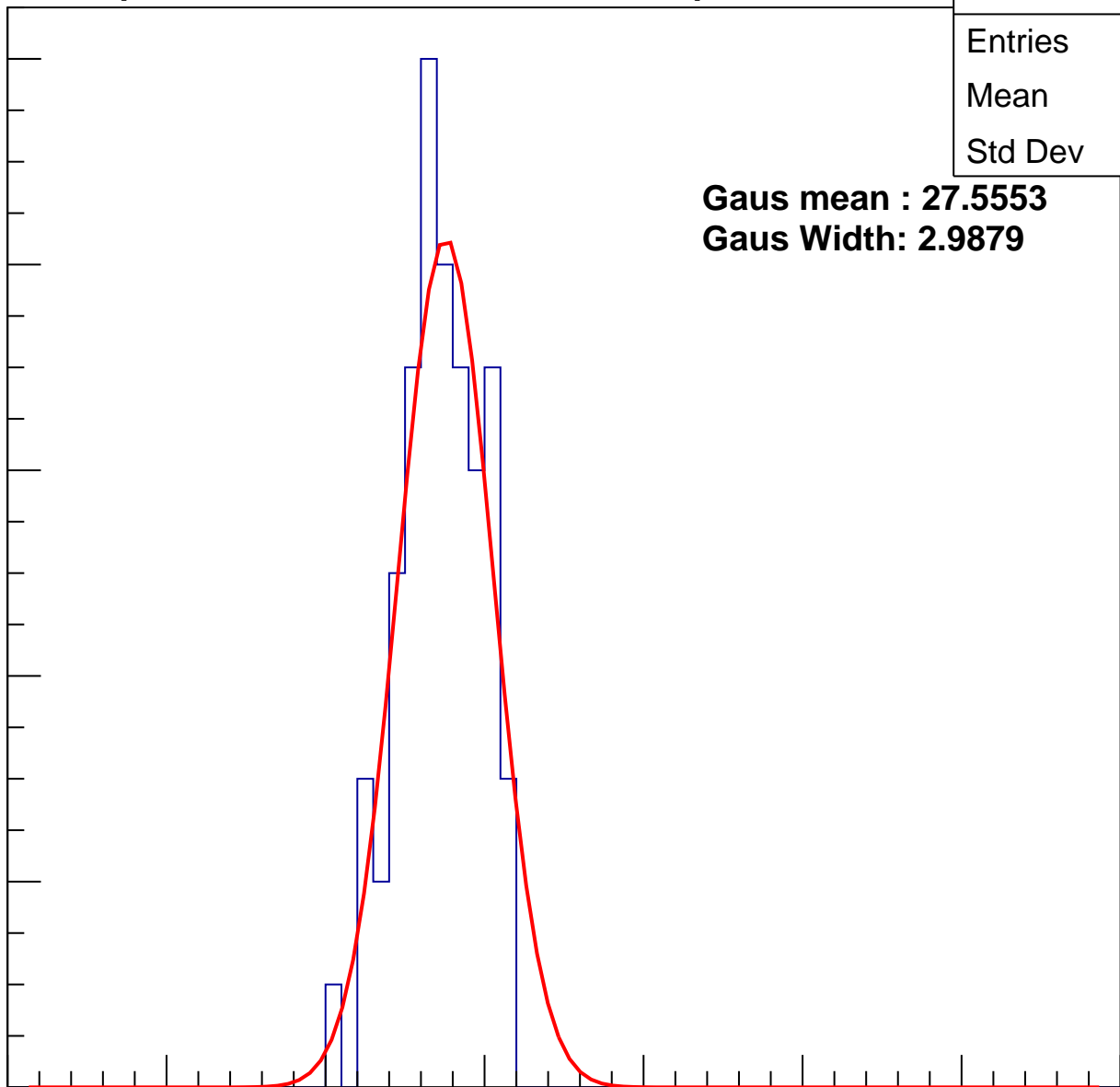
**Gaus Width: 2.9879**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



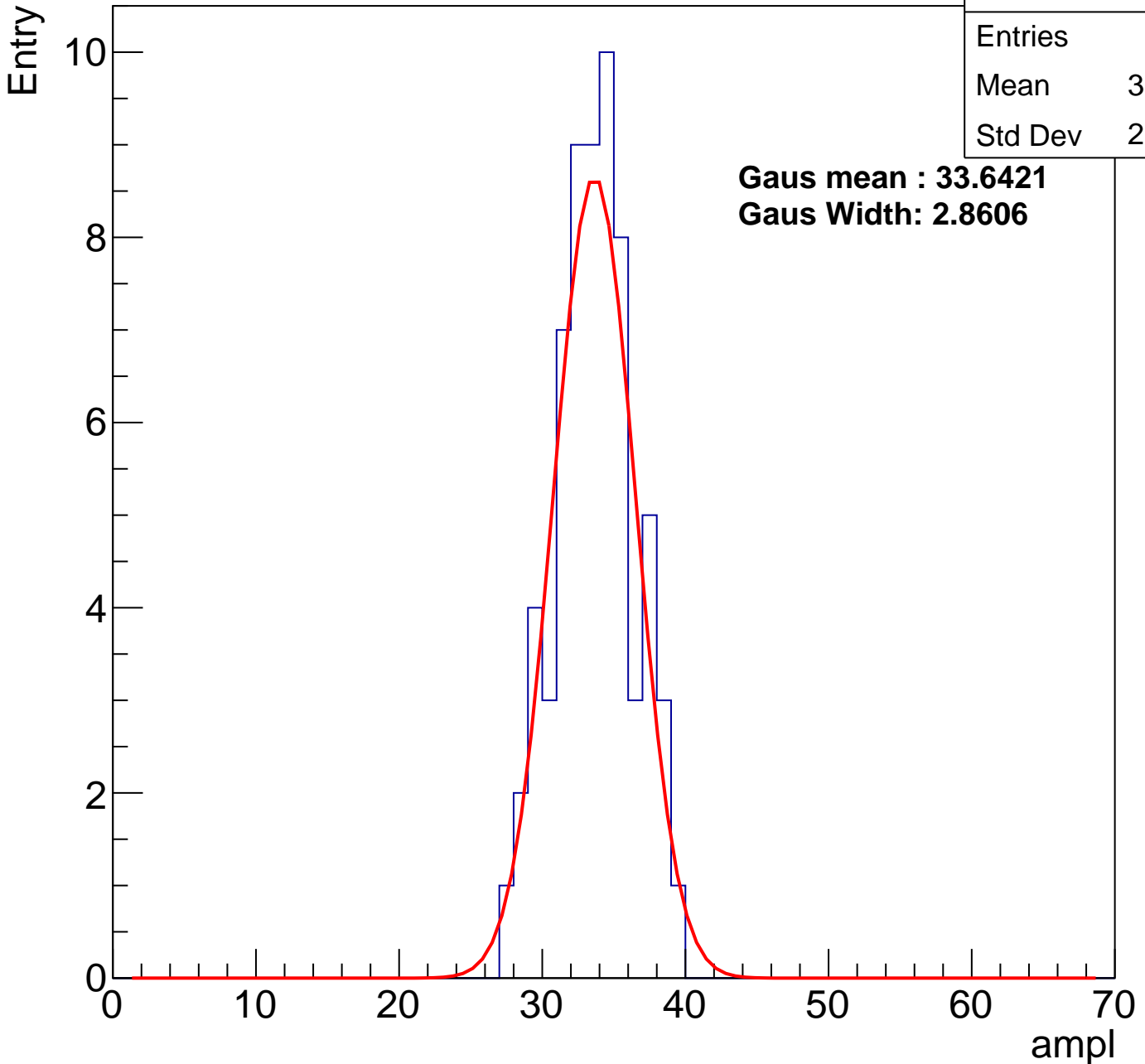
# B1L002S, U8-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	33.18
Std Dev	2.694

**Gaus mean : 33.6421**

**Gaus Width: 2.8606**



# B1L002S, U8-ch4, adc2

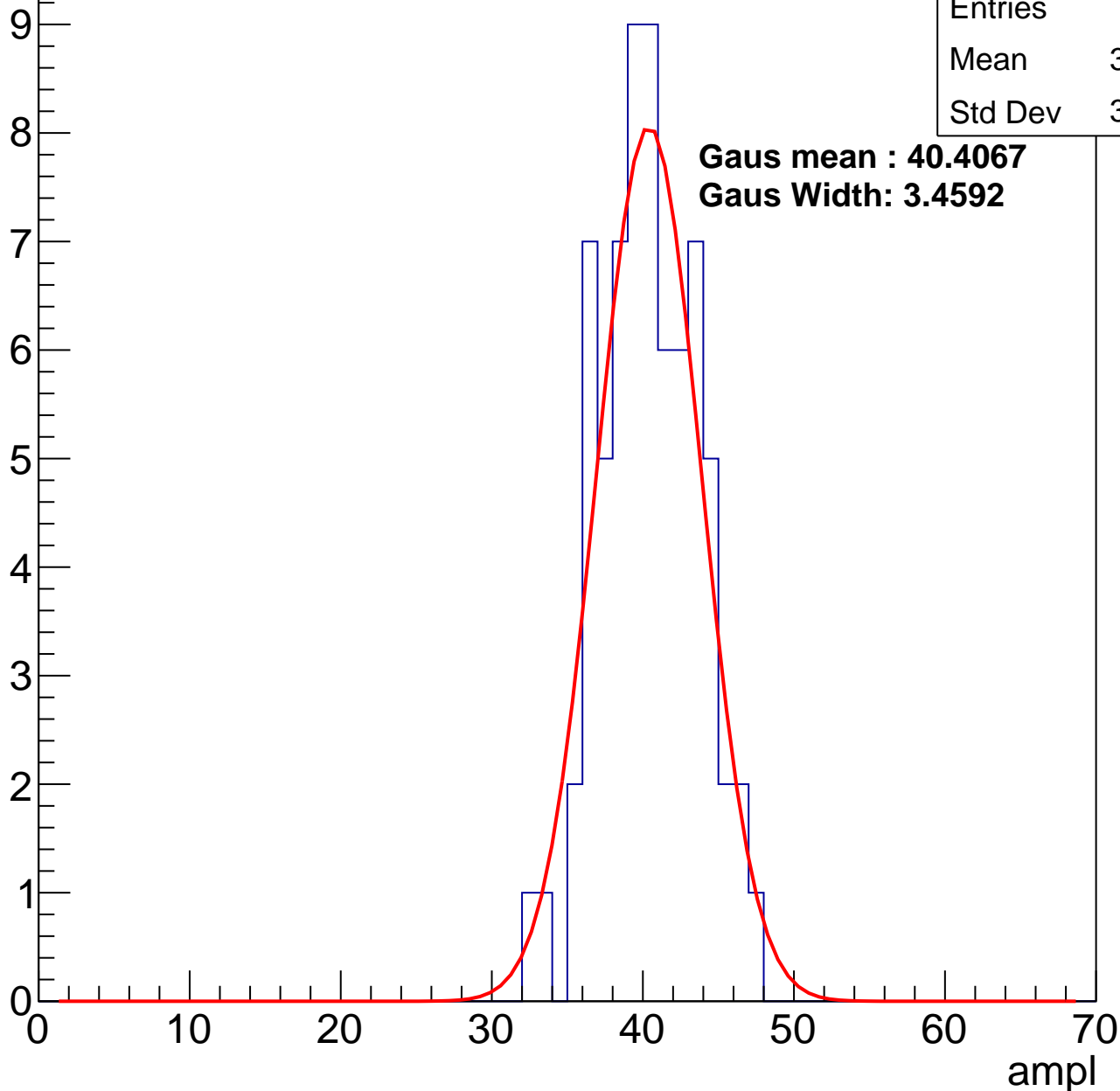
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	39.96
Std Dev	3.164

**Gaus mean : 40.4067**

**Gaus Width: 3.4592**

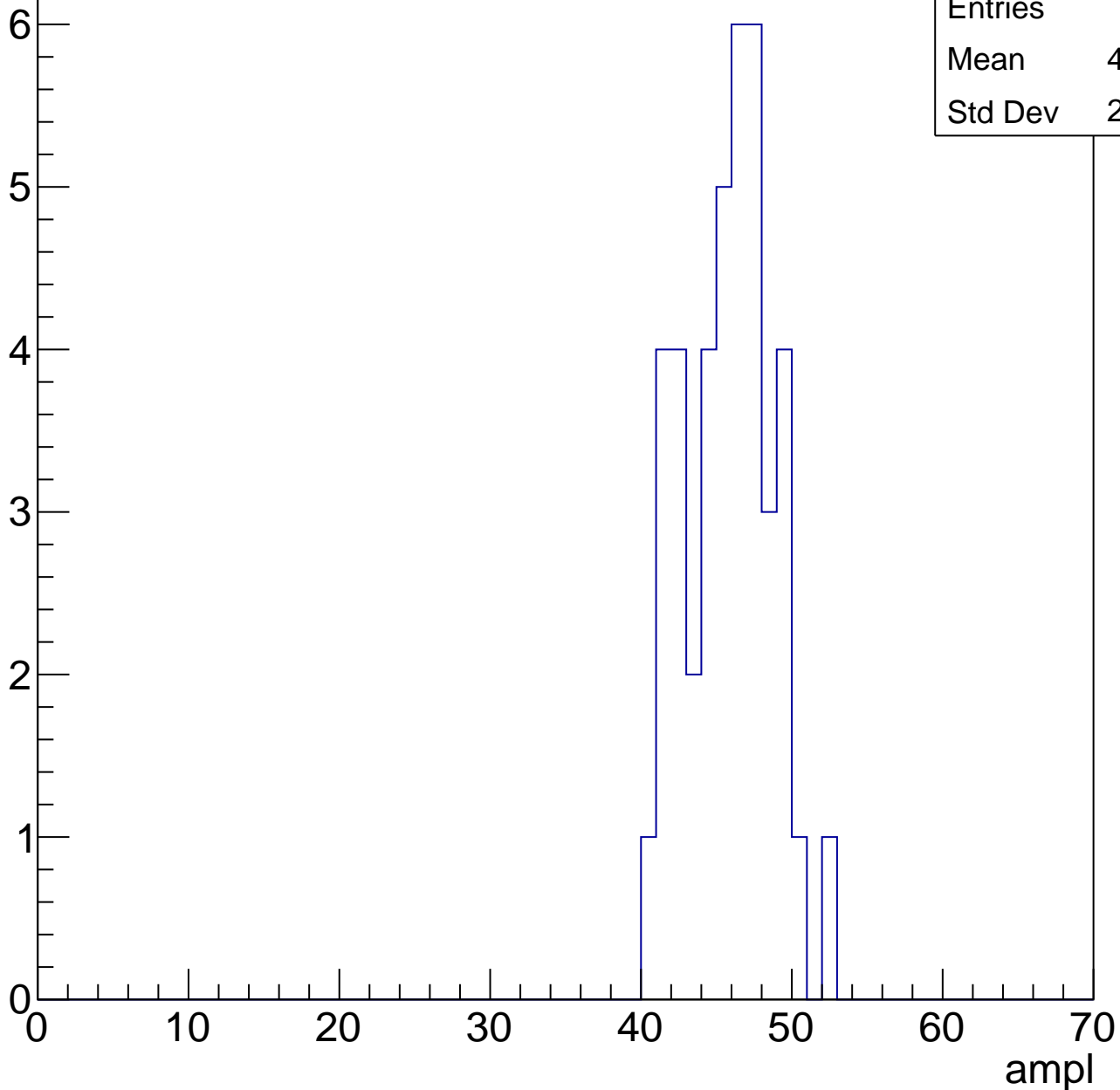


# B1L002S, U8-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

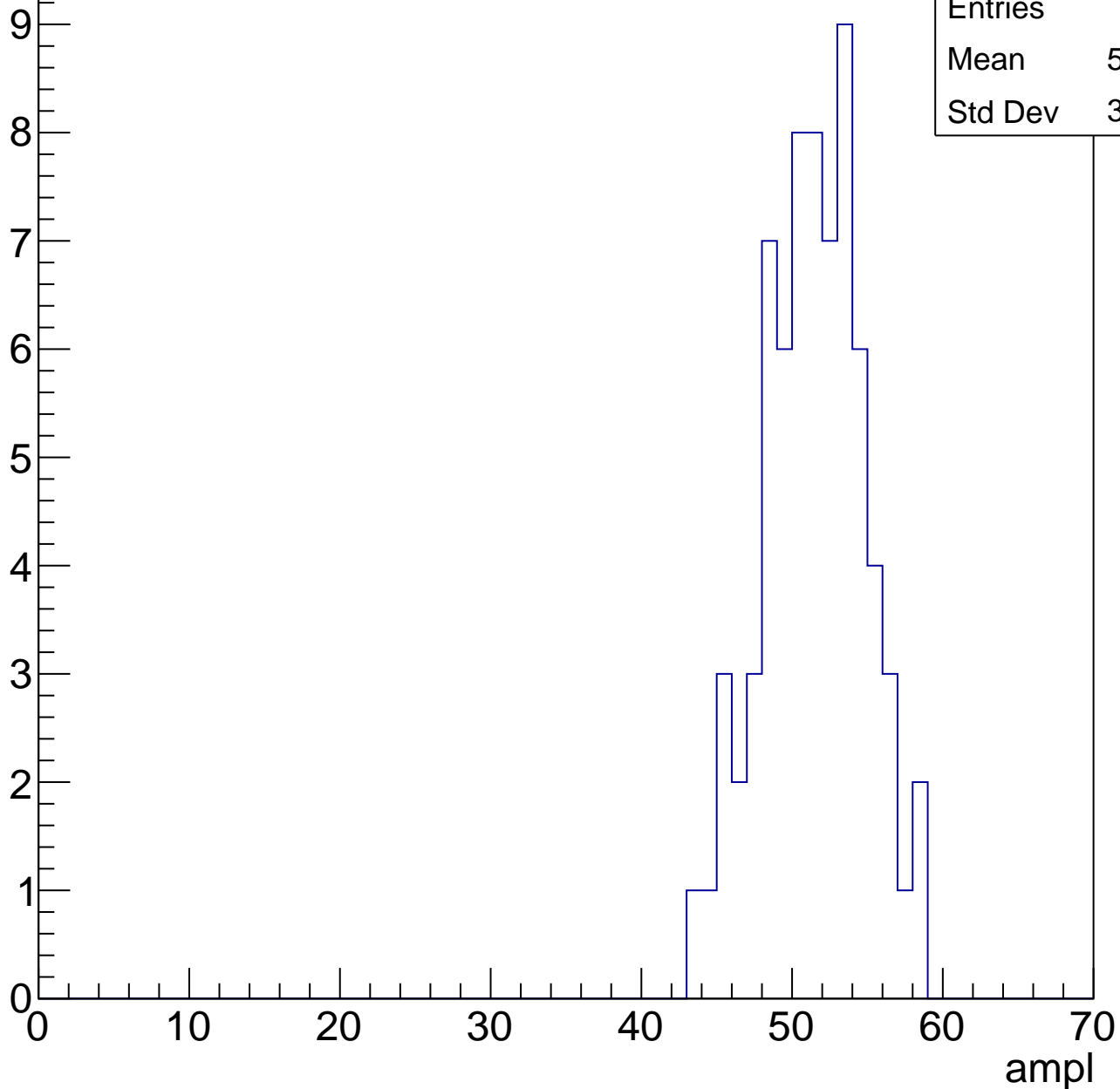
Entries	41
Mean	45.34
Std Dev	2.825



# B1L002S, U8-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

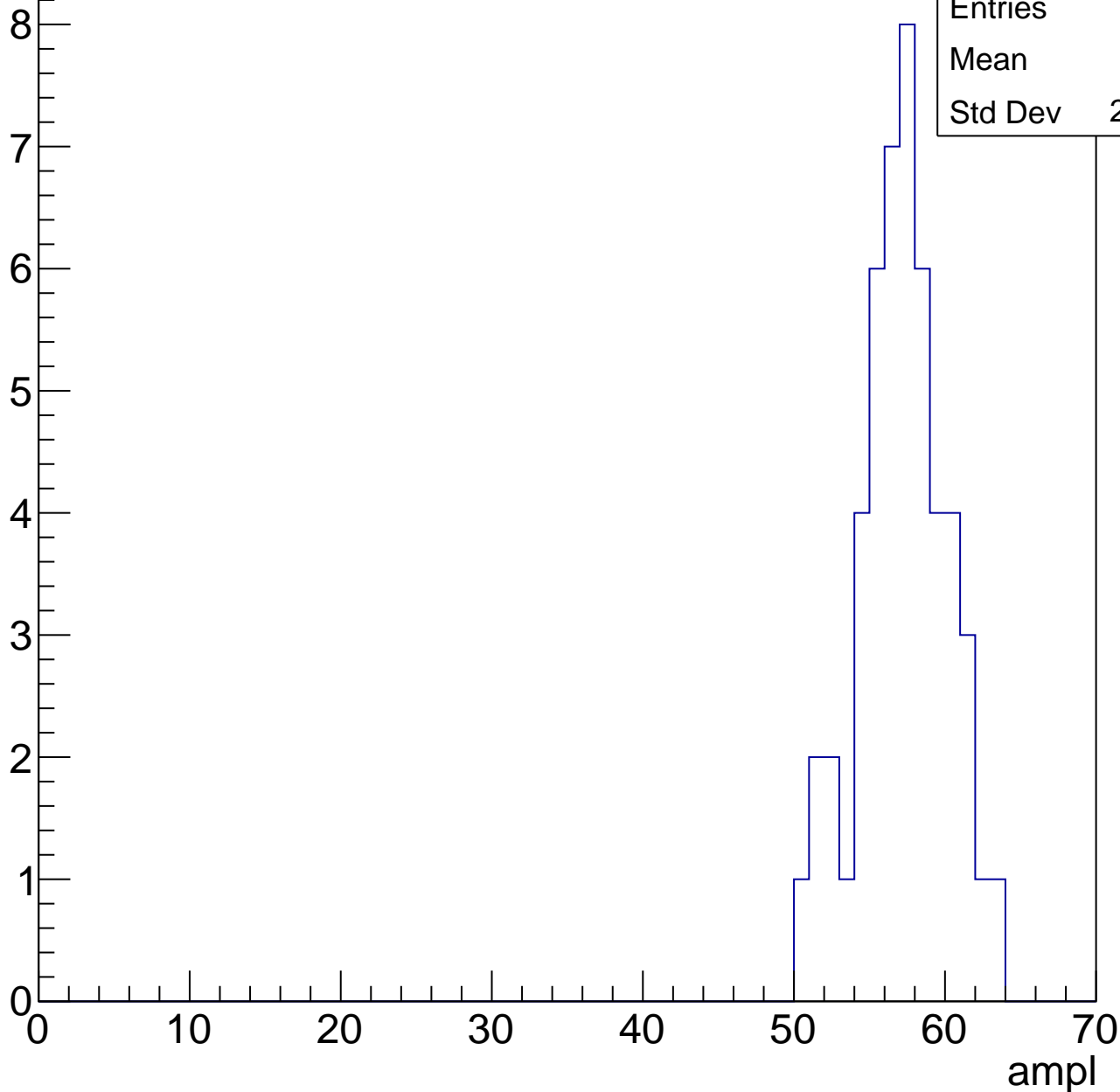
Entry



# B1L002S, U8-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

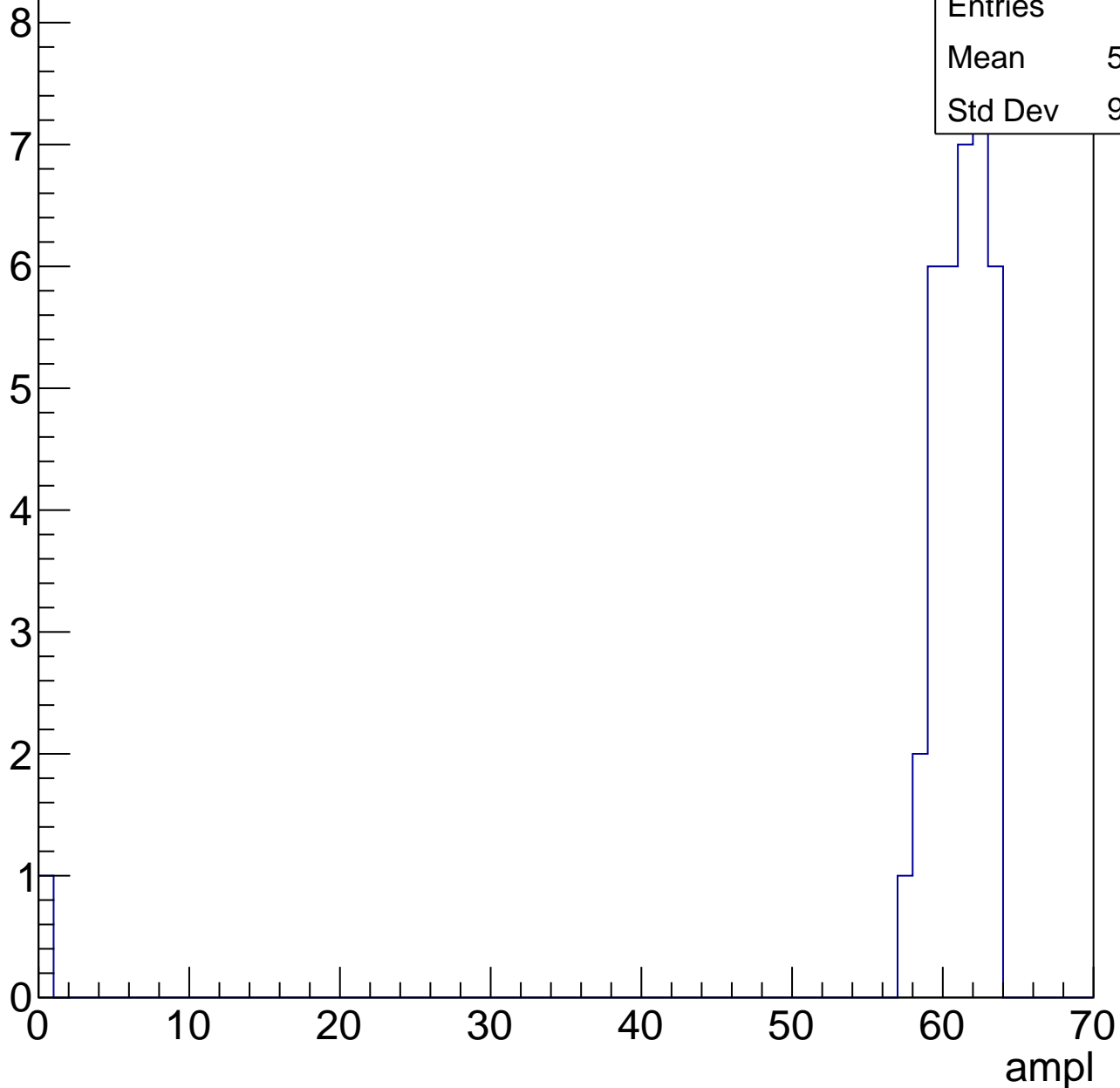
Entry



# B1L002S, U8-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

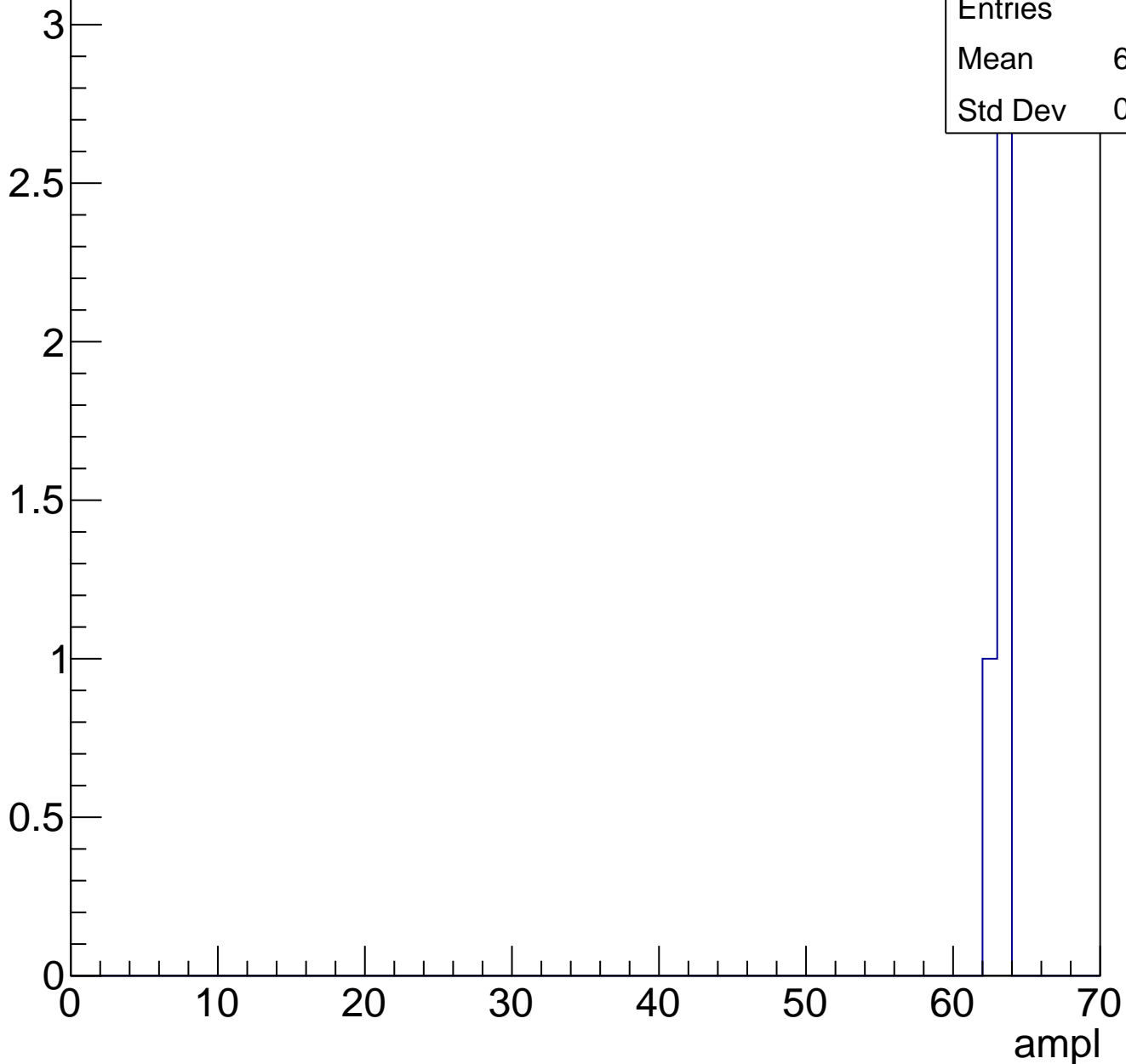




# B1L002S, U8-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch5, adc0

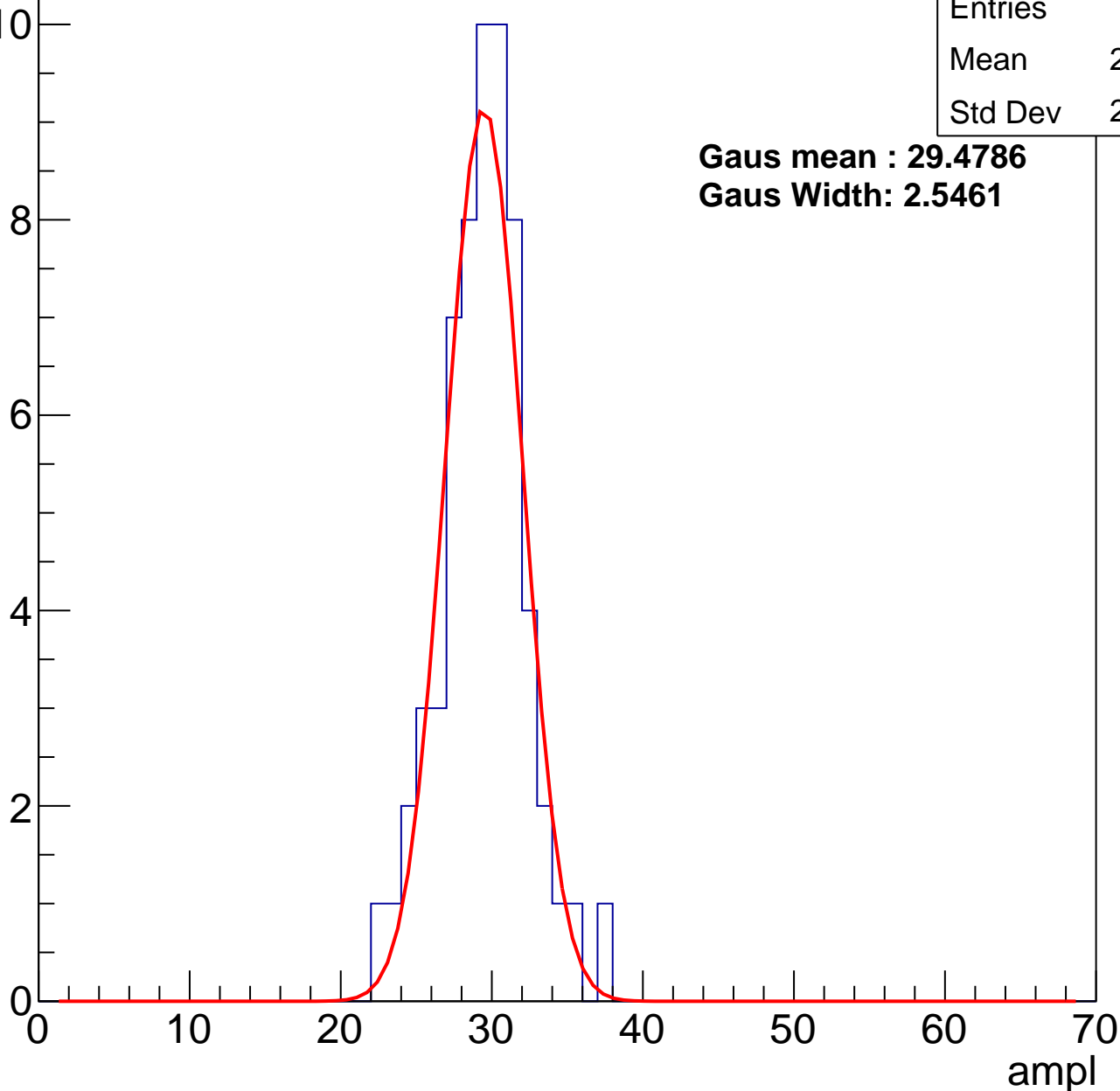
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.98
Std Dev	2.785

**Gaus mean : 29.4786**

**Gaus Width: 2.5461**



# B1L002S, U8-ch5, adc1

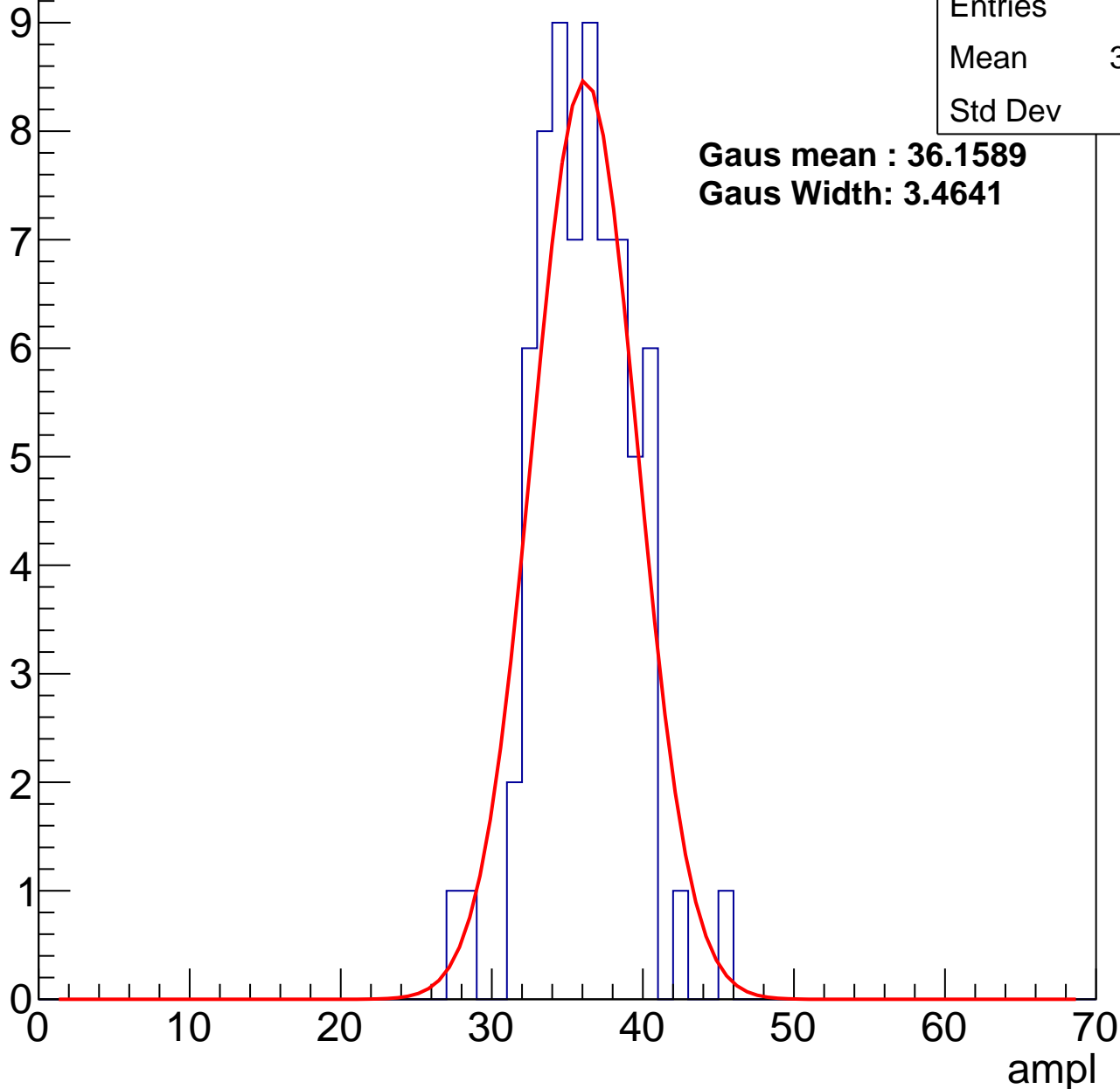
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	35.64
Std Dev	3.14

**Gaus mean : 36.1589**

**Gaus Width: 3.4641**



# B1L002S, U8-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	41.98
Std Dev	3.008

**Gaus mean : 42.9245**

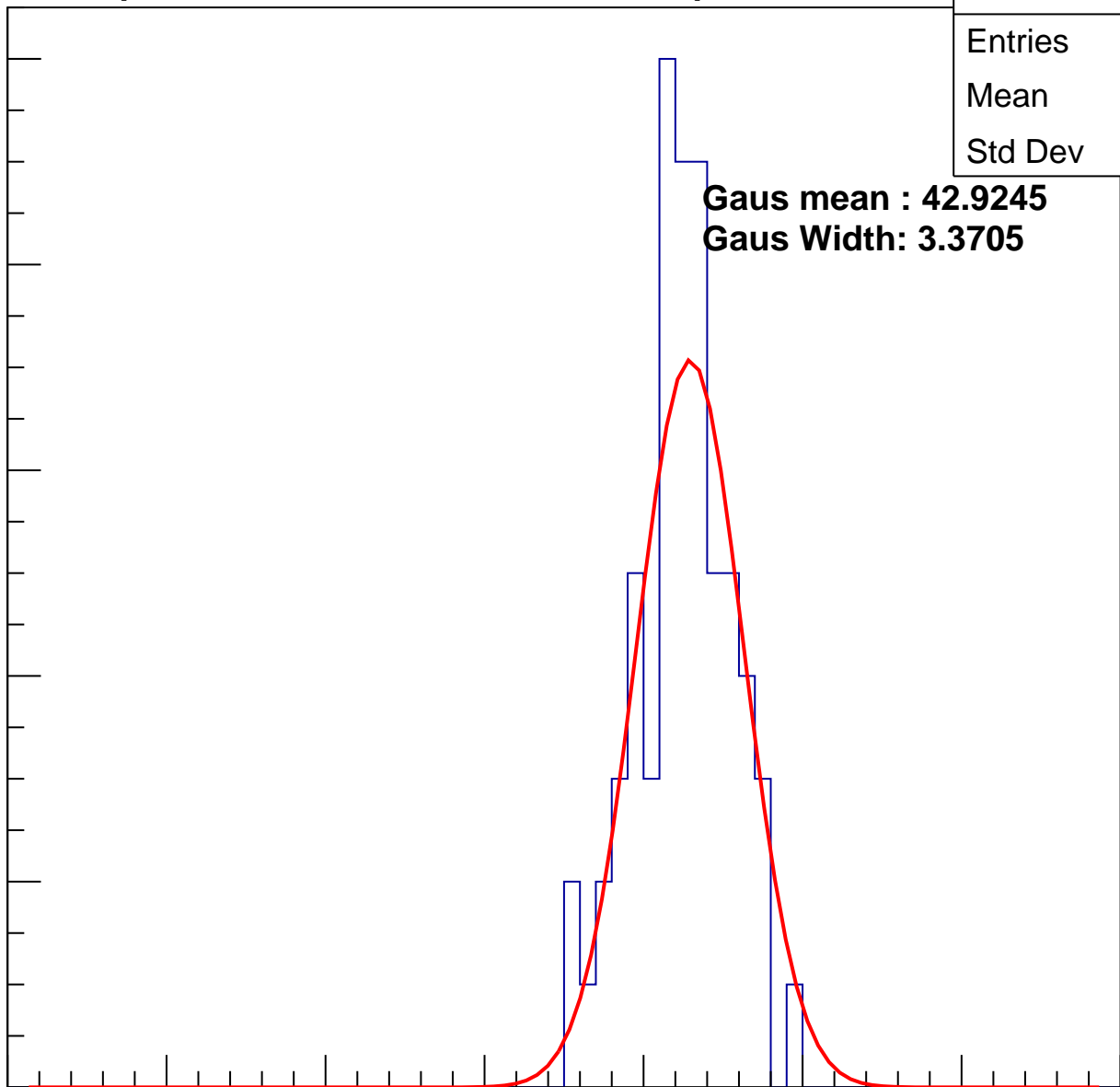
**Gaus Width: 3.3705**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

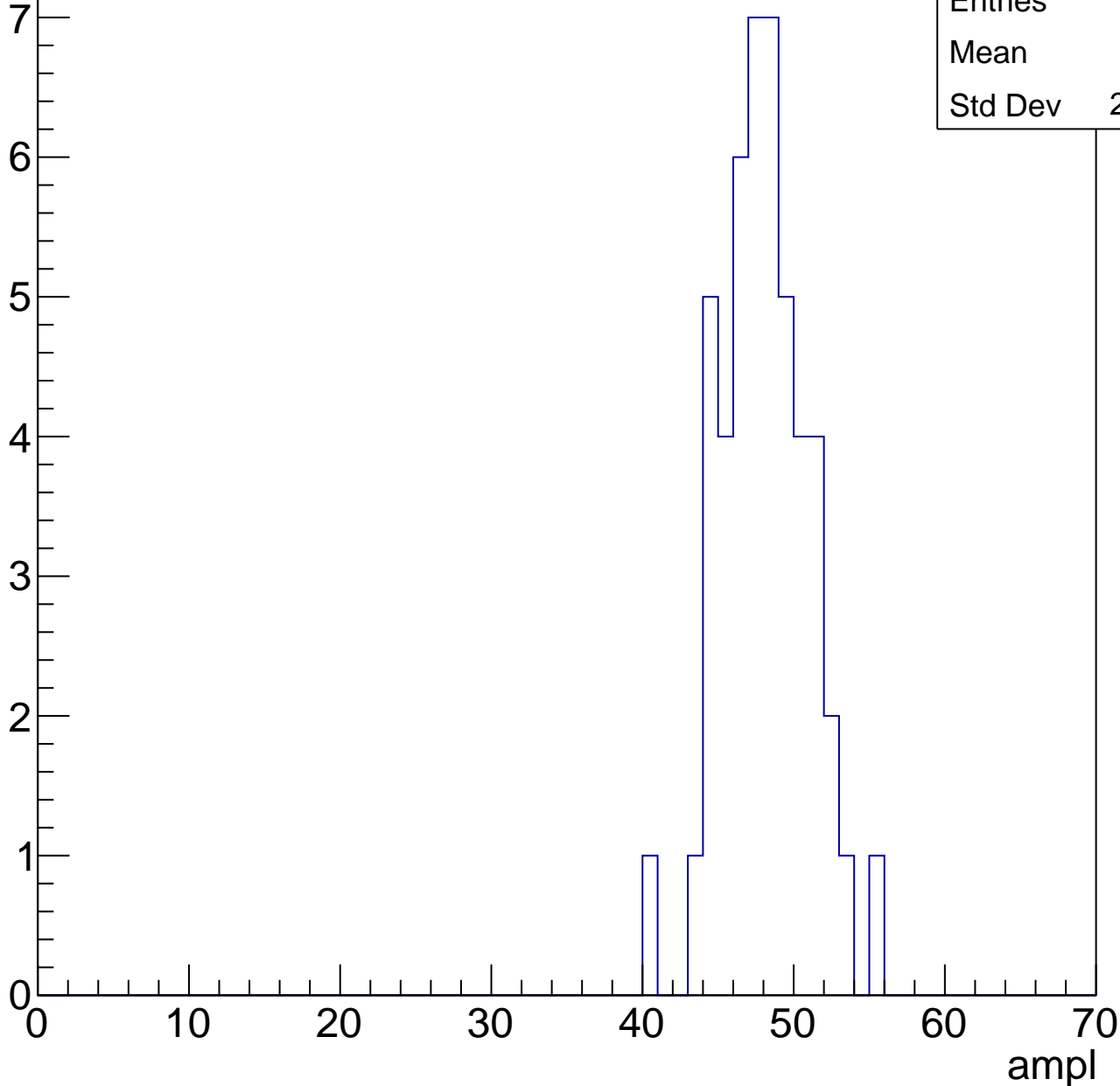


# B1L002S, U8-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	47.6
Std Dev	2.856

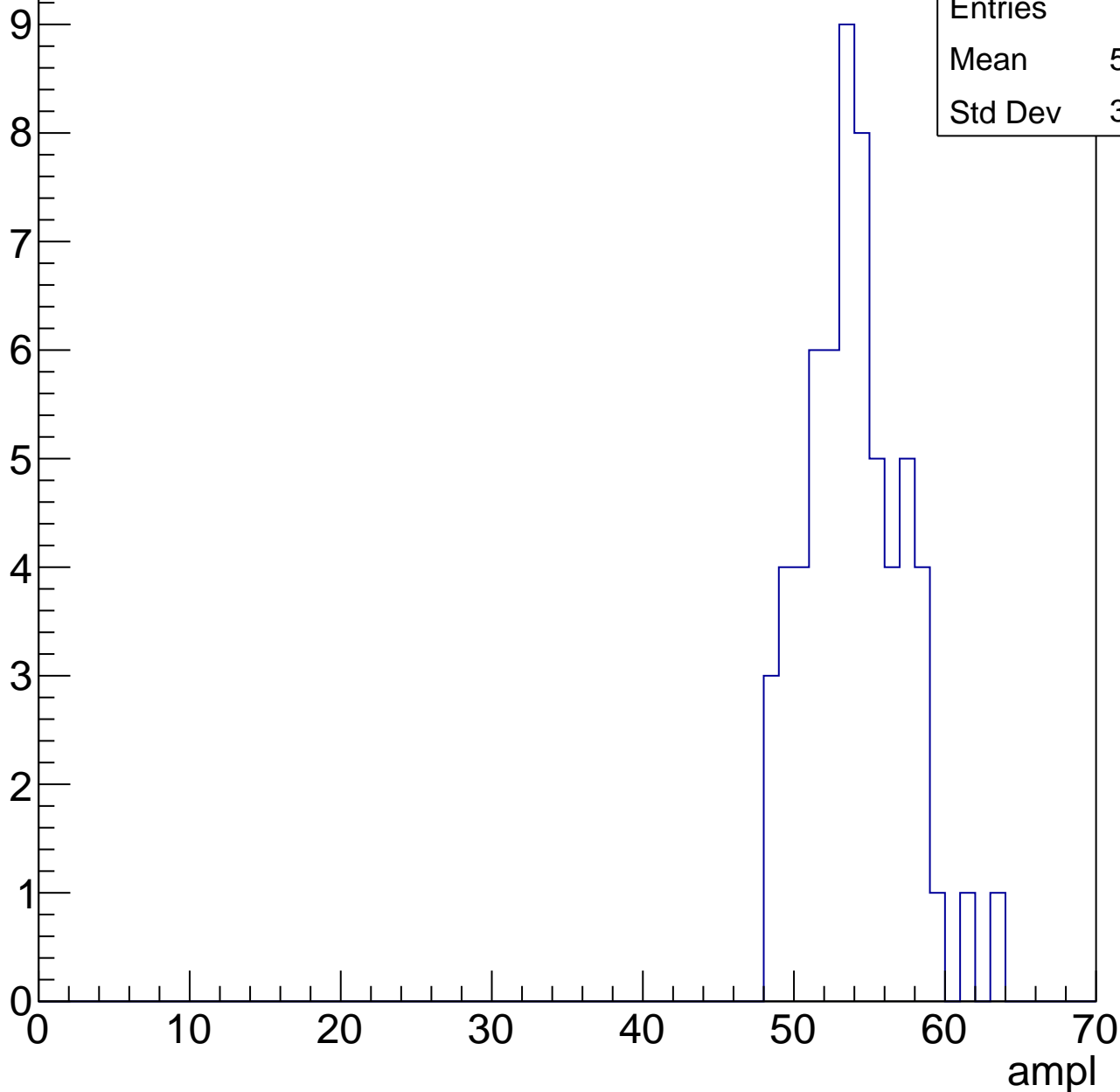


# B1L002S, U8-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

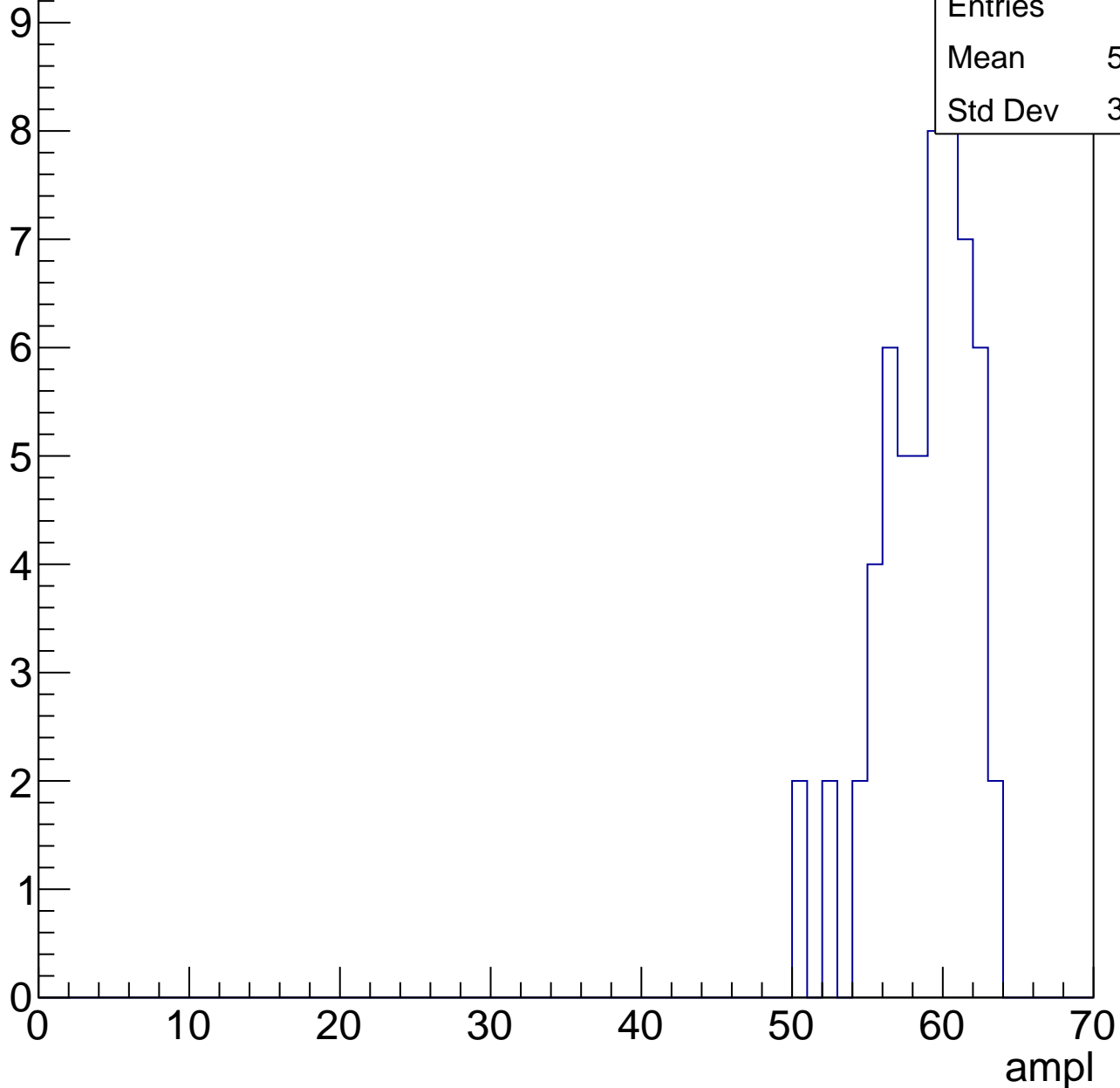
Entries	61
Mean	53.54
Std Dev	3.217



# B1L002S, U8-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	58
Mean	58.28
Std Dev	3.067

# B1L002S, U8-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries

17

Mean

58.12

Std Dev

14.6

ampl

0

10

20

30

40

50

60

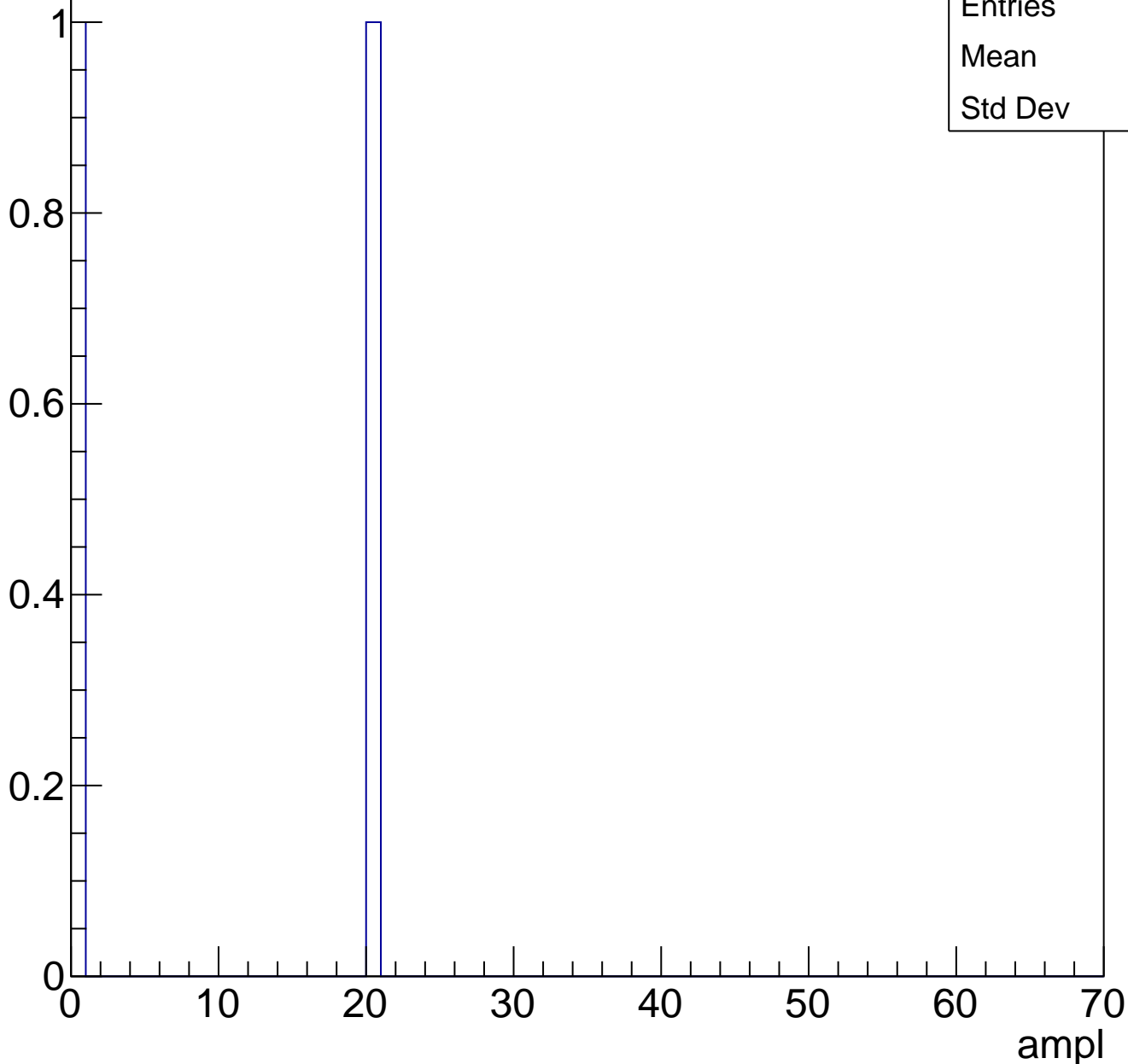
70



# B1L002S, U8-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L002S, U8-ch6, adc0

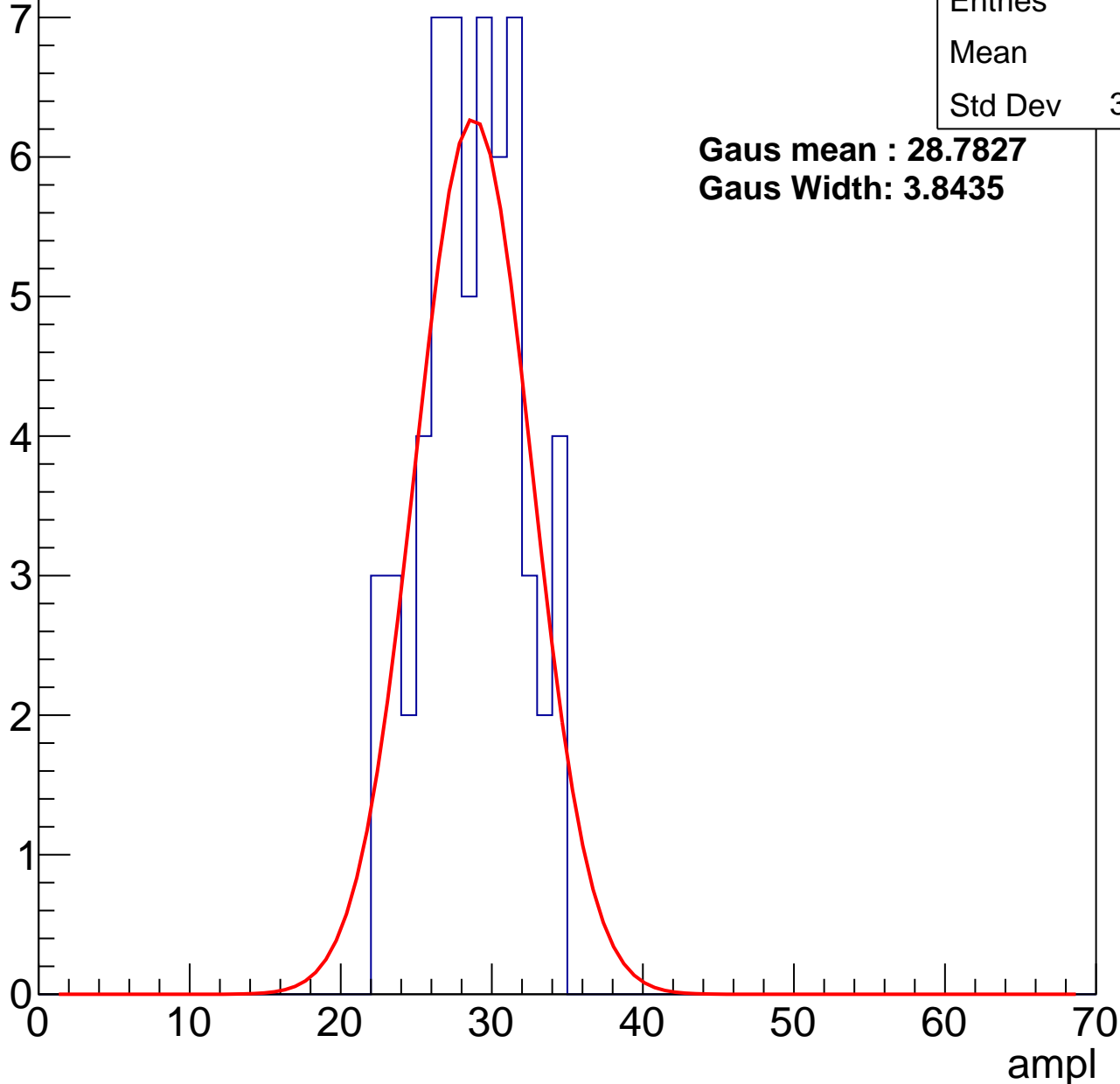
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.2
Std Dev	3.214

**Gaus mean : 28.7827**

**Gaus Width: 3.8435**



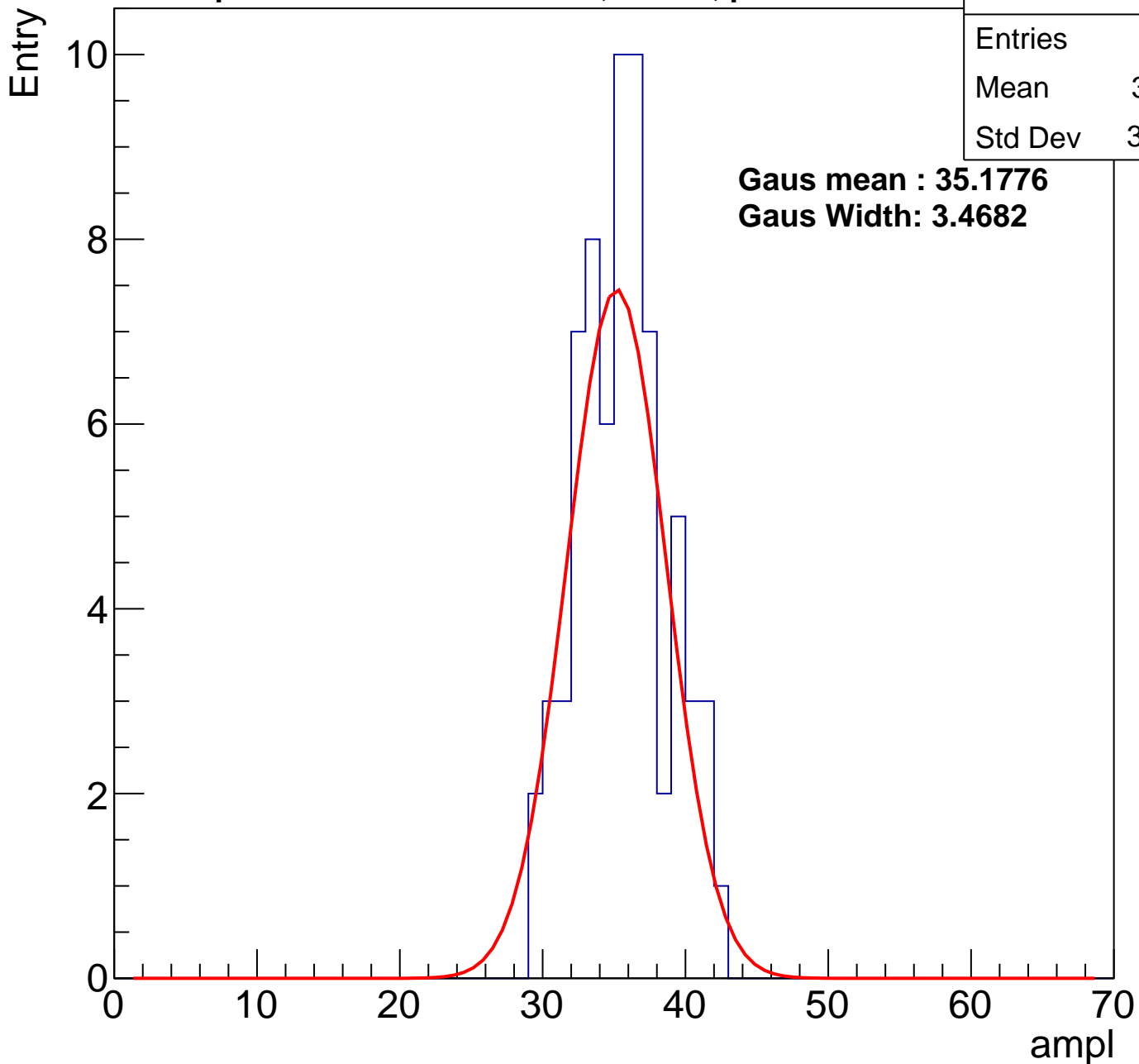
# B1L002S, U8-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	35.11
Std Dev	3.078

**Gaus mean : 35.1776**

**Gaus Width: 3.4682**



# B1L002S, U8-ch6, adc2

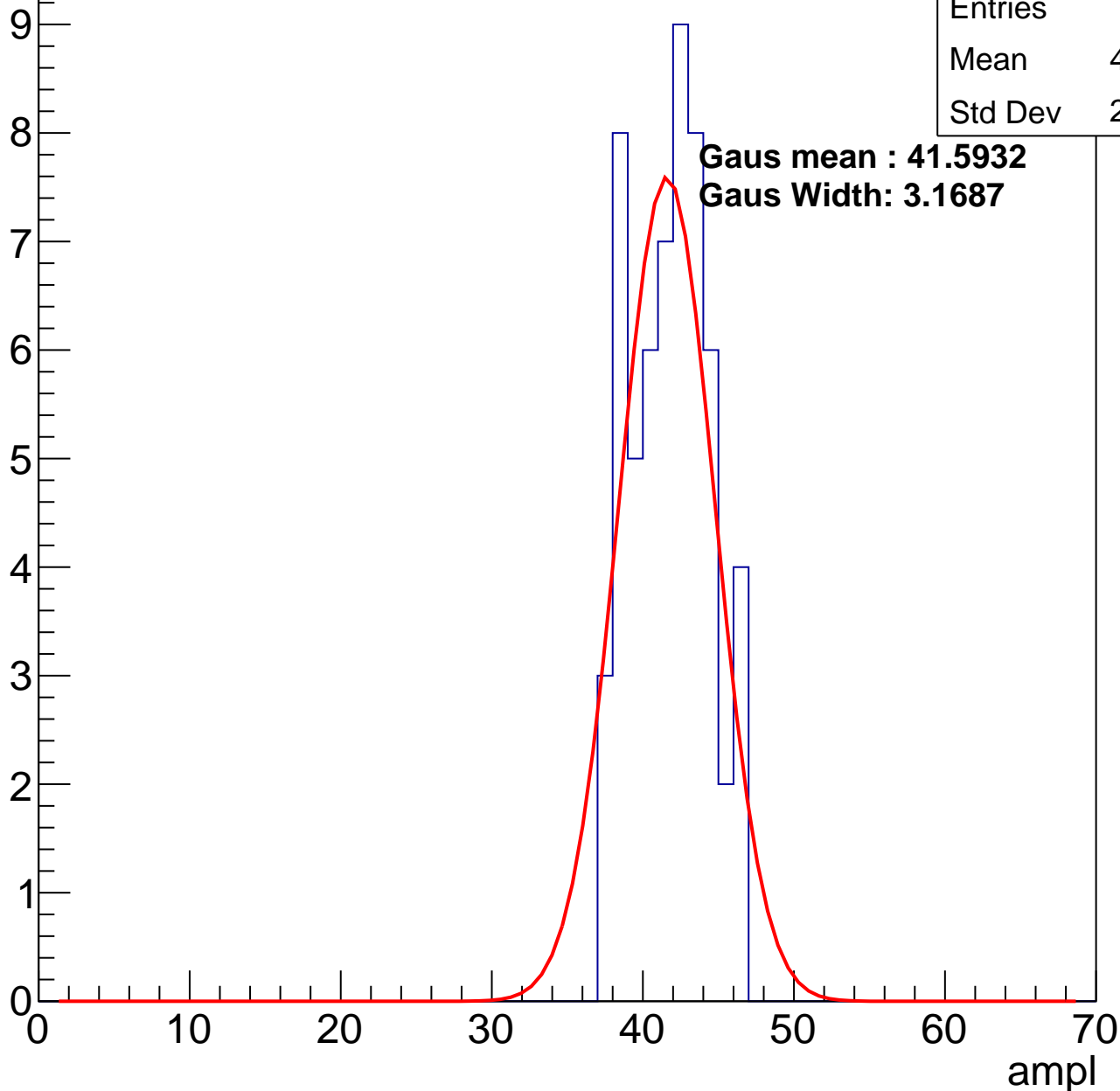
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.33
Std Dev	2.515

**Gaus mean : 41.5932**

**Gaus Width: 3.1687**

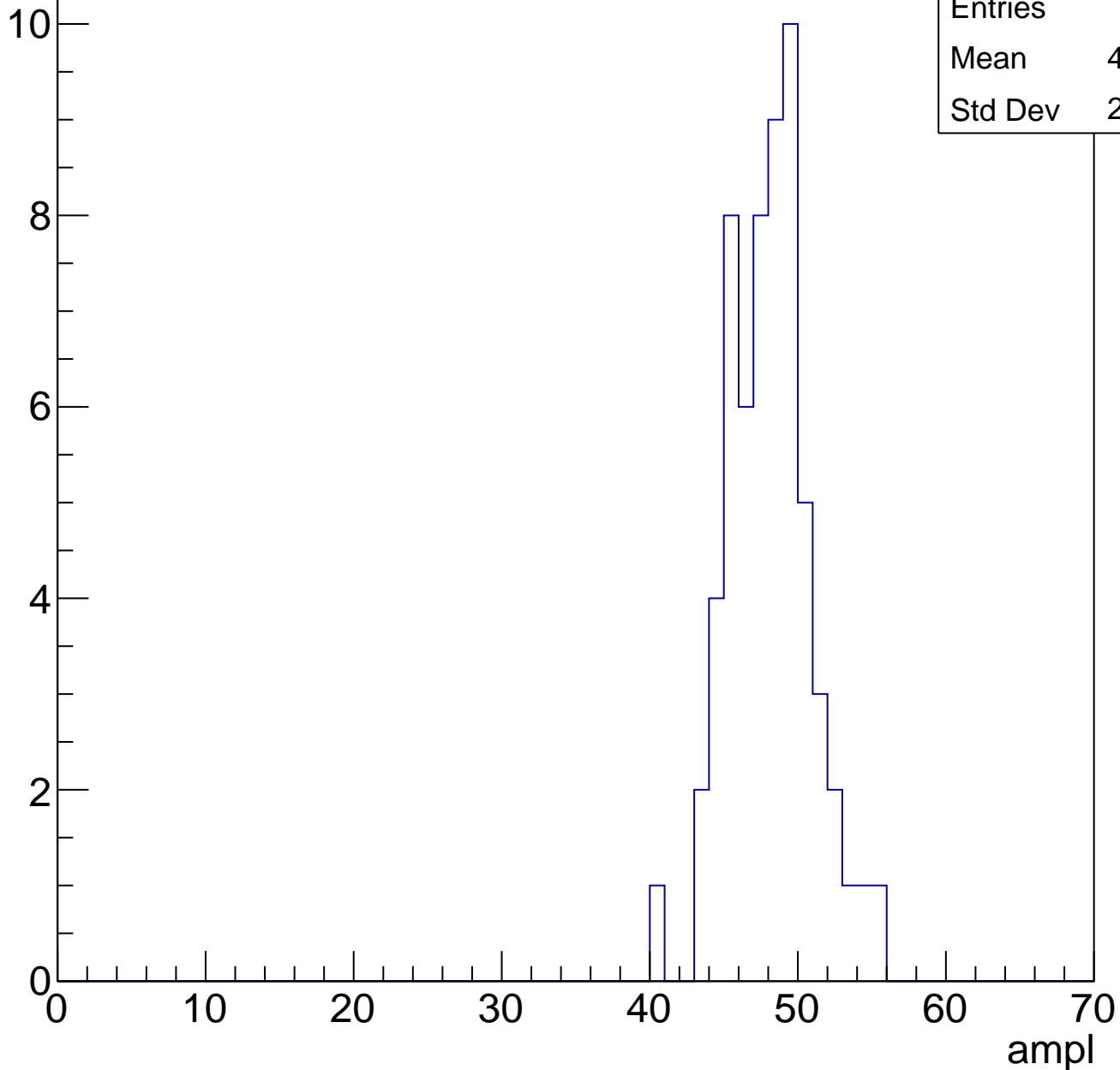


# B1L002S, U8-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	47.62
Std Dev	2.783

Entry

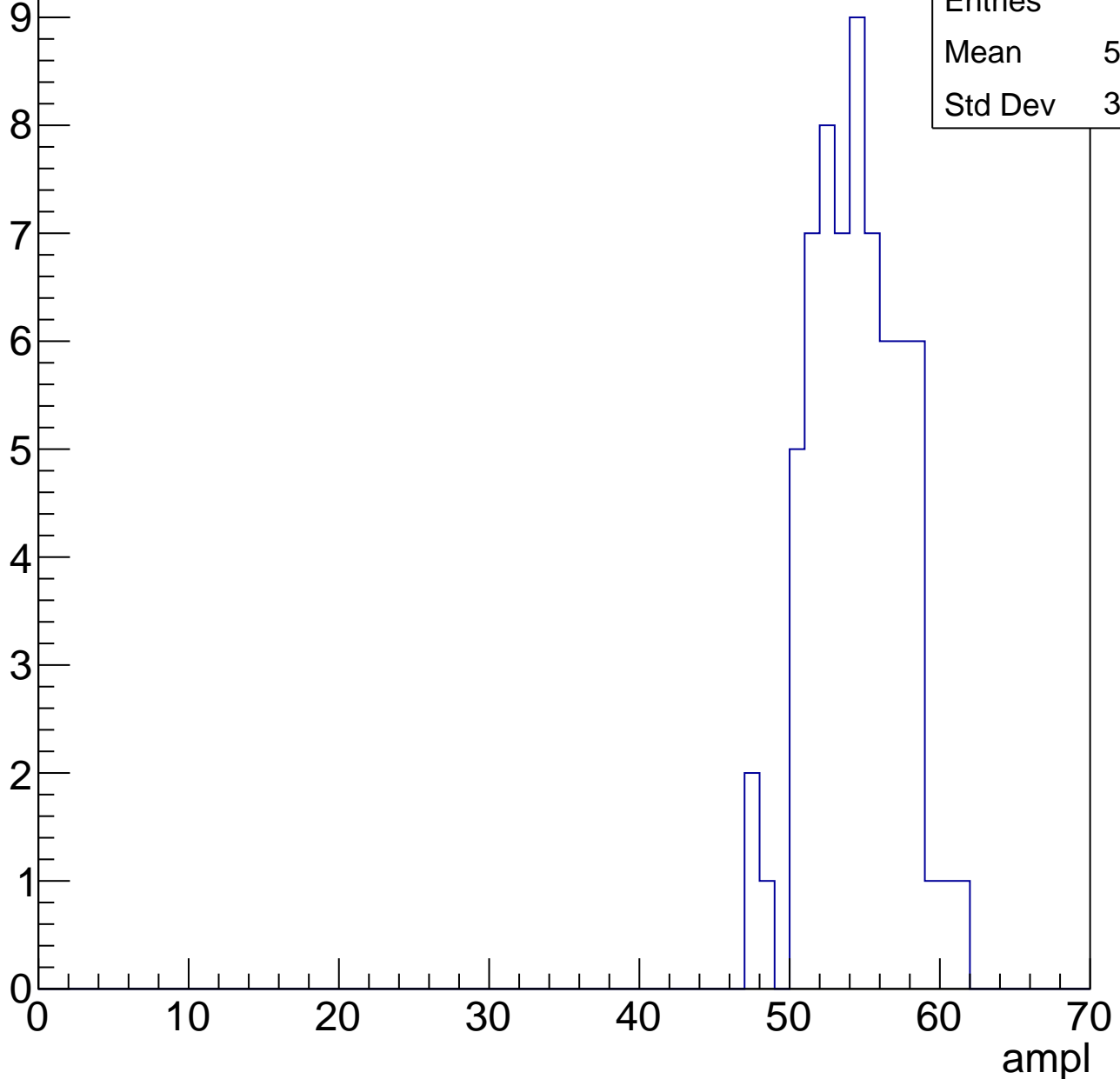


# B1L002S, U8-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

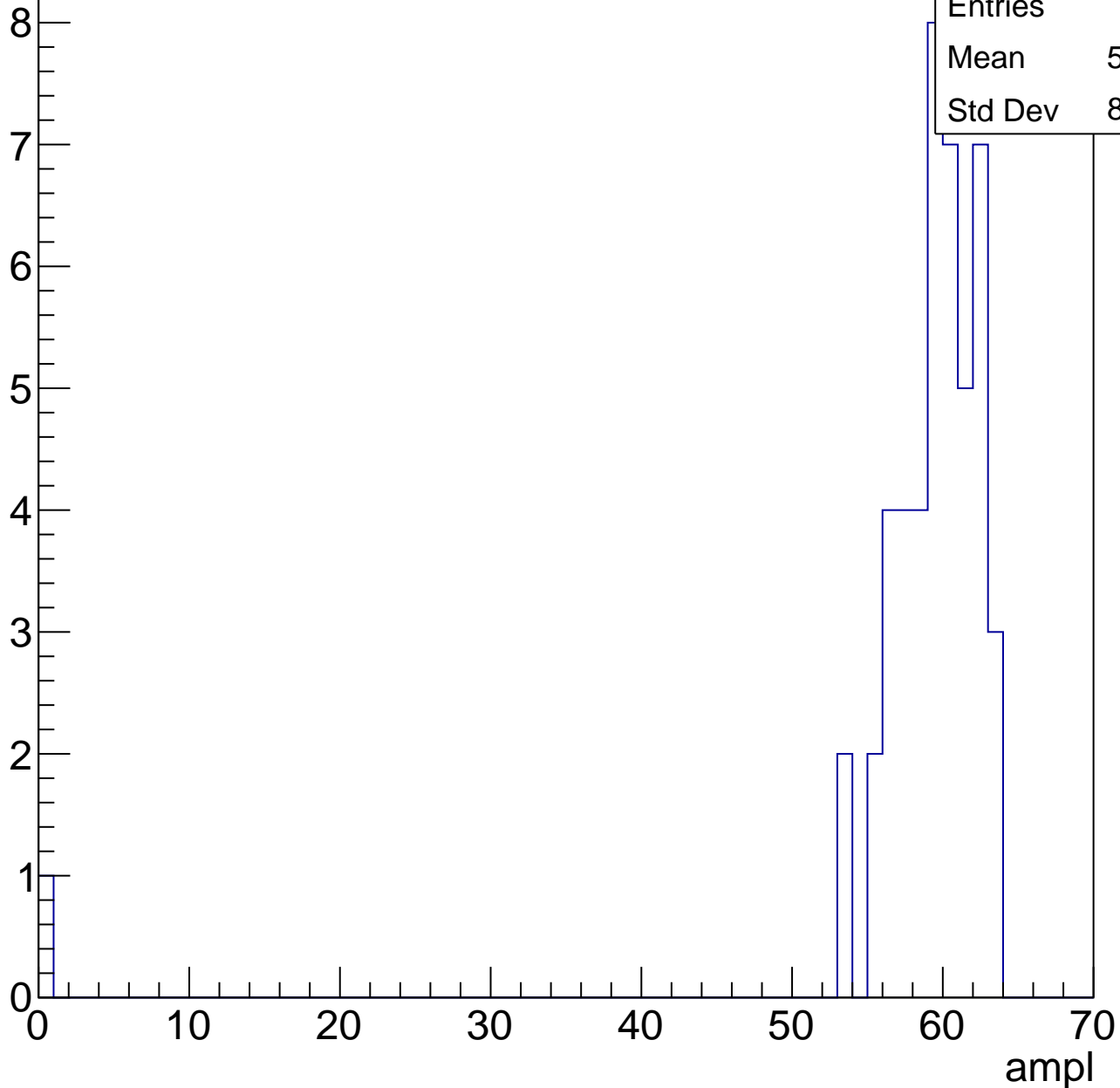
Entries	67
Mean	53.93
Std Dev	3.009



# B1L002S, U8-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

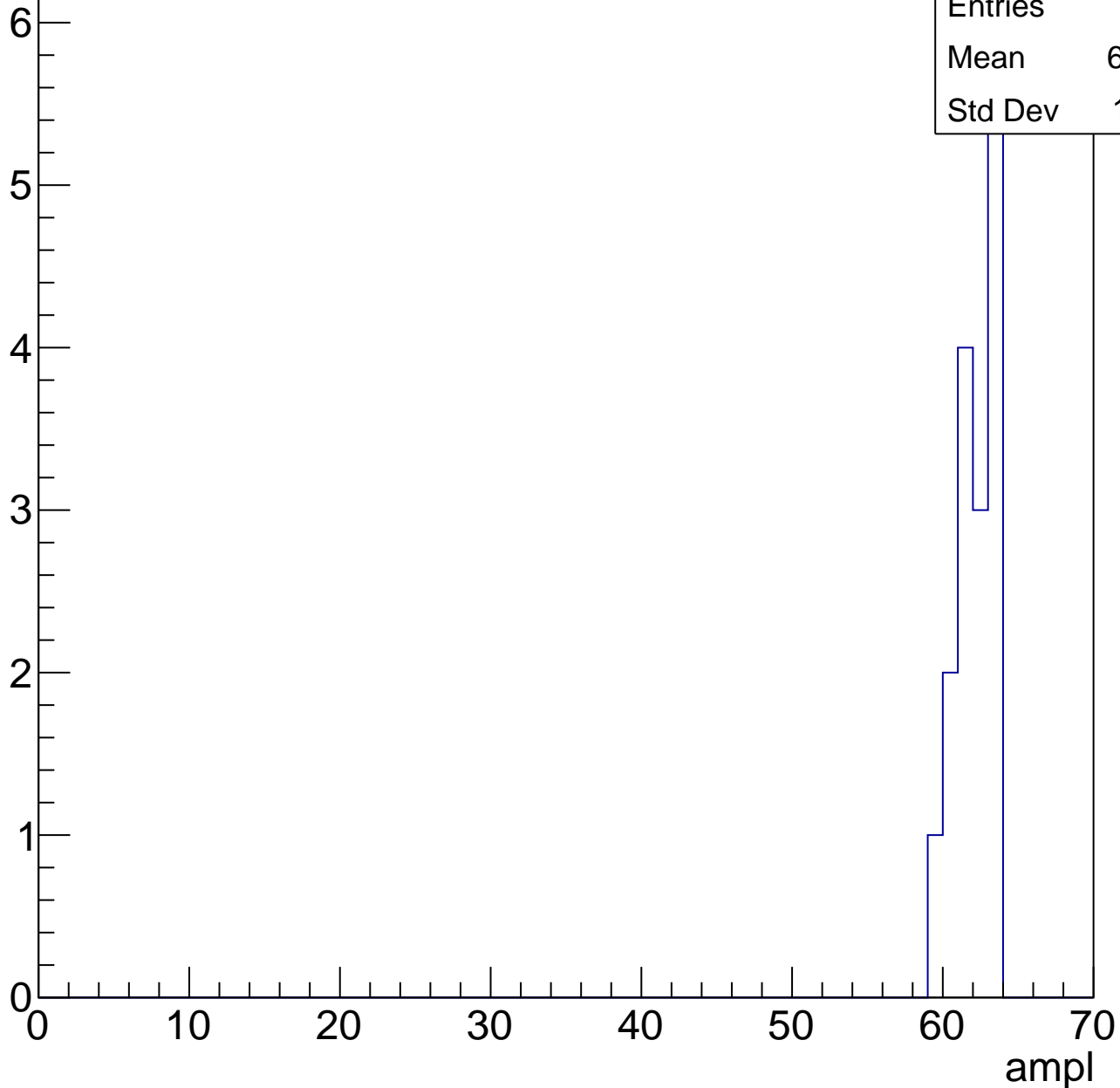


# B1L002S, U8-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	61.69
Std Dev	1.261





# B1L002S, U8-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch7, adc0

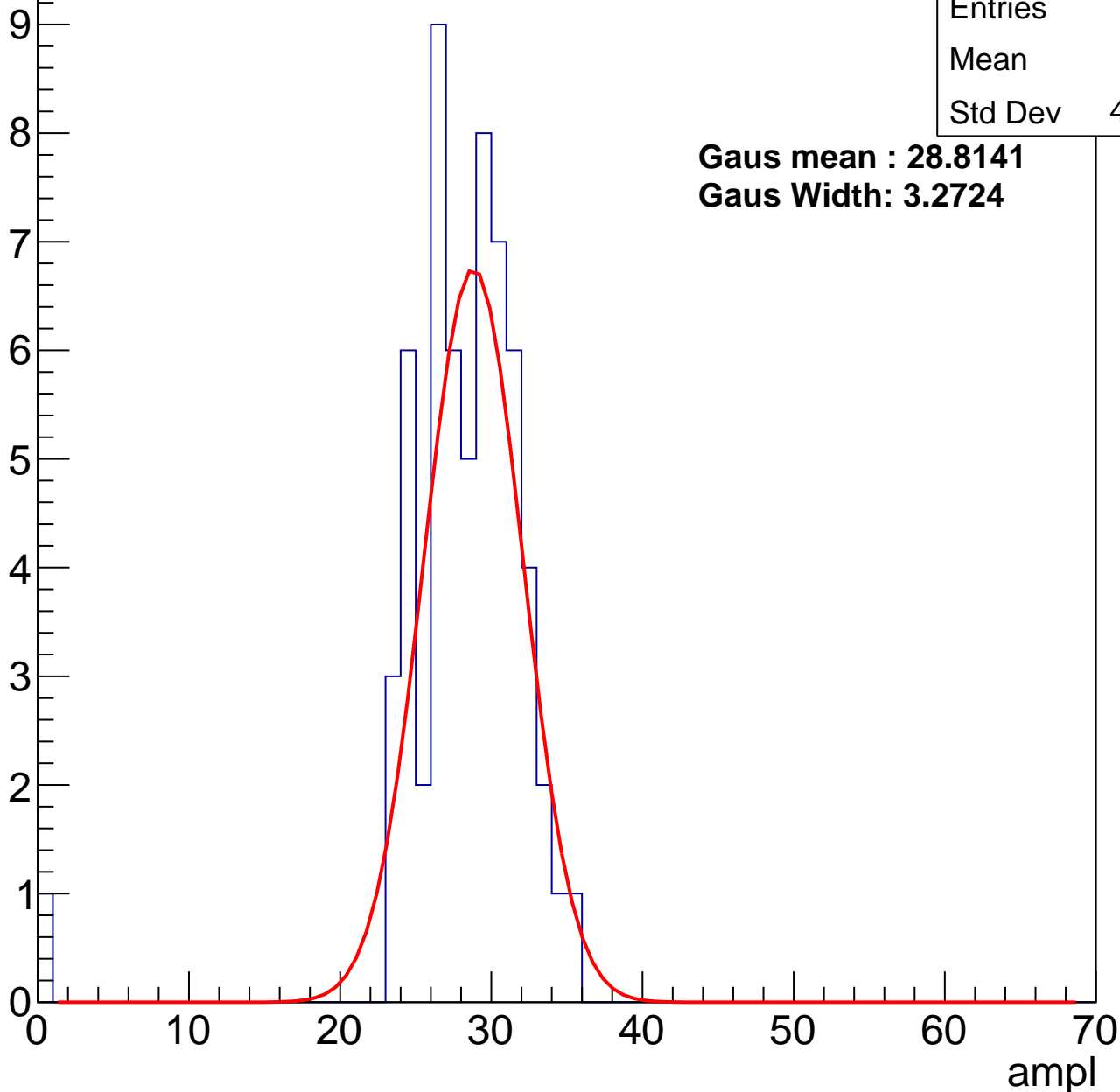
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.7
Std Dev	4.614

**Gaus mean : 28.8141**

**Gaus Width: 3.2724**



# B1L002S, U8-ch7, adc1

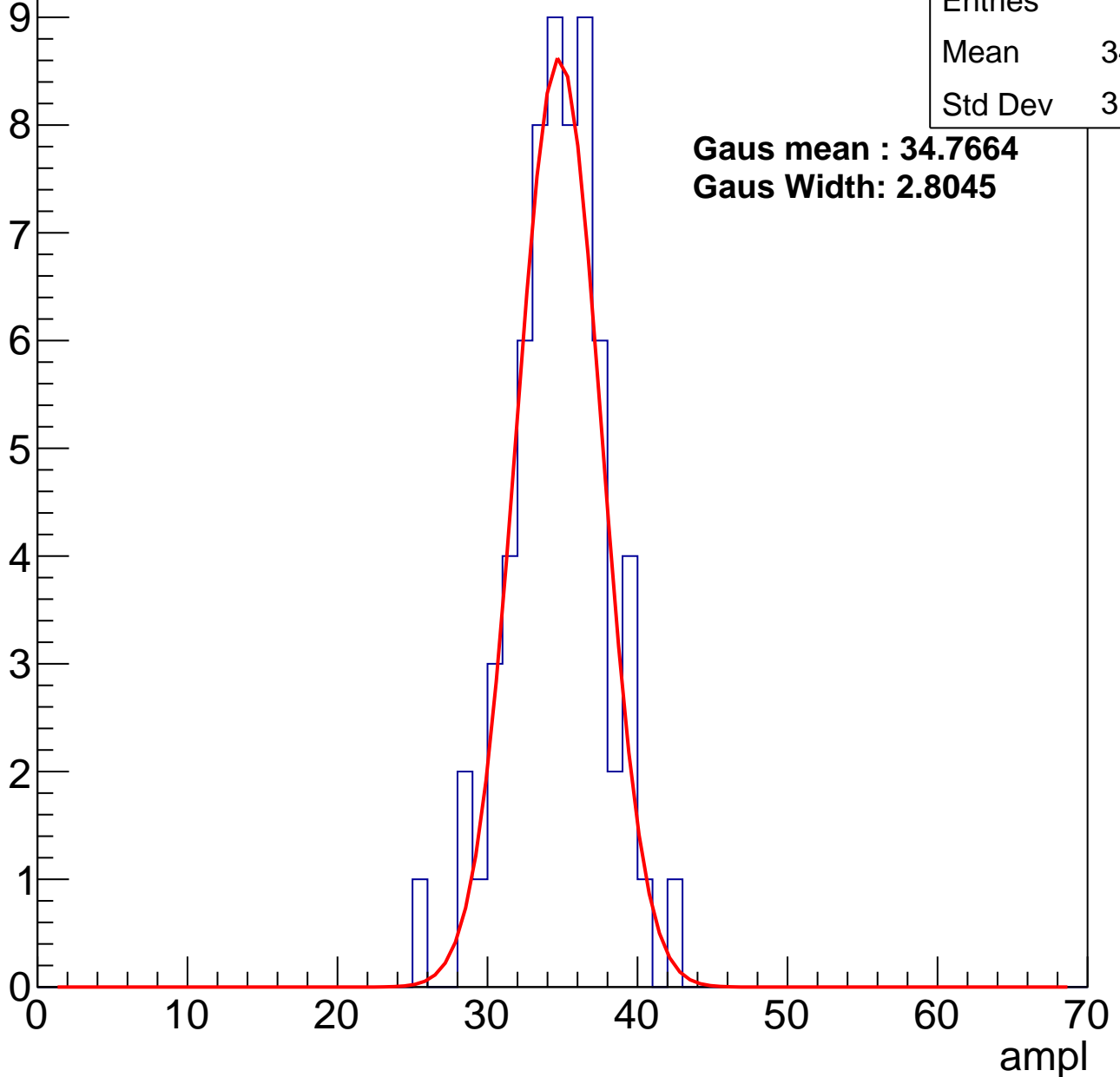
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.25
Std Dev	3.089

**Gaus mean : 34.7664**

**Gaus Width: 2.8045**



# B1L002S, U8-ch7, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	42.04
Std Dev	3.329

**Gaus mean : 42.5035**

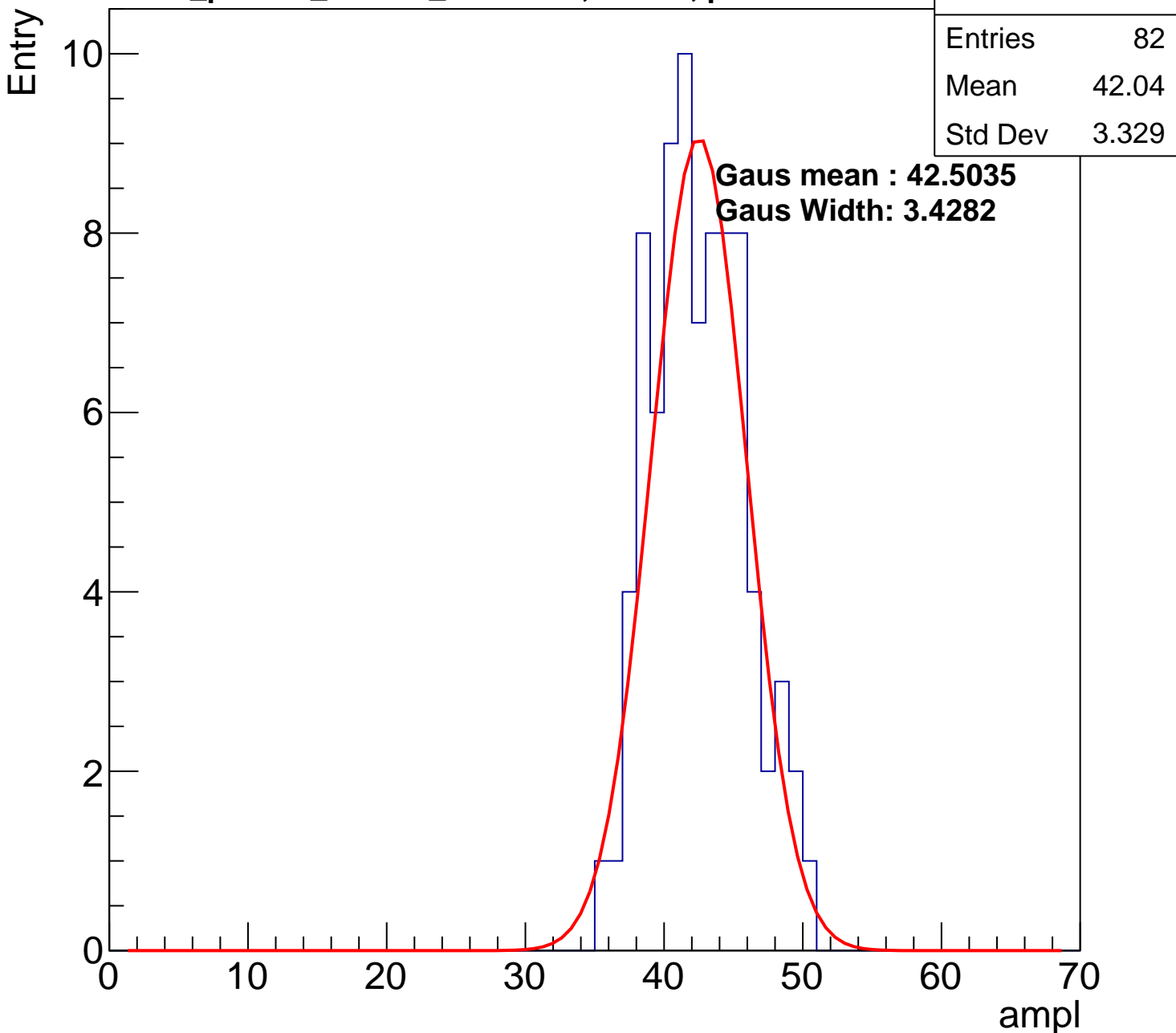
**Gaus Width: 3.4282**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

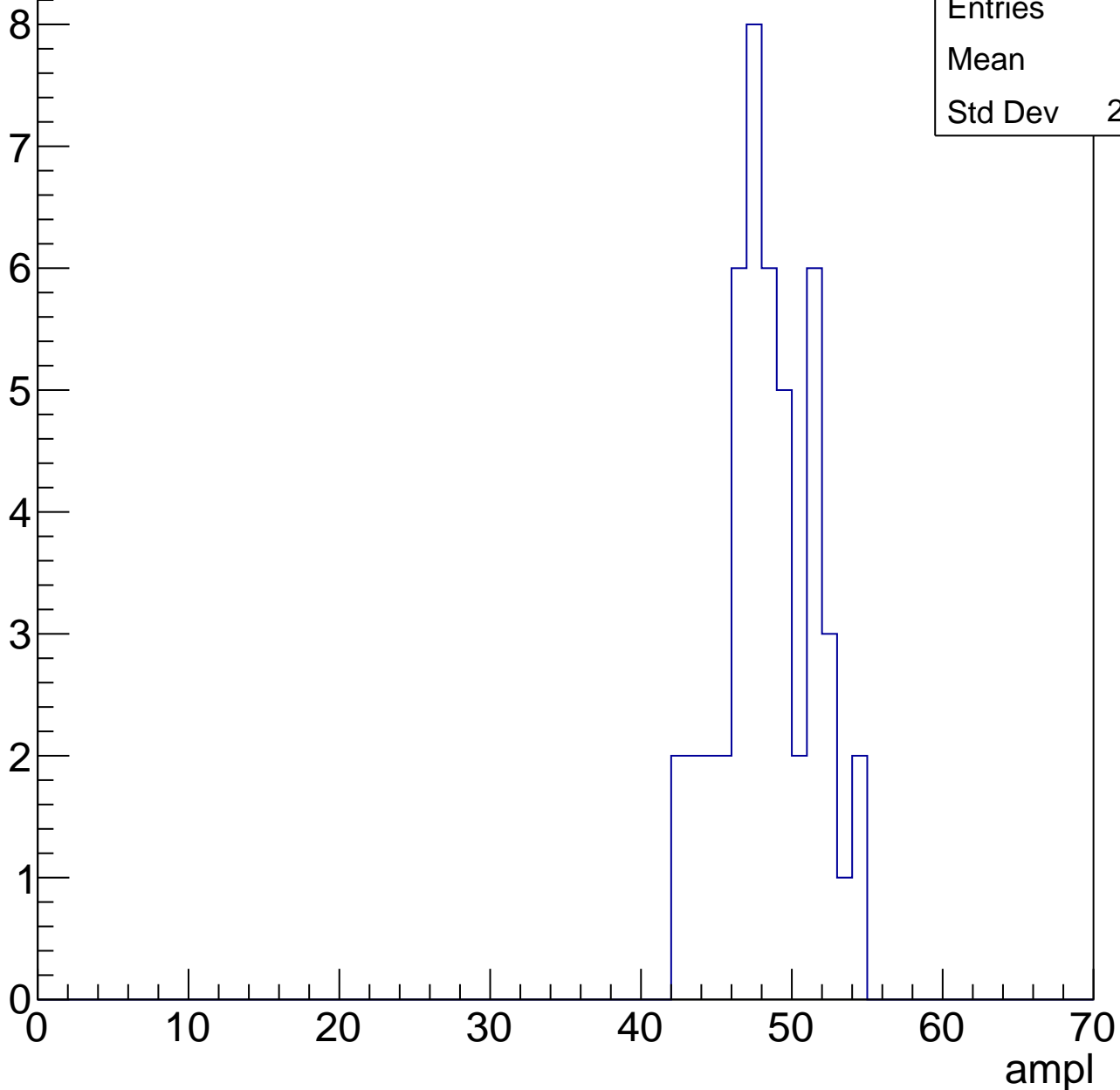


# B1L002S, U8-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	48
Std Dev	2.975

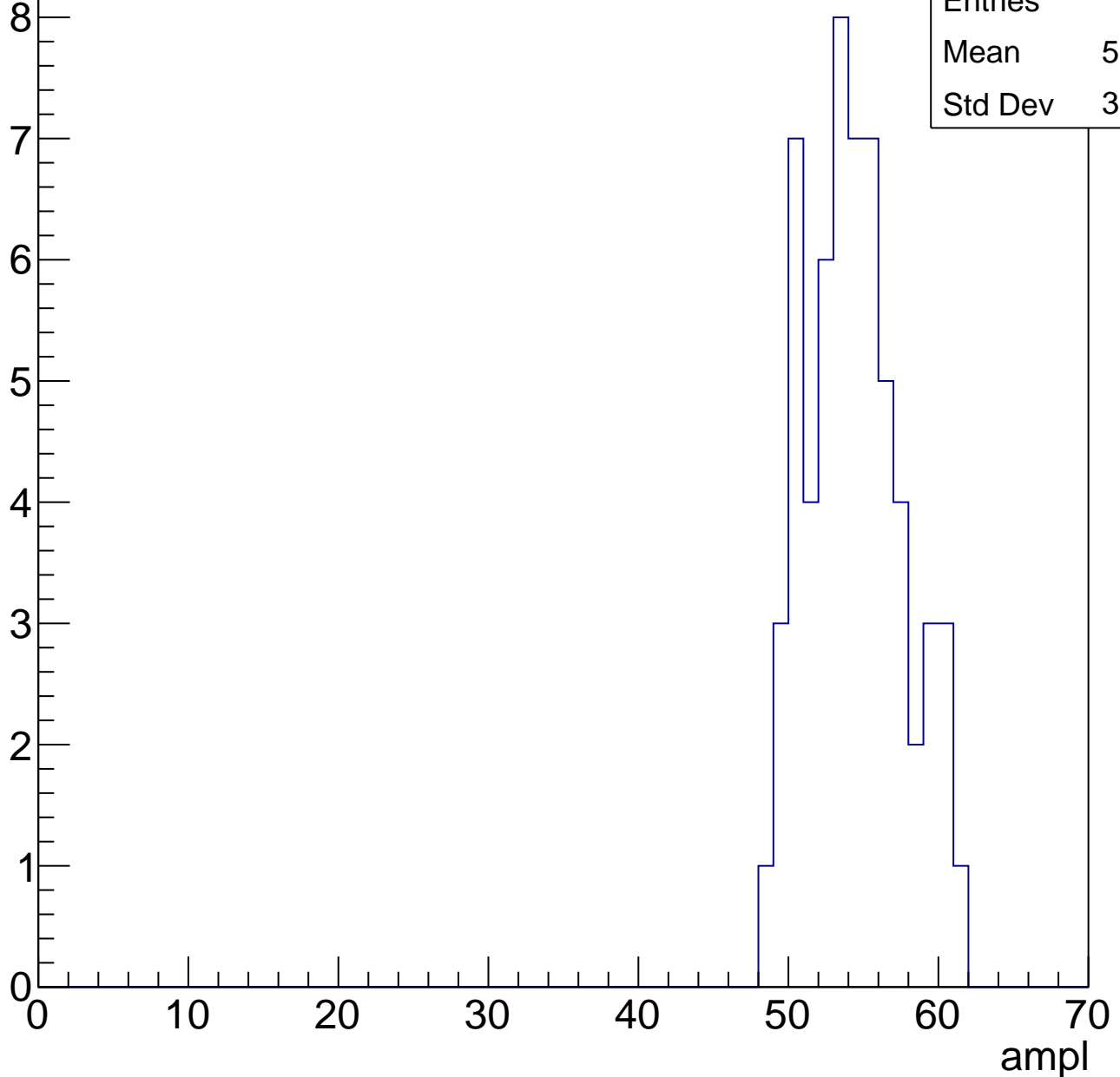


# B1L002S, U8-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	53.93
Std Dev	3.182



# B1L002S, U8-ch7, adc5

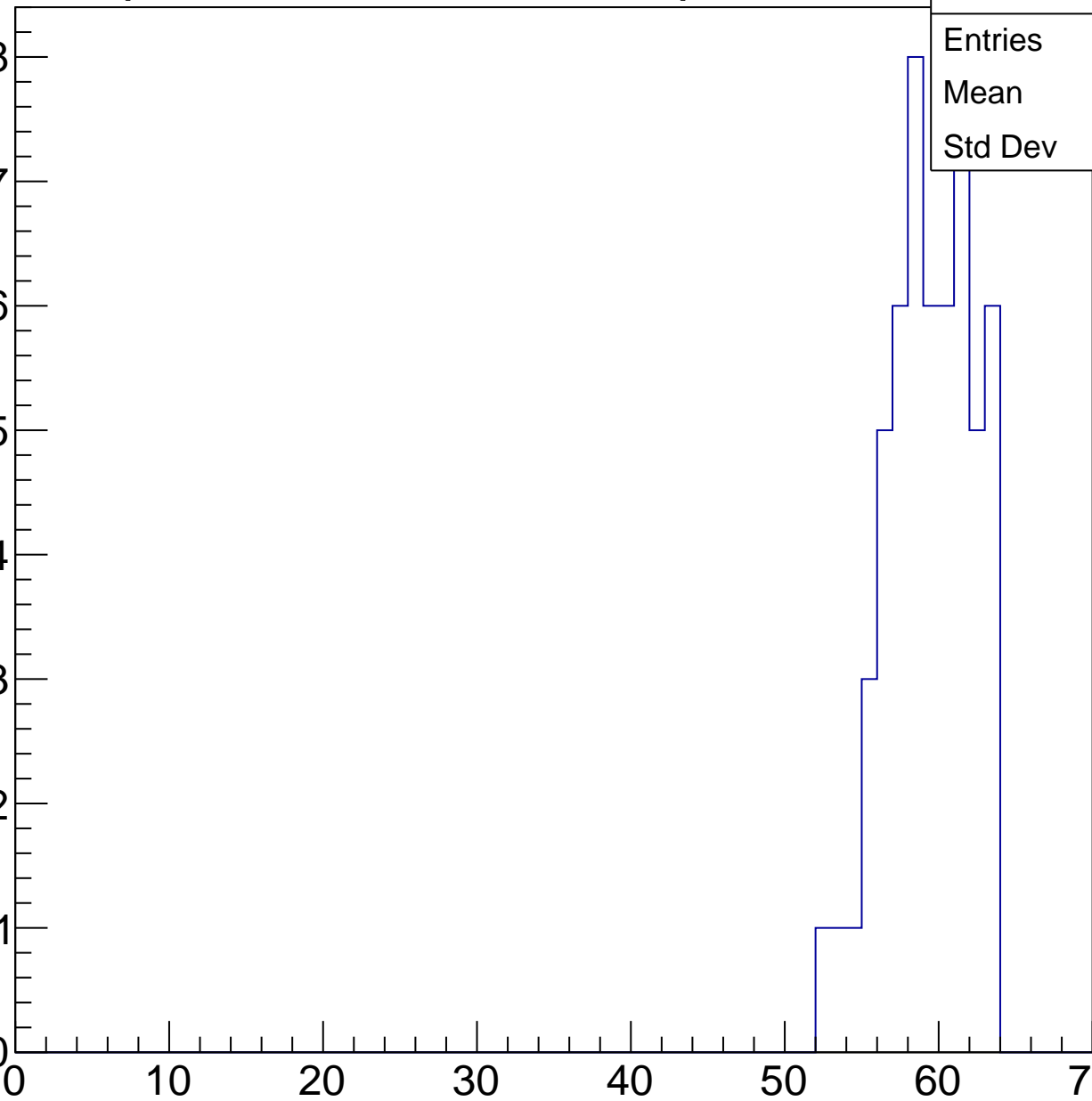
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	58.93
Std Dev	2.718

ampl

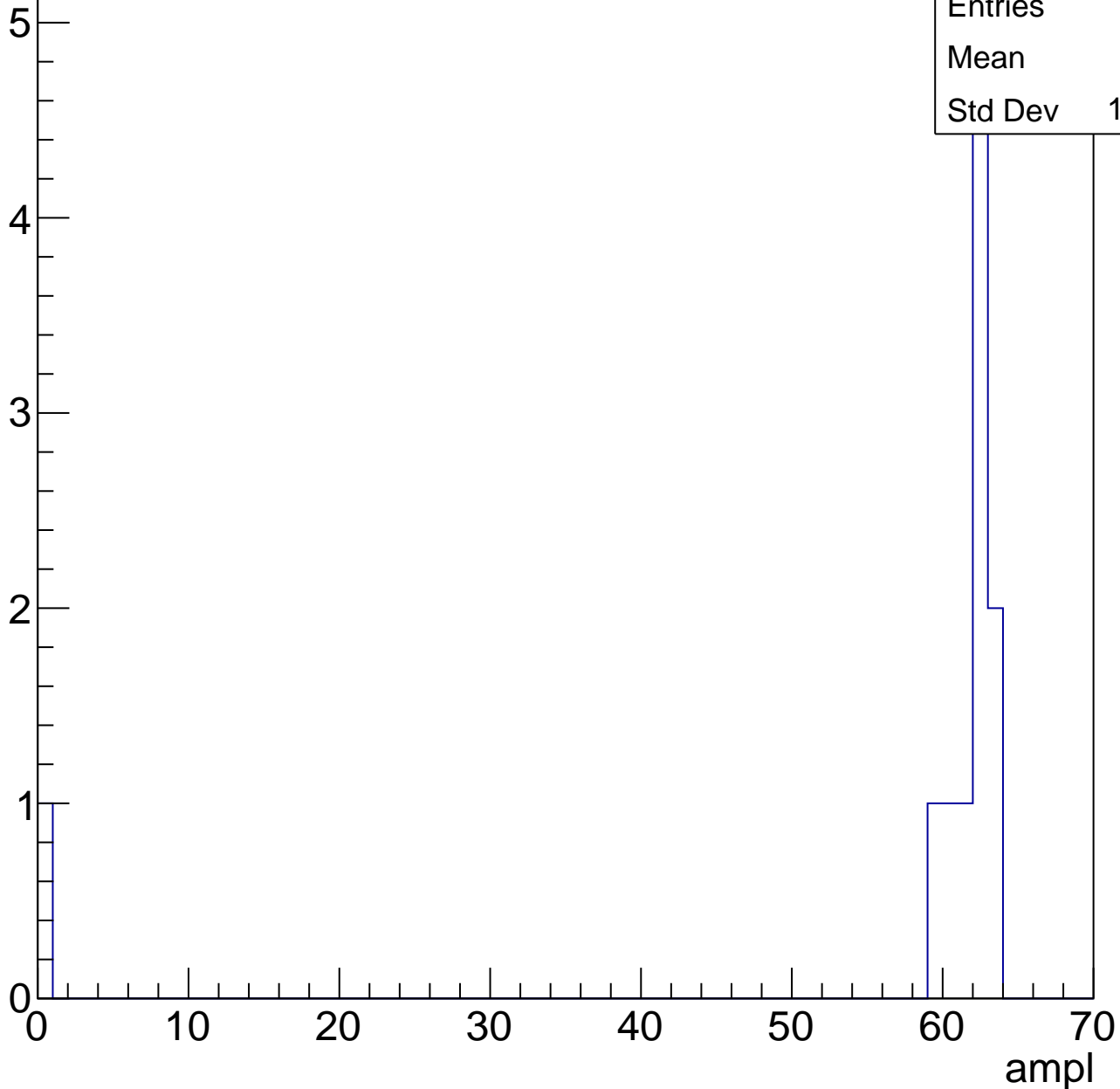


# B1L002S, U8-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	56
Std Dev	17.75

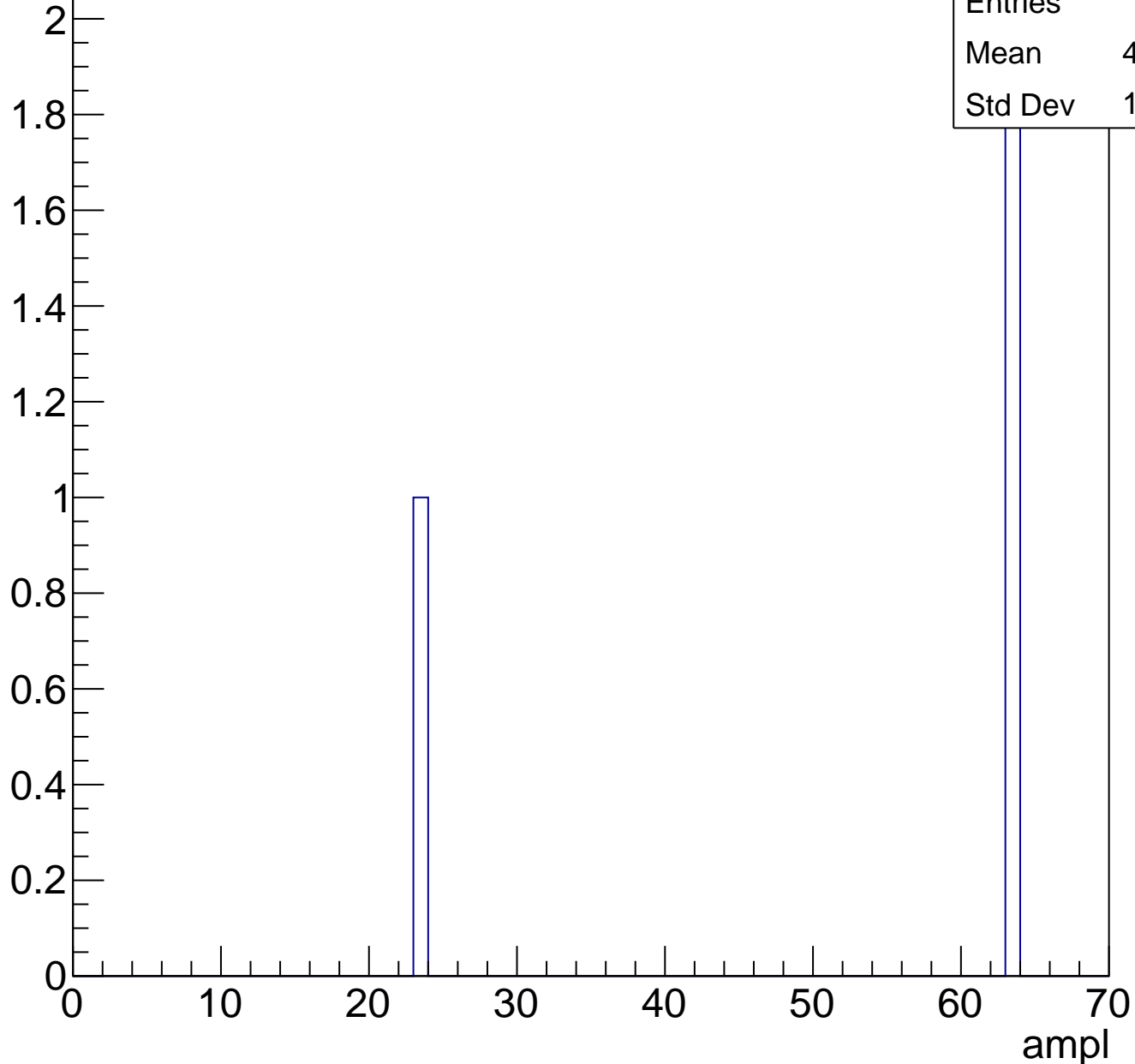




# B1L002S, U8-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	49.67
Std Dev	18.86

# B1L002S, U8-ch8, adc0

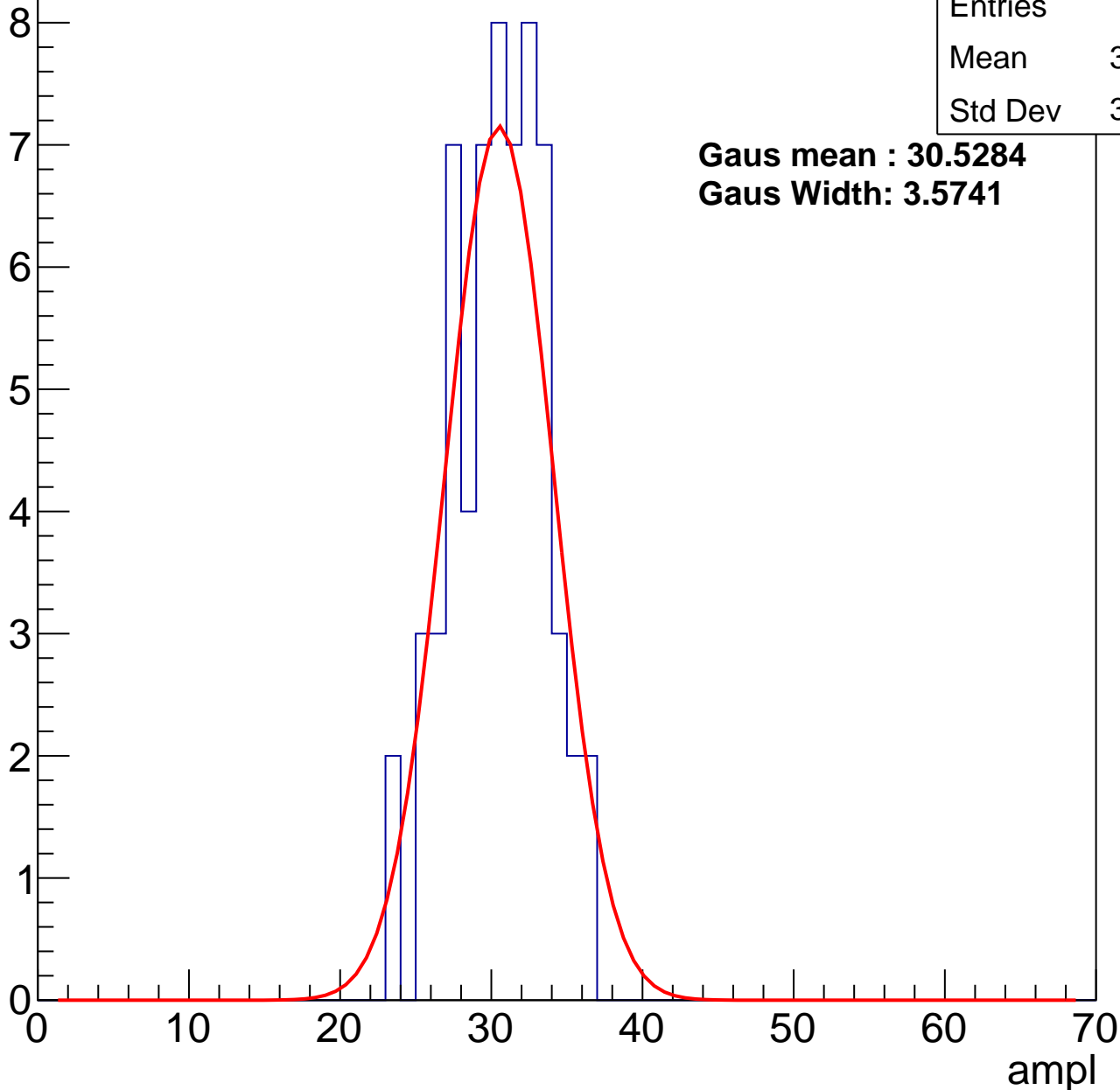
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	30.02
Std Dev	3.032

**Gaus mean : 30.5284**

**Gaus Width: 3.5741**



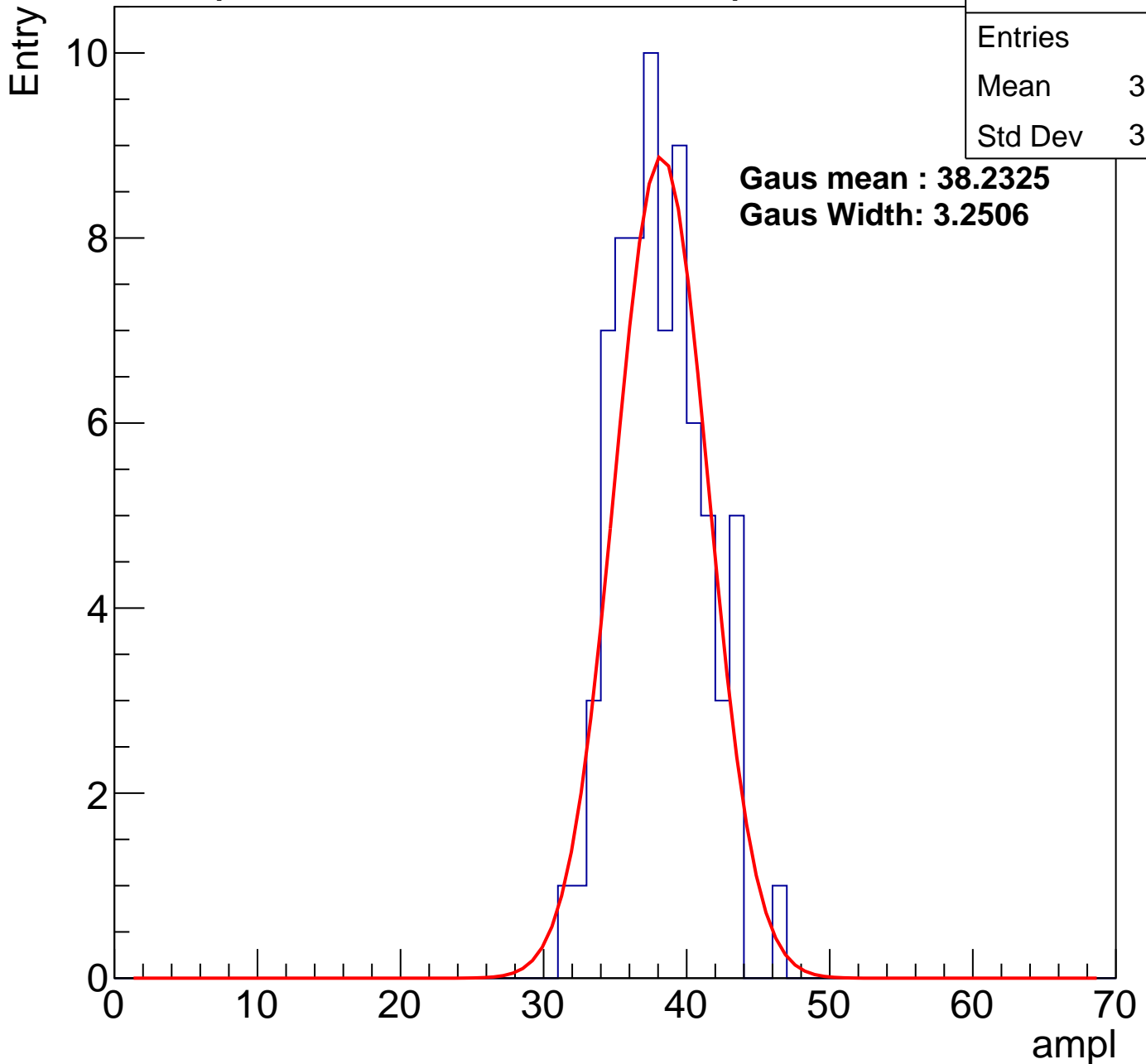
# B1L002S, U8-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	37.66
Std Dev	3.059

**Gaus mean : 38.2325**

**Gaus Width: 3.2506**



# B1L002S, U8-ch8, adc2

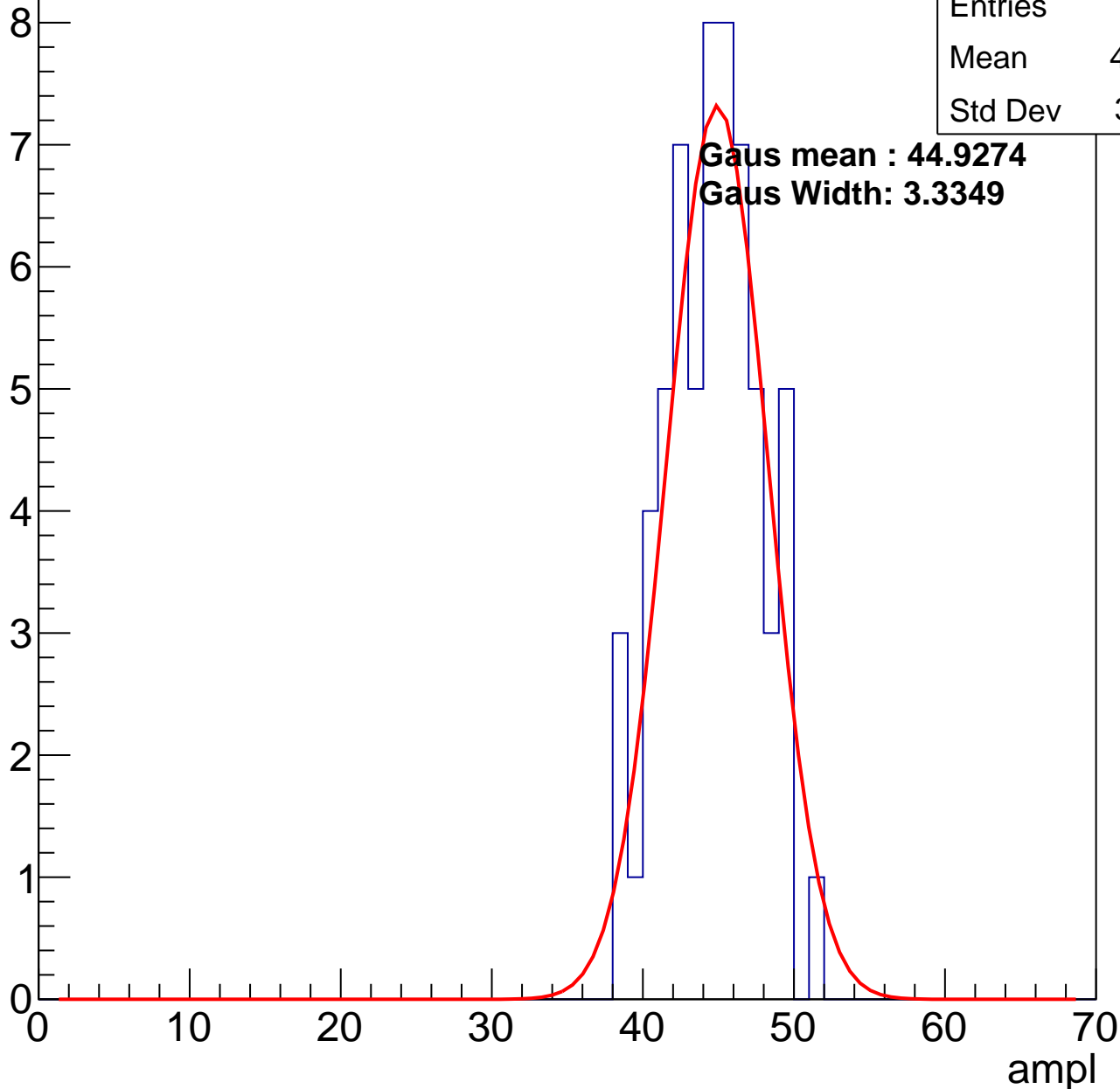
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	44.13
Std Dev	3.051

**Gaus mean : 44.9274**

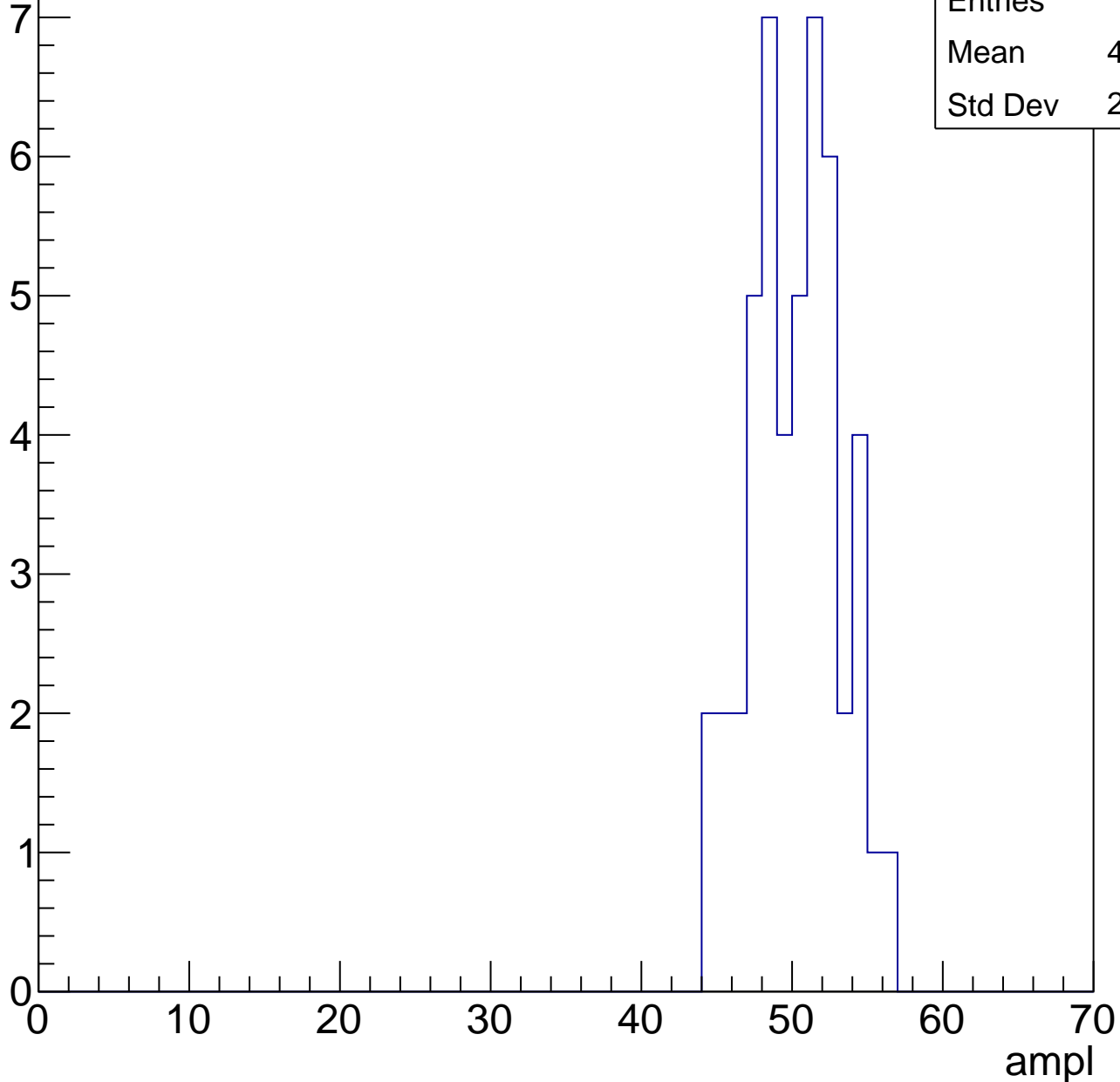
**Gaus Width: 3.3349**



# B1L002S, U8-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



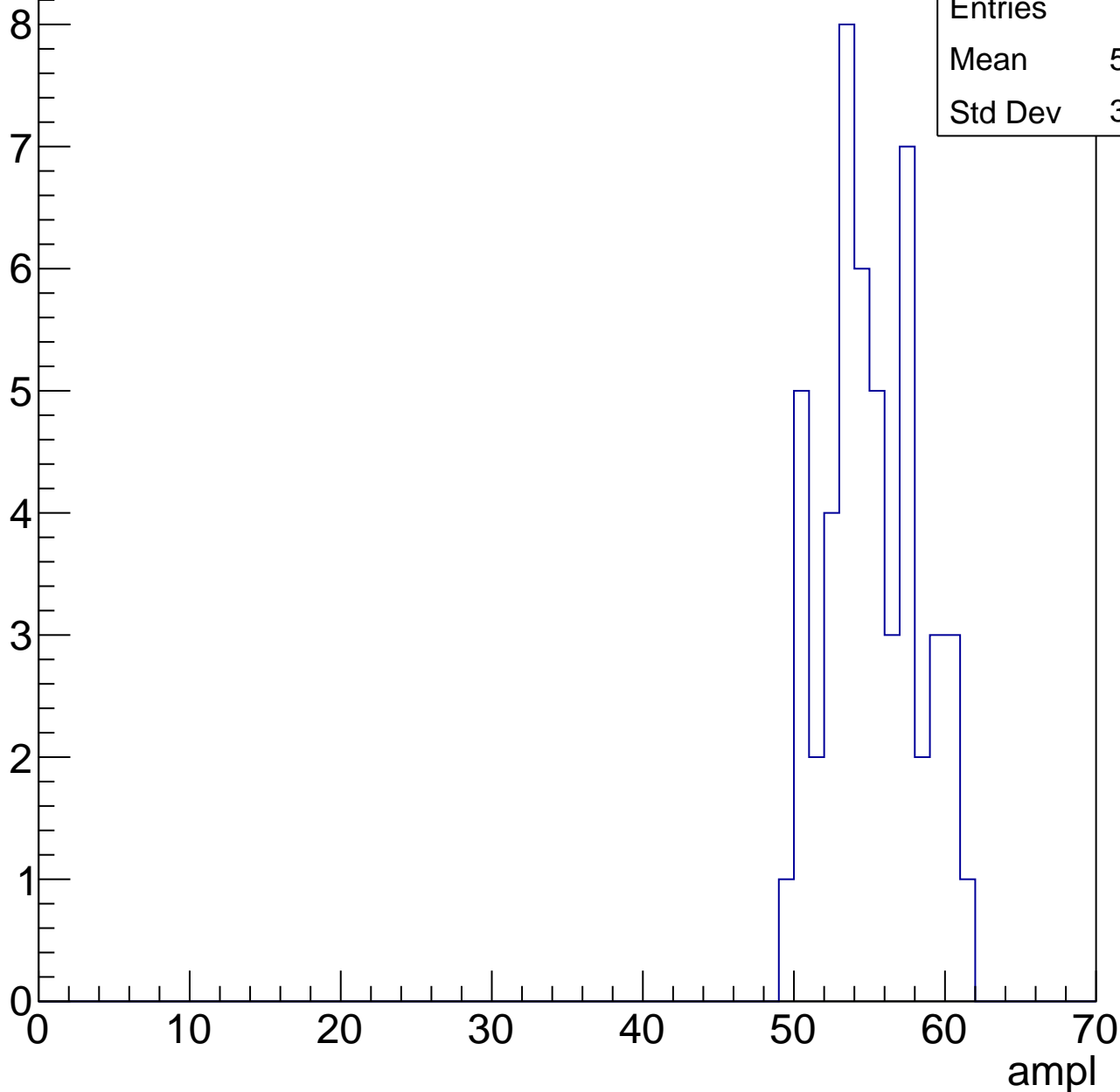
Entries	48
Mean	49.77
Std Dev	2.896

# B1L002S, U8-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	54.66
Std Dev	3.063



# B1L002S, U8-ch8, adc5

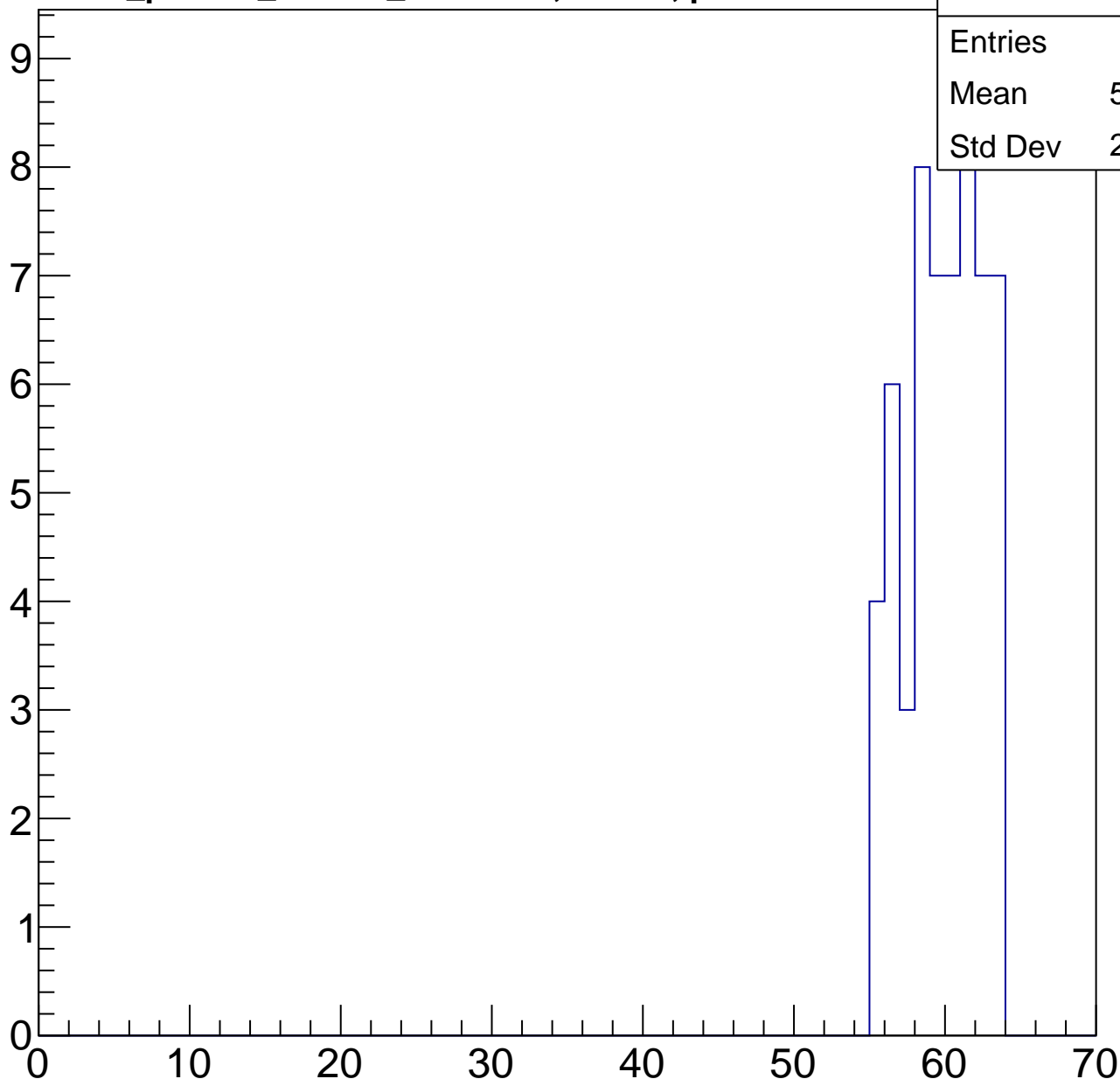
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	59.45
Std Dev	2.437

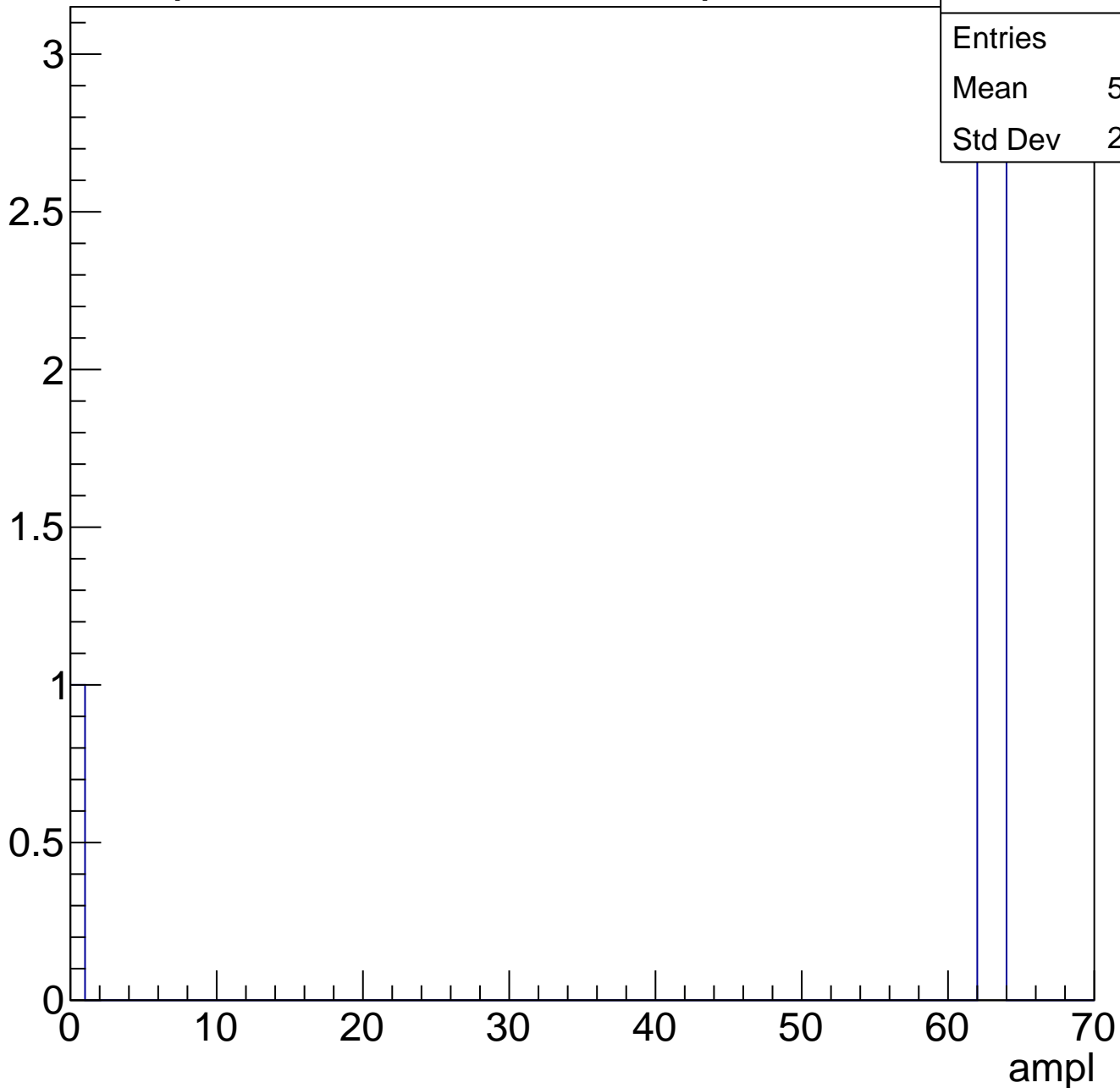
ampl



# B1L002S, U8-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch9, adc0

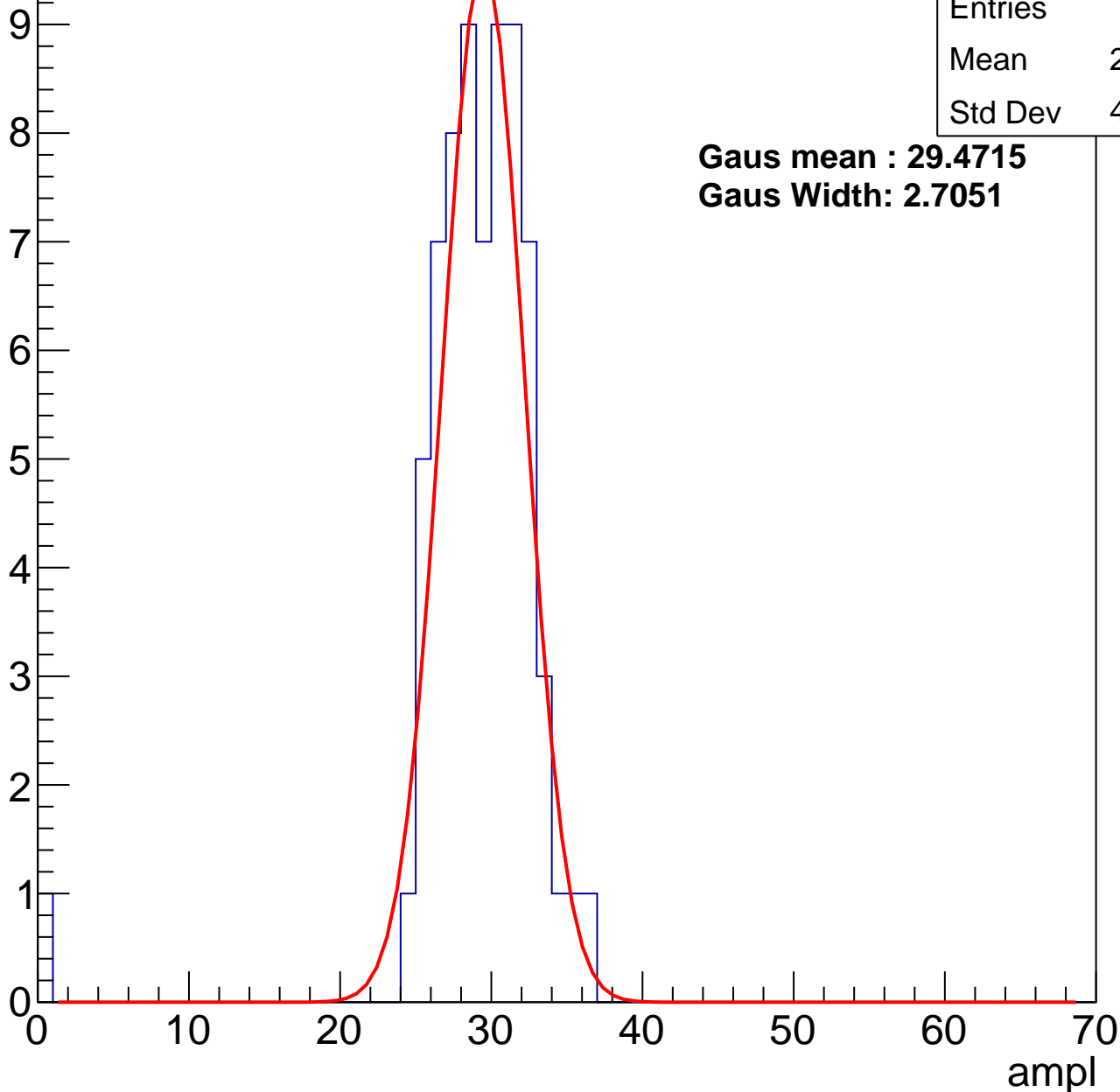
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	28.68
Std Dev	4.356

**Gaus mean : 29.4715**

**Gaus Width: 2.7051**



# B1L002S, U8-ch9, adc1

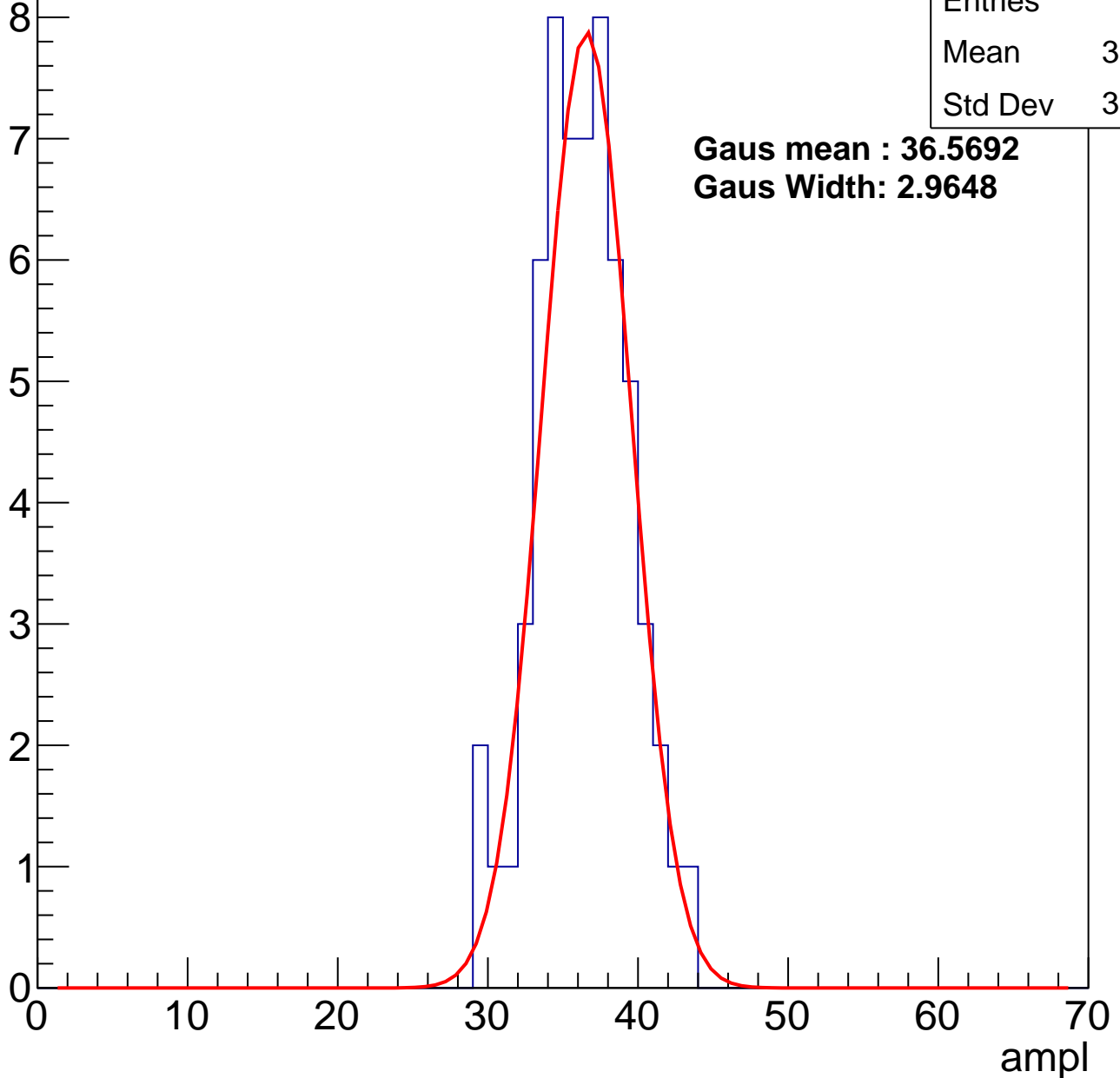
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.87
Std Dev	3.027

**Gaus mean : 36.5692**

**Gaus Width: 2.9648**



# B1L002S, U8-ch9, adc2

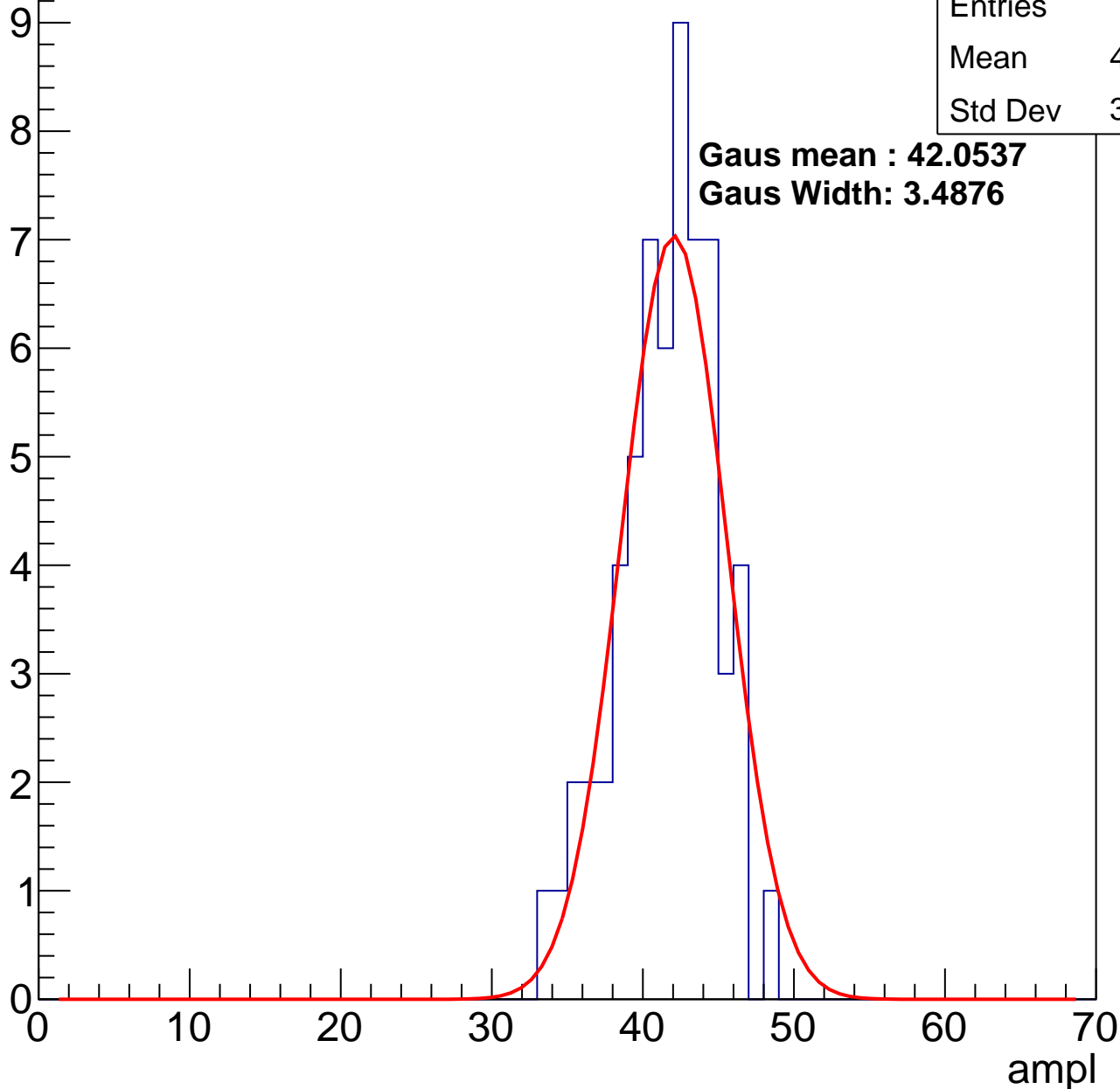
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.15
Std Dev	3.203

**Gaus mean : 42.0537**

**Gaus Width: 3.4876**

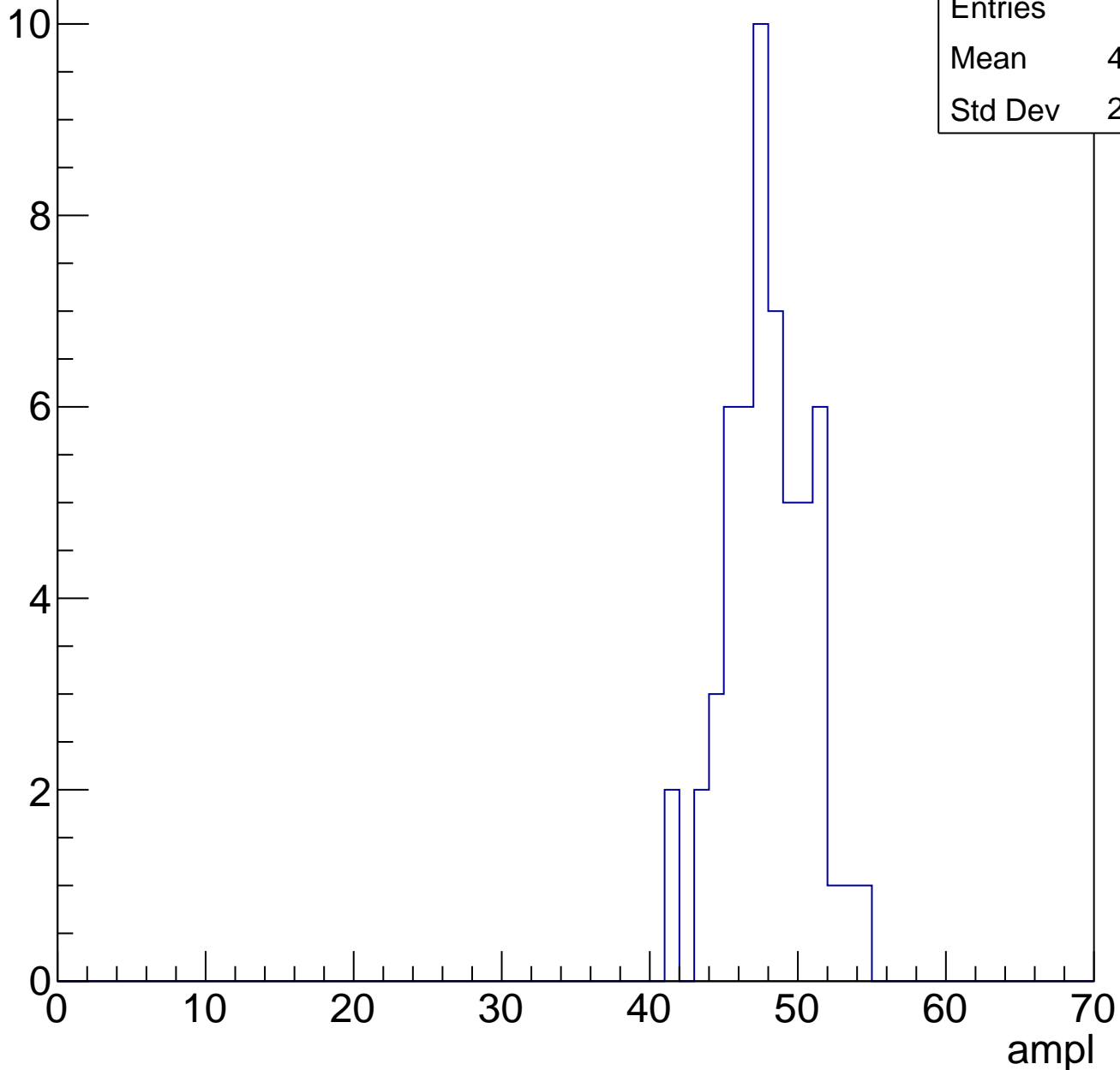


# B1L002S, U8-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	55
Mean	47.49
Std Dev	2.782

Entry

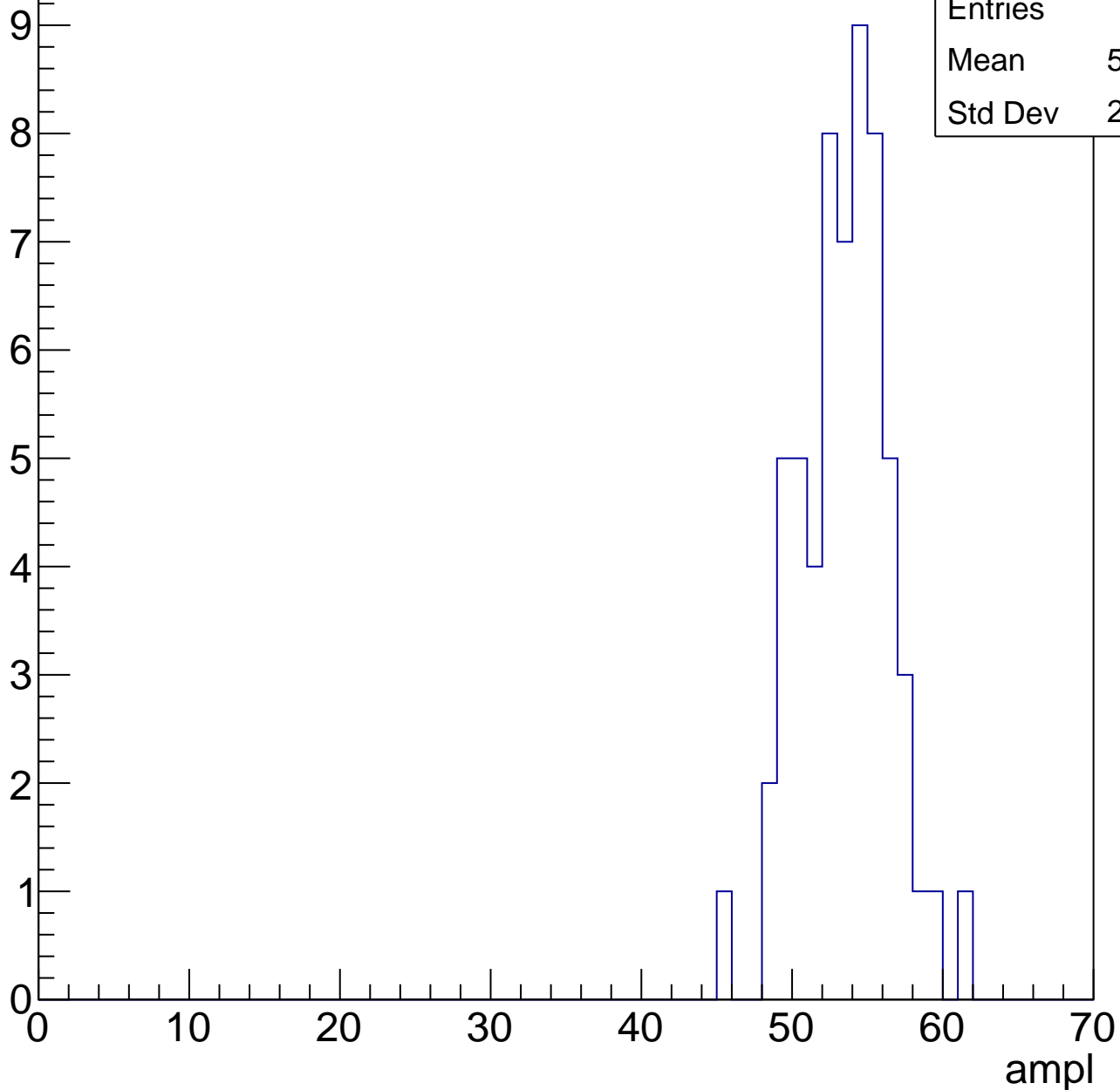


# B1L002S, U8-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

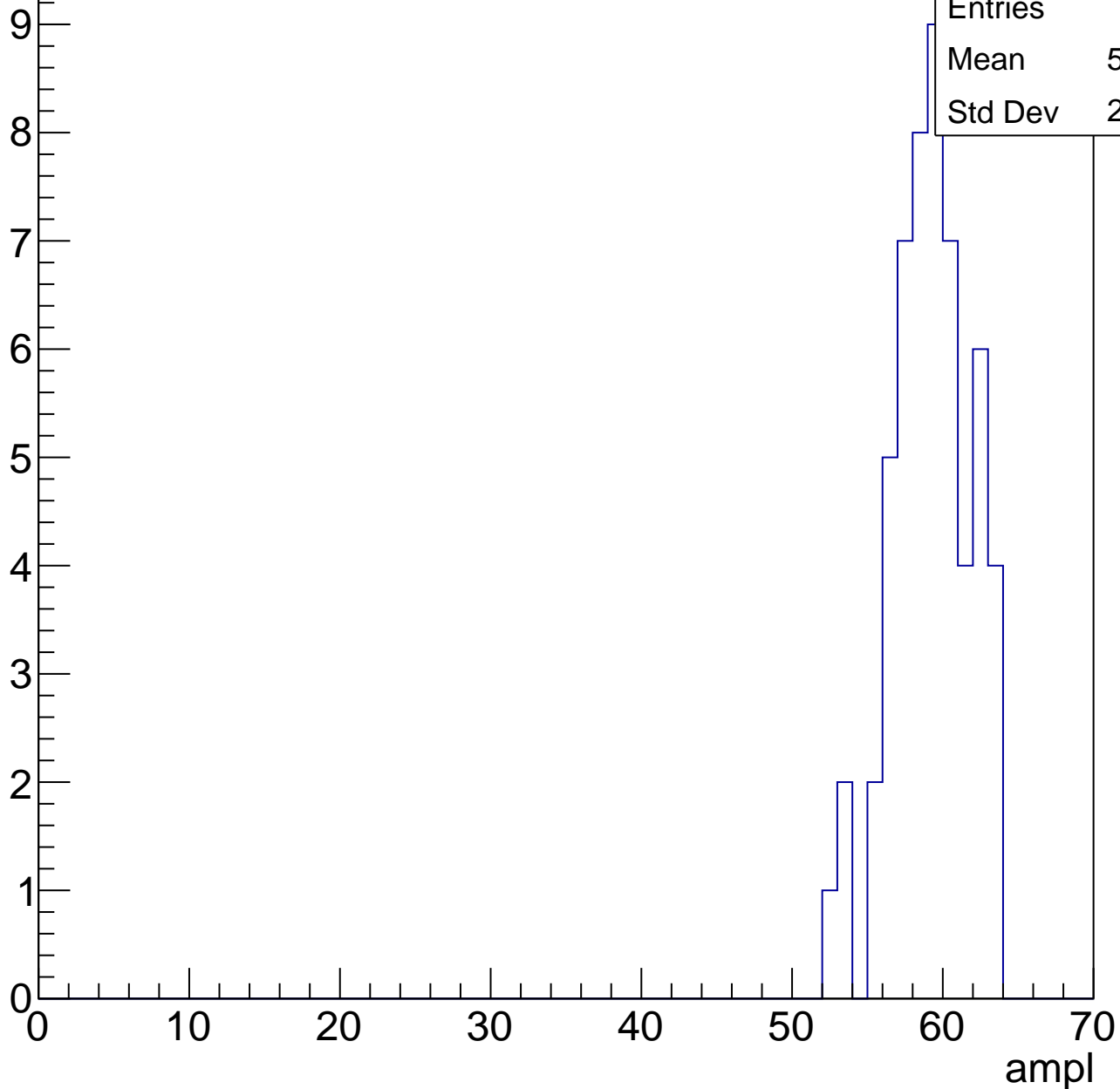
Entries	60
Mean	53.03
Std Dev	2.949



# B1L002S, U8-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

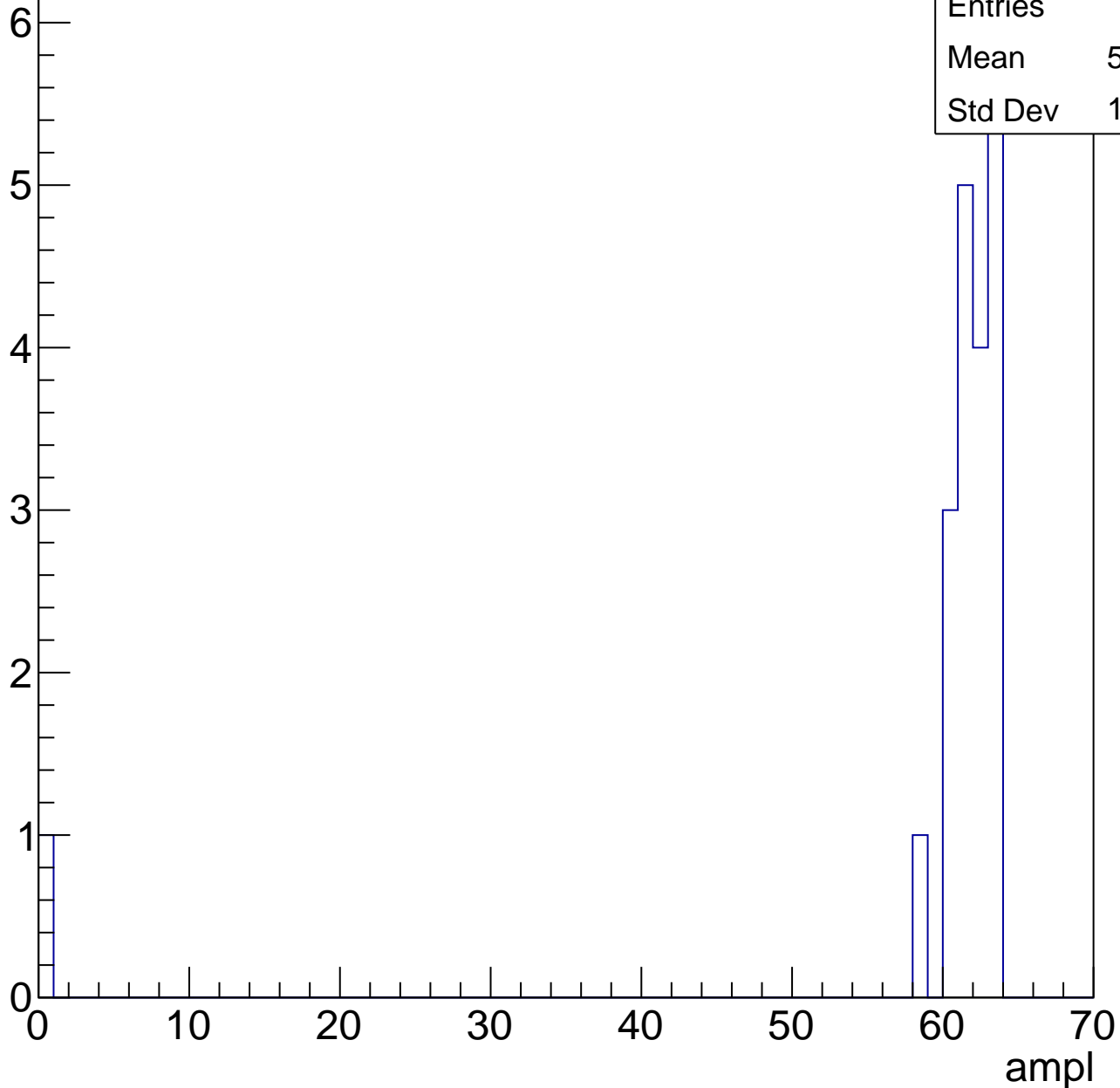


# B1L002S, U8-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.47





# B1L002S, U8-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U8-ch10, adc0

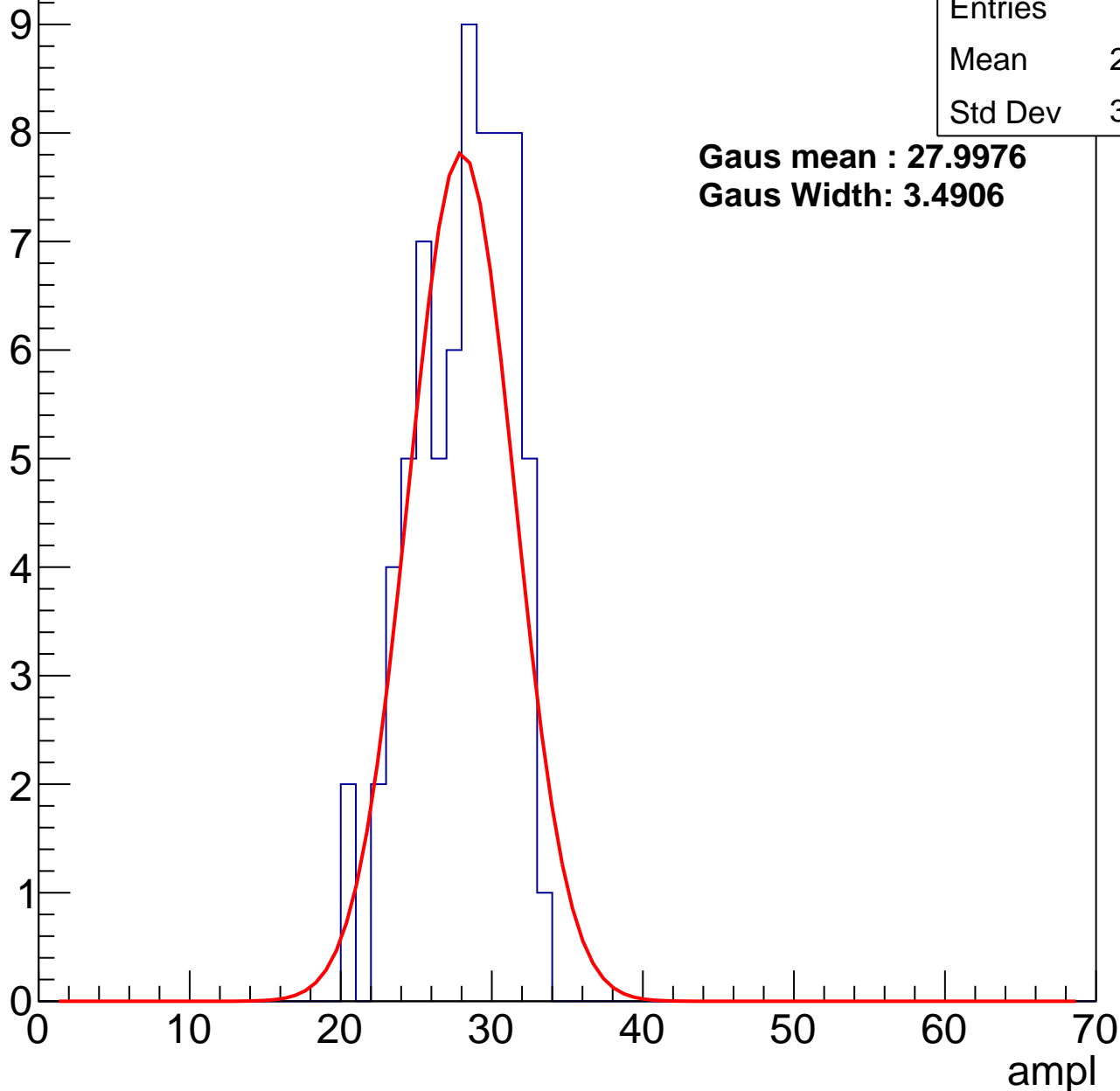
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	27.54
Std Dev	3.097

**Gaus mean : 27.9976**

**Gaus Width: 3.4906**



# B1L002S, U8-ch10, adc1

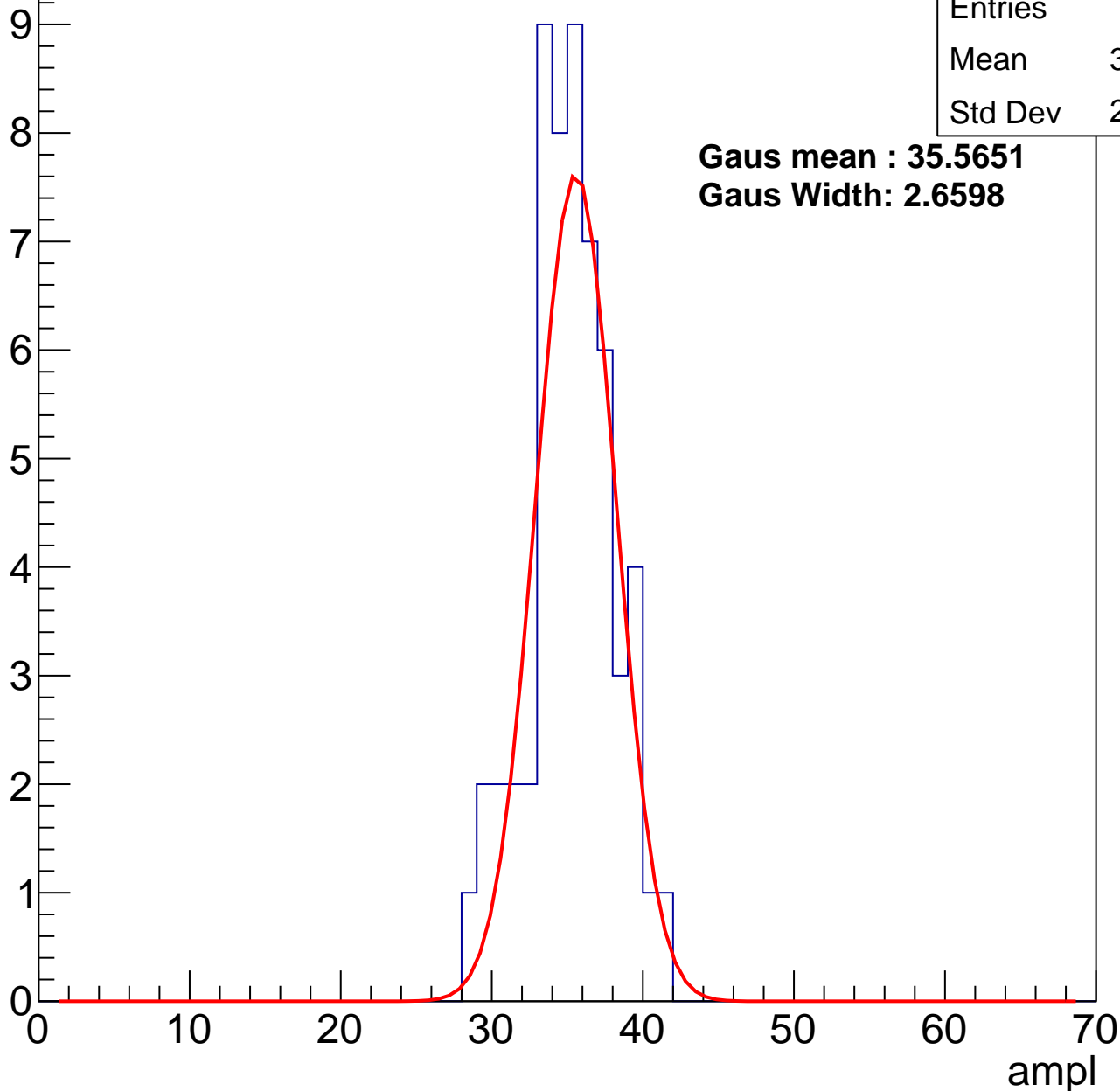
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.75
Std Dev	2.793

**Gaus mean : 35.5651**

**Gaus Width: 2.6598**



# B1L002S, U8-ch10, adc2

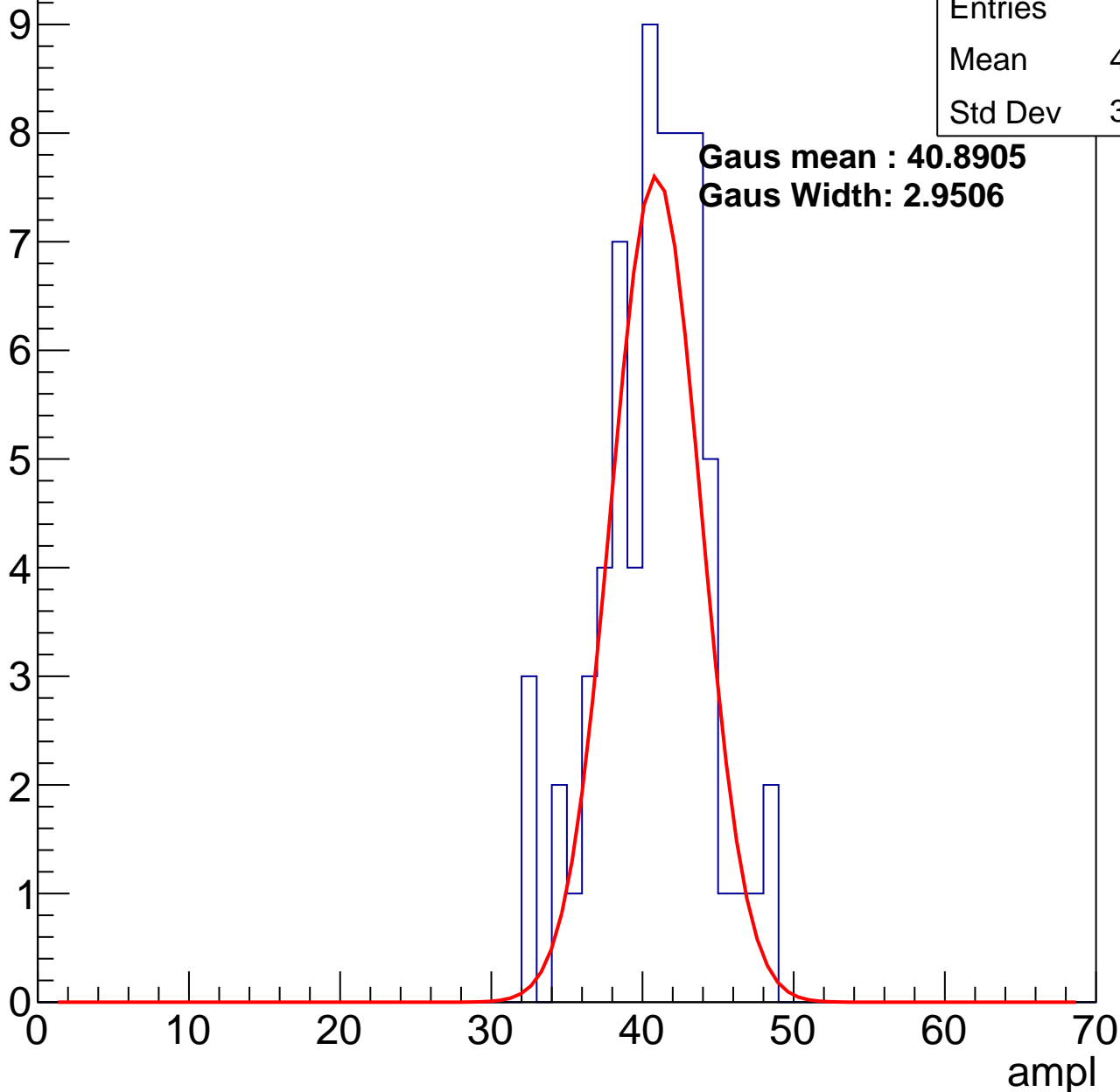
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.28
Std Dev	3.519

**Gaus mean : 40.8905**

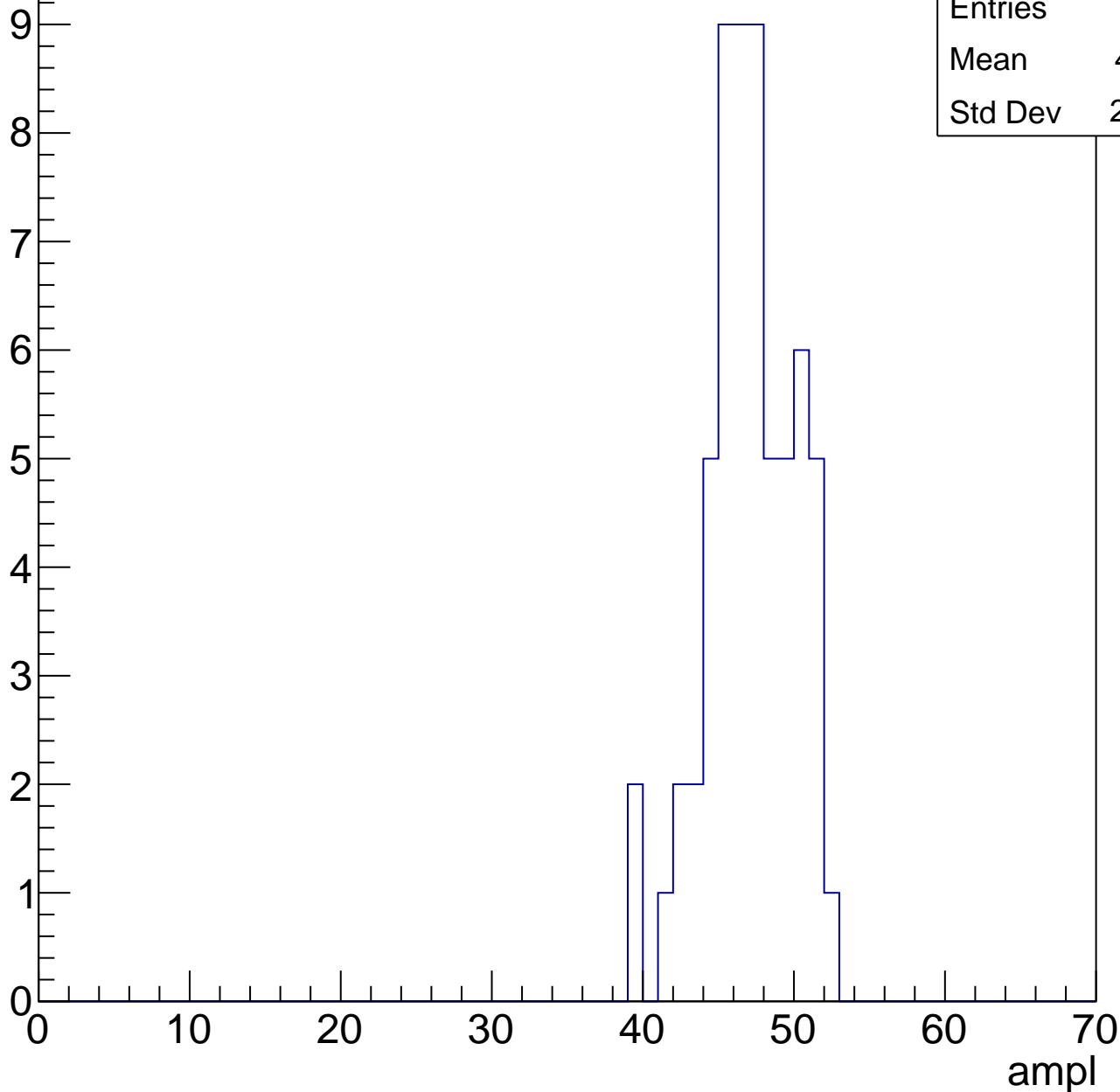
**Gaus Width: 2.9506**



# B1L002S, U8-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

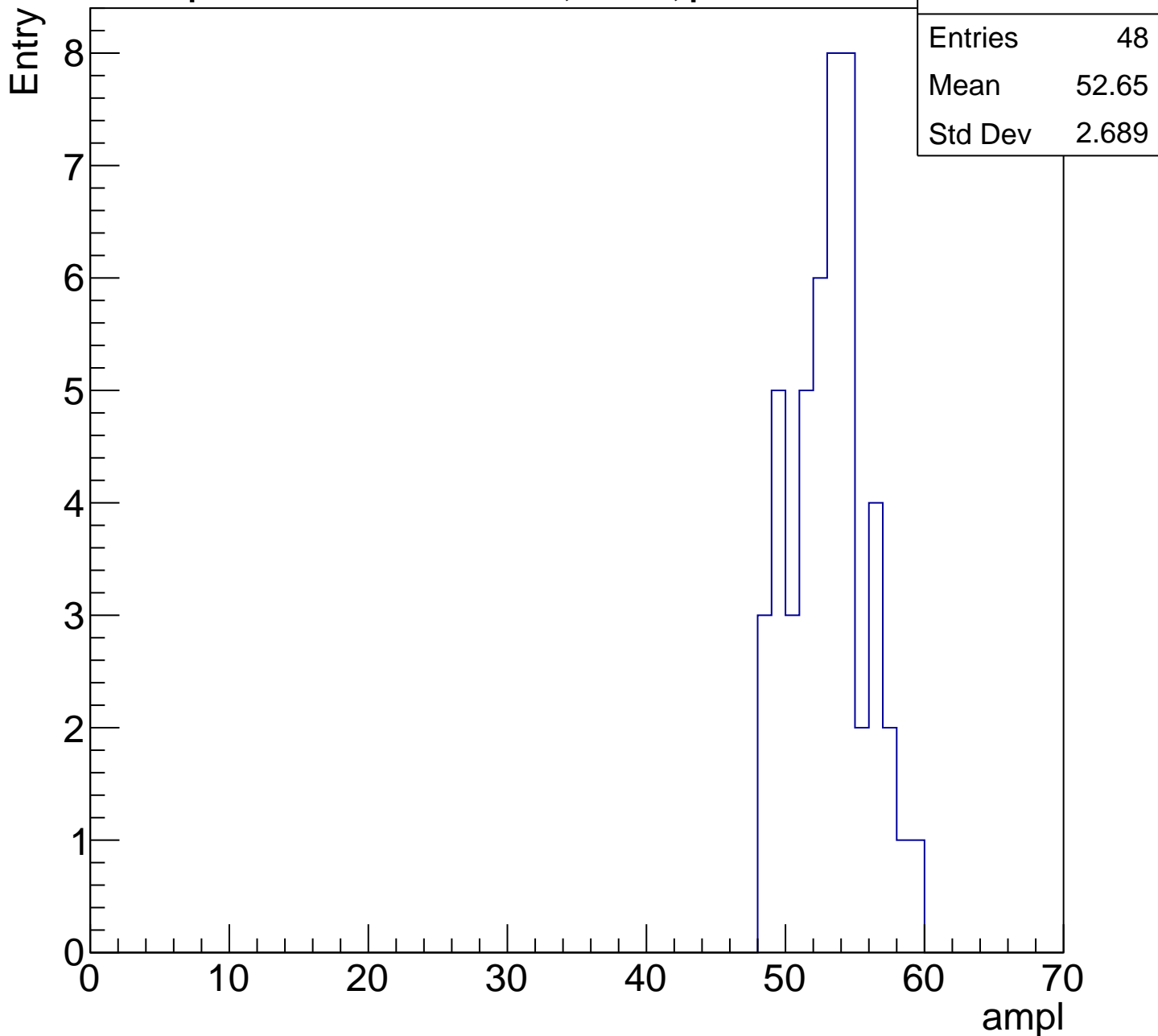
Entry



Entries	61
Mean	46.61
Std Dev	2.893

# B1L002S, U8-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

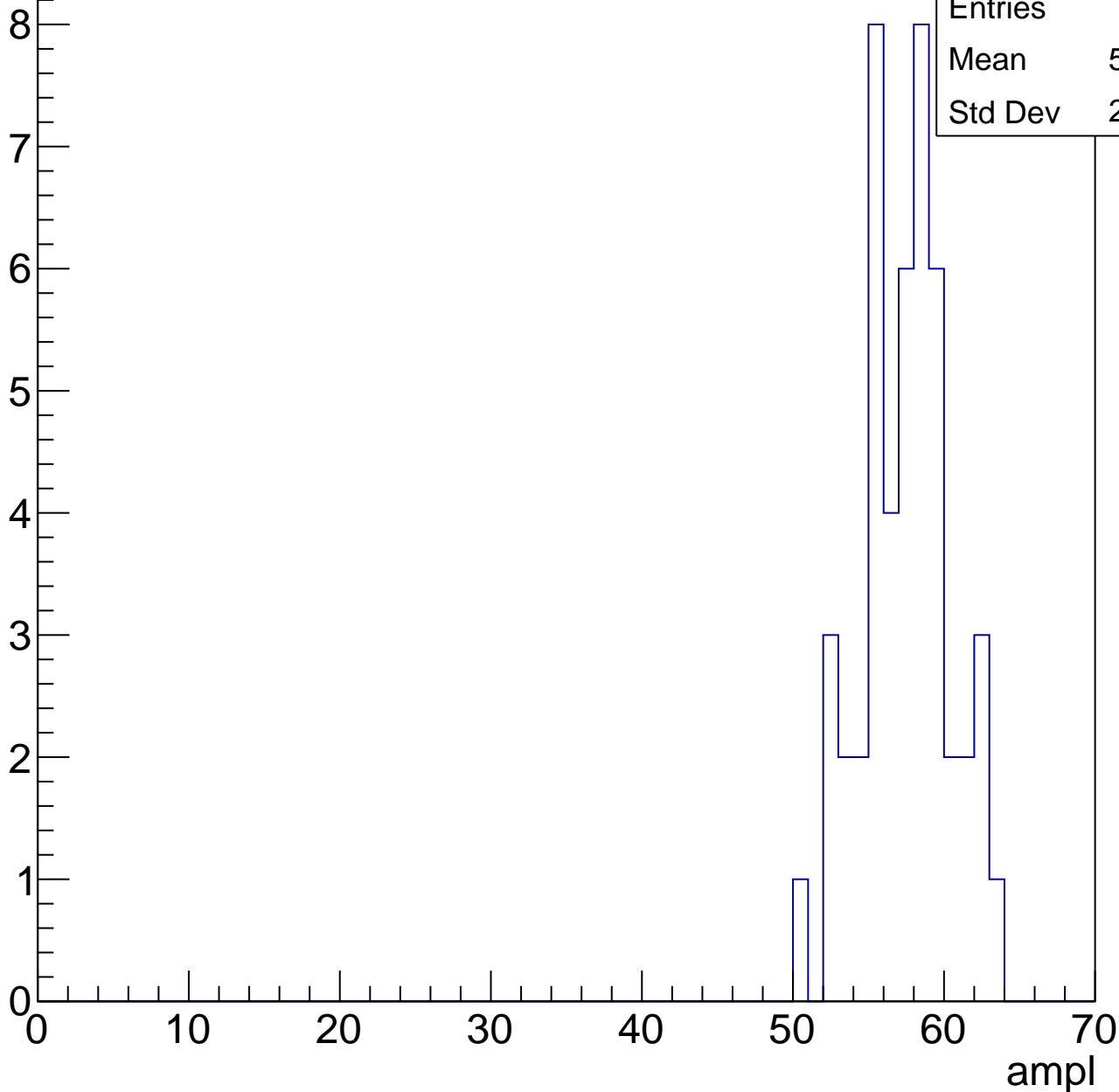


# B1L002S, U8-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

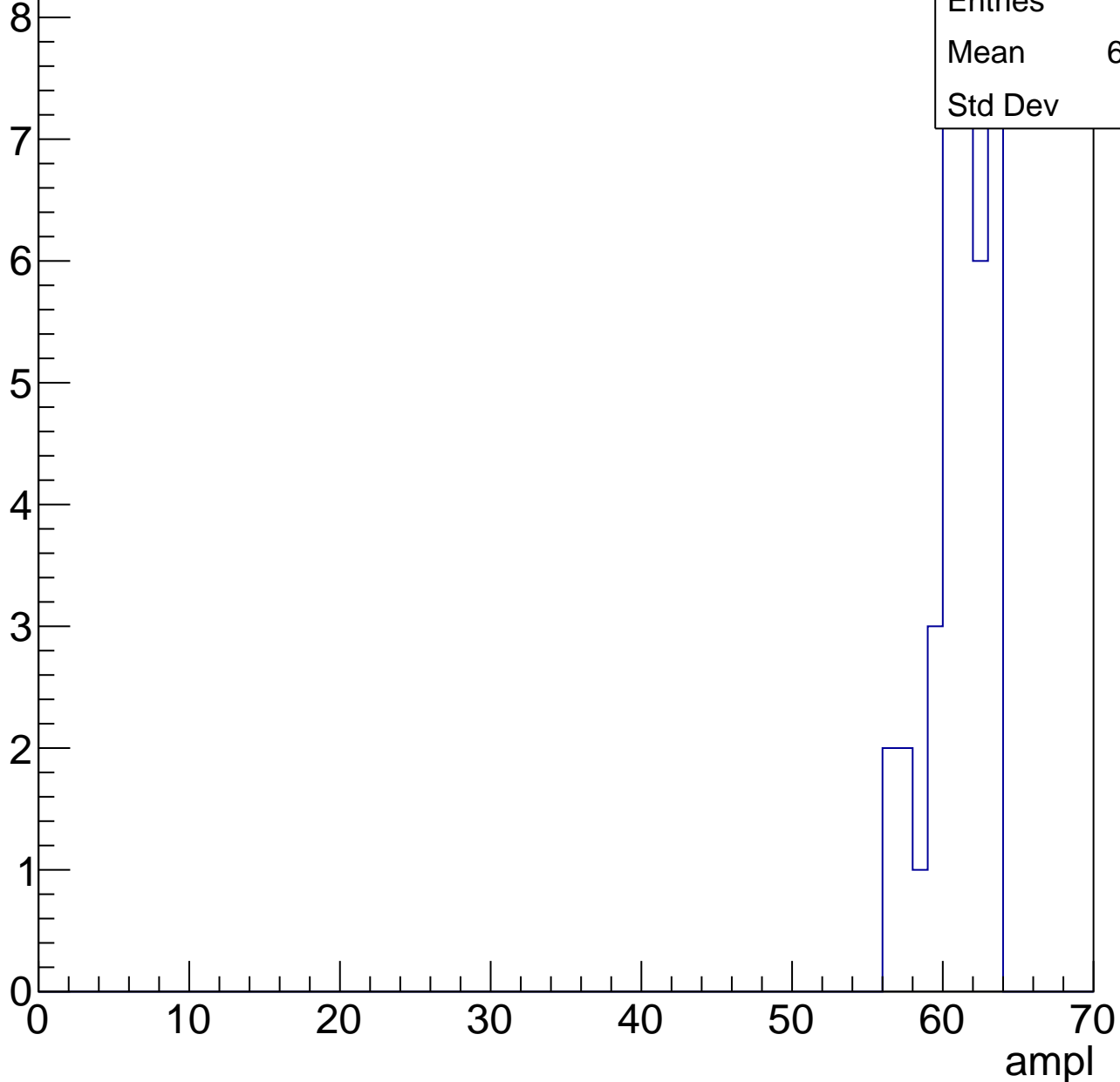
Entries	48
Mean	56.98
Std Dev	2.897



# B1L002S, U8-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

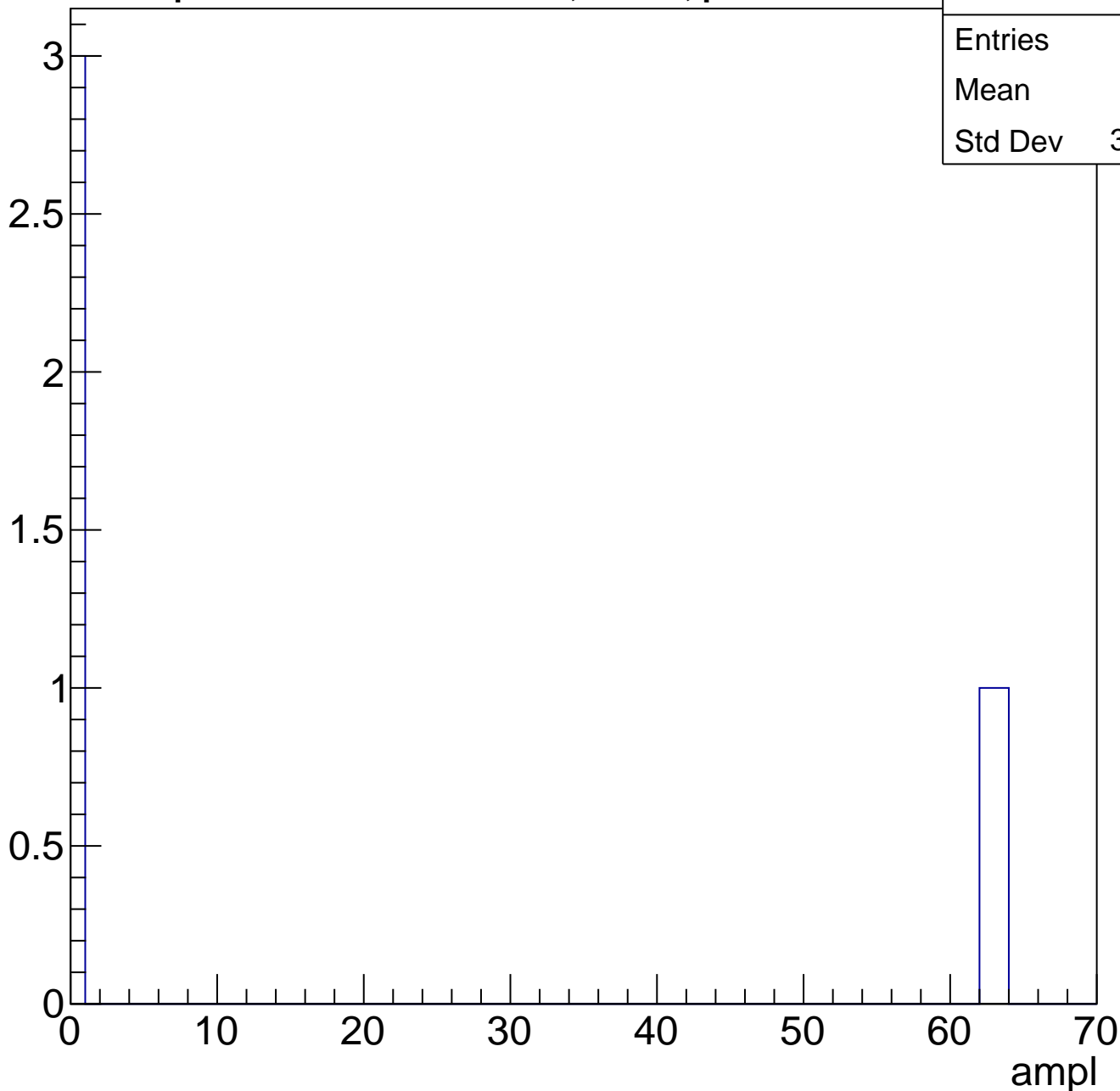




# B1L002S, U8-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	25
Std Dev	30.62

# B1L002S, U8-ch11, adc0

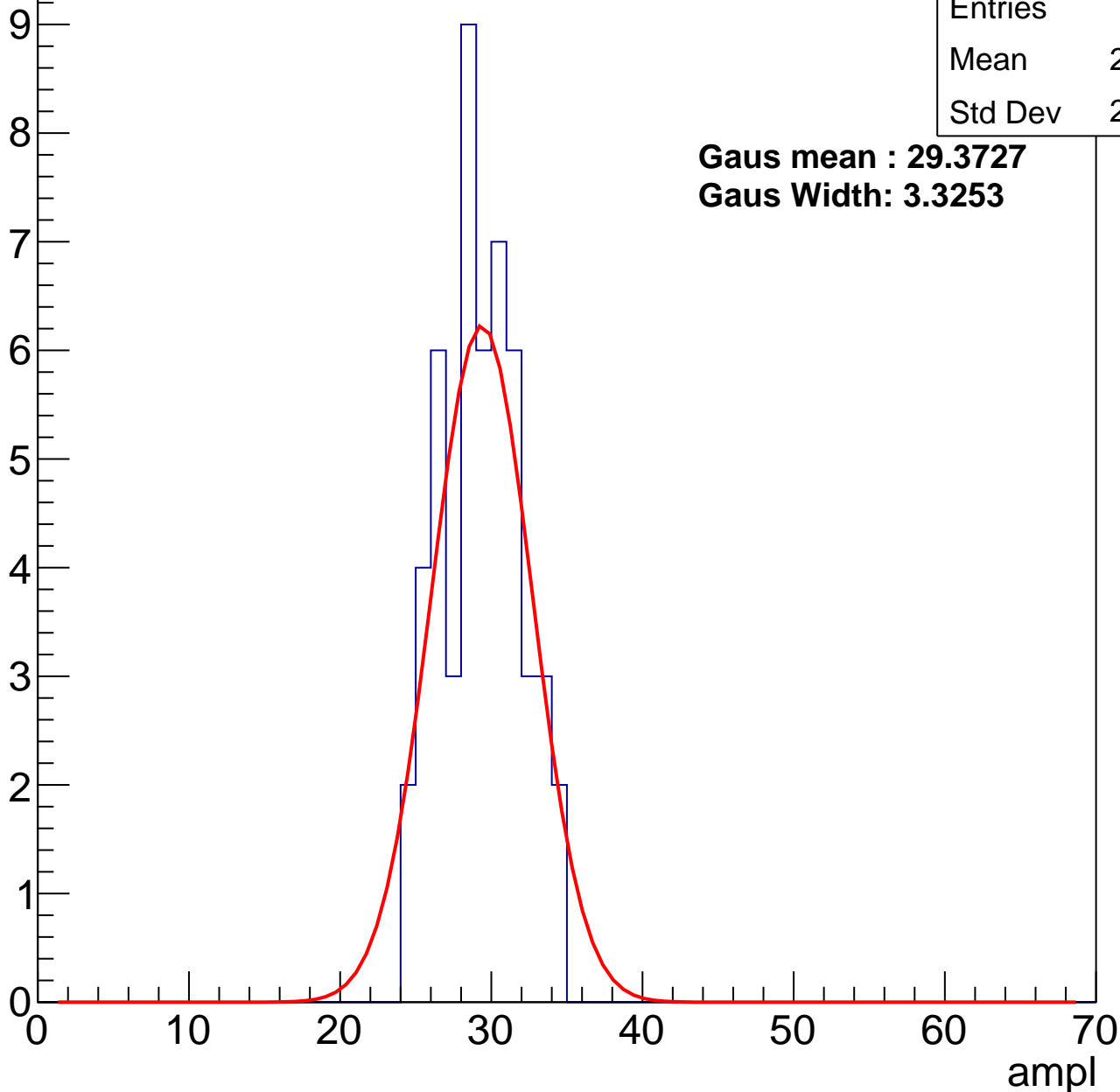
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	28.82
Std Dev	2.595

**Gaus mean : 29.3727**

**Gaus Width: 3.3253**



# B1L002S, U8-ch11, adc1

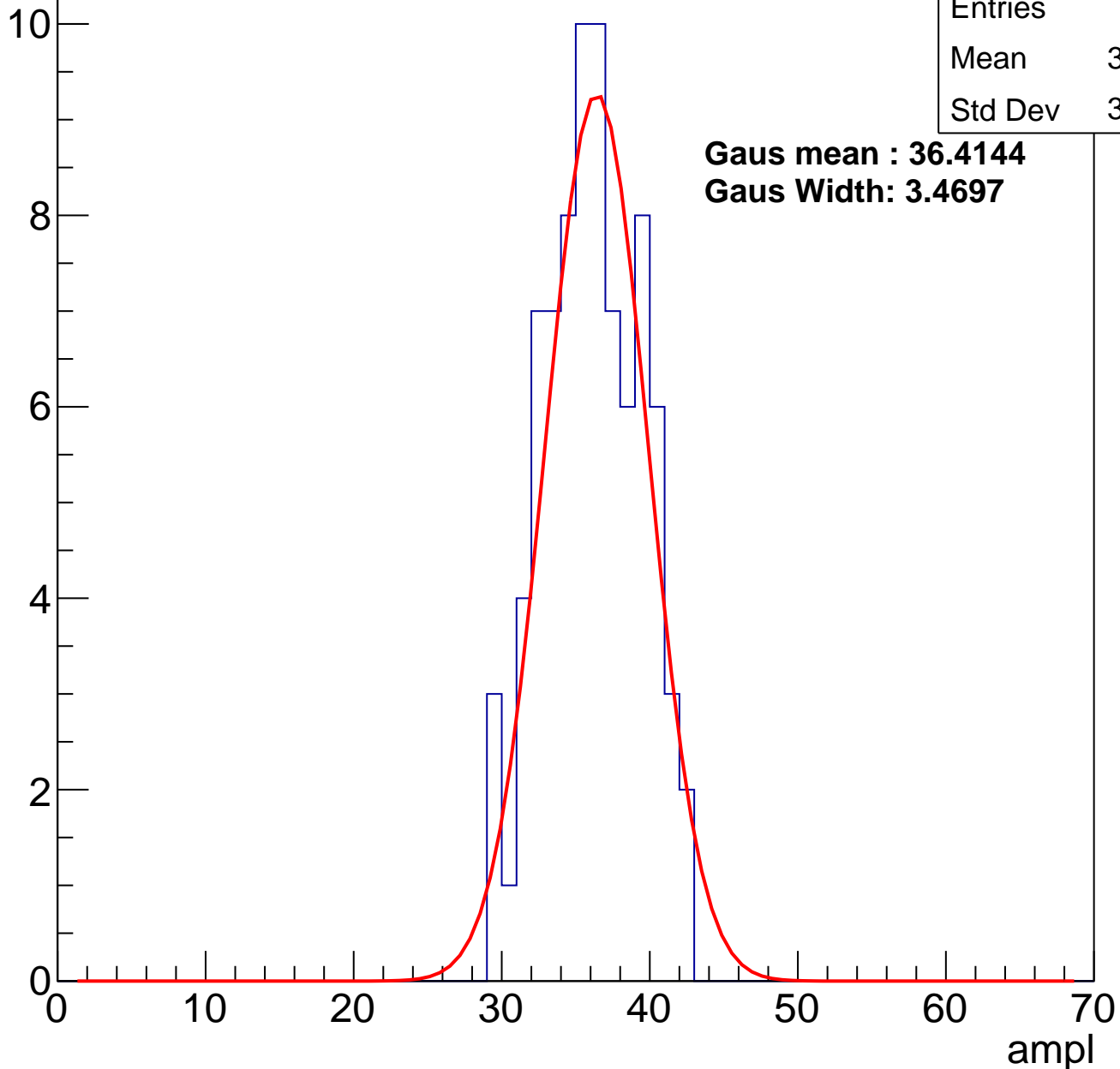
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	35.66
Std Dev	3.194

**Gaus mean : 36.4144**

**Gaus Width: 3.4697**

Entry



# B1L002S, U8-ch11, adc2

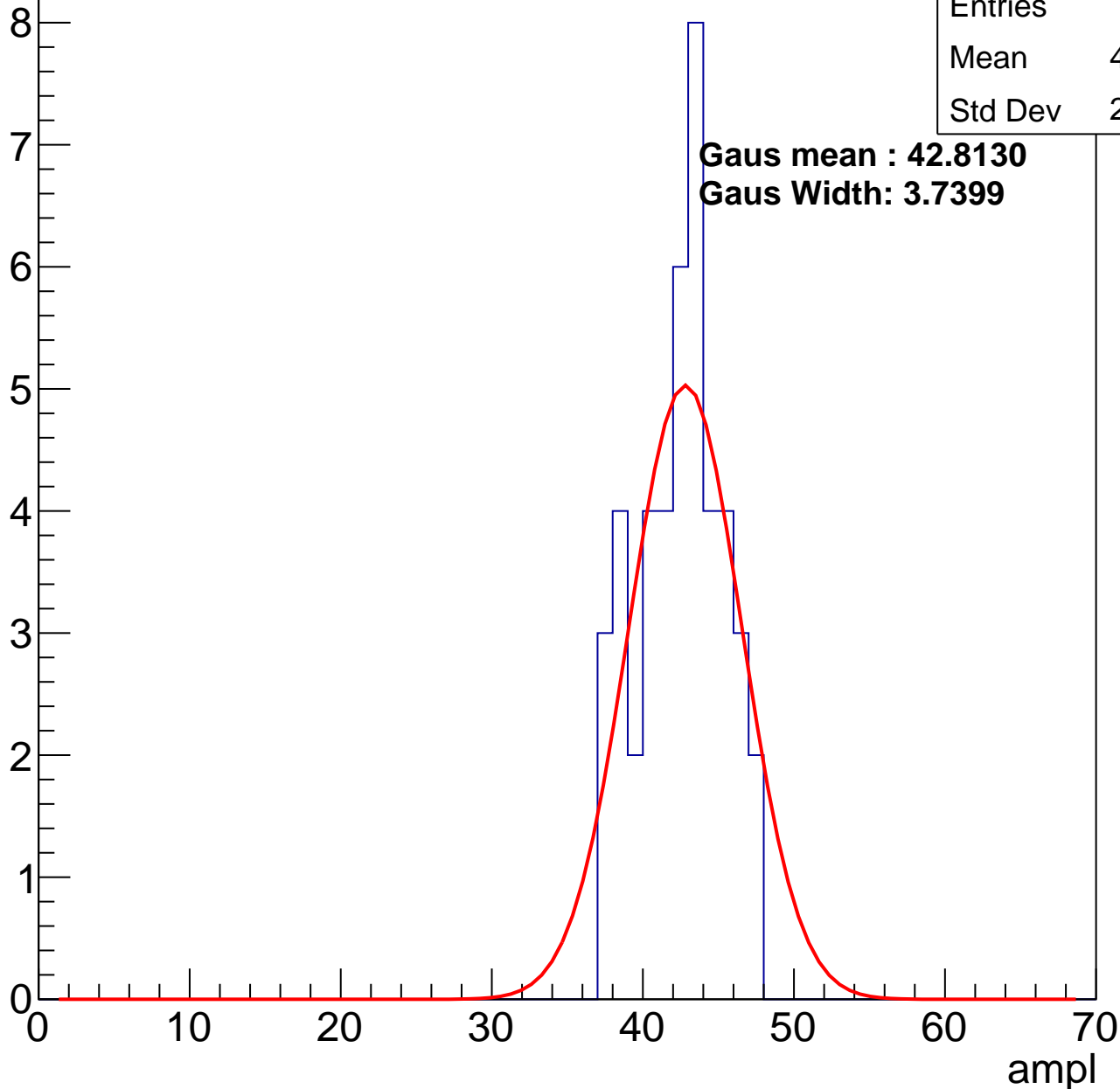
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	42.02
Std Dev	2.759

**Gaus mean : 42.8130**

**Gaus Width: 3.7399**



# B1L002S, U8-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	47.48
Std Dev	3.183

Entry

10

8

6

4

2

0

0

10

20

30

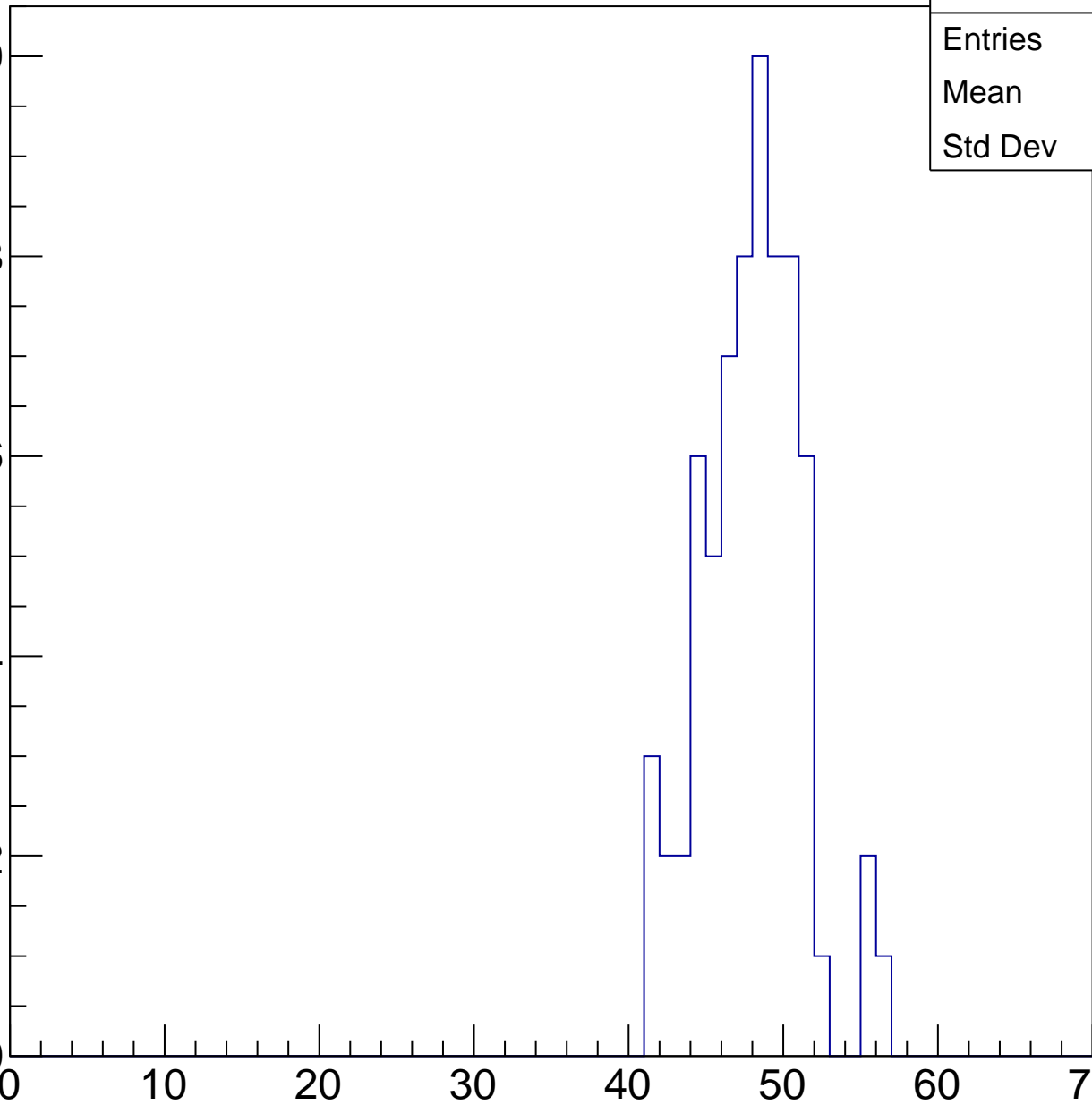
40

50

60

70

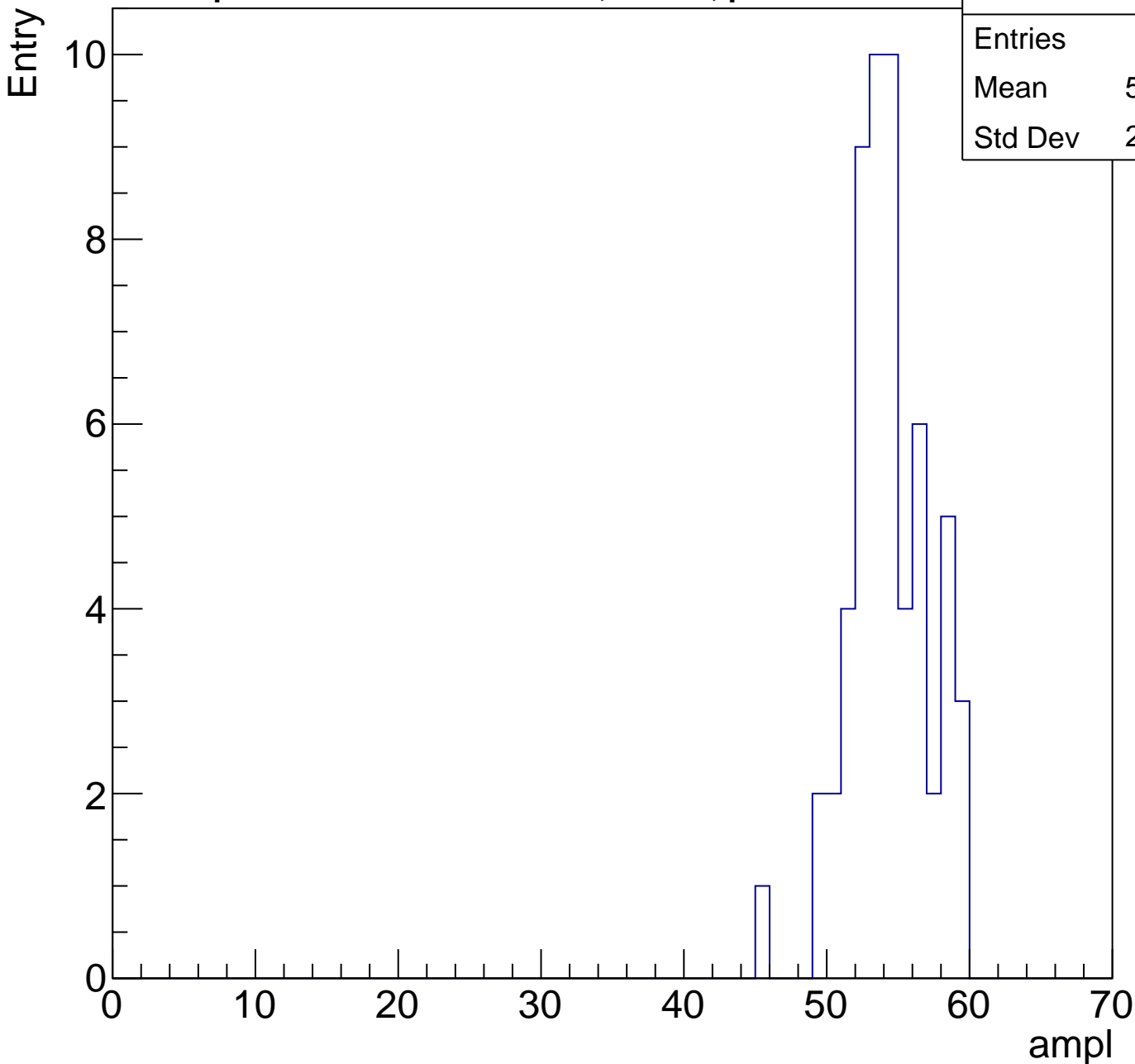
ampl



# B1L002S, U8-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	53.83
Std Dev	2.768

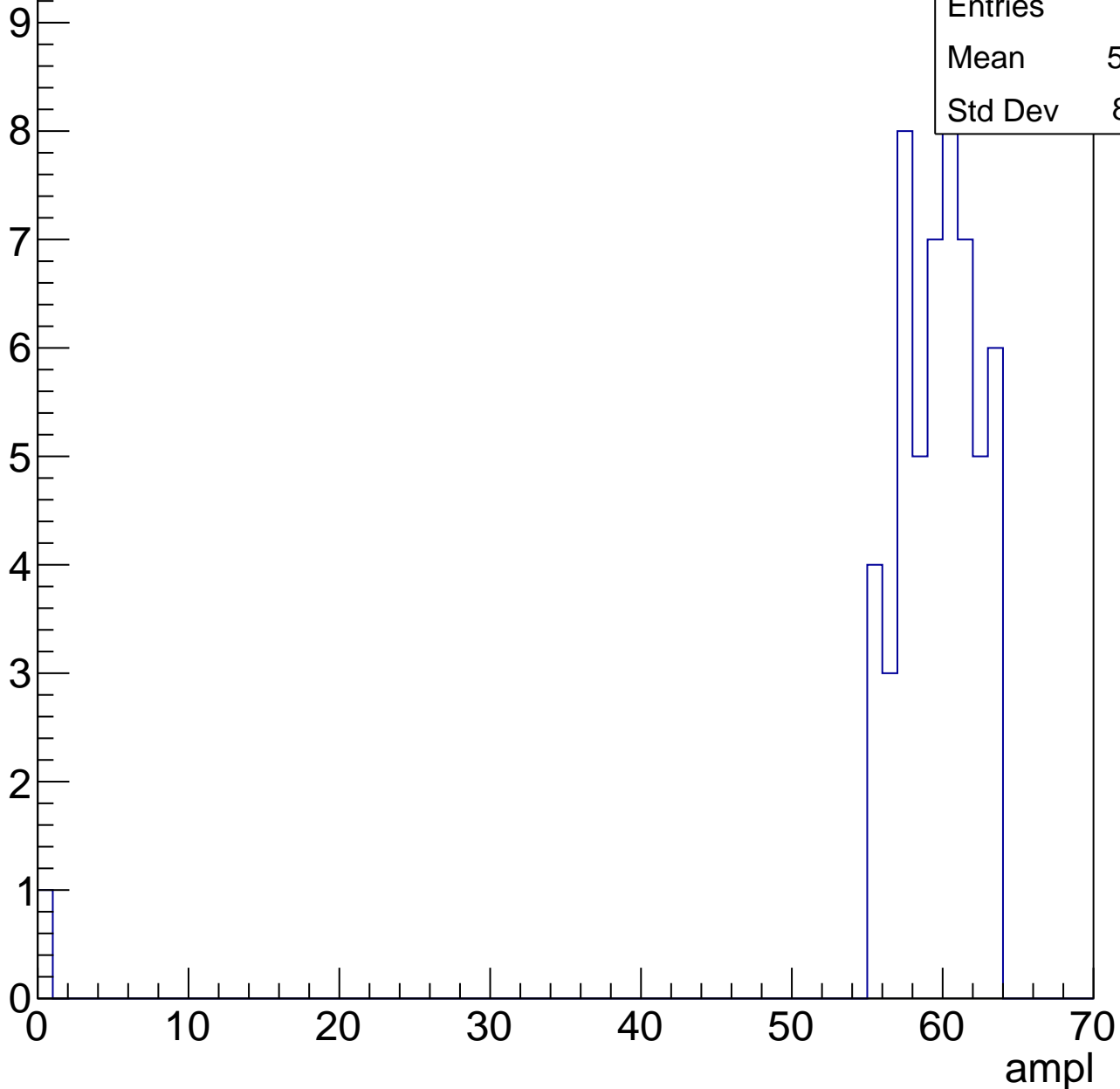


# B1L002S, U8-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	58.22
Std Dev	8.261

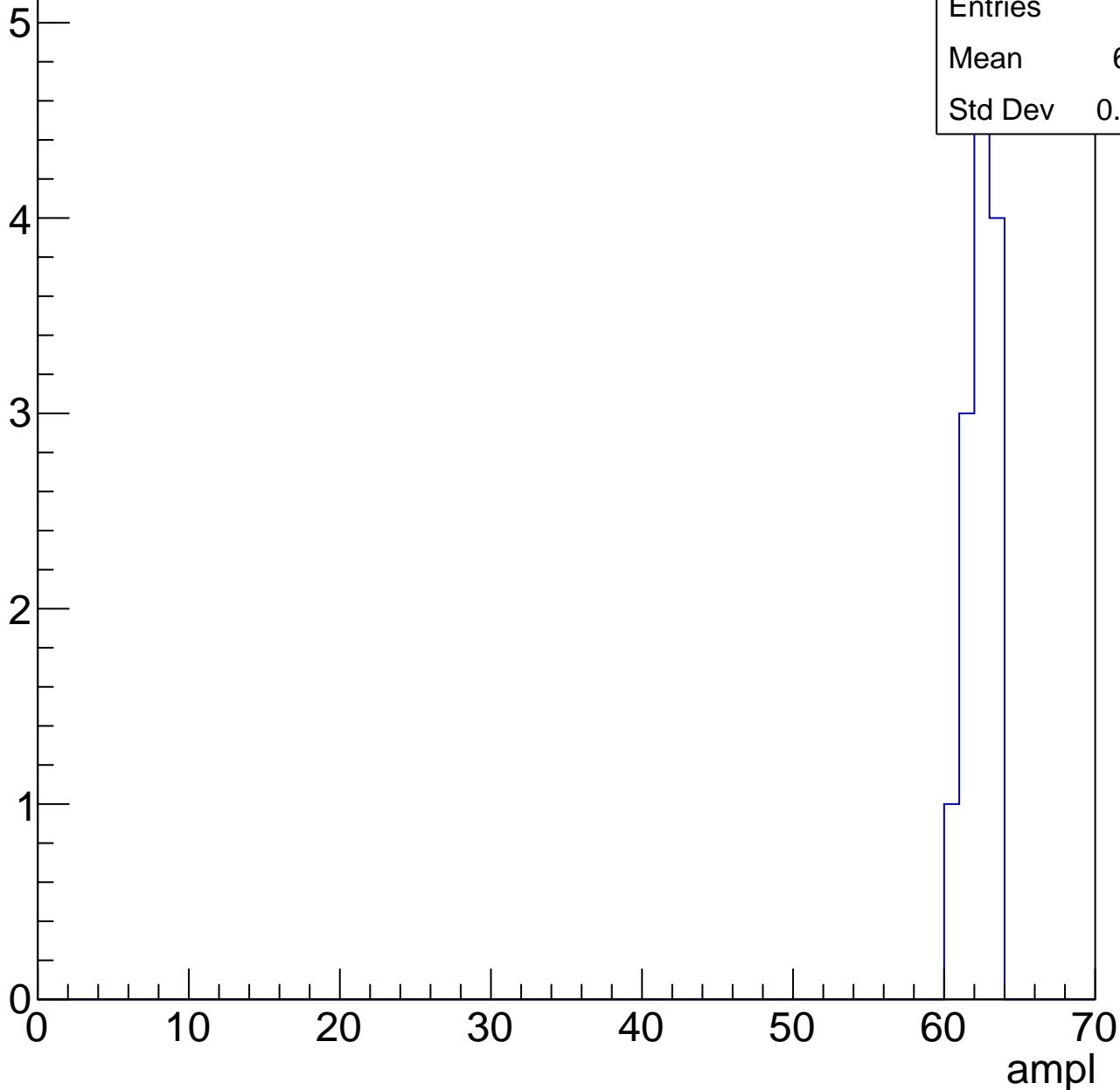


# B1L002S, U8-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.92
Std Dev	0.9166





# B1L002S, U8-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B1L002S, U8-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	89
Mean	27.76
Std Dev	3.247

**Gaus mean : 28.3721**

**Gaus Width: 3.2063**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

# B1L002S, U8-ch12, adc1

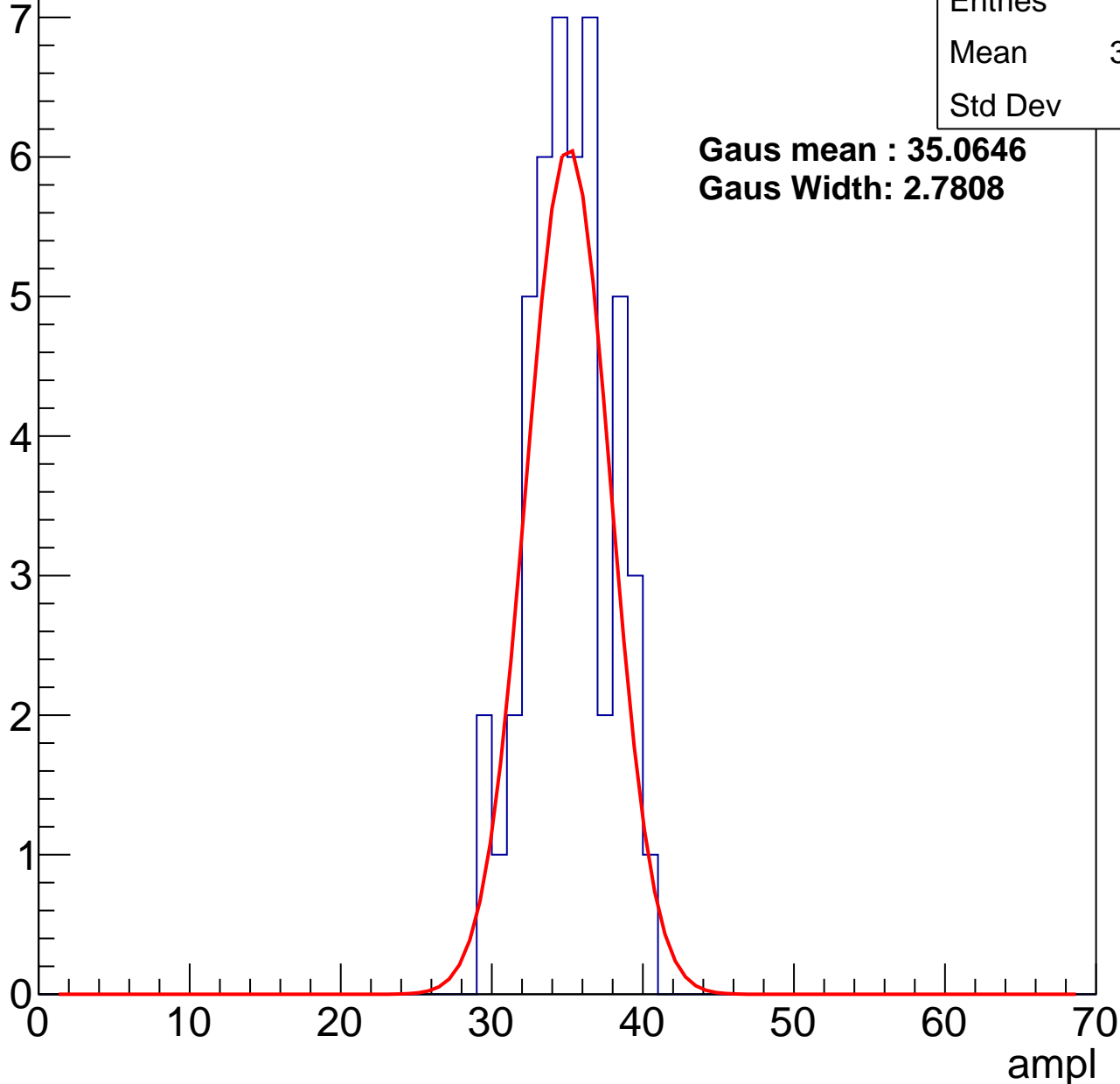
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	34.66
Std Dev	2.66

**Gaus mean : 35.0646**

**Gaus Width: 2.7808**



# B1L002S, U8-ch12, adc2

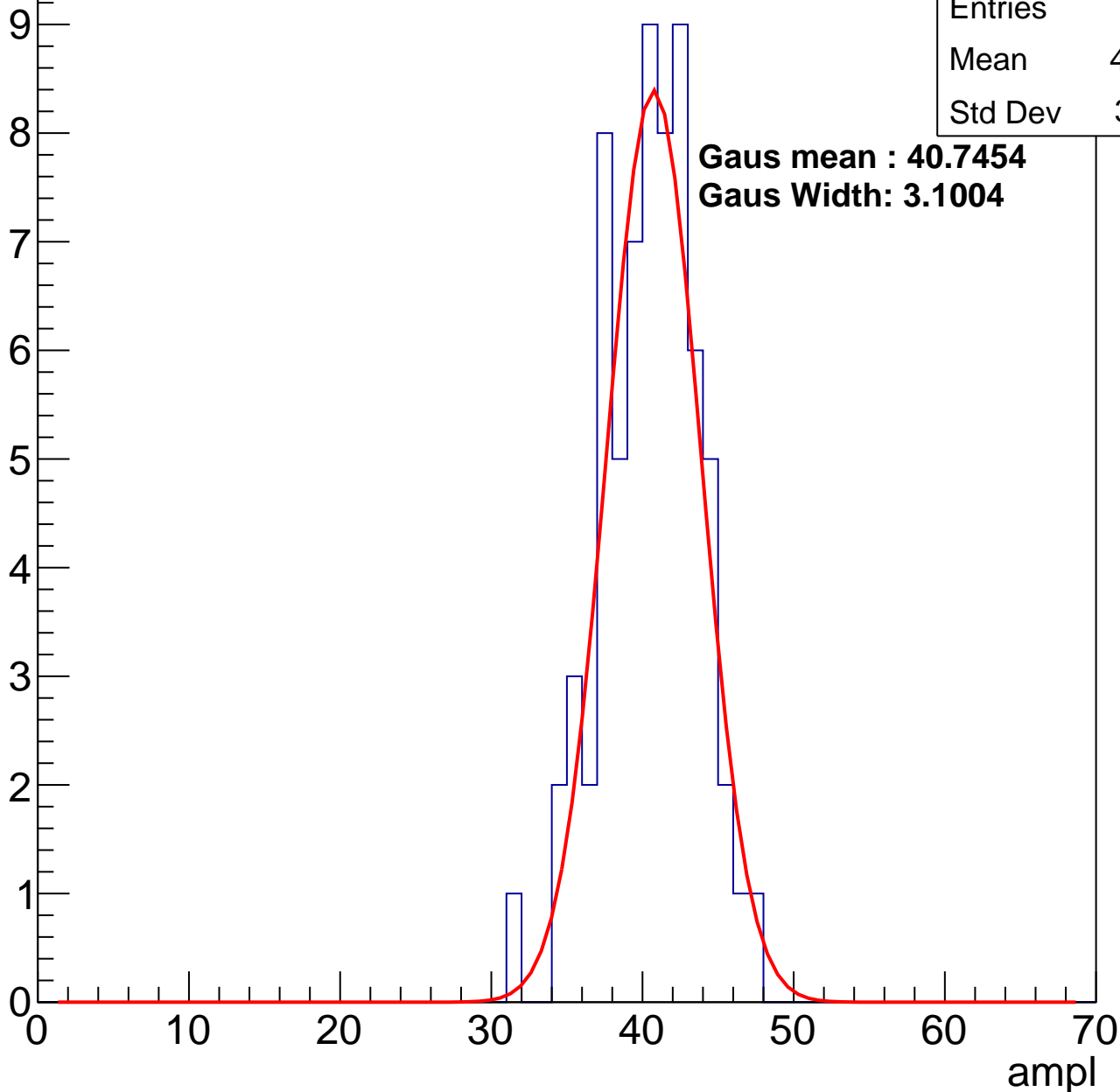
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.03
Std Dev	3.121

**Gaus mean : 40.7454**

**Gaus Width: 3.1004**

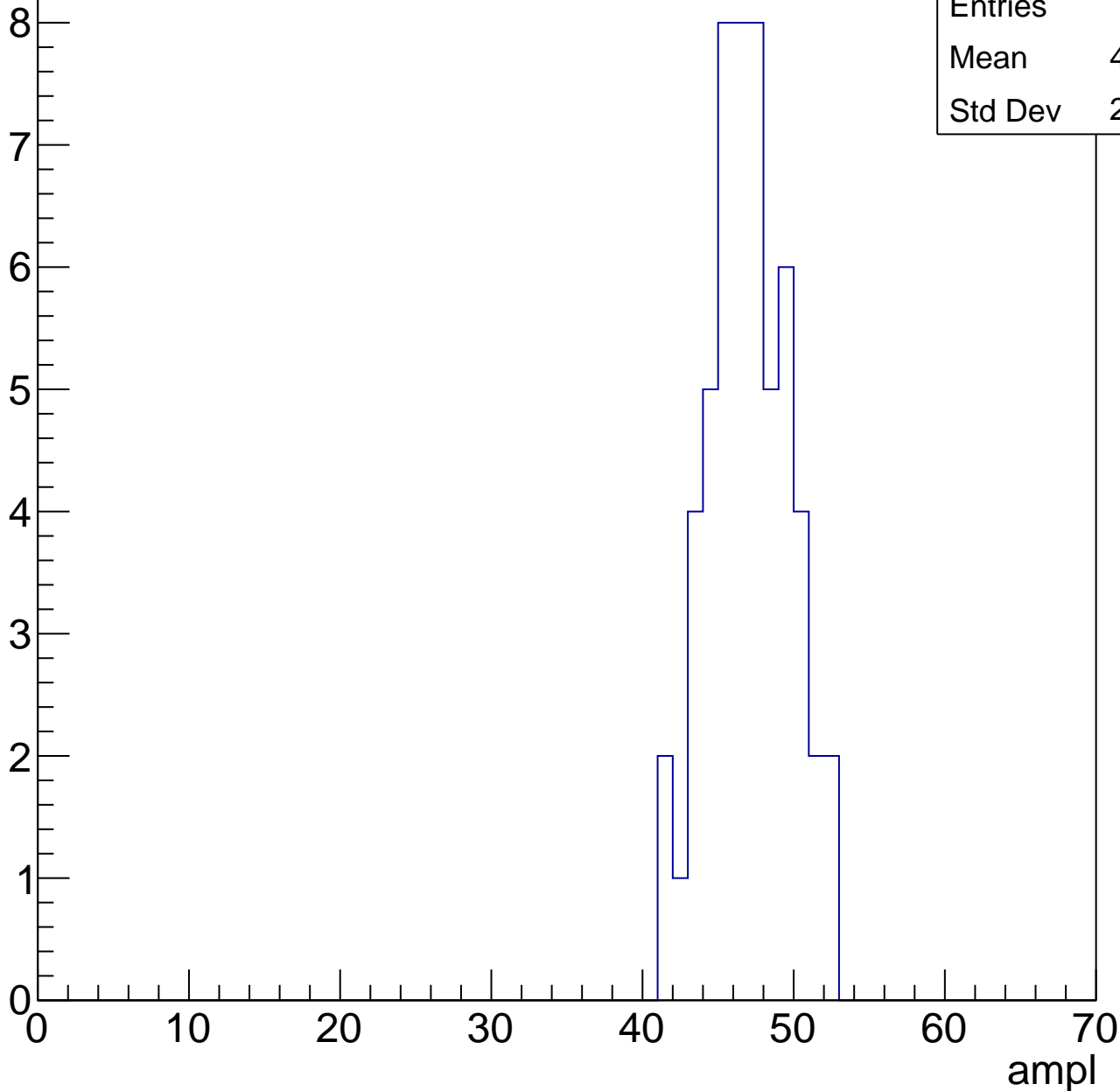


# B1L002S, U8-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.55
Std Dev	2.634

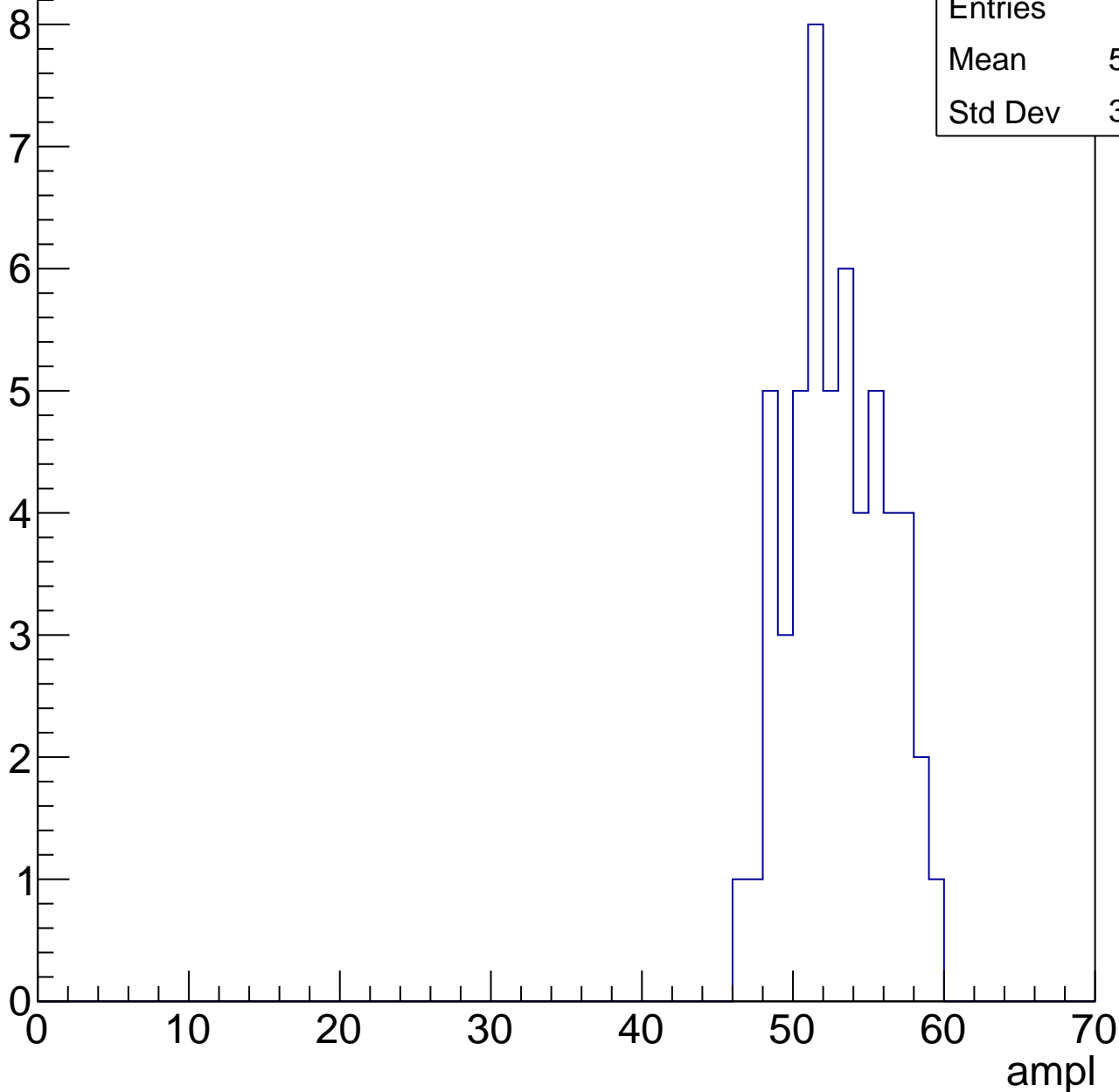


# B1L002S, U8-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	52.48
Std Dev	3.149

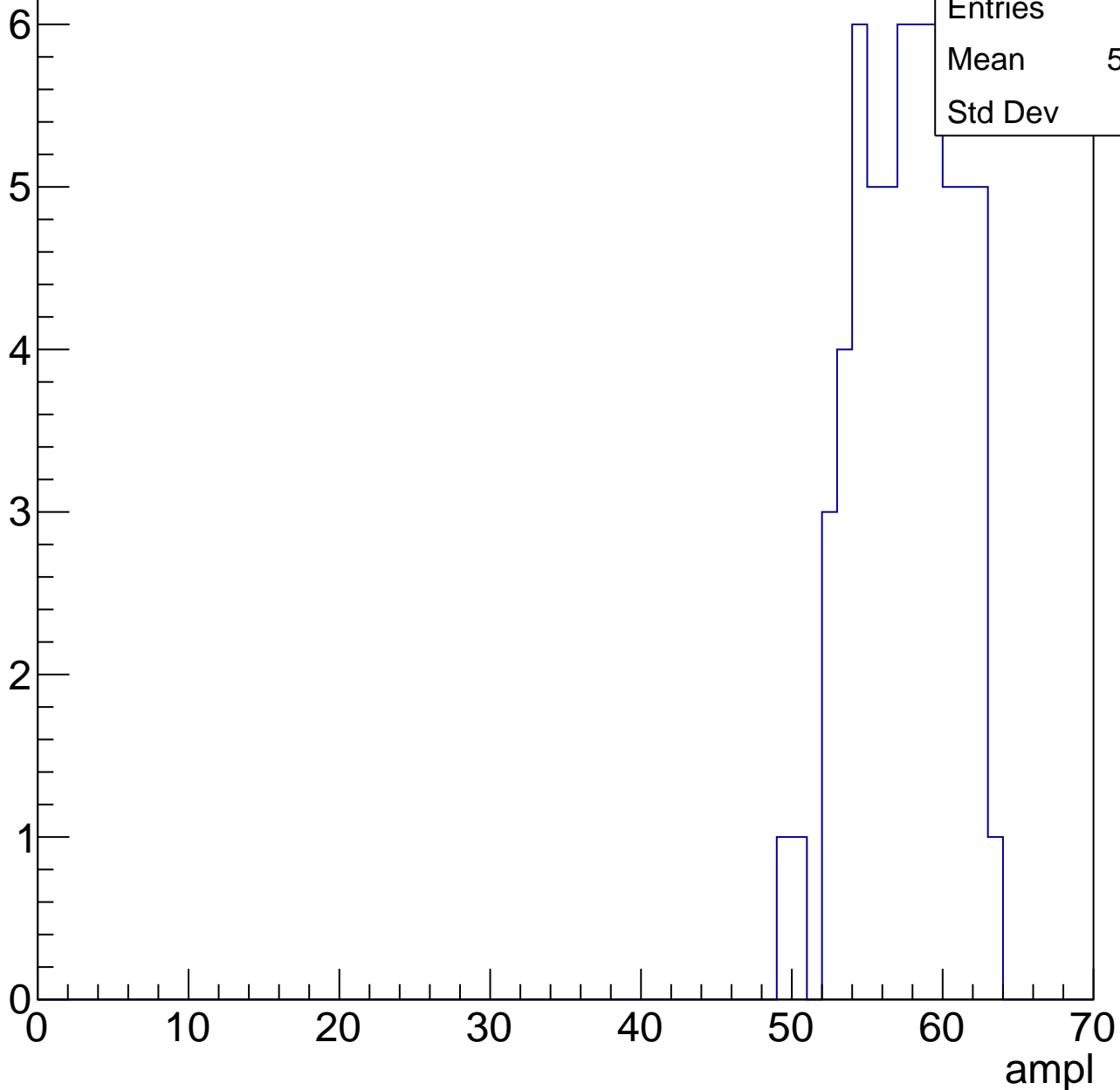


# B1L002S, U8-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	57.08
Std Dev	3.31

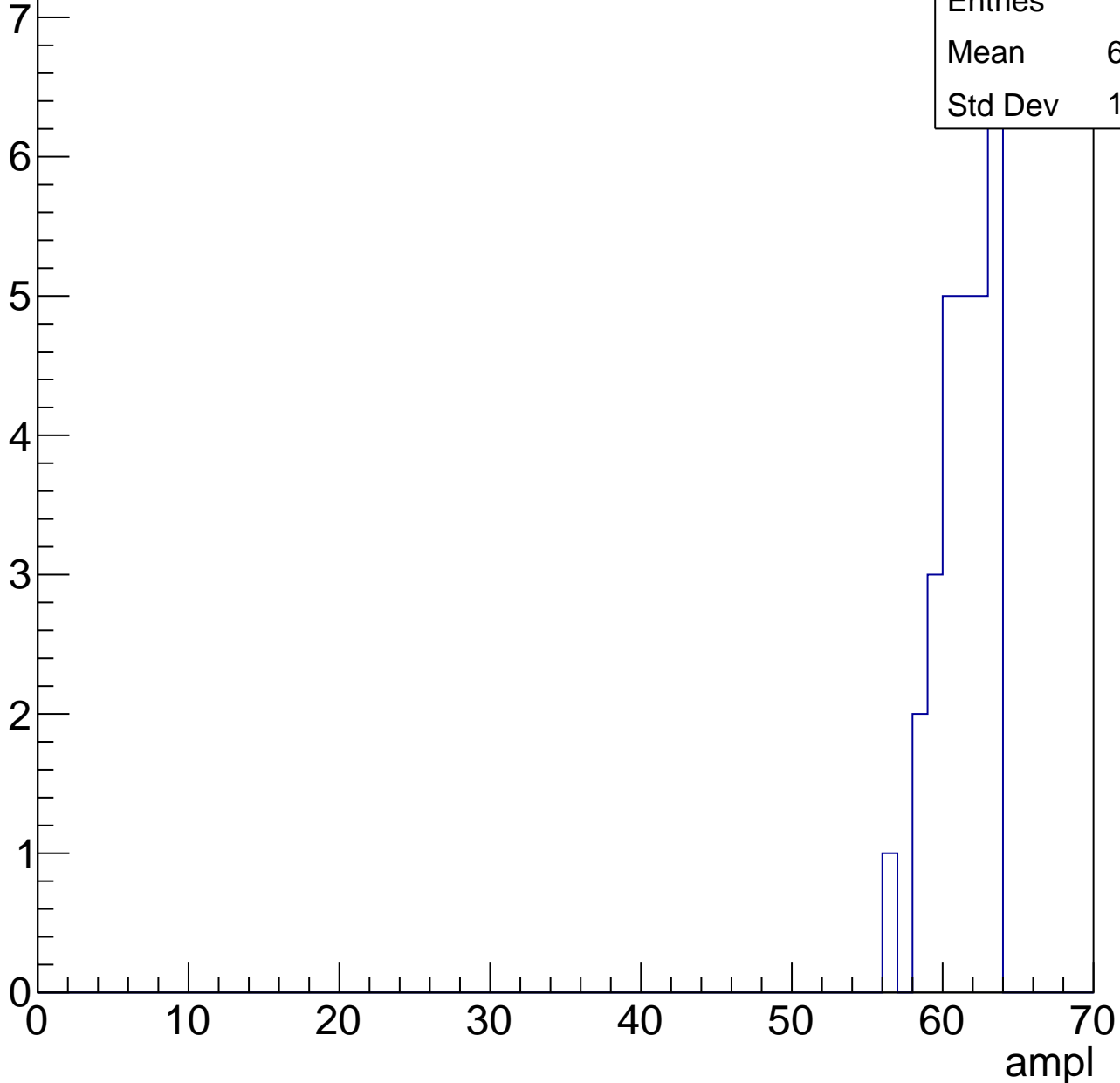


# B1L002S, U8-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	60.89
Std Dev	1.819

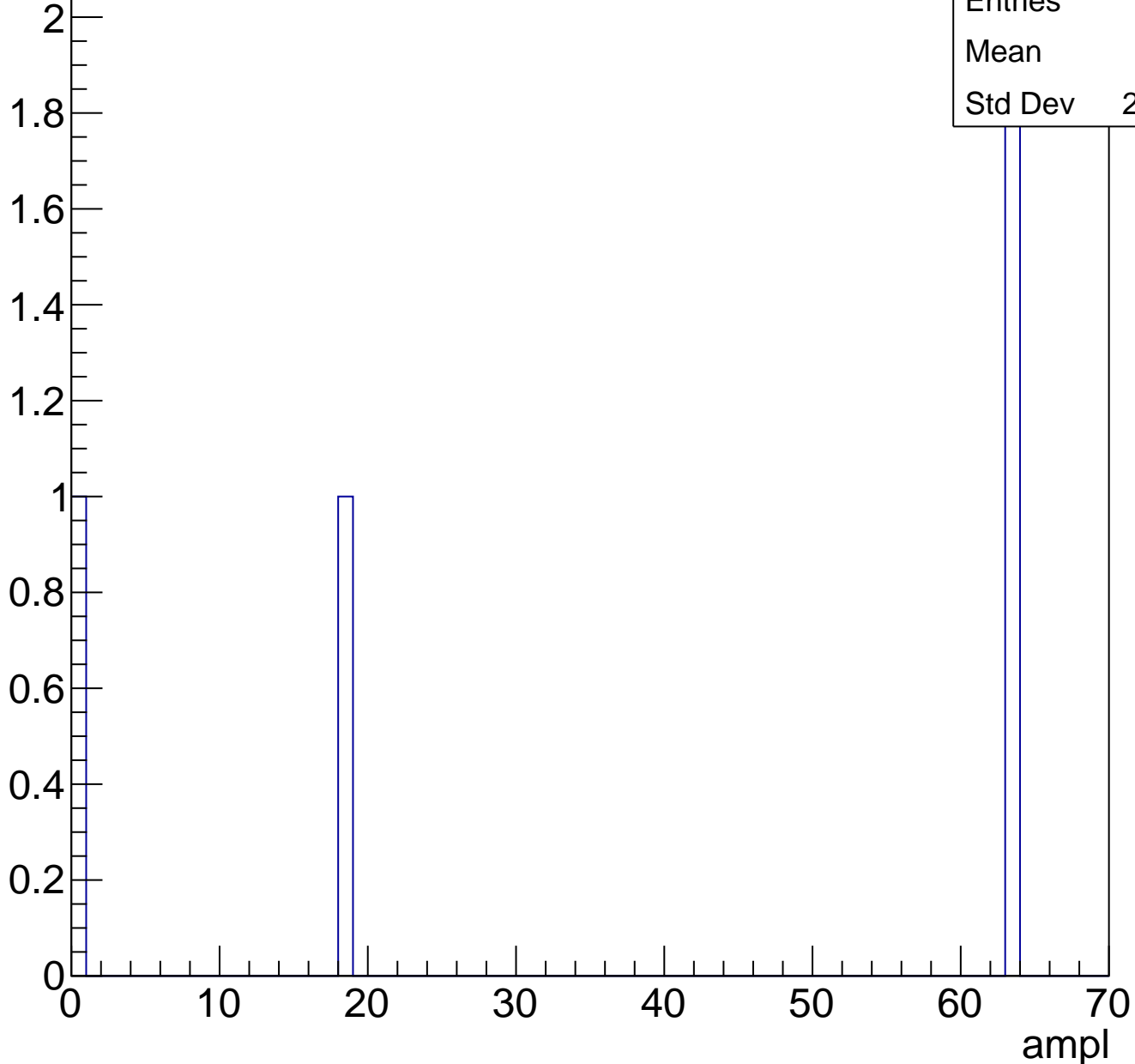




# B1L002S, U8-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	36
Std Dev	27.74

# B1L002S, U8-ch13, adc0

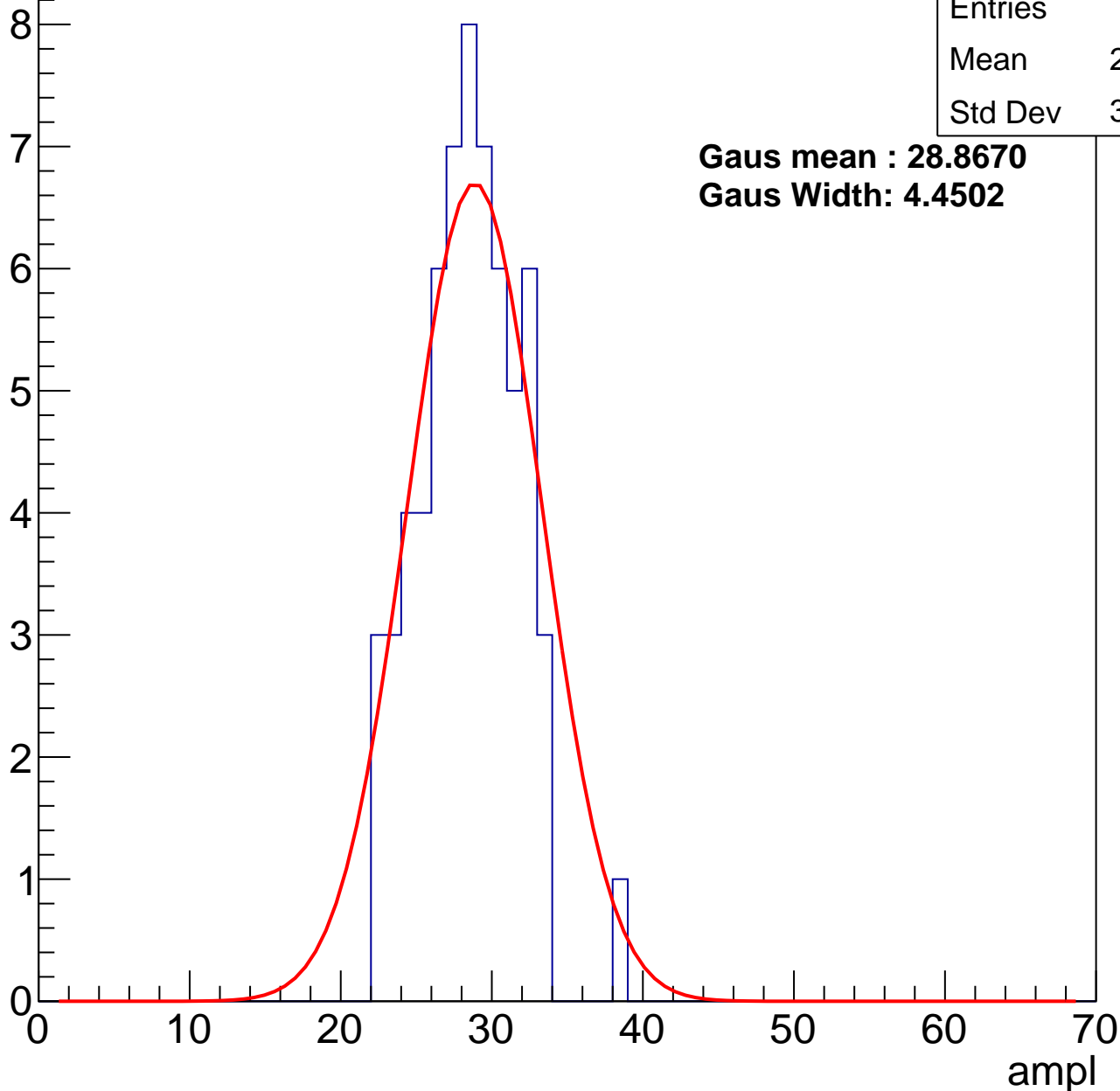
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.05
Std Dev	3.239

**Gaus mean : 28.8670**

**Gaus Width: 4.4502**



# B1L002S, U8-ch13, adc1

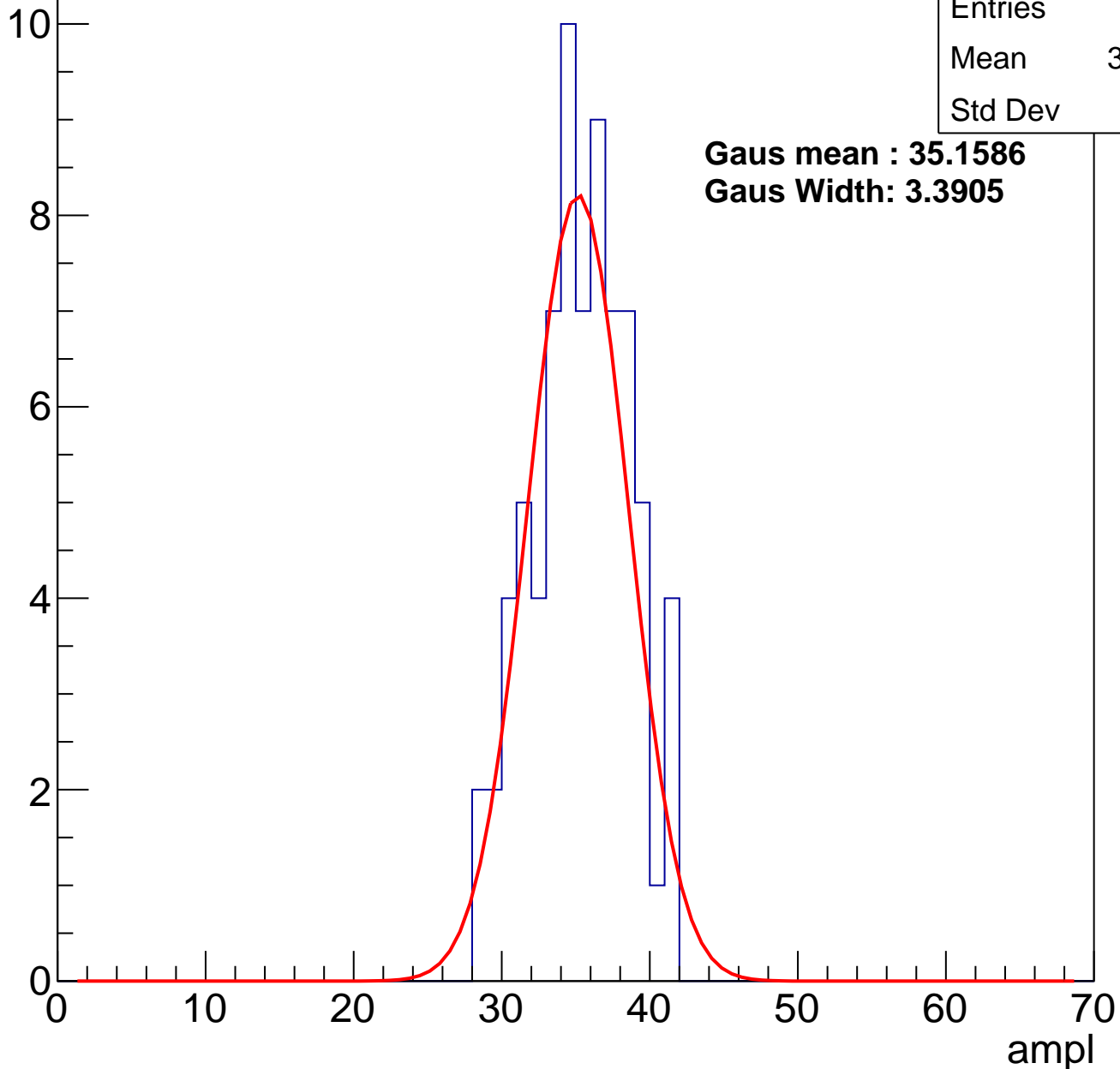
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	34.88
Std Dev	3.23

**Gaus mean : 35.1586**

**Gaus Width: 3.3905**

Entry



# B1L002S, U8-ch13, adc2

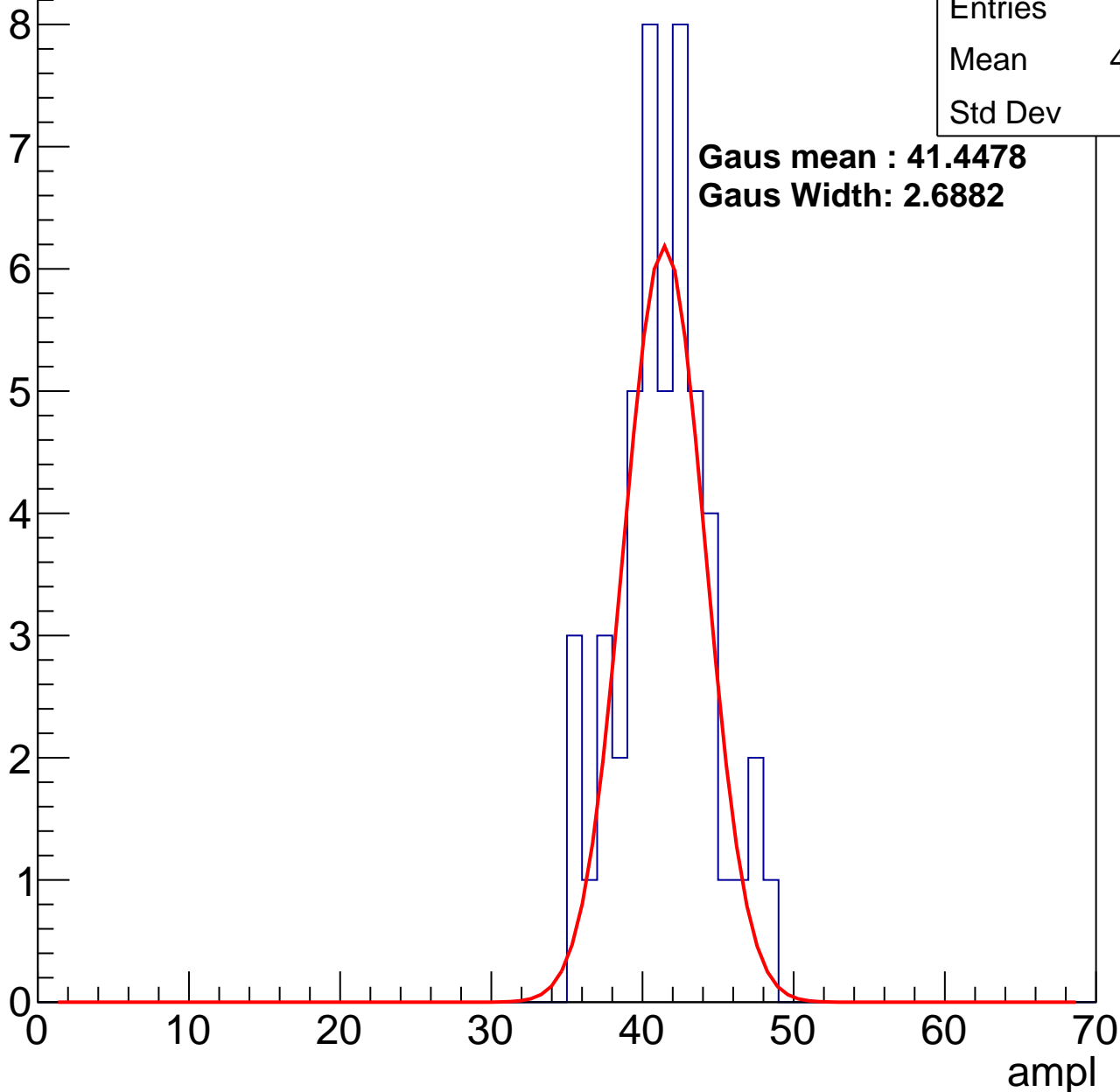
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.98
Std Dev	3.04

**Gaus mean : 41.4478**

**Gaus Width: 2.6882**

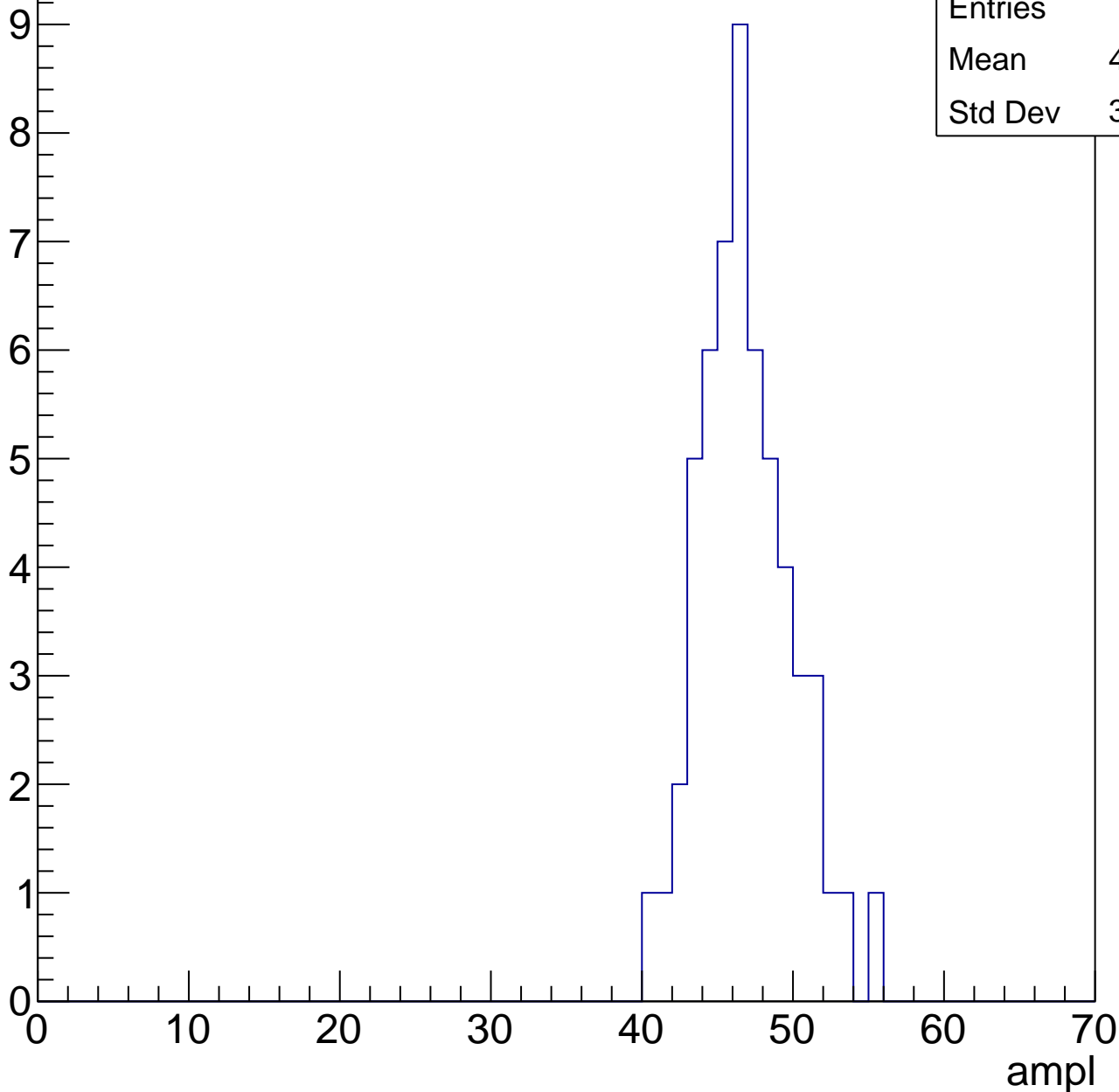


# B1L002S, U8-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

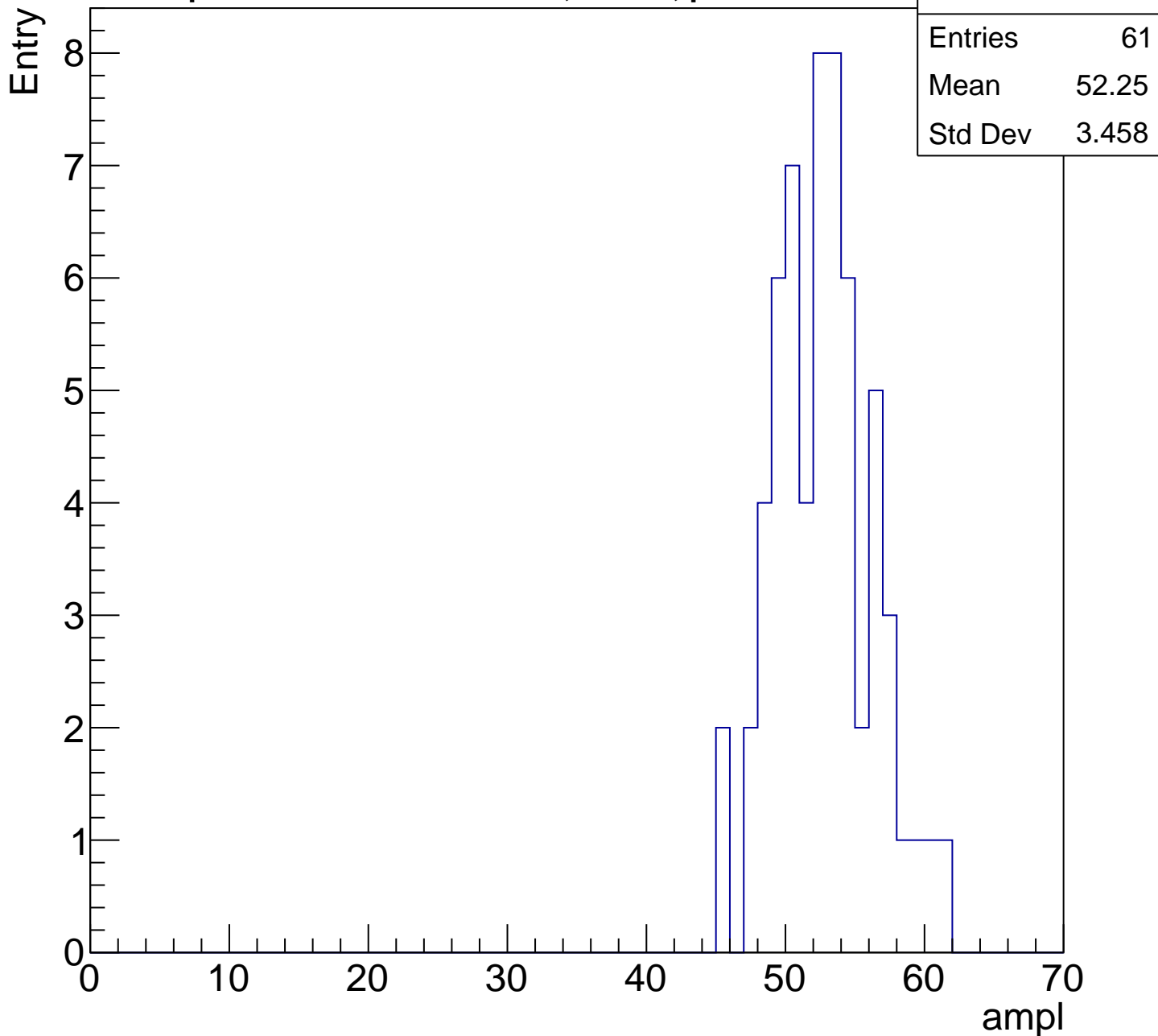
Entry

Entries	55
Mean	46.44
Std Dev	3.044



# B1L002S, U8-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch13, adc5

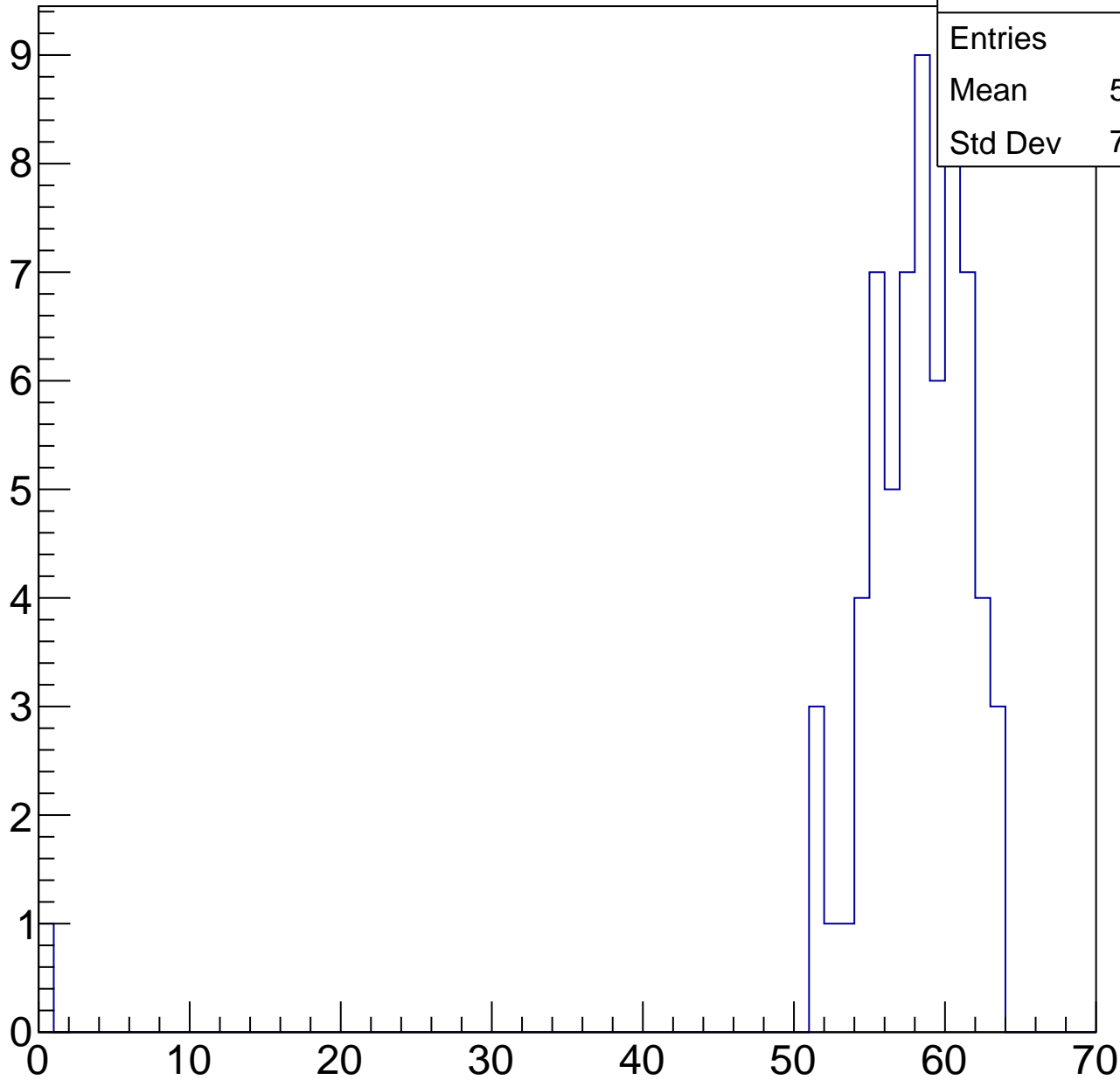
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	66
Mean	56.94
Std Dev	7.679

ampl

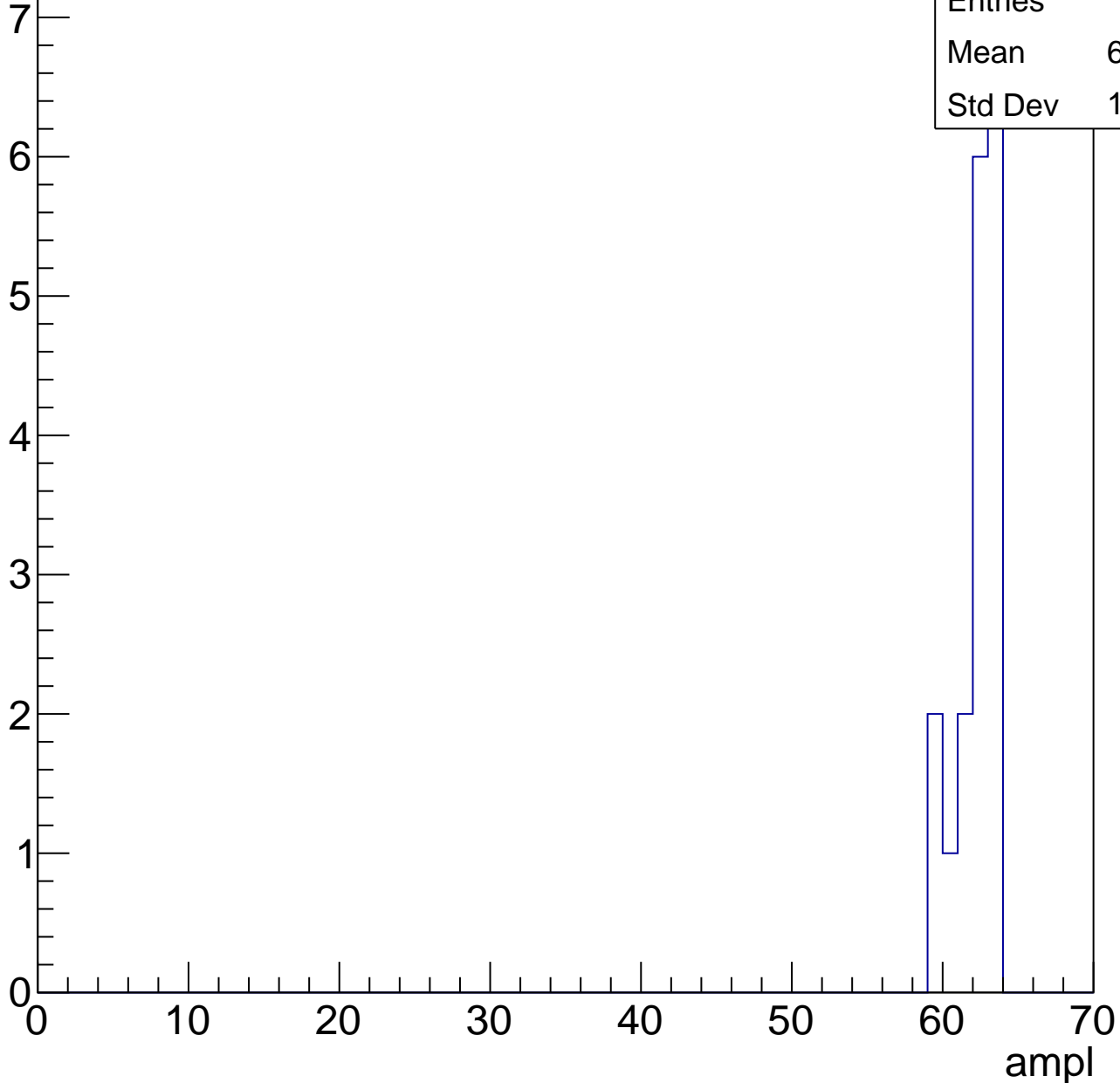


# B1L002S, U8-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.83
Std Dev	1.302

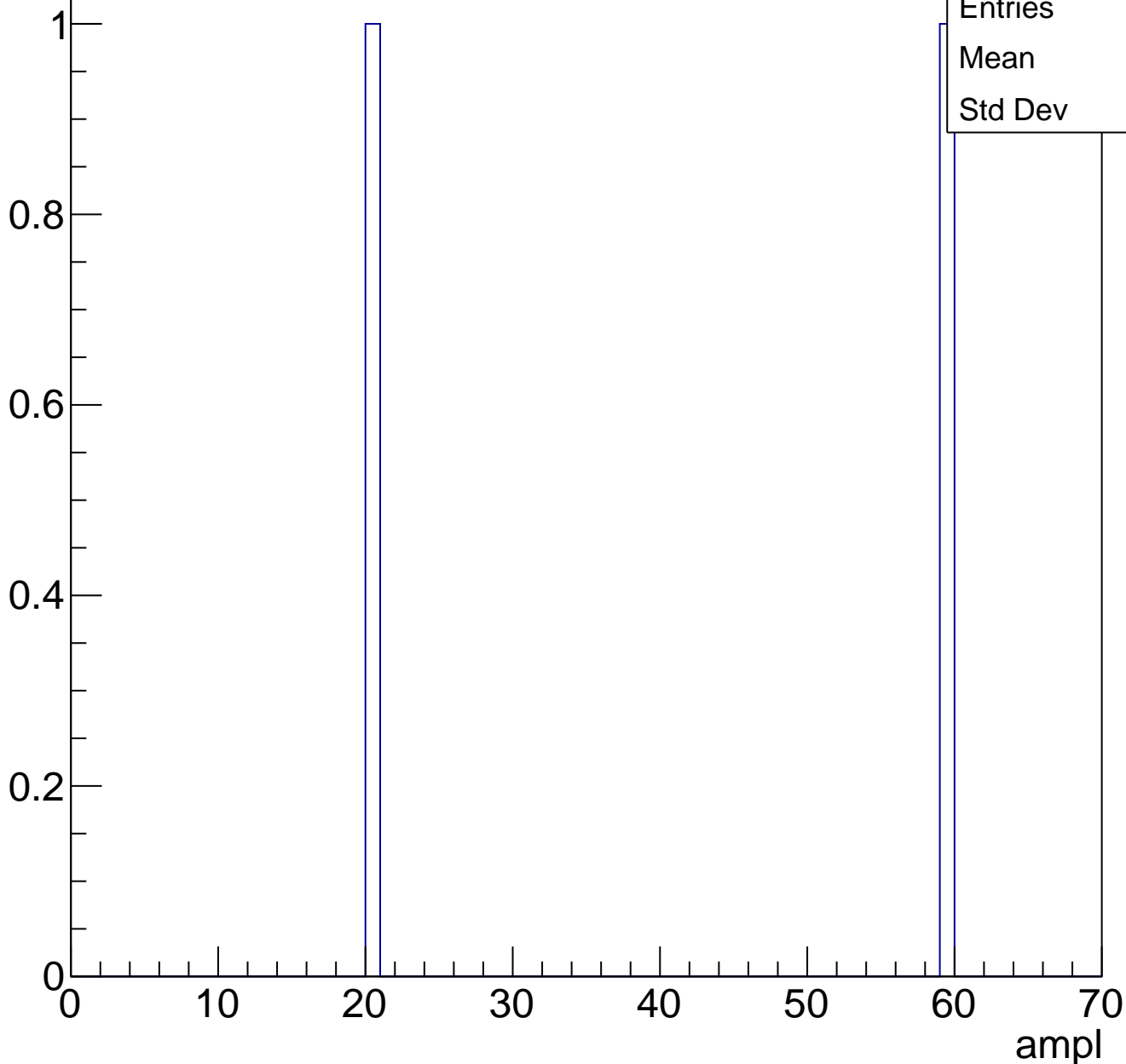




# B1L002S, U8-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch14, adc0

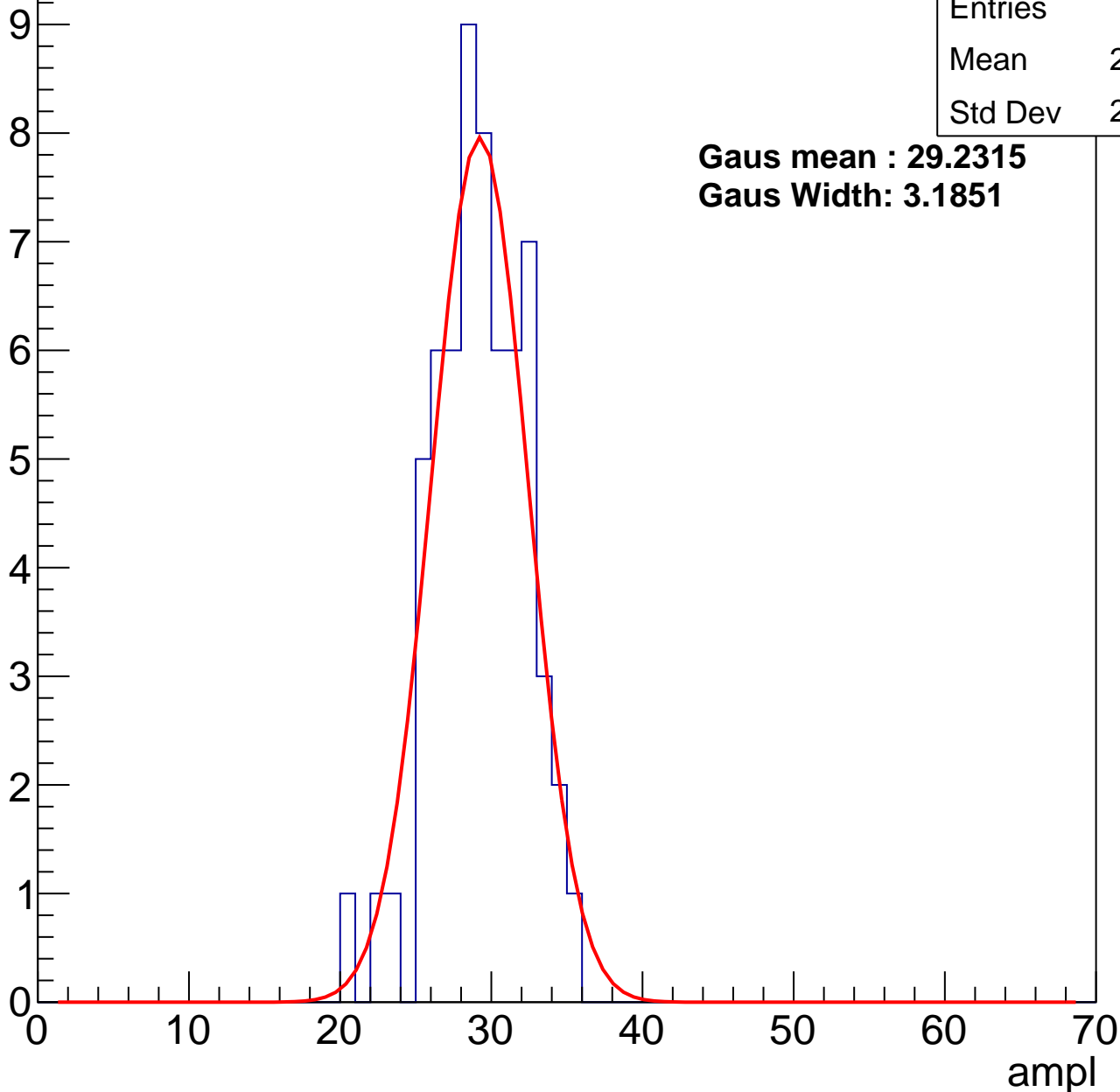
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.77
Std Dev	2.997

**Gaus mean : 29.2315**

**Gaus Width: 3.1851**



# B1L002S, U8-ch14, adc1

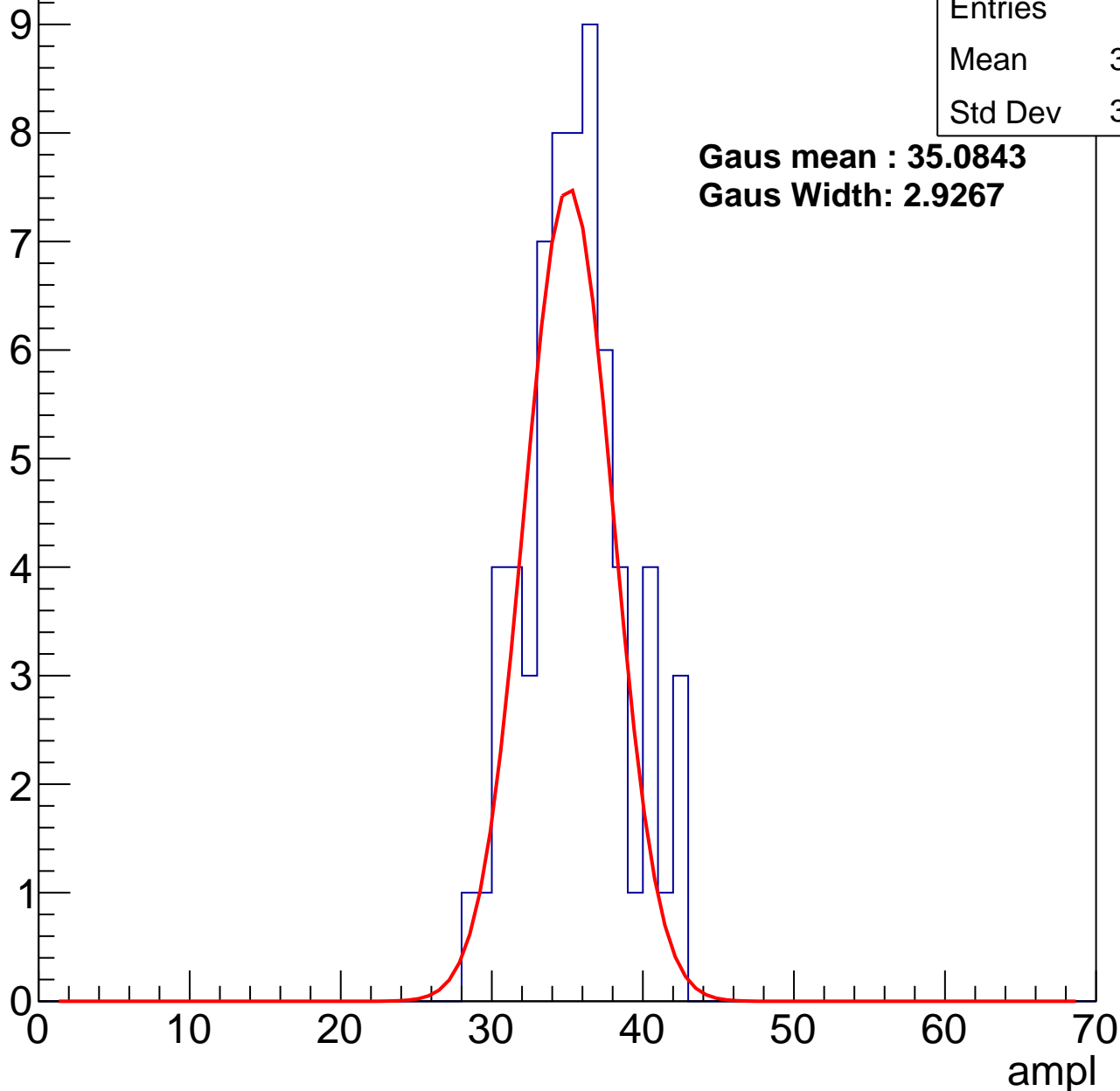
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.06
Std Dev	3.259

**Gaus mean : 35.0843**

**Gaus Width: 2.9267**



# B1L002S, U8-ch14, adc2

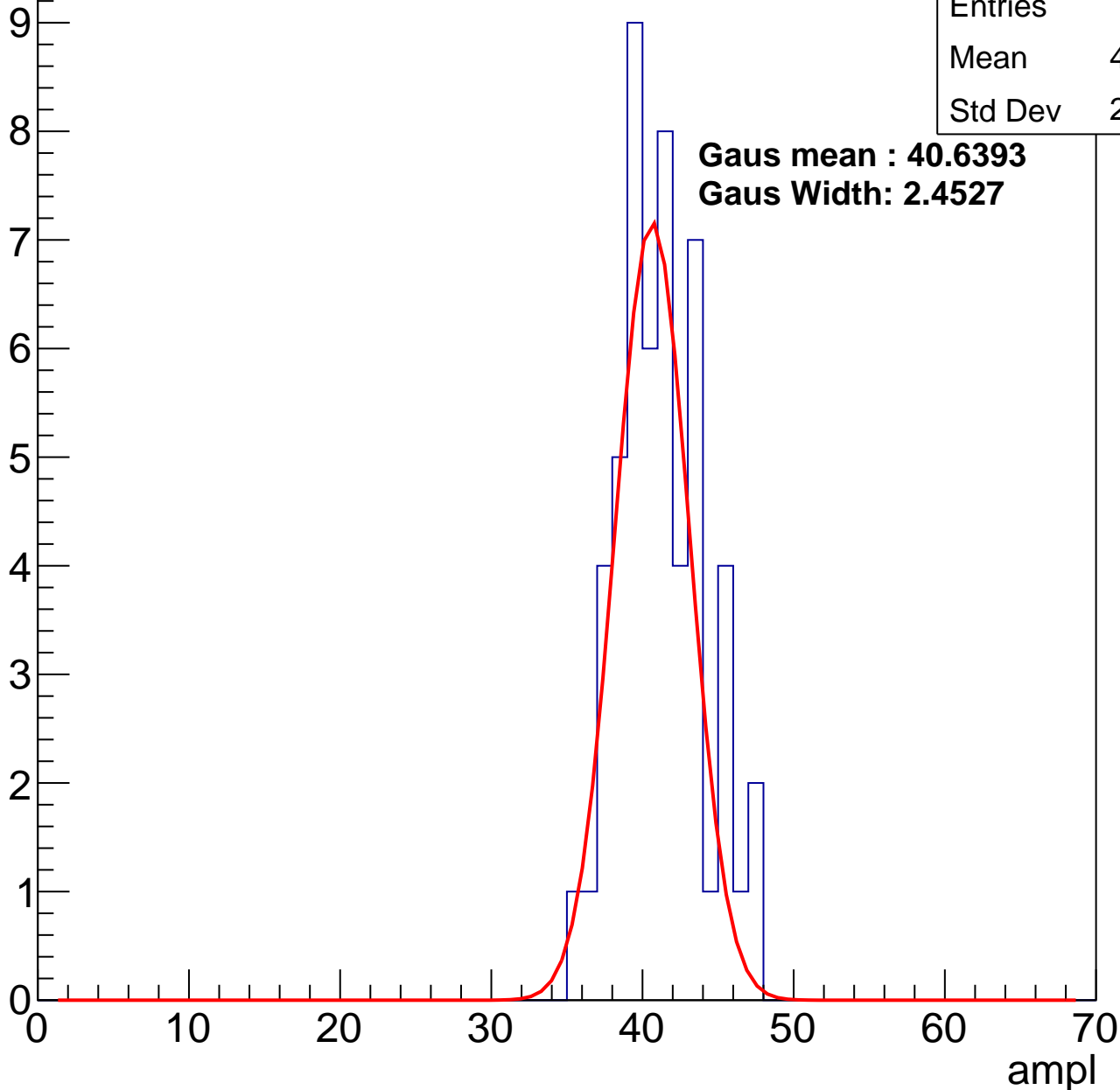
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.77
Std Dev	2.786

**Gaus mean : 40.6393**

**Gaus Width: 2.4527**

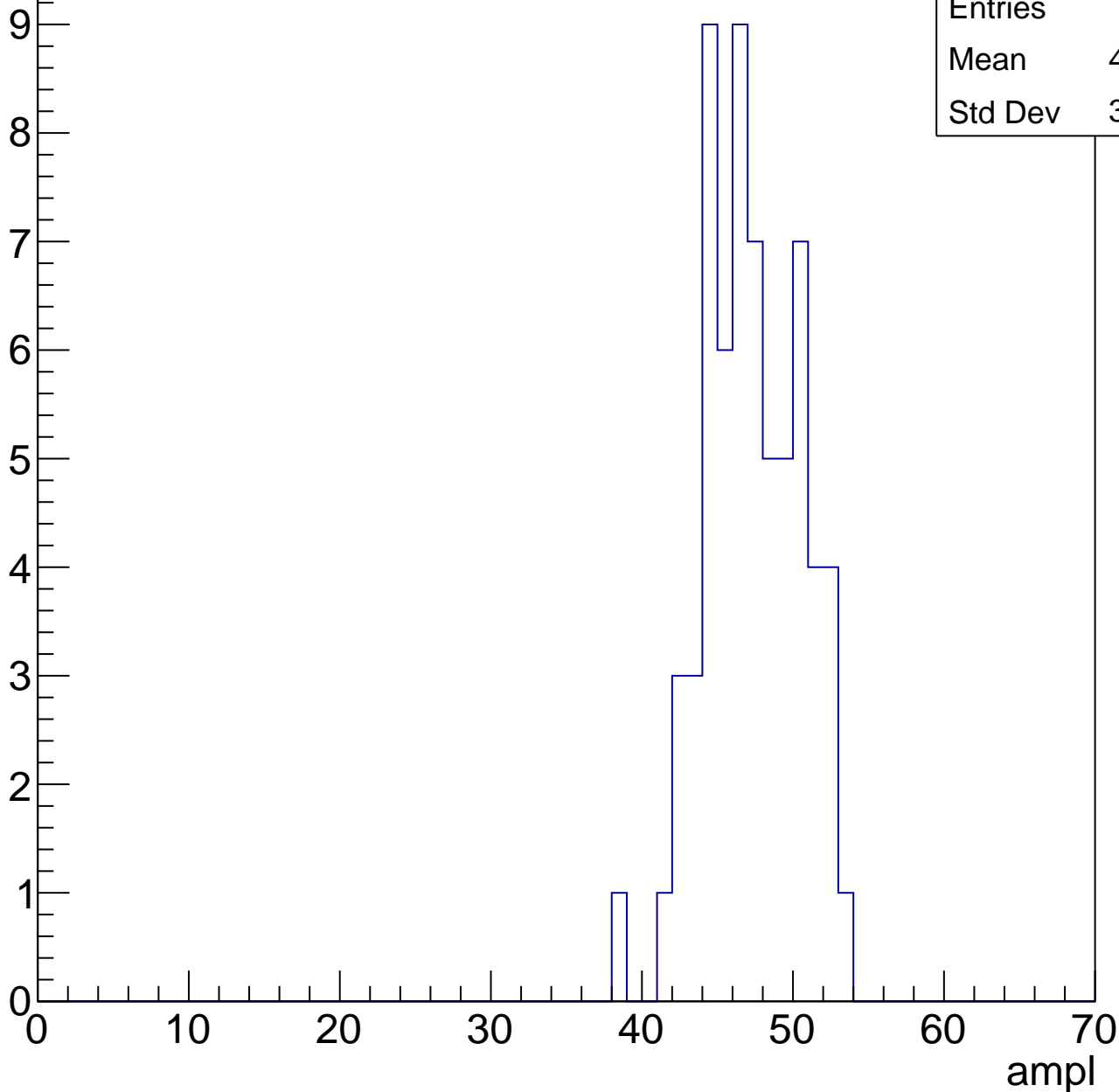


# B1L002S, U8-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

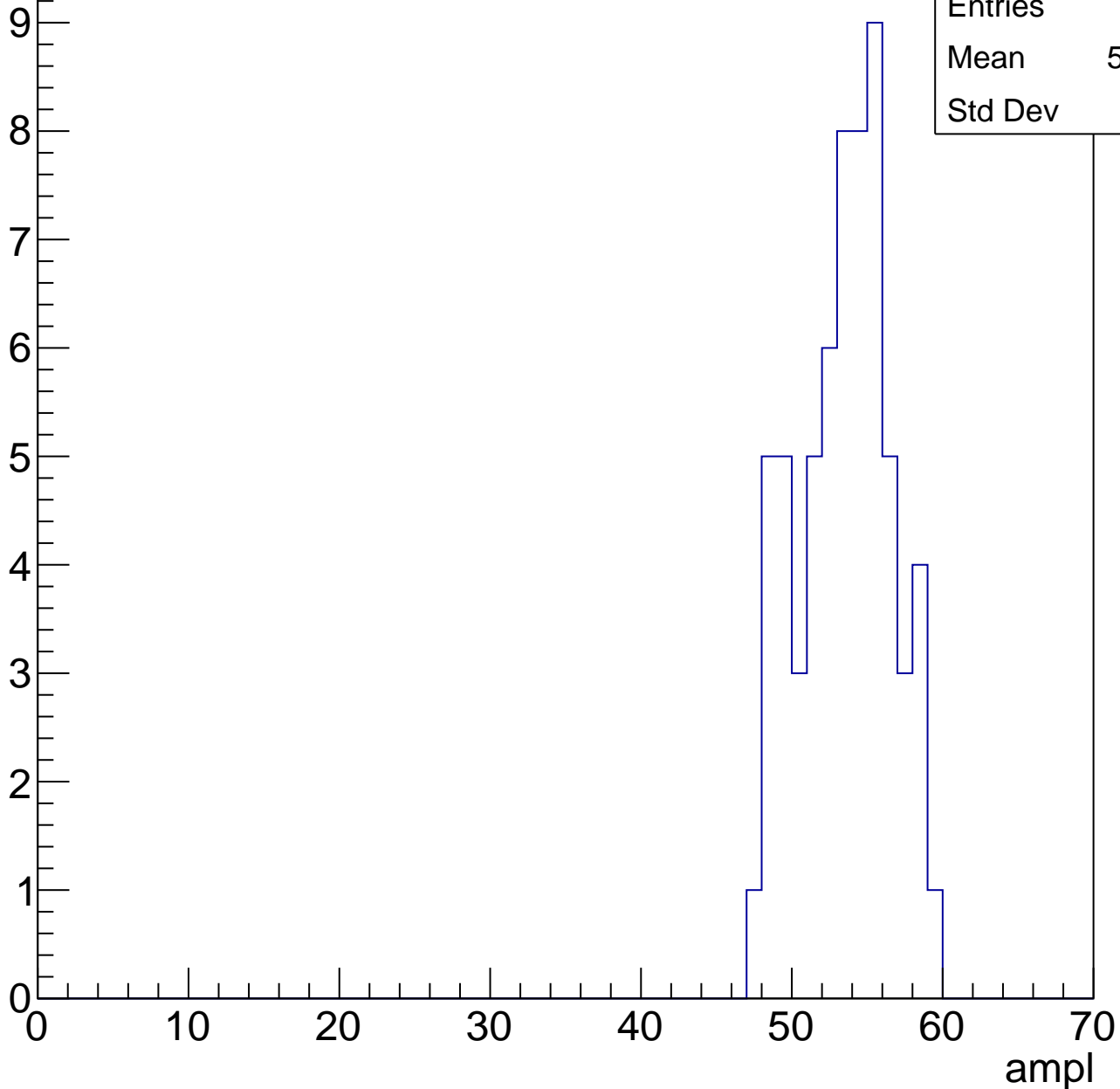
Entries	65
Mean	46.82
Std Dev	3.137



# B1L002S, U8-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

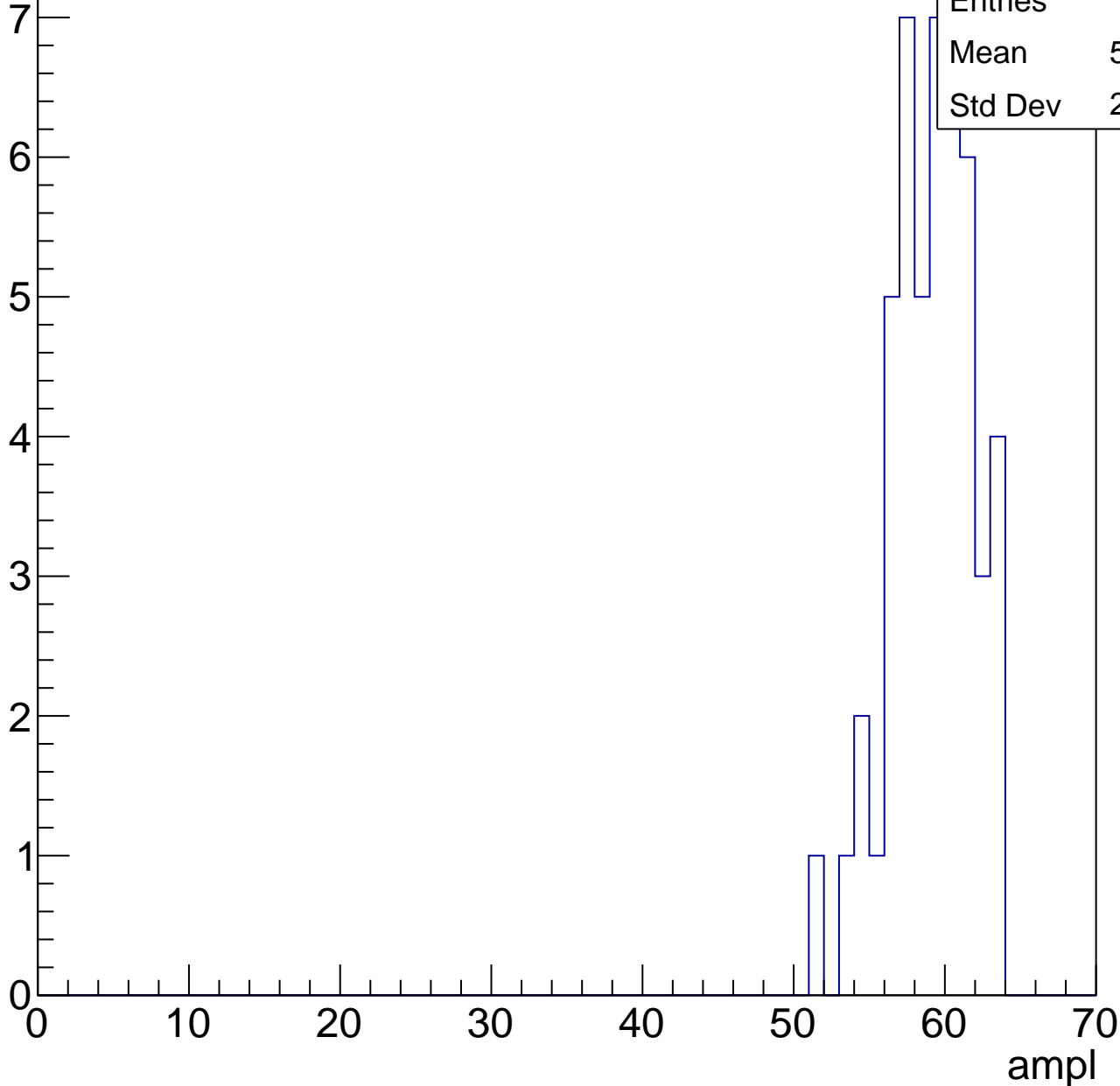


# B1L002S, U8-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	58.63
Std Dev	2.708

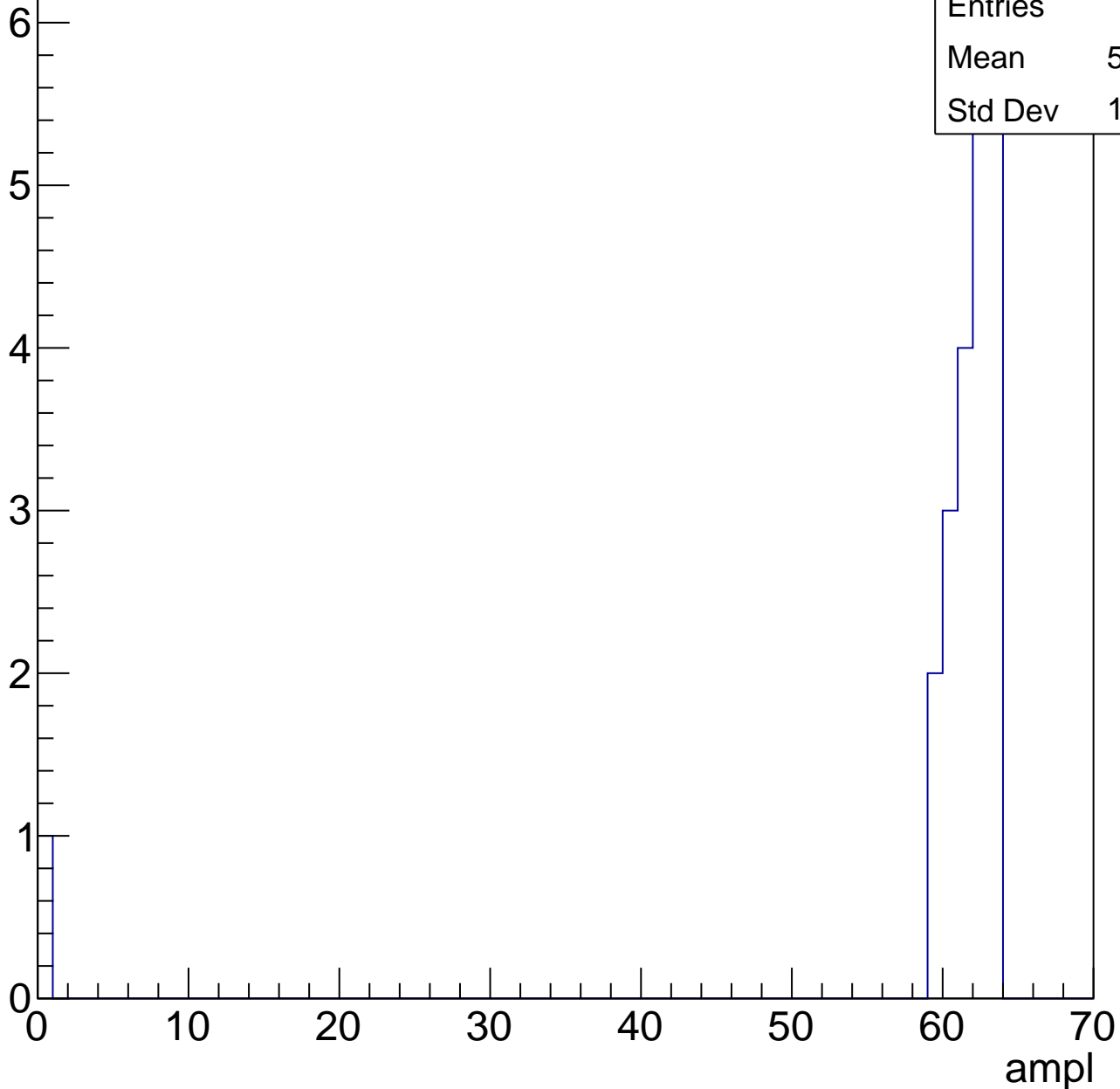


# B1L002S, U8-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.73
Std Dev	12.88

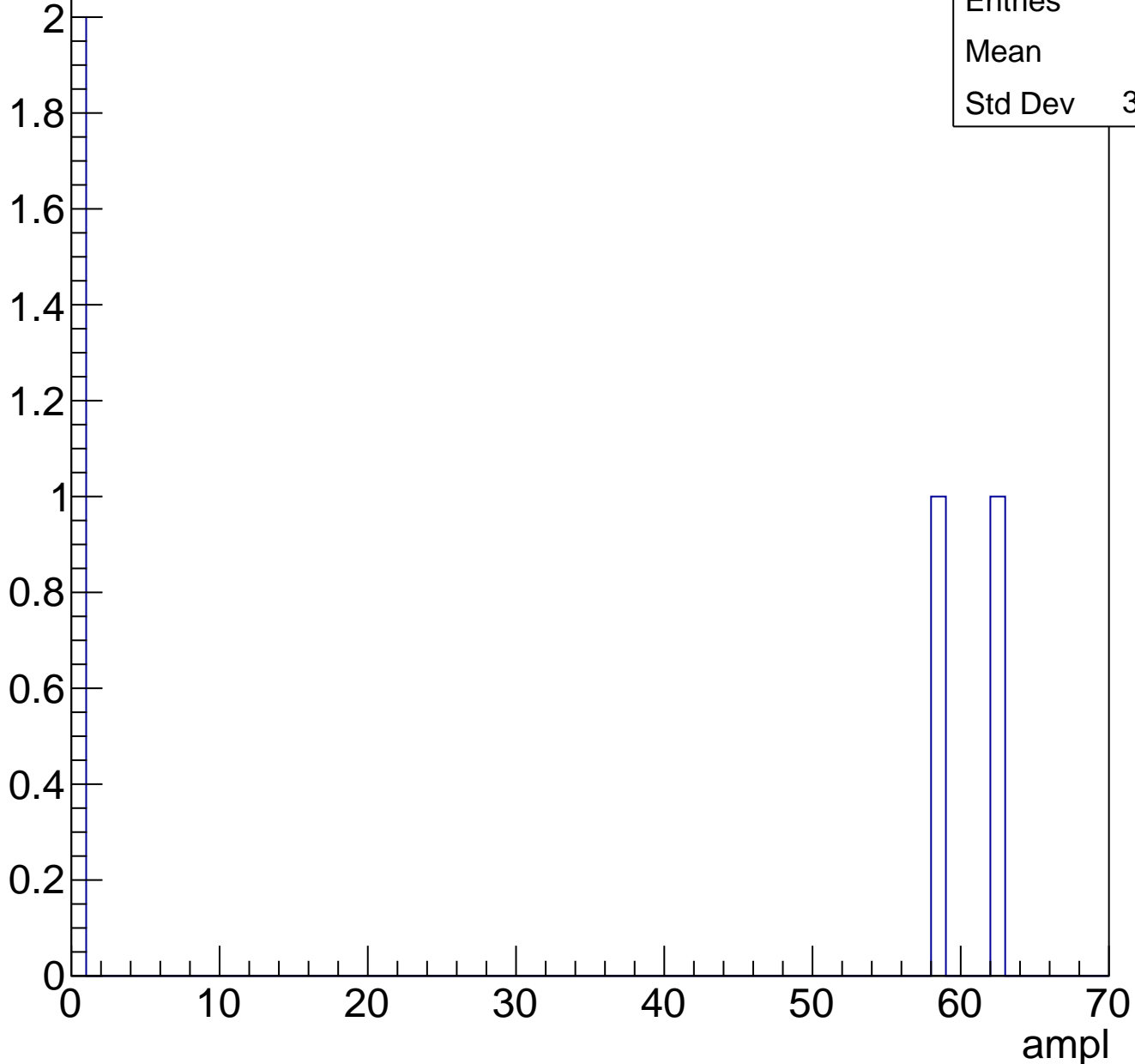




# B1L002S, U8-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	30
Std Dev	30.03

# B1L002S, U8-ch15, adc0

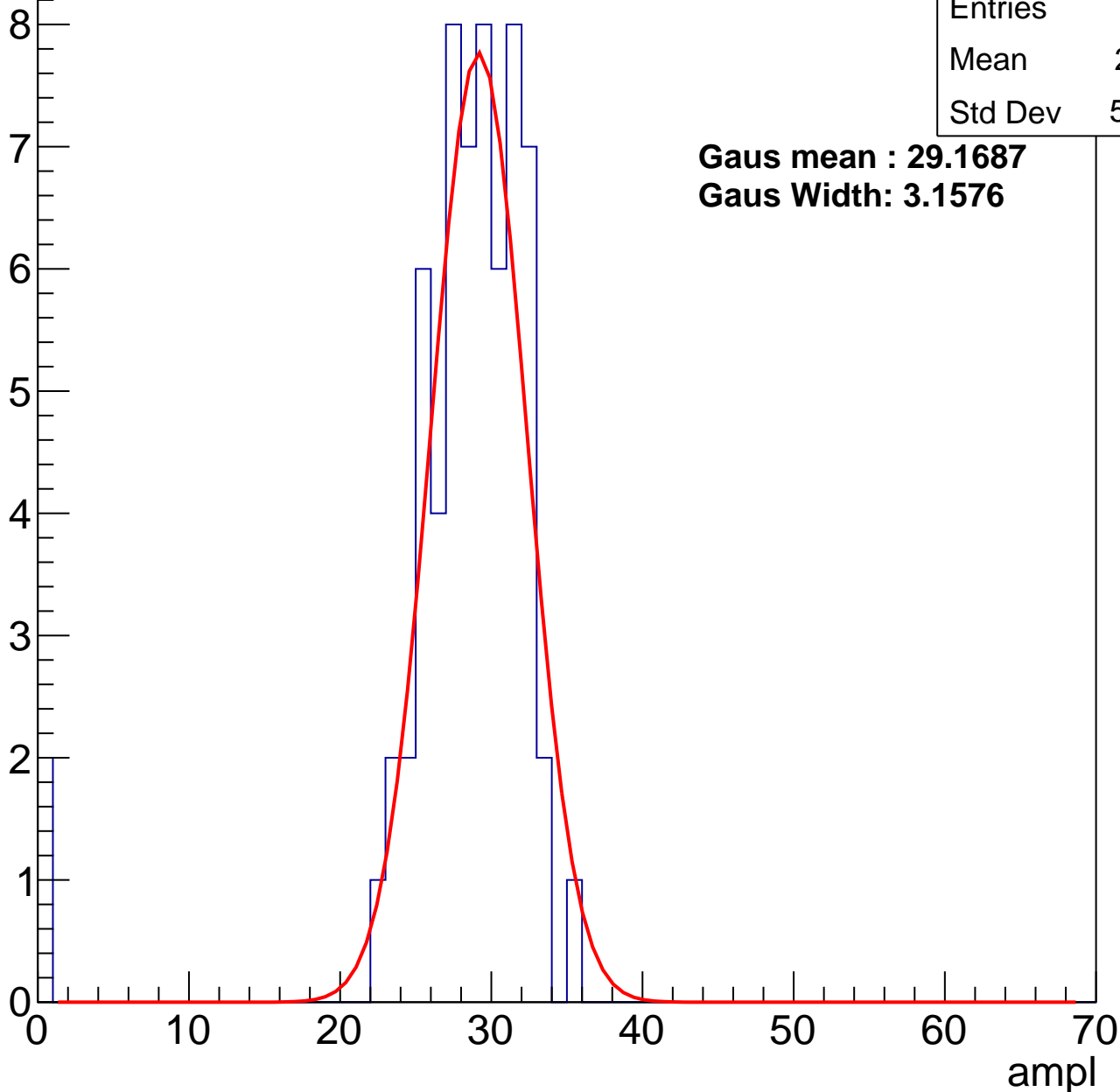
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.61
Std Dev	5.686

**Gaus mean : 29.1687**

**Gaus Width: 3.1576**



# B1L002S, U8-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	50
Mean	35.32
Std Dev	2.716

**Gaus mean : 35.3174**

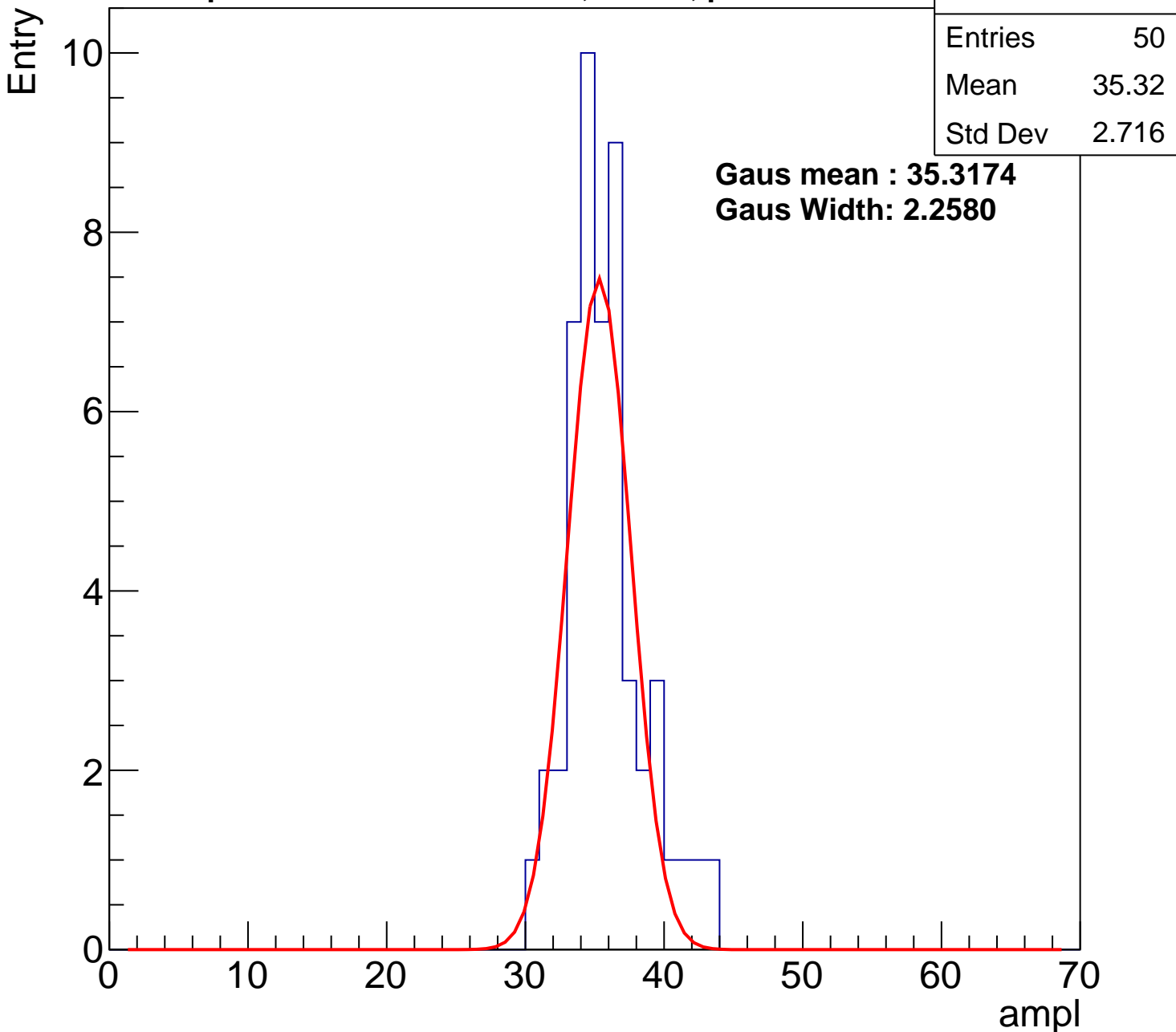
**Gaus Width: 2.2580**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



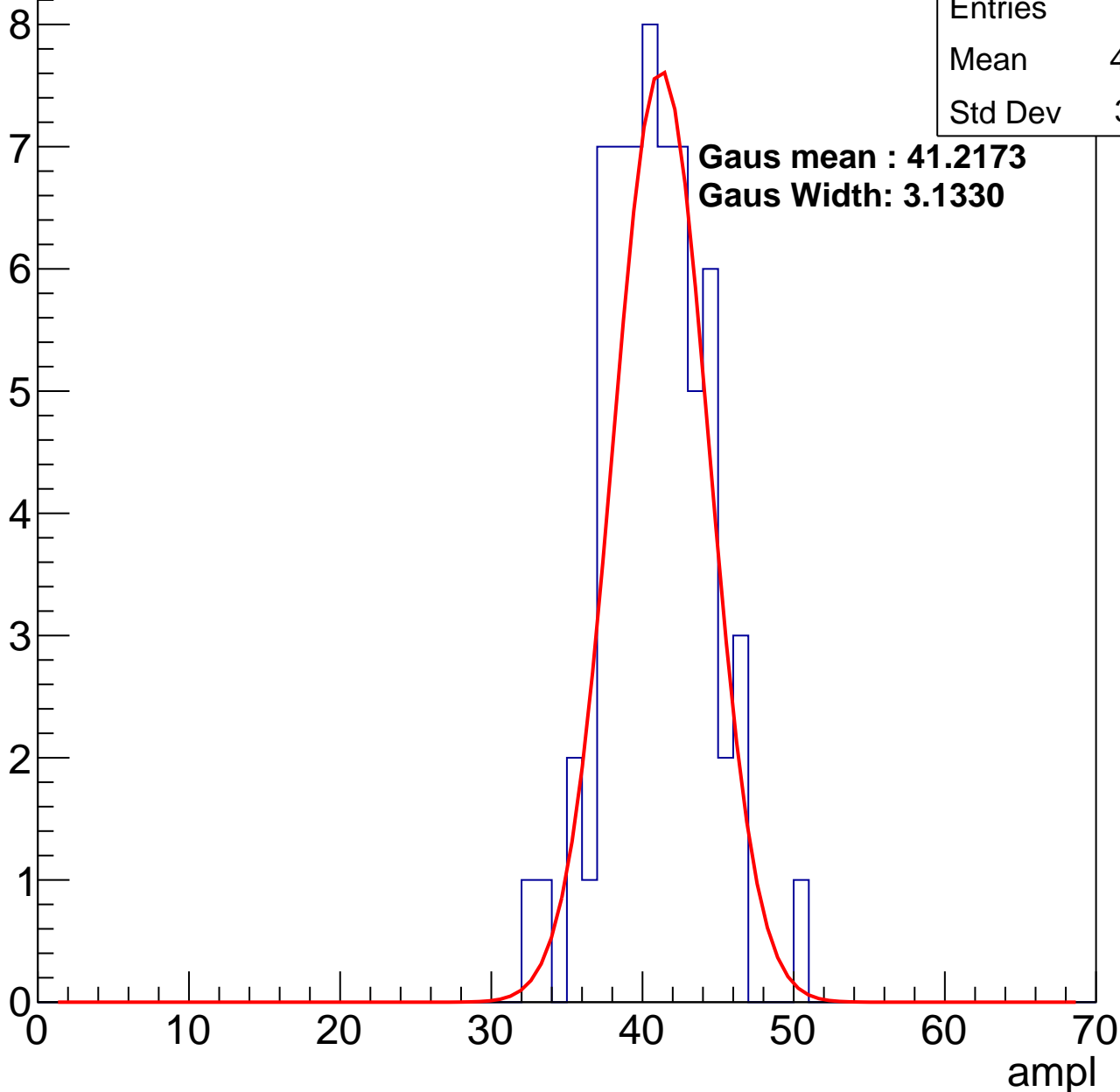
# B1L002S, U8-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.42
Std Dev	3.281

**Gaus mean : 41.2173**  
**Gaus Width: 3.1330**

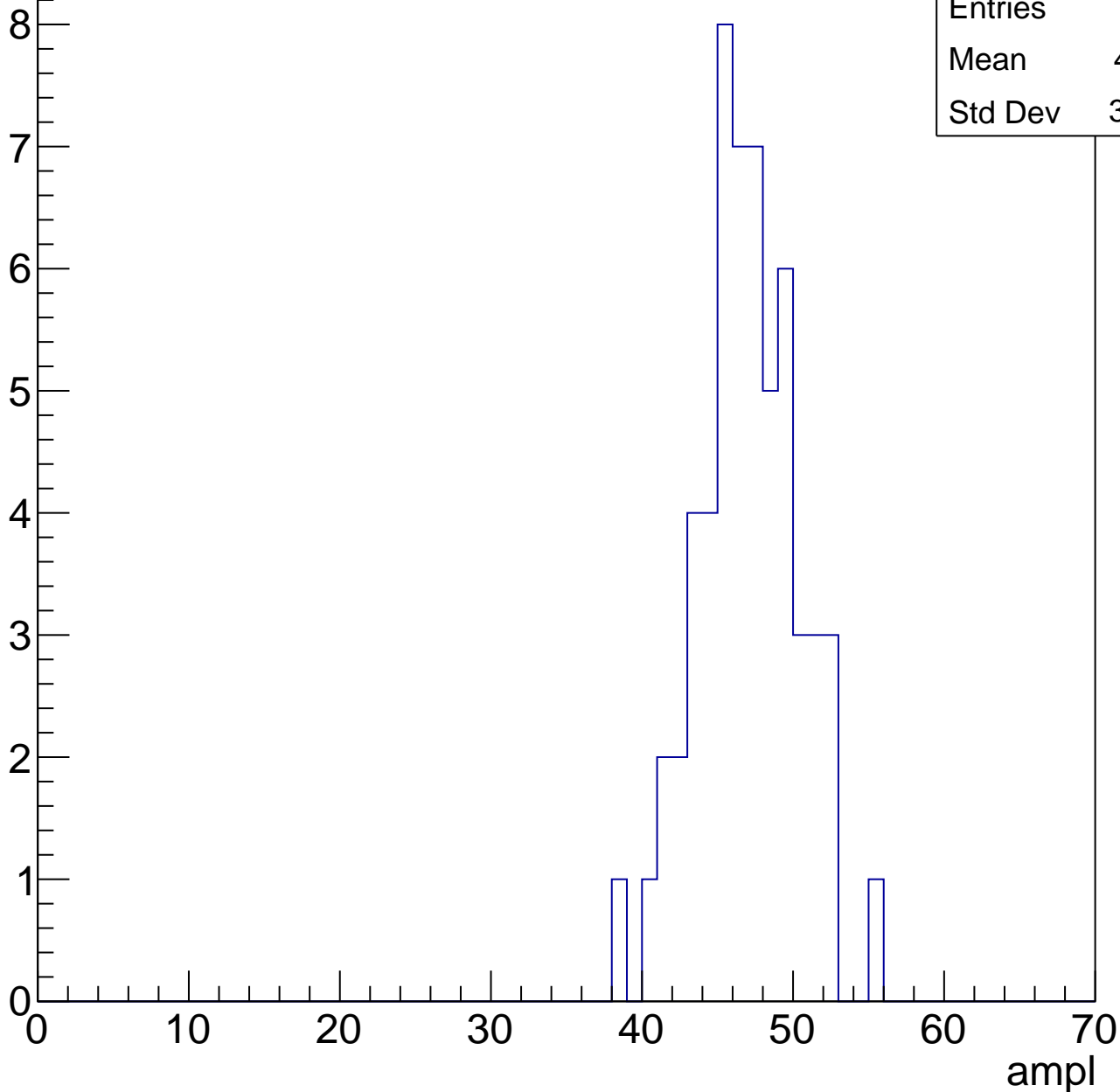


# B1L002S, U8-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.51
Std Dev	3.299

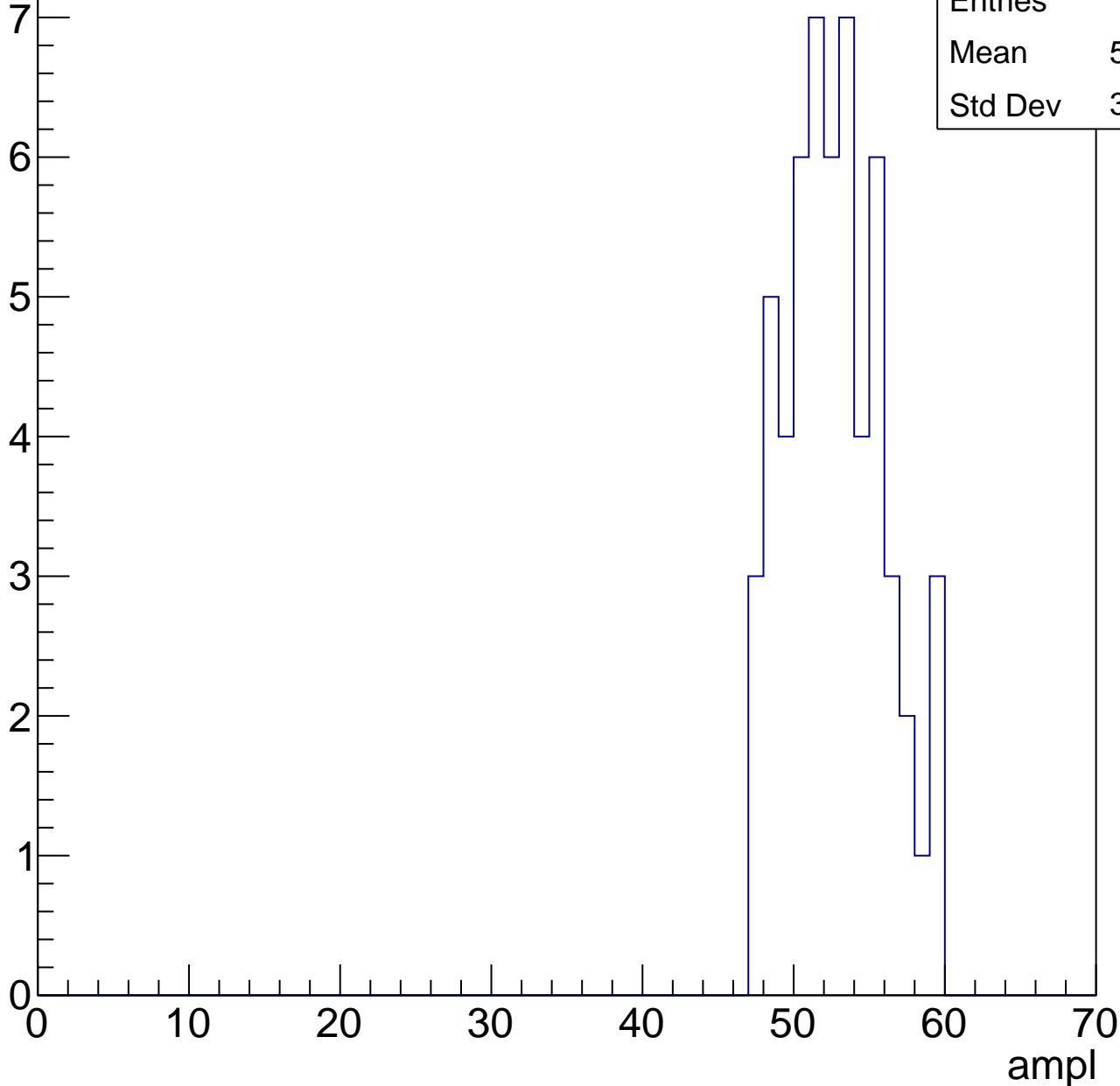


# B1L002S, U8-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.28
Std Dev	3.178

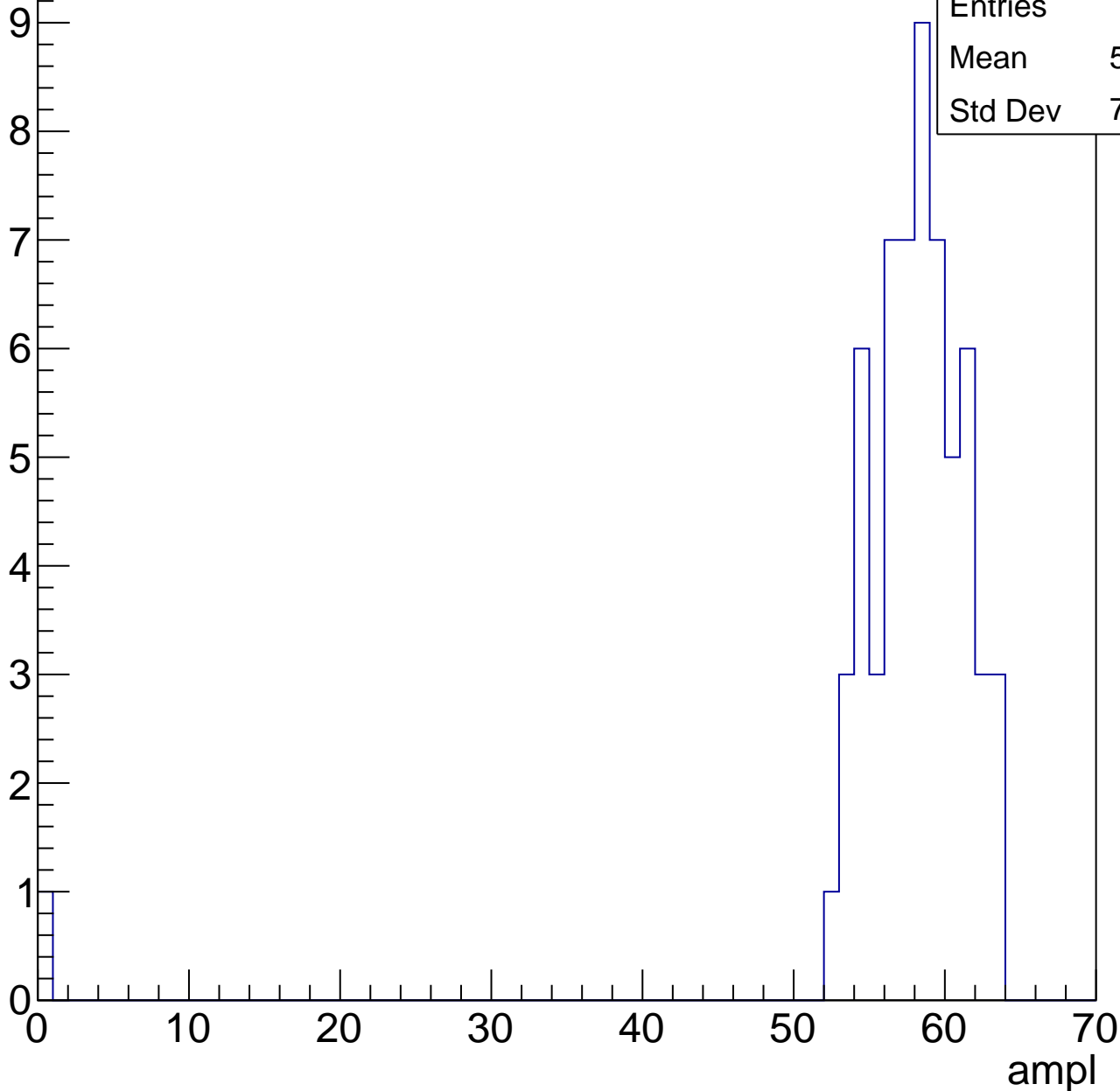


# B1L002S, U8-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	56.84
Std Dev	7.845

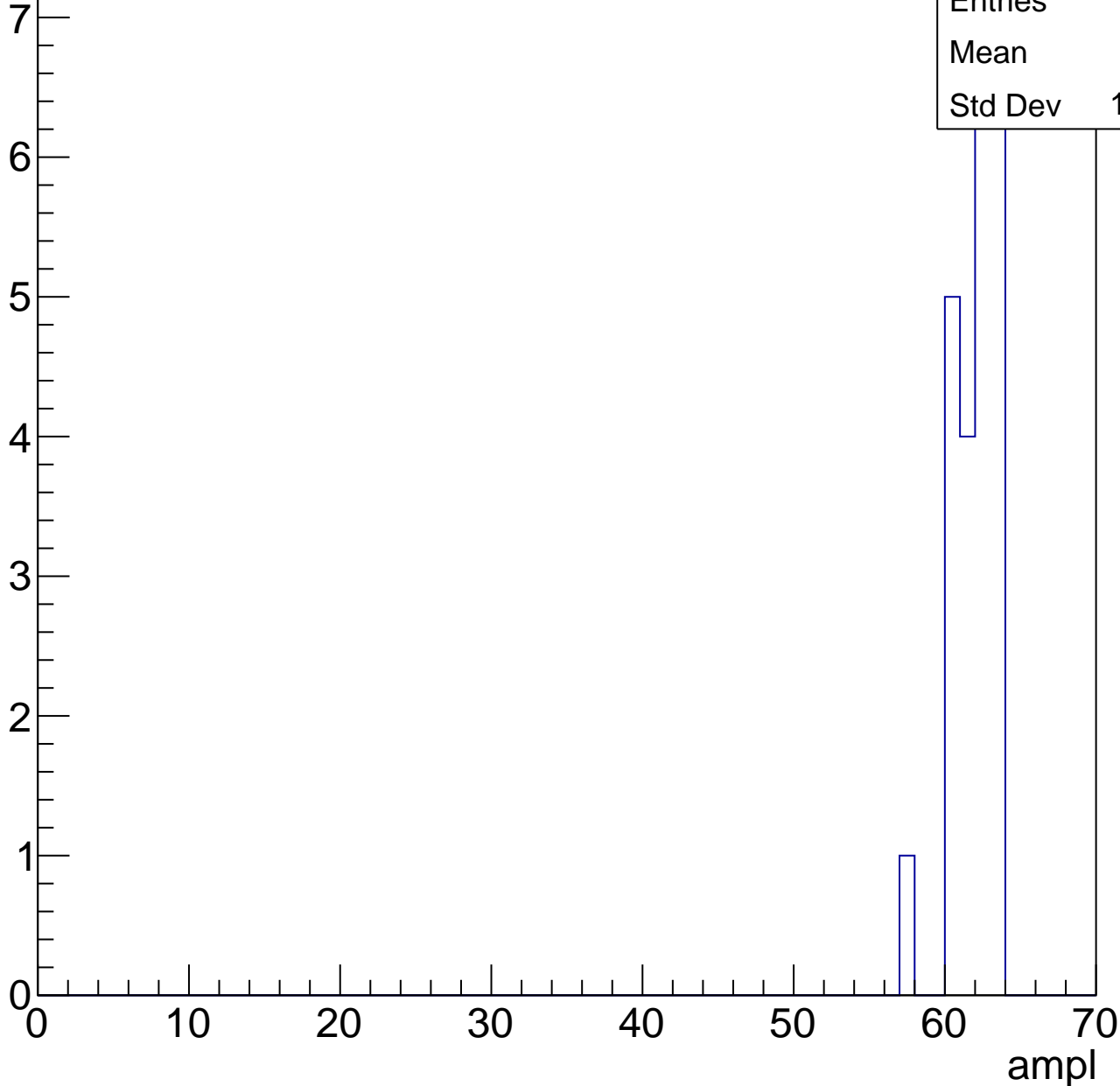


# B1L002S, U8-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	61.5
Std Dev	1.443





# B1L002S, U8-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch16, adc0

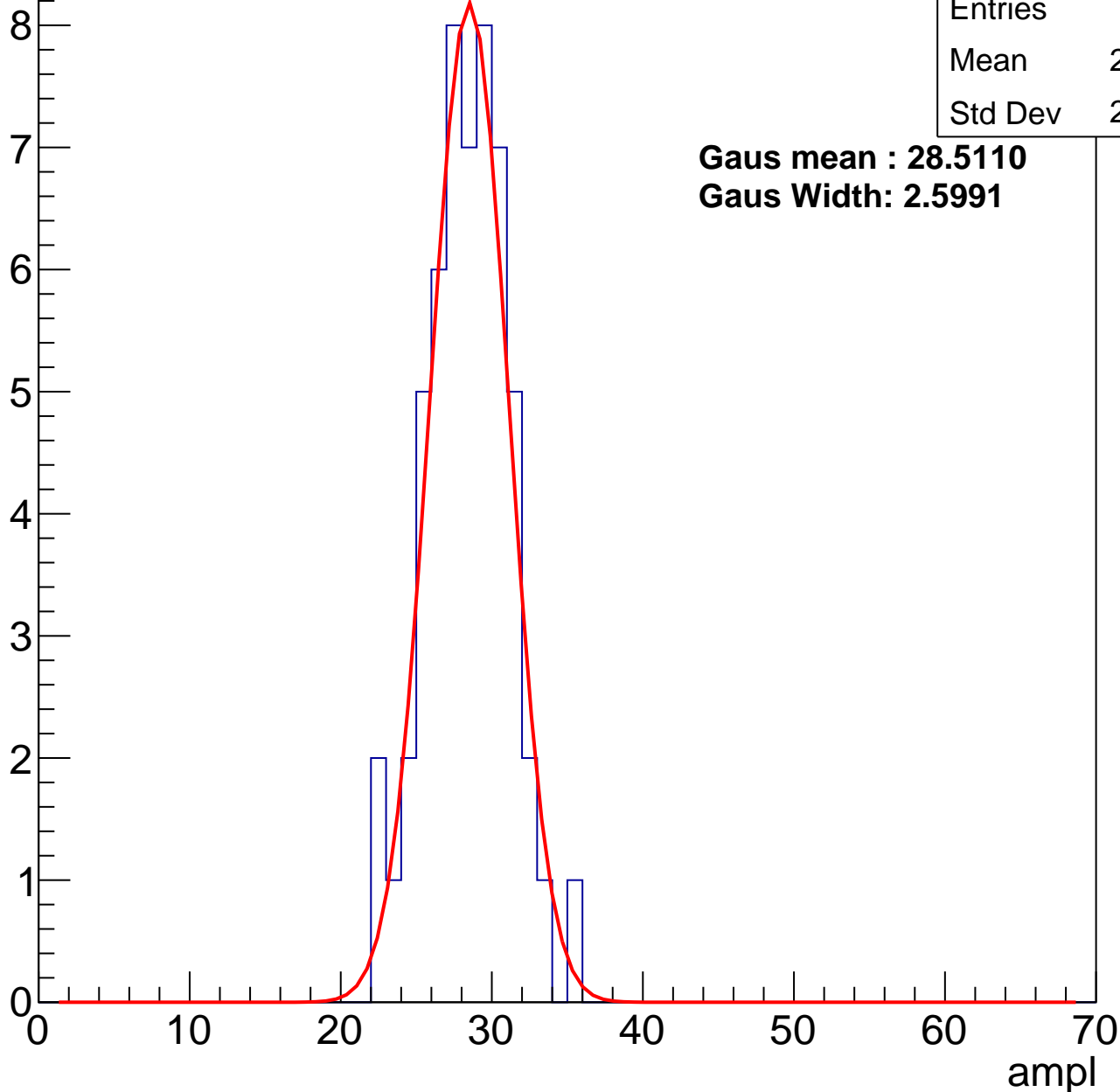
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.95
Std Dev	2.673

**Gaus mean : 28.5110**

**Gaus Width: 2.5991**



# B1L002S, U8-ch16, adc1

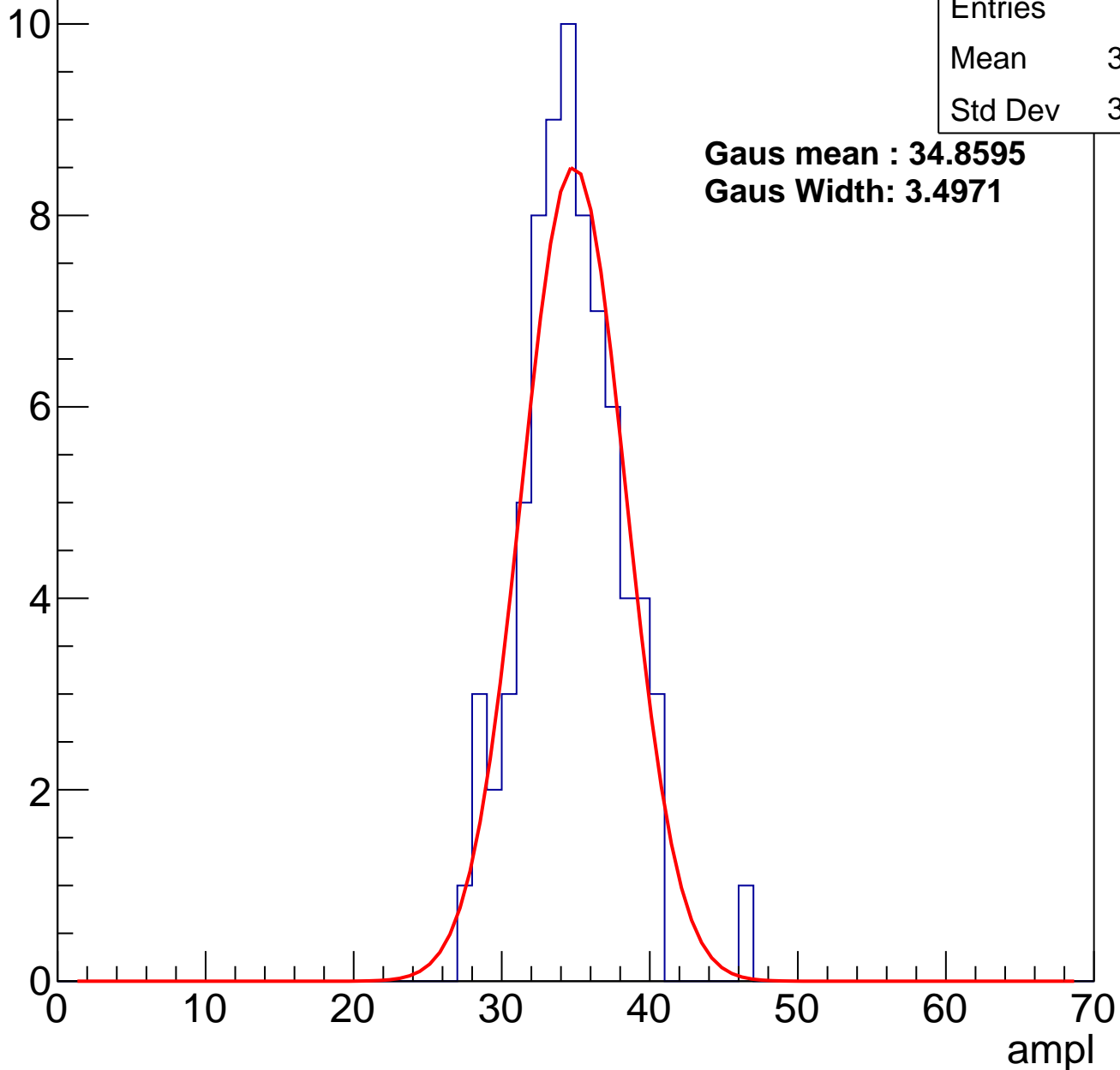
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	34.26
Std Dev	3.373

**Gaus mean : 34.8595**

**Gaus Width: 3.4971**

Entry



# B1L002S, U8-ch16, adc2

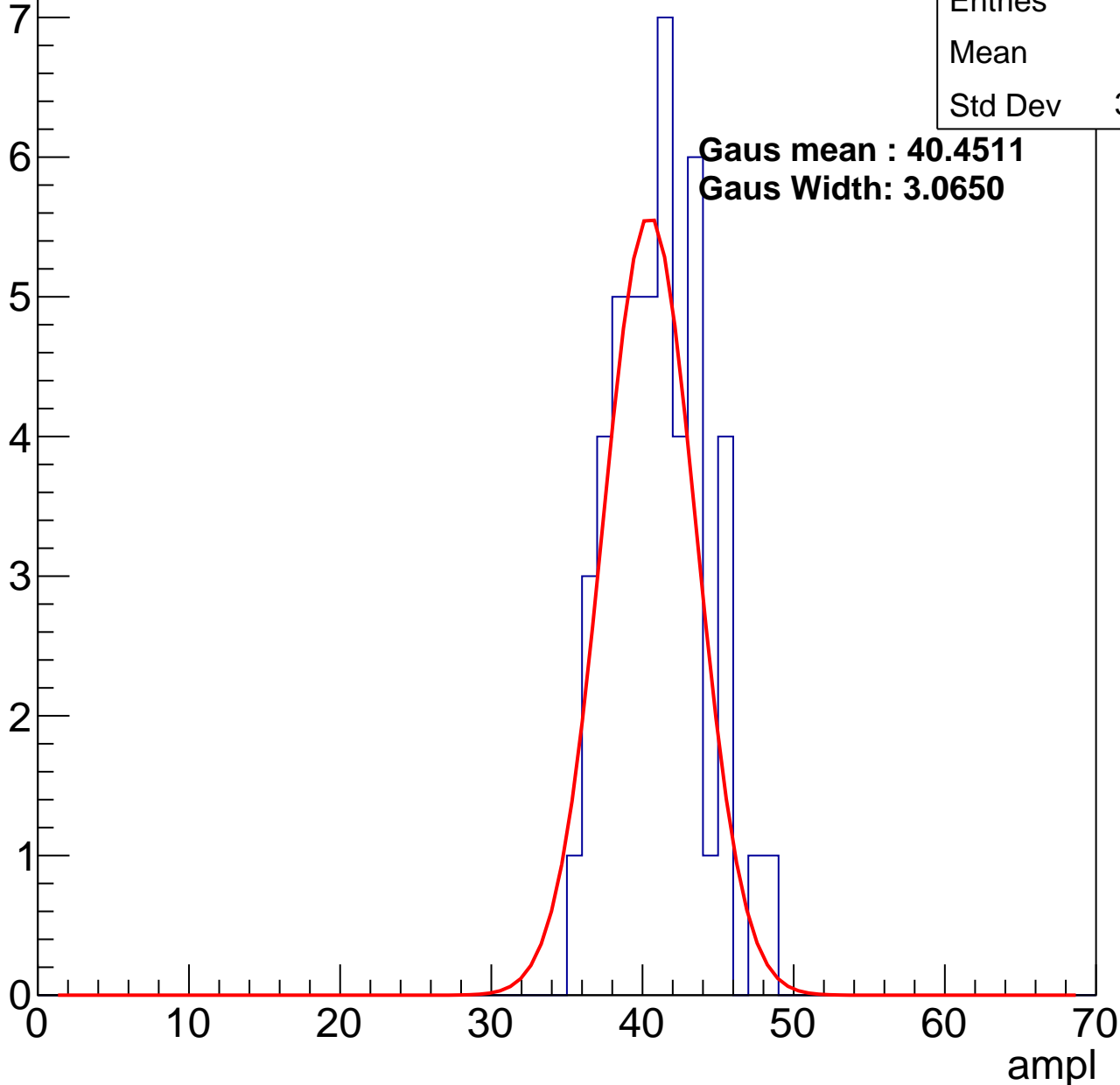
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	40.6
Std Dev	3.001

**Gaus mean : 40.4511**

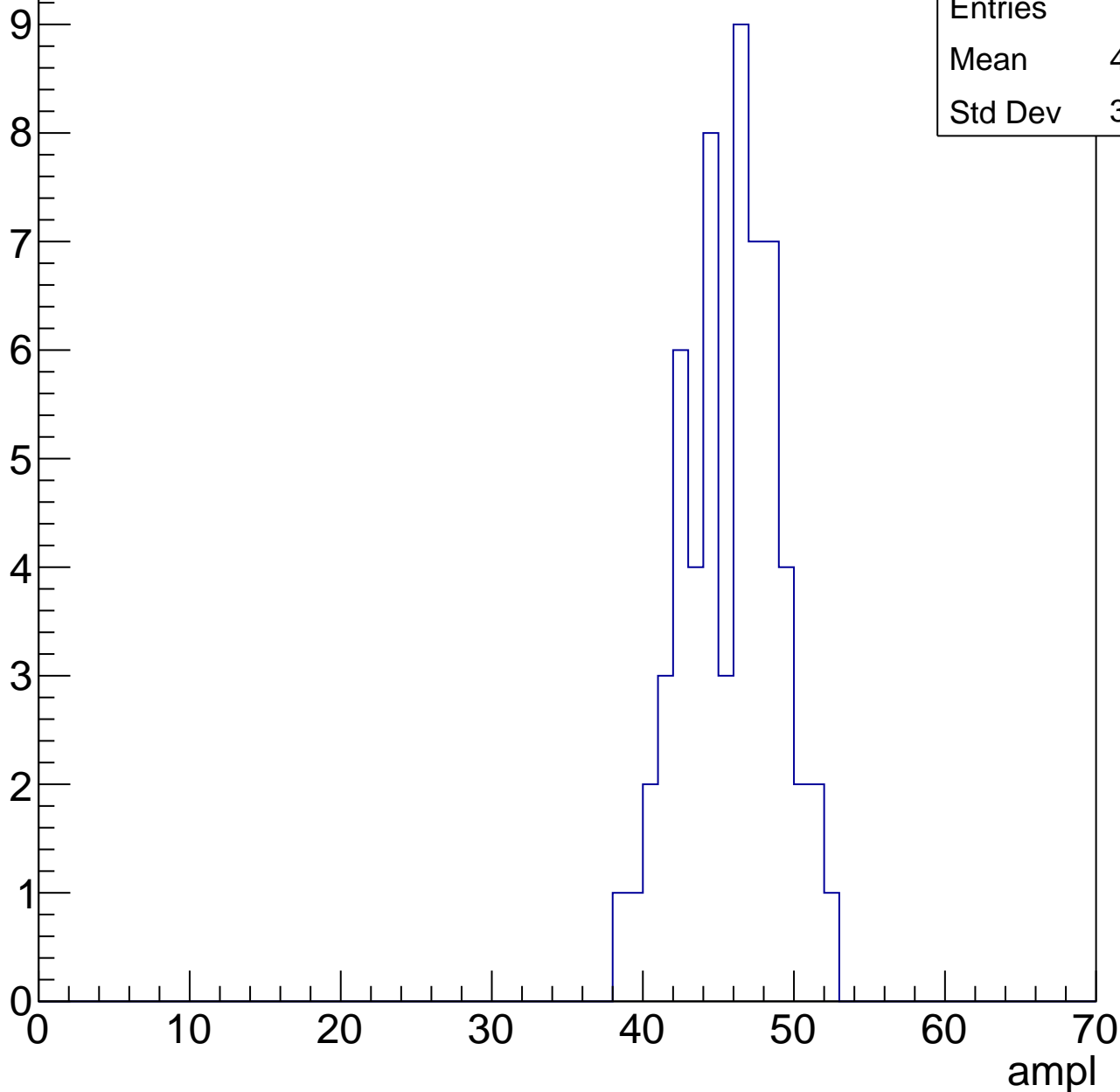
**Gaus Width: 3.0650**



# B1L002S, U8-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

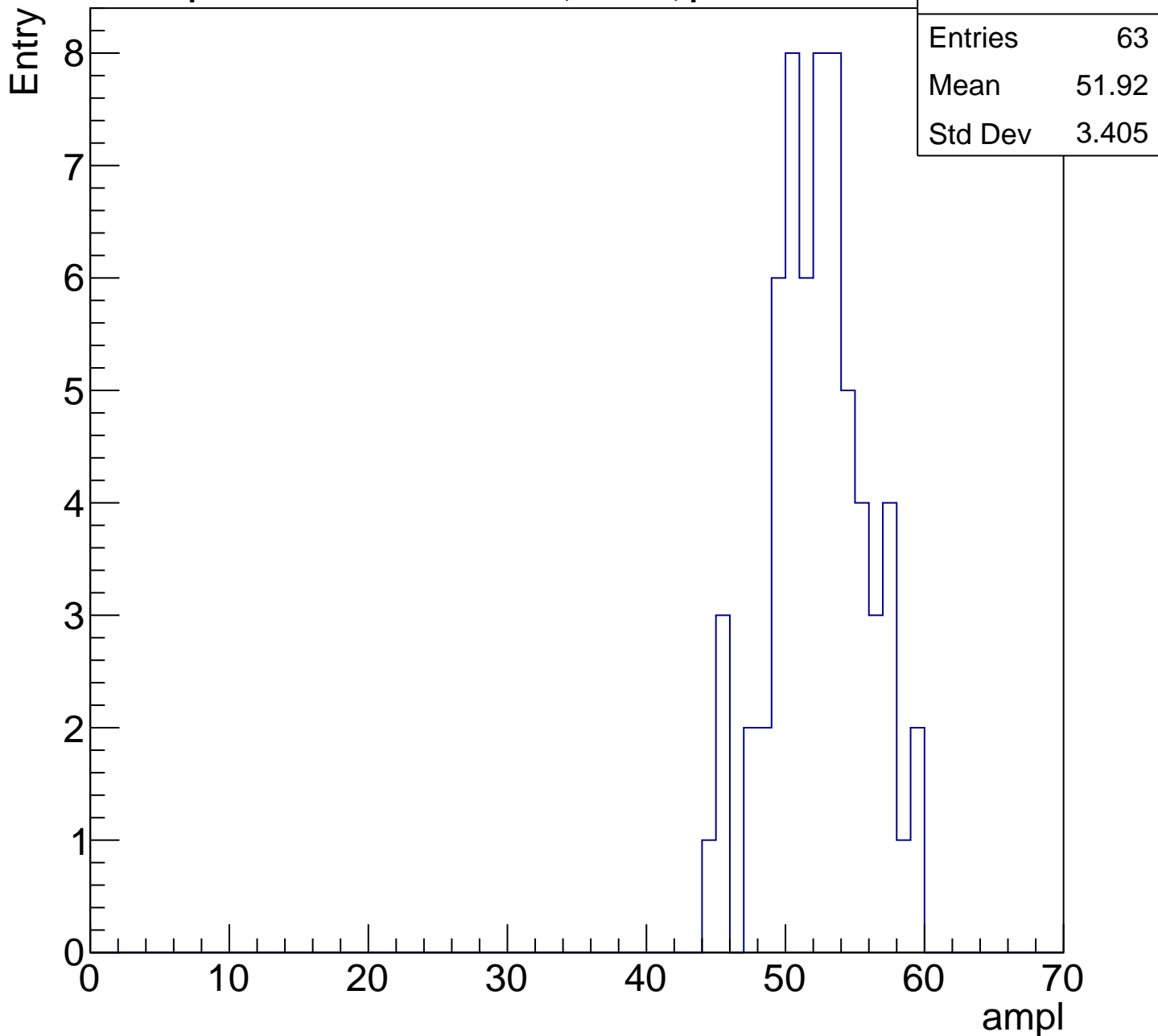
Entry



Entries	60
Mean	45.33
Std Dev	3.134

# B1L002S, U8-ch16, adc4

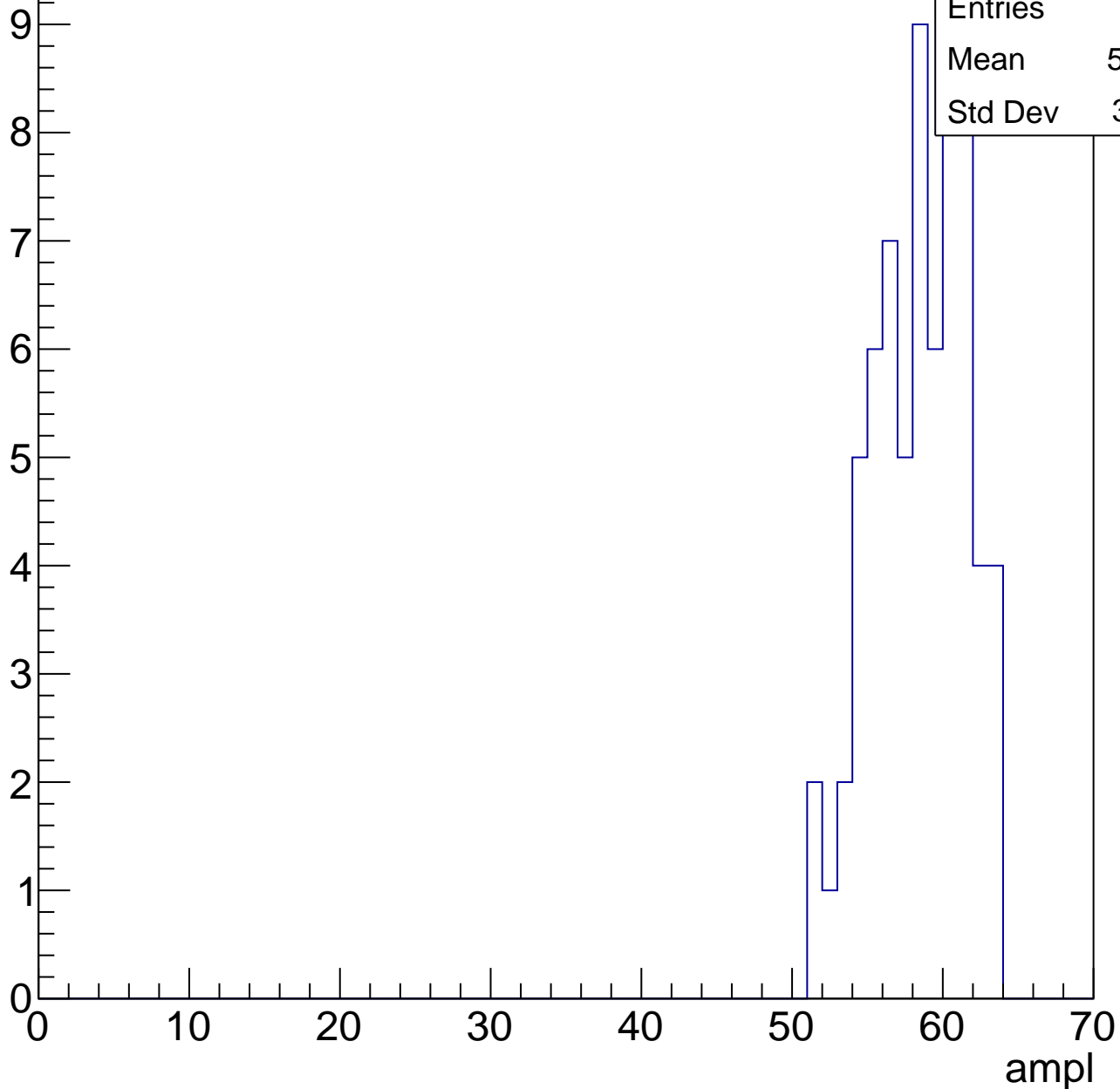
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

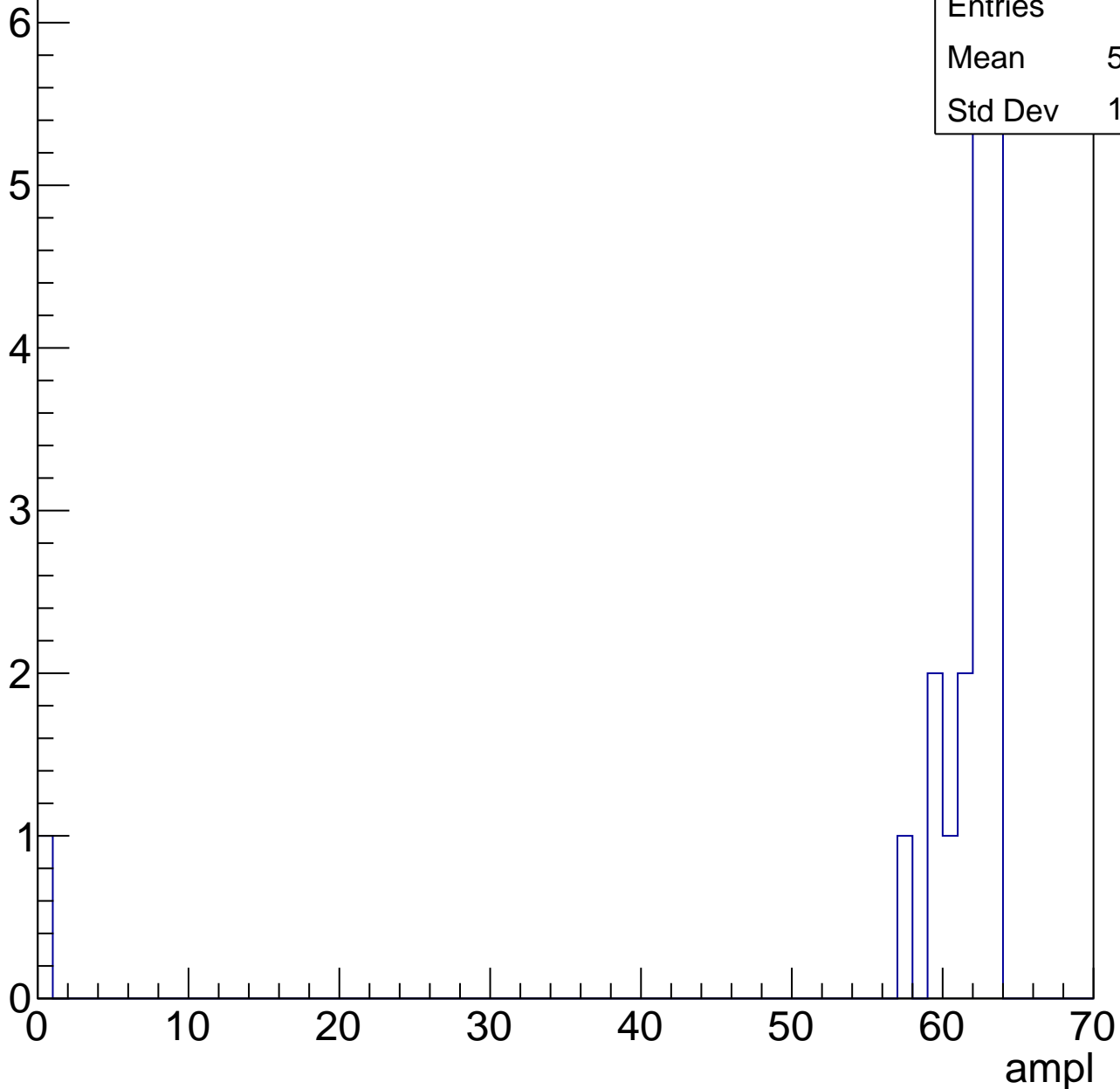


# B1L002S, U8-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.26
Std Dev	13.83

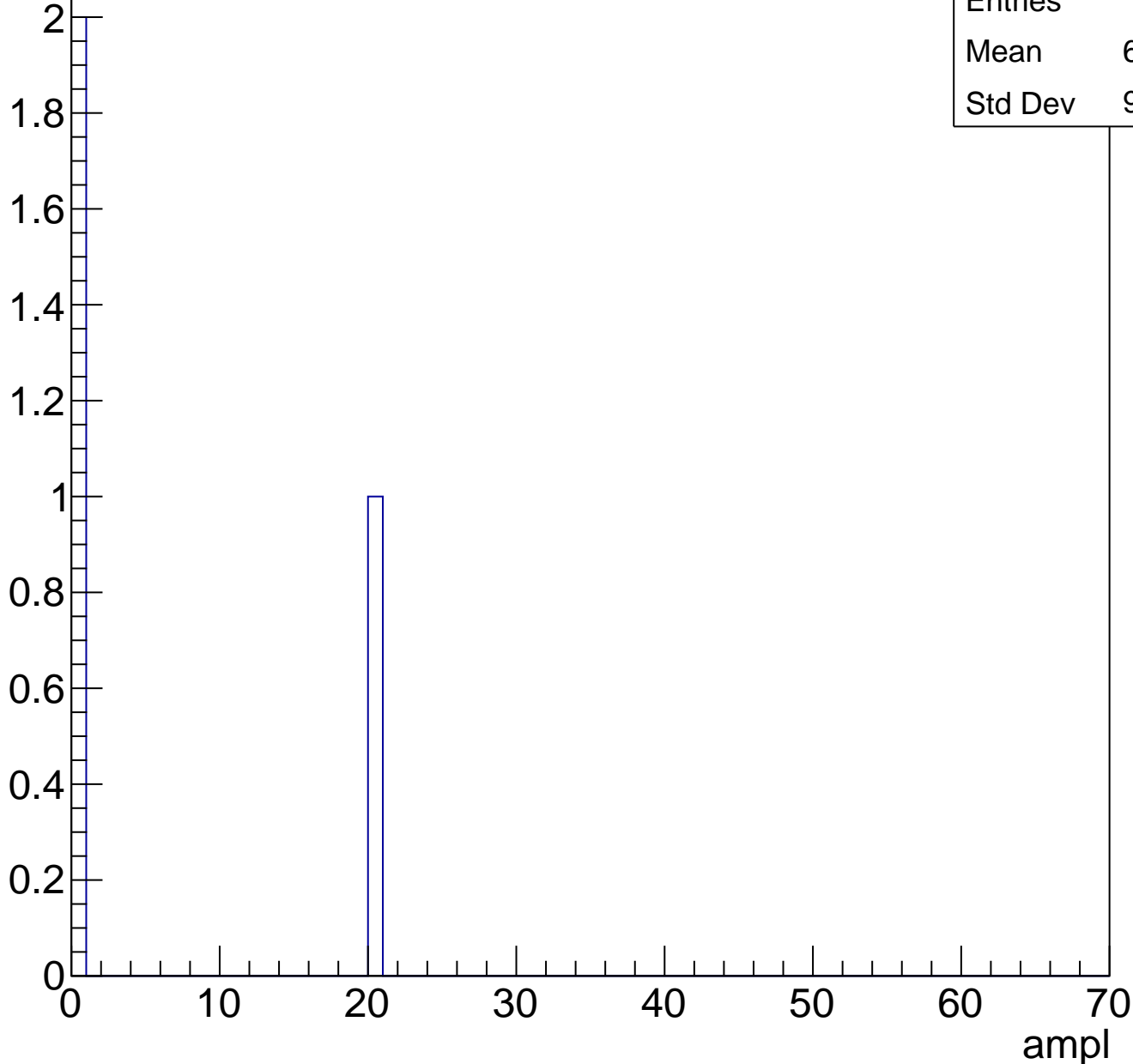




# B1L002S, U8-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B1L002S, U8-ch17, adc0

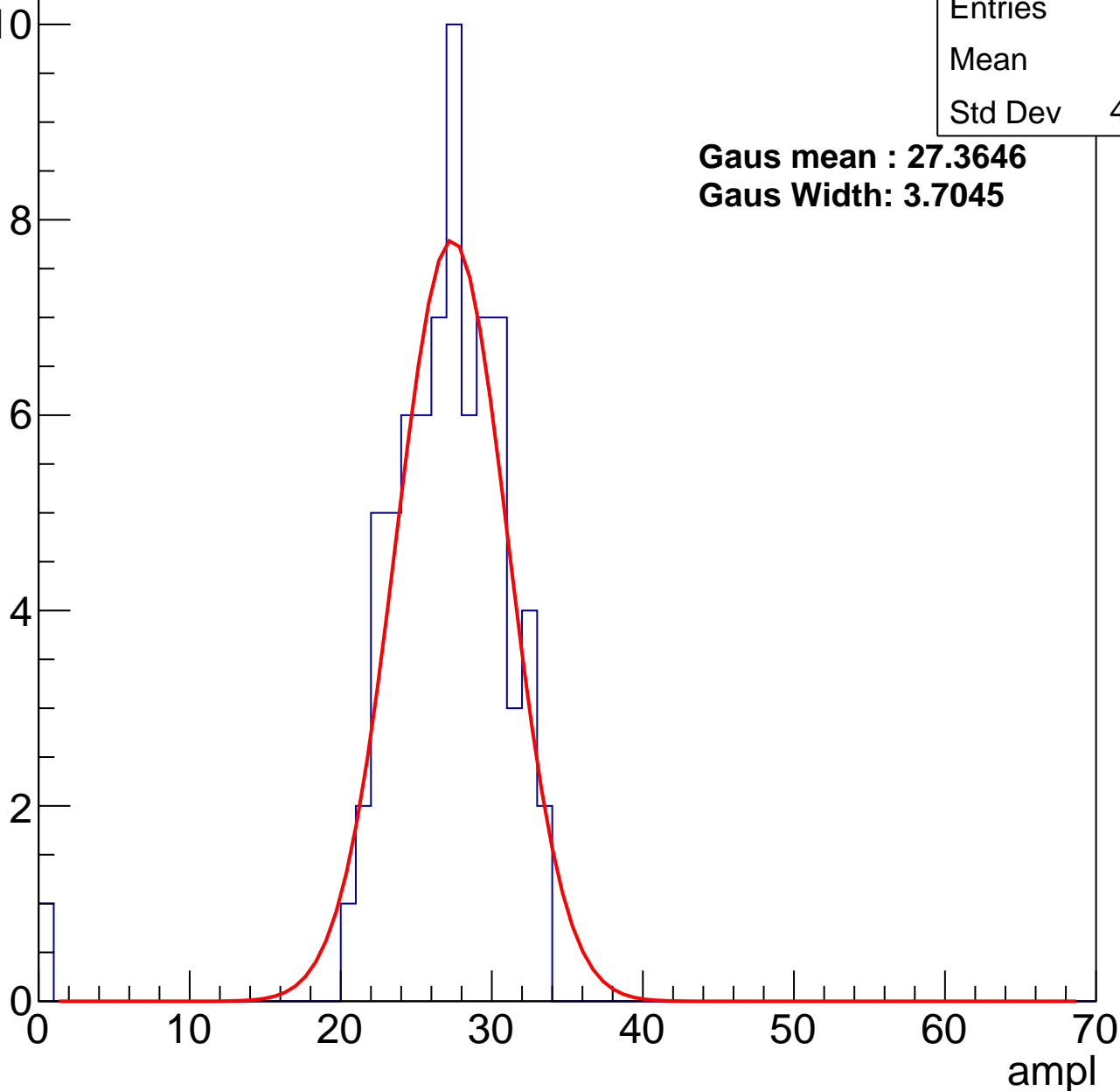
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	26.4
Std Dev	4.459

**Gaus mean : 27.3646**

**Gaus Width: 3.7045**



# B1L002S, U8-ch17, adc1

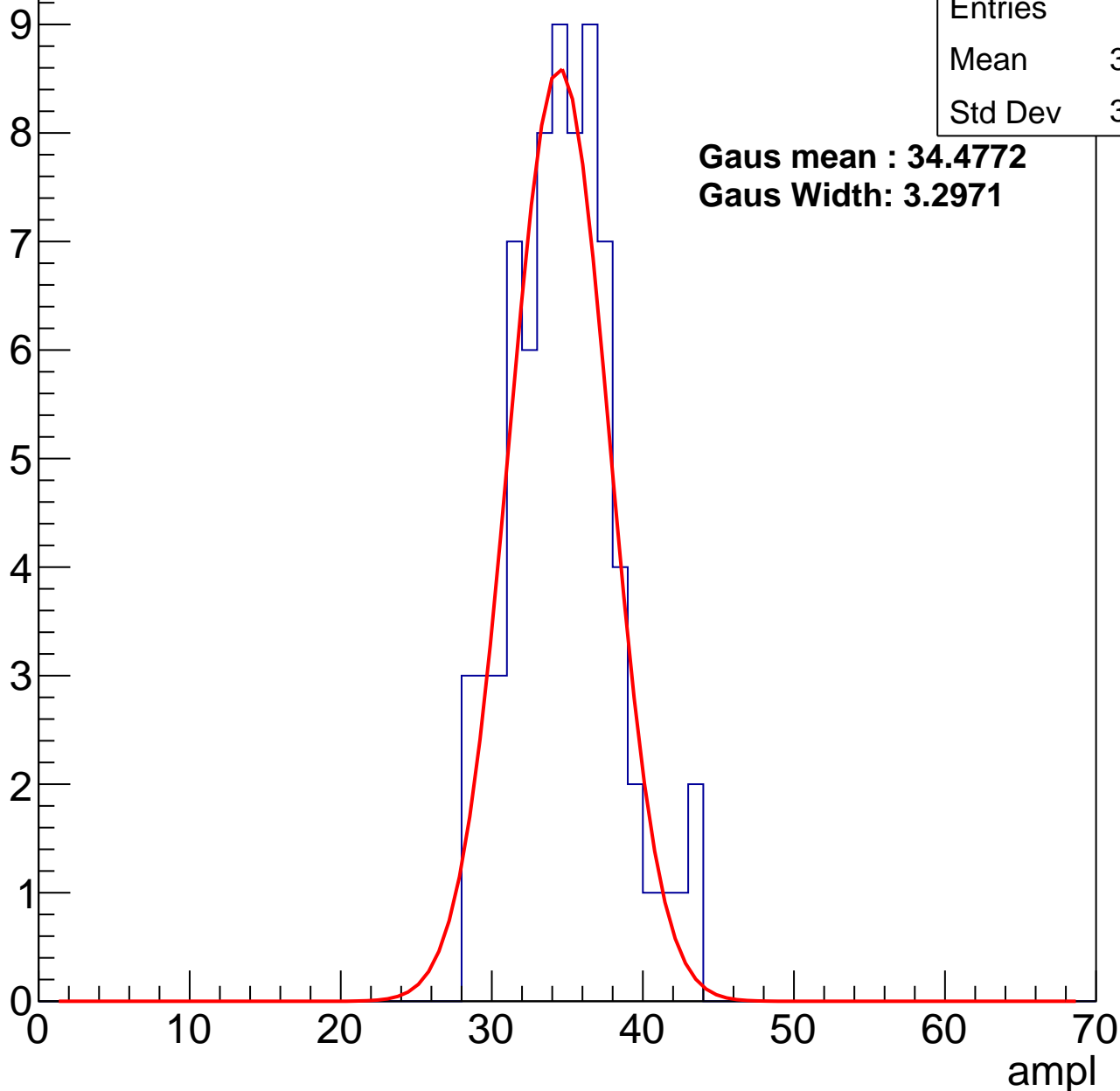
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	34.35
Std Dev	3.387

**Gaus mean : 34.4772**

**Gaus Width: 3.2971**



# B1L002S, U8-ch17, adc2

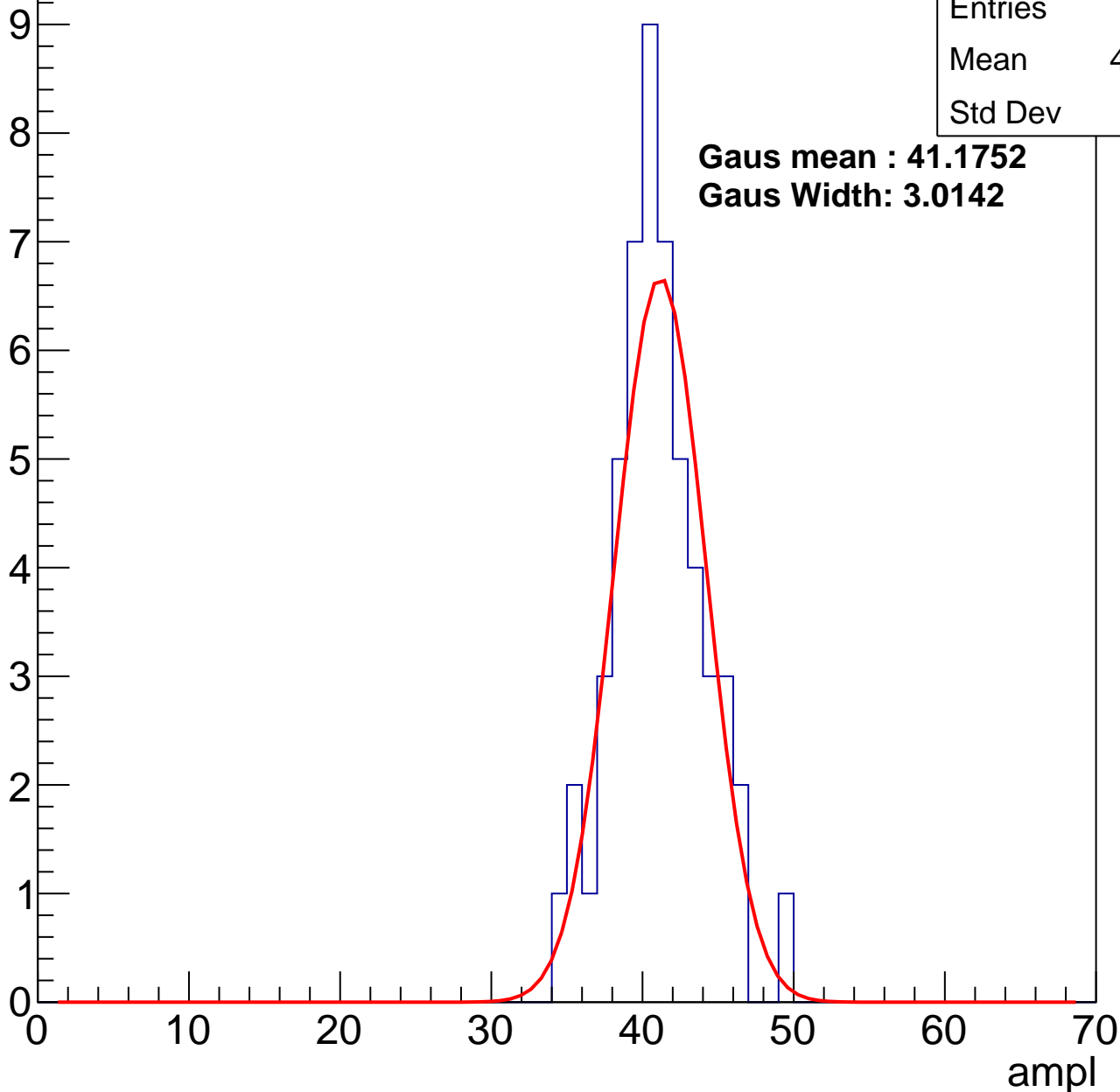
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.58
Std Dev	3

**Gaus mean : 41.1752**

**Gaus Width: 3.0142**

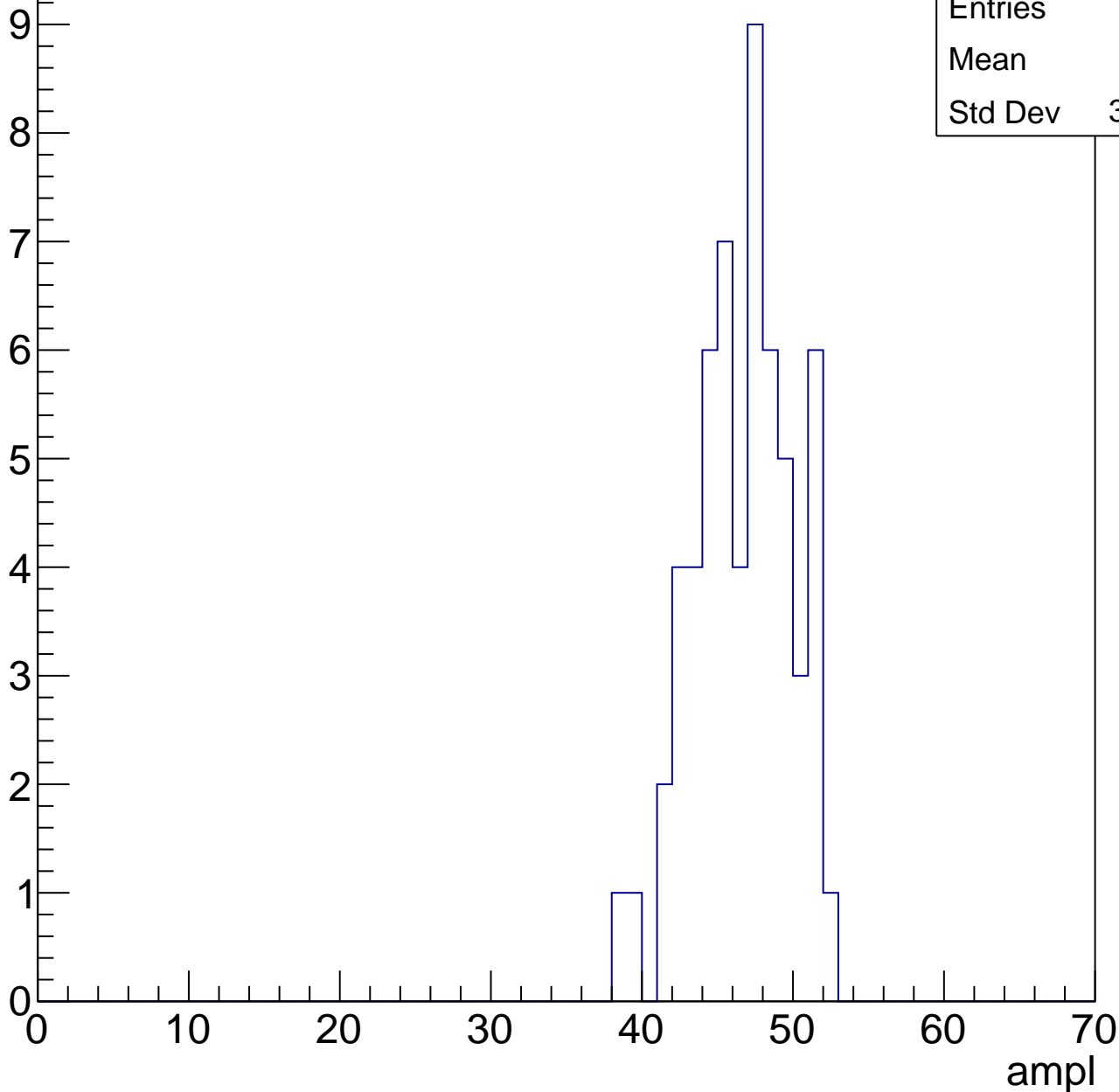


# B1L002S, U8-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

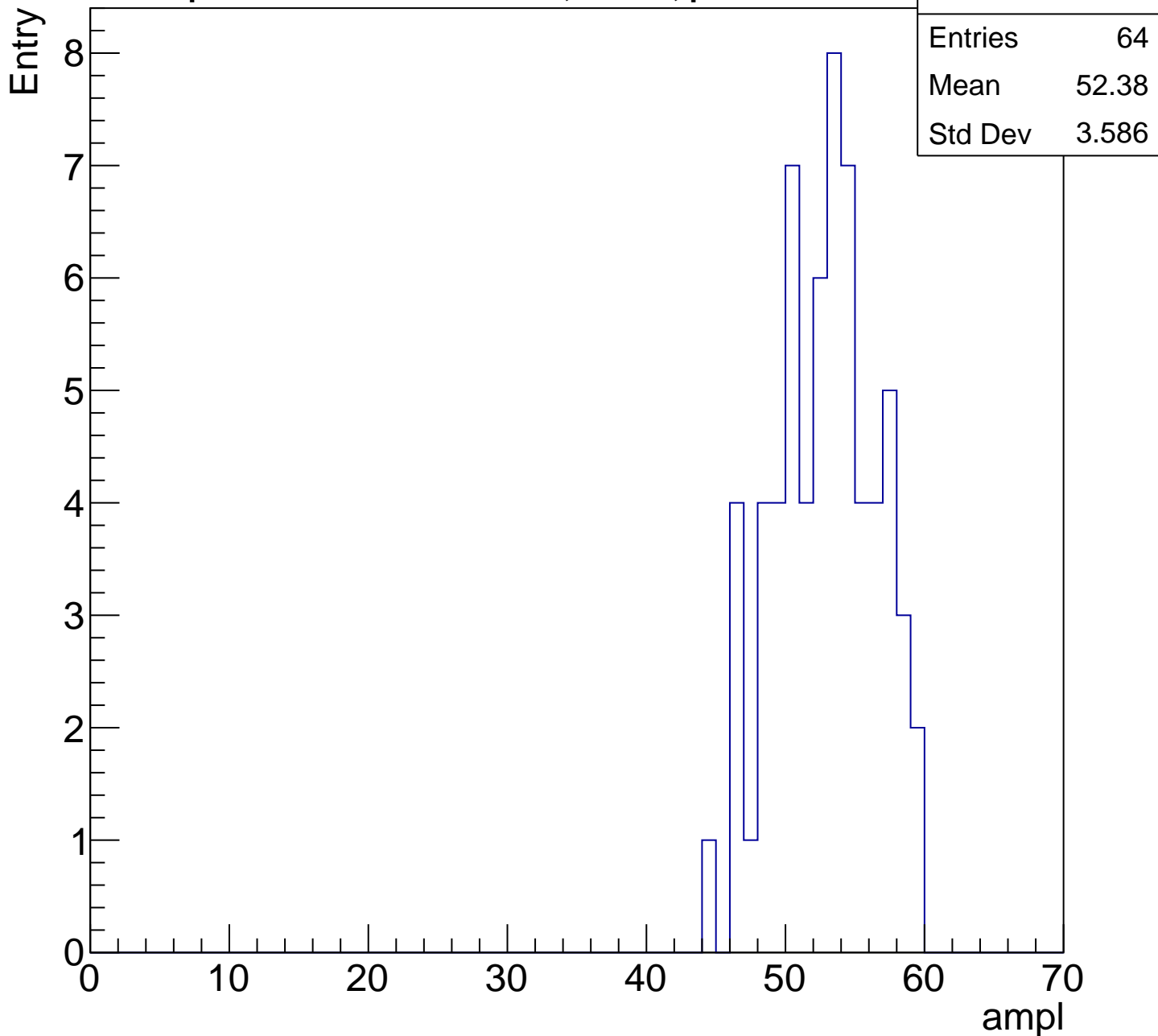
Entry

Entries	59
Mean	46.2
Std Dev	3.198



# B1L002S, U8-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

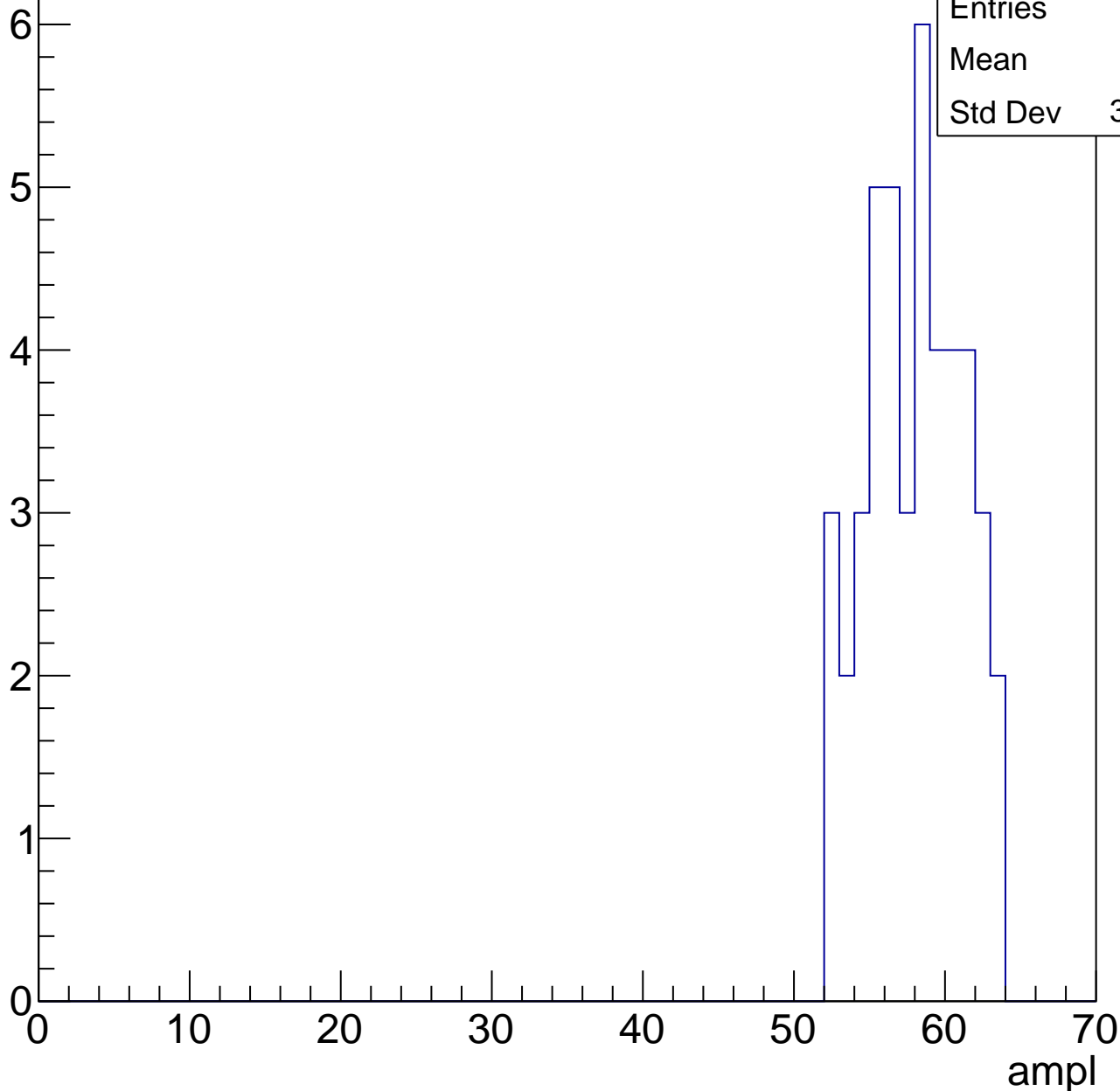


# B1L002S, U8-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	57.5
Std Dev	3.079

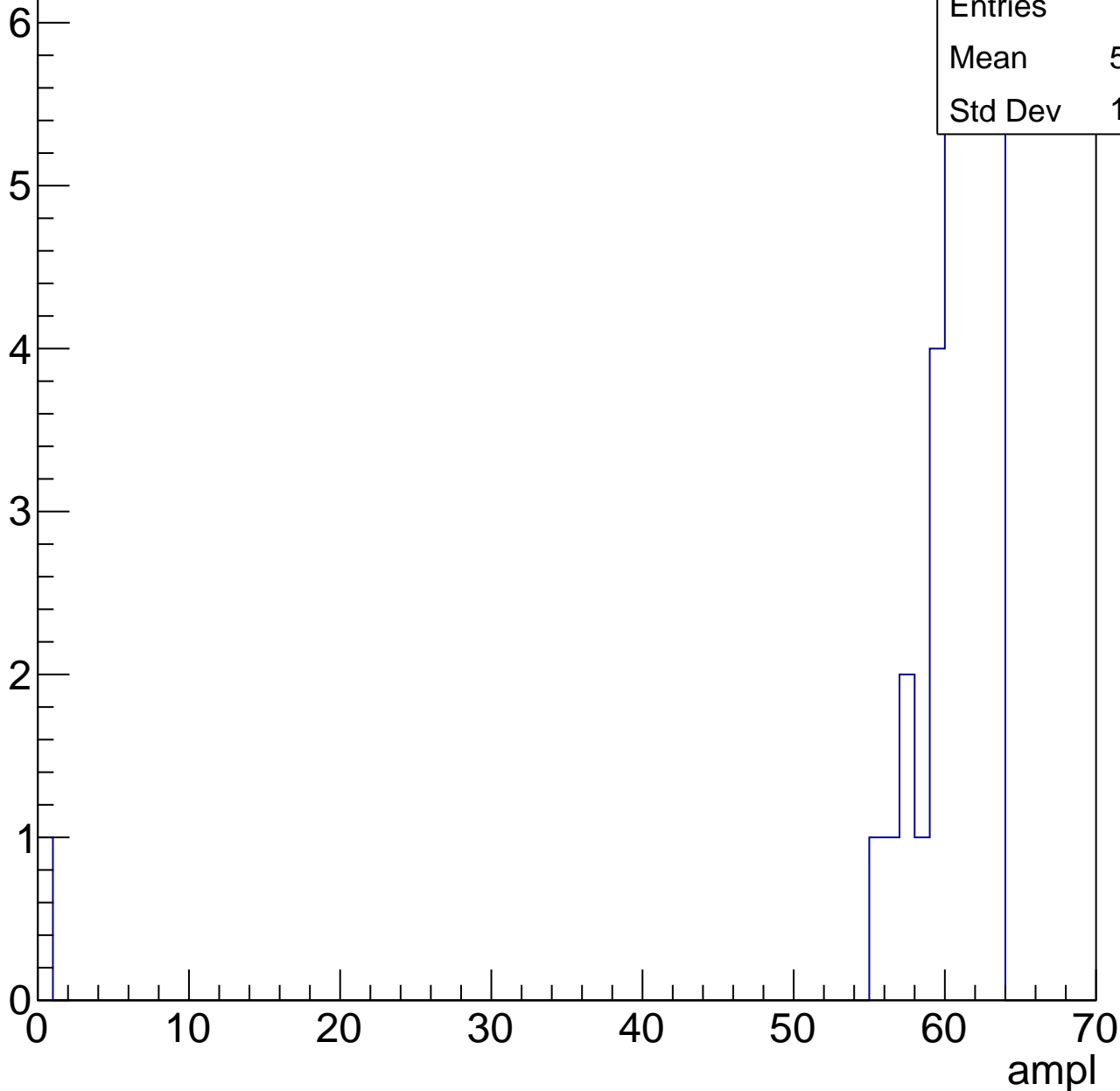


# B1L002S, U8-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	58.68
Std Dev	10.42





# B1L002S, U8-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch18, adc0

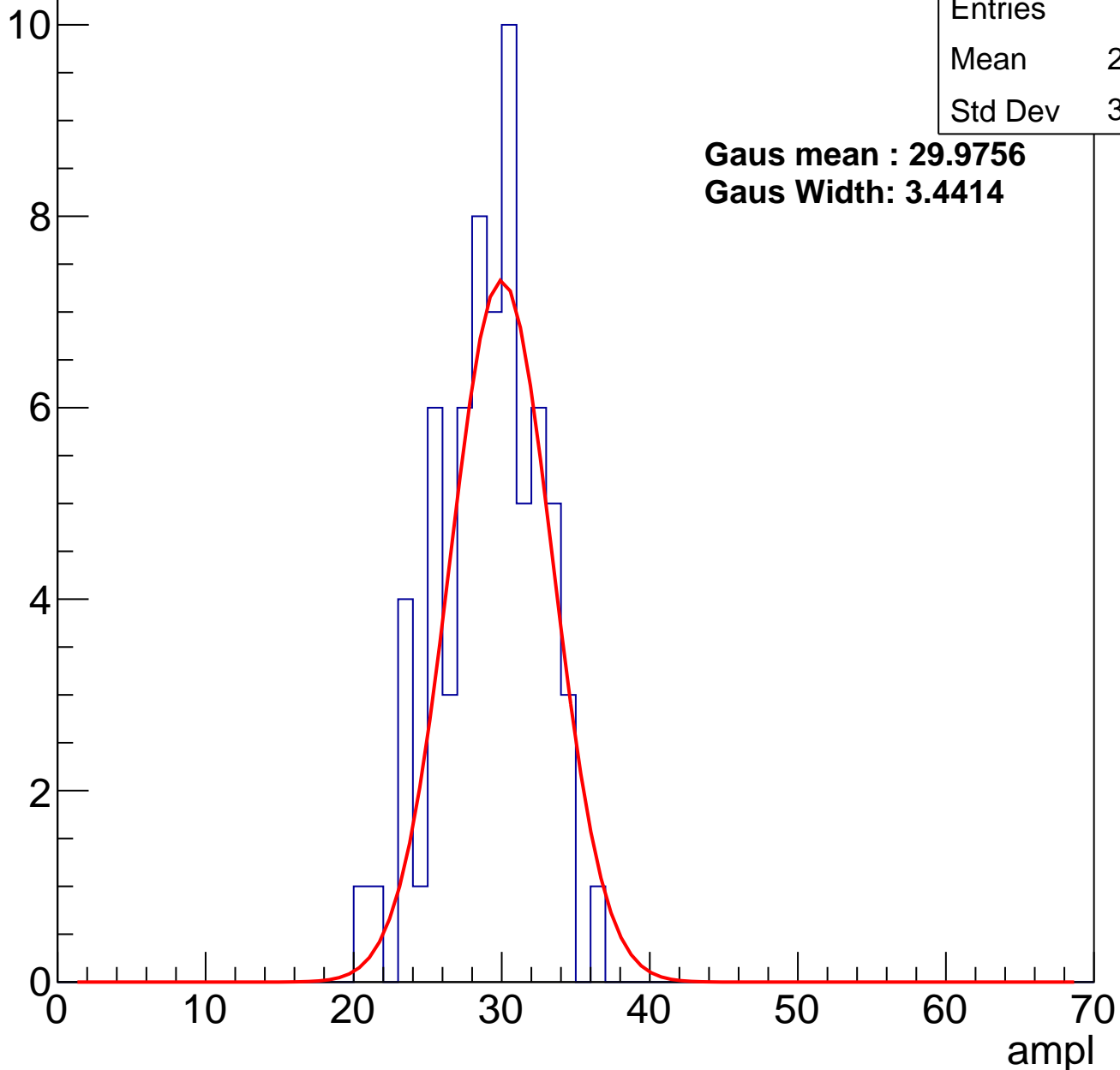
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	28.72
Std Dev	3.349

**Gaus mean : 29.9756**

**Gaus Width: 3.4414**

Entry



# B1L002S, U8-ch18, adc1

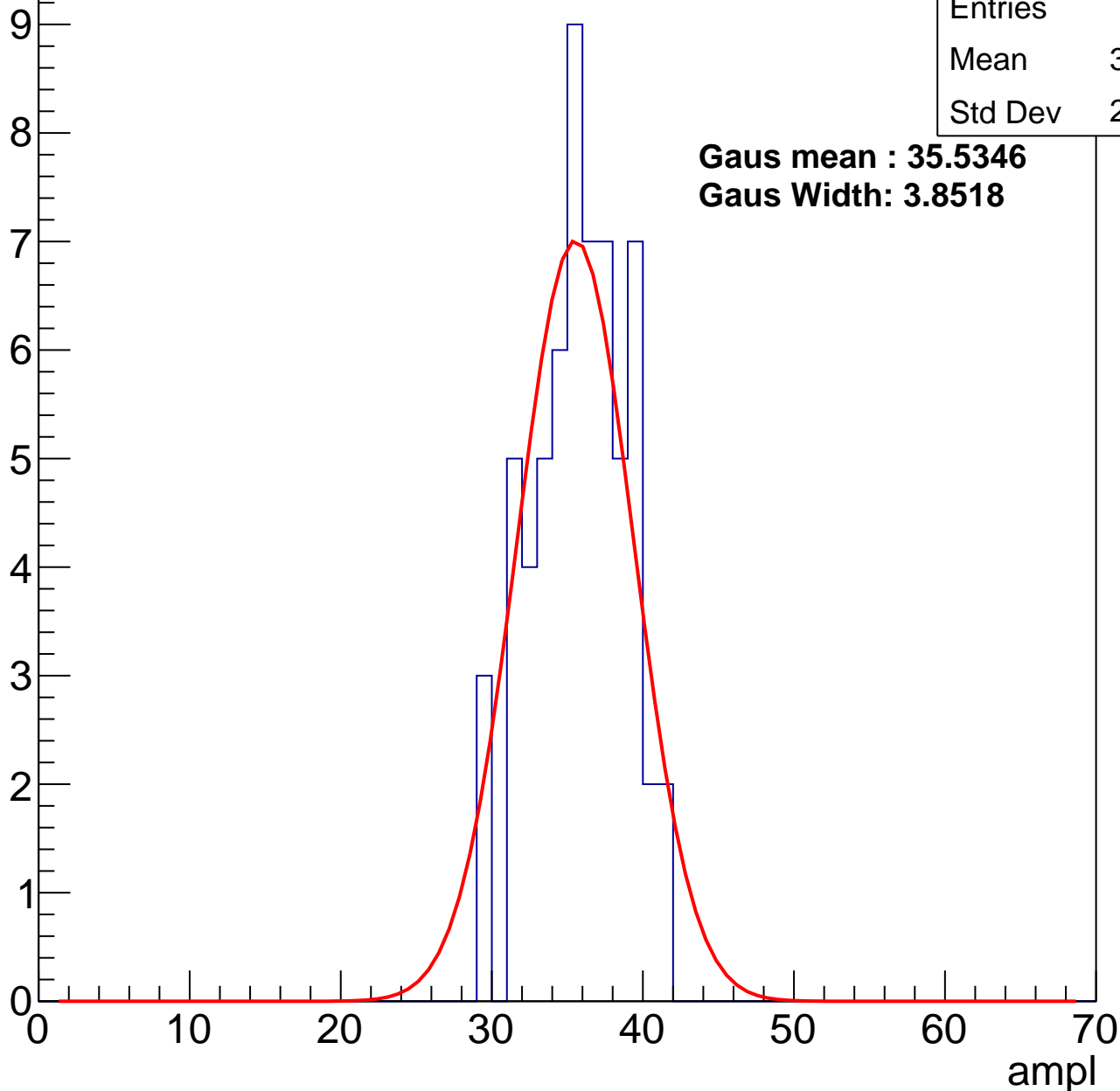
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	35.32
Std Dev	2.999

**Gaus mean : 35.5346**

**Gaus Width: 3.8518**

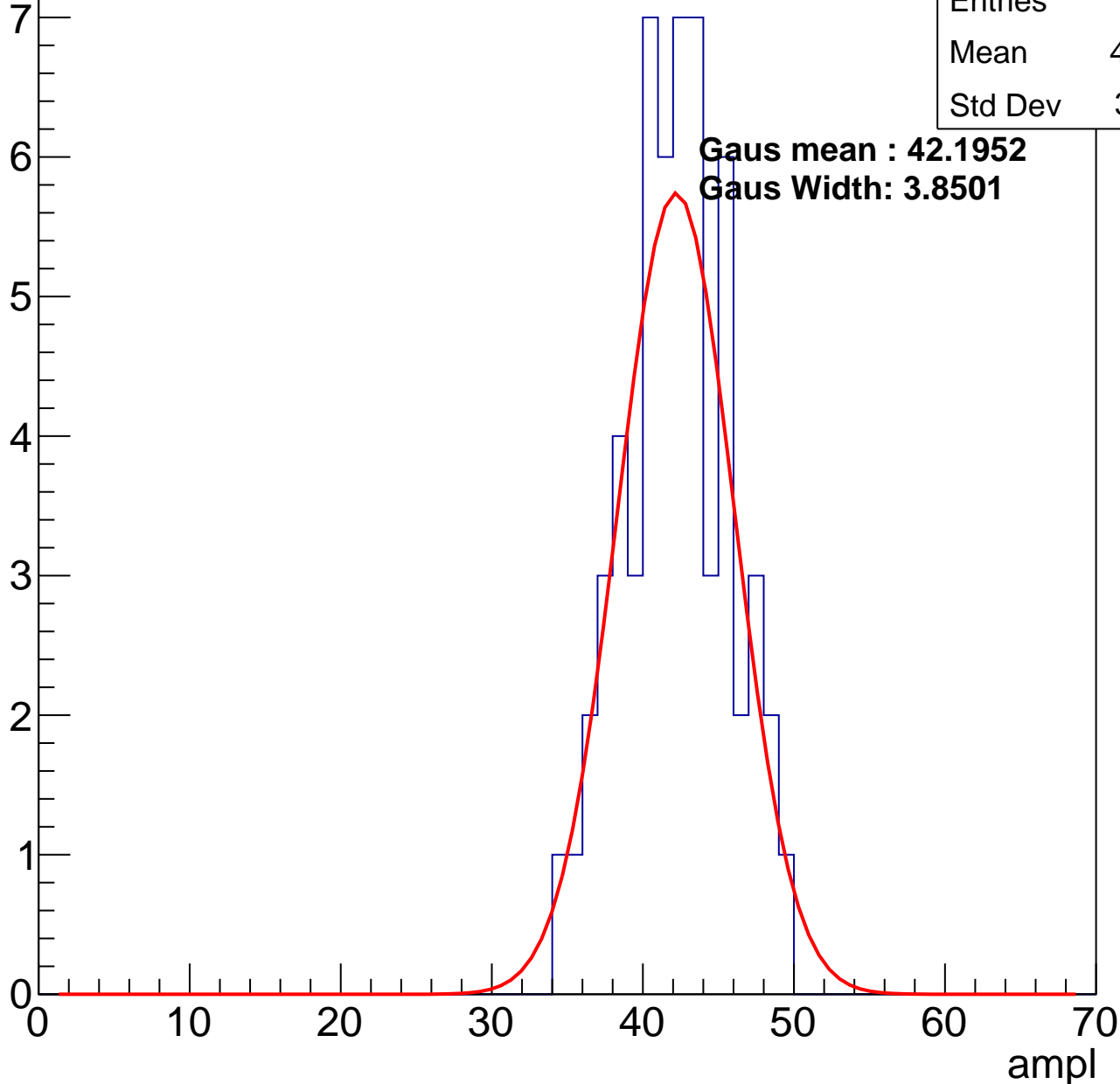


# B1L002S, U8-ch18, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.76
Std Dev	3.431

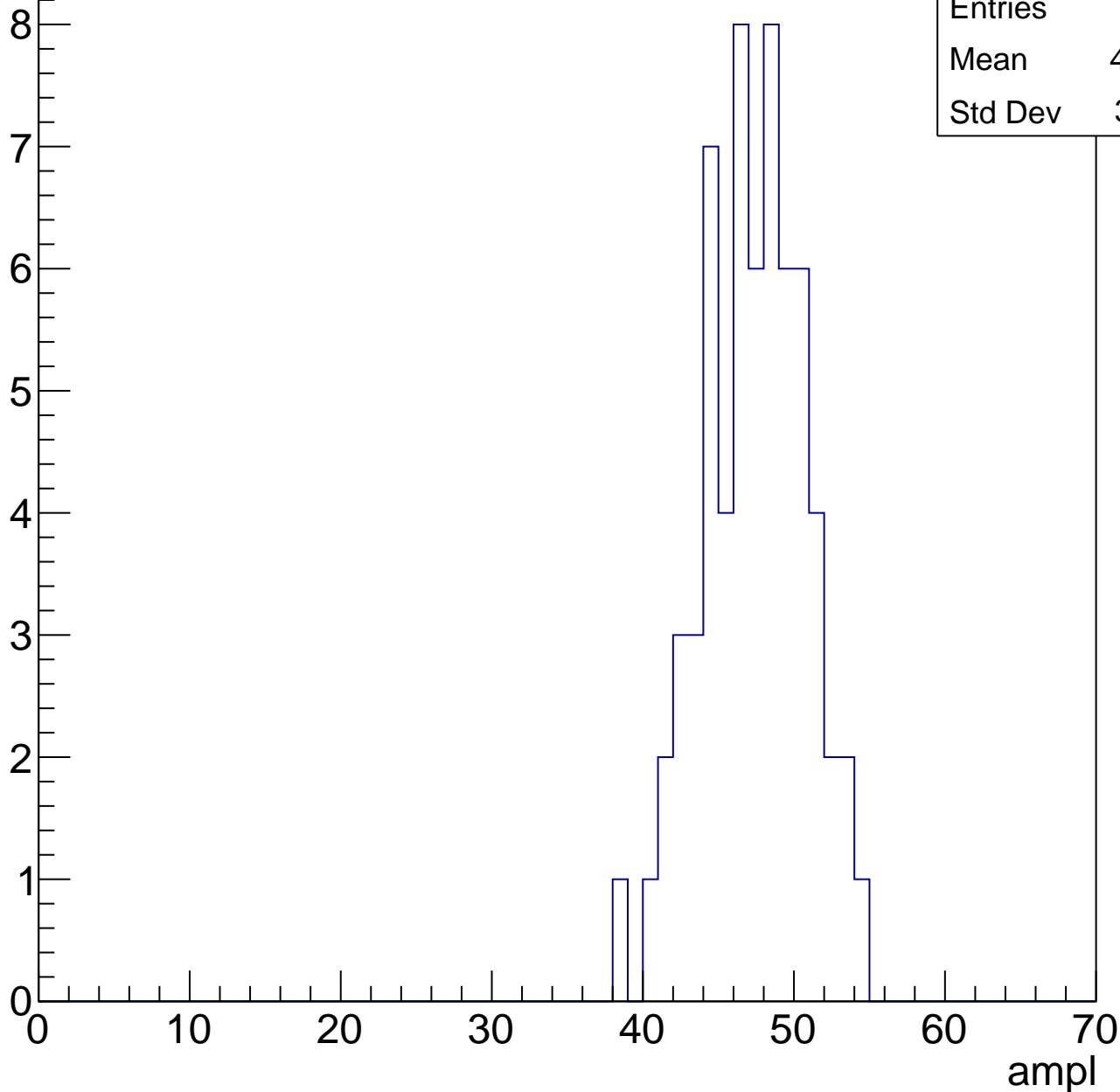


# B1L002S, U8-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

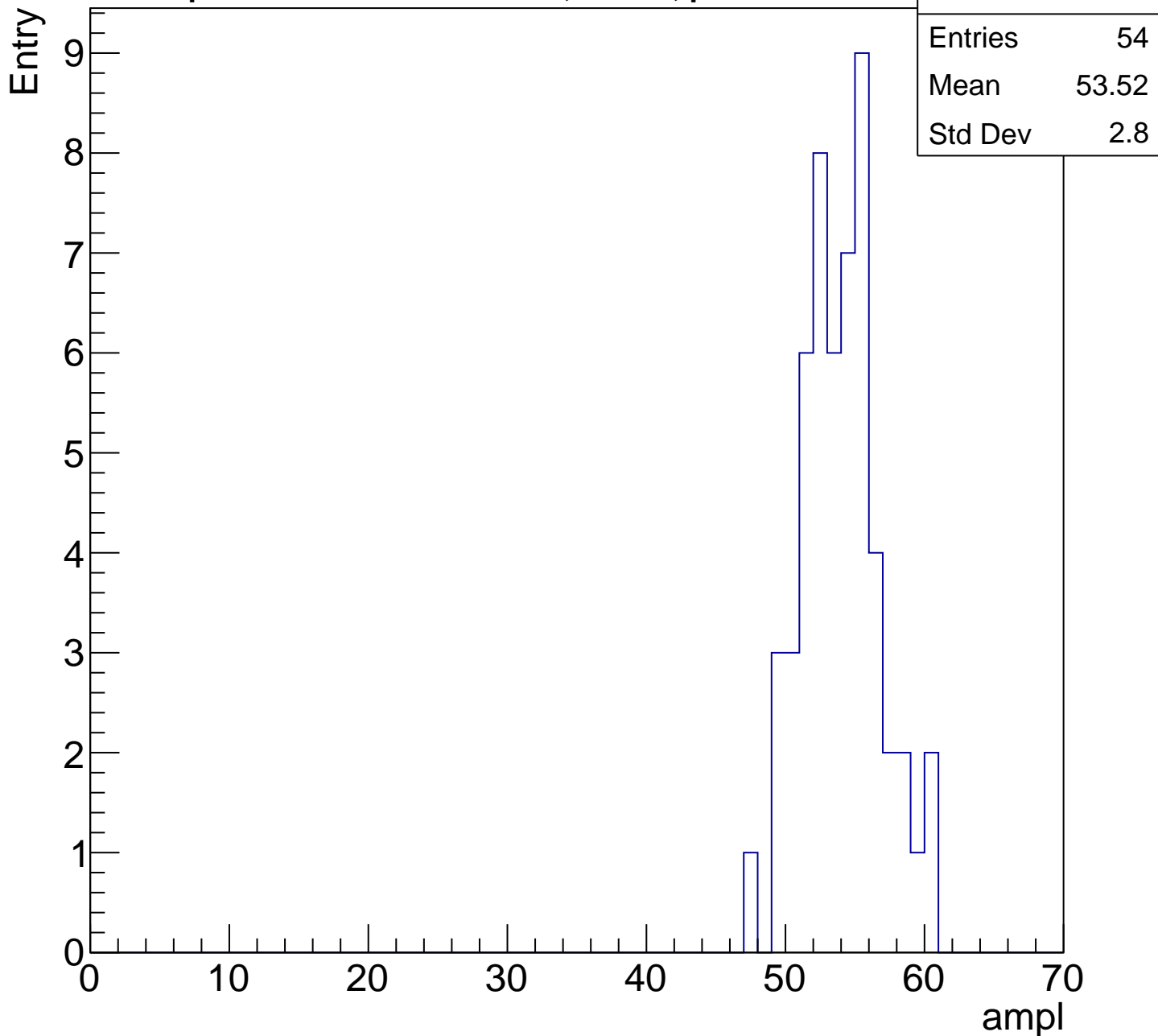
Entry

Entries	64
Mean	46.86
Std Dev	3.381



# B1L002S, U8-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

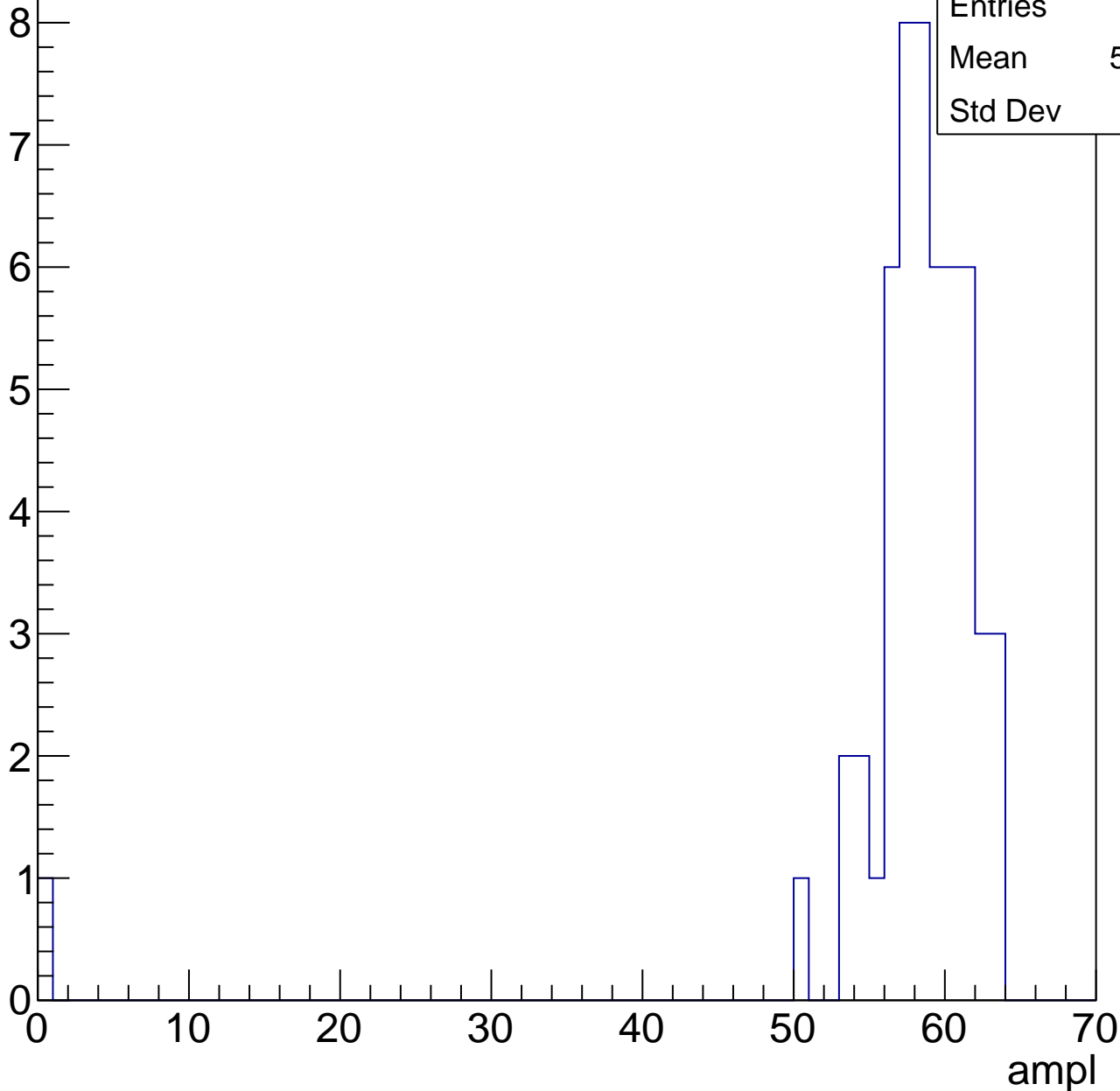


# B1L002S, U8-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.17
Std Dev	8.38

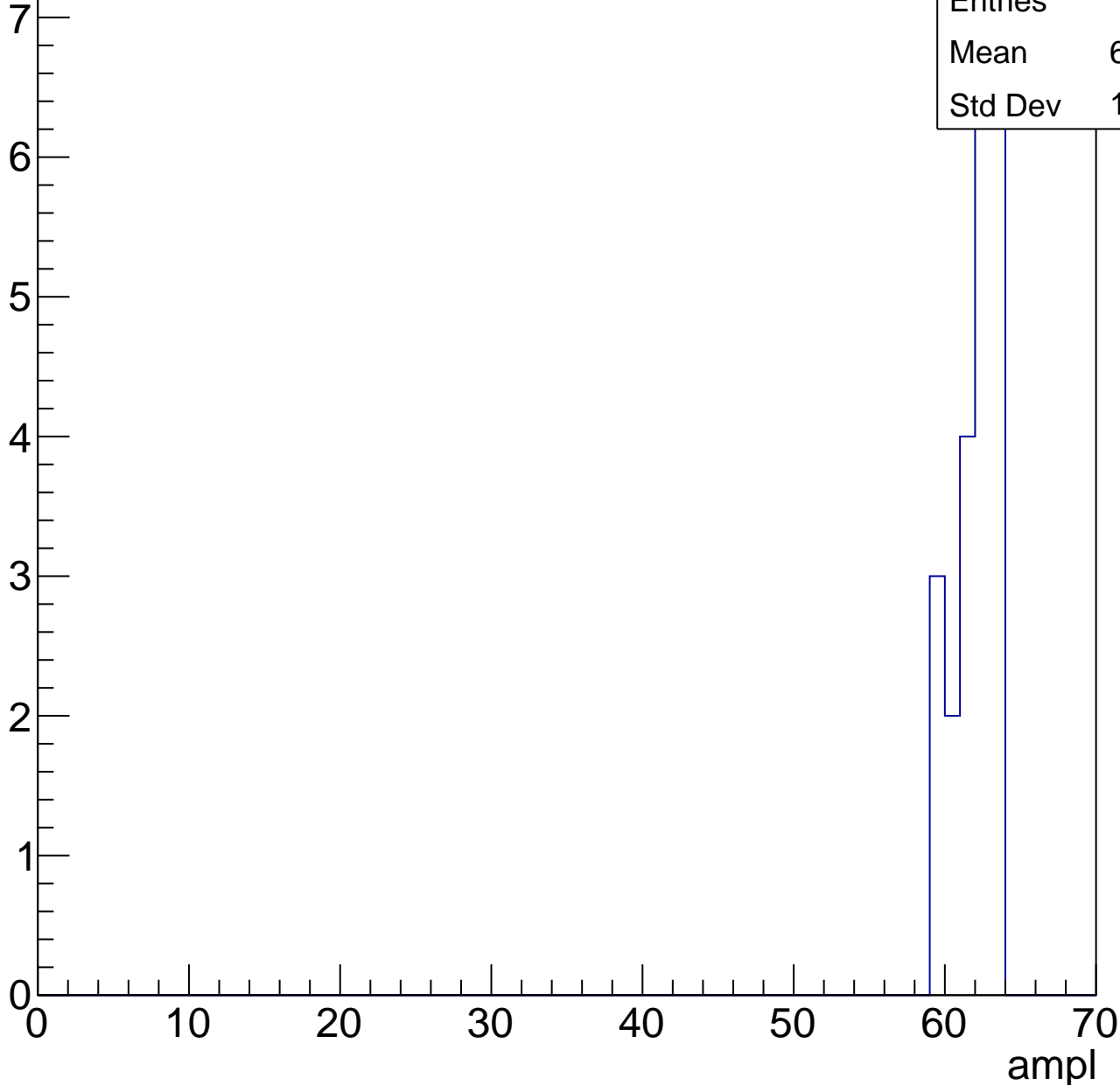


# B1L002S, U8-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.57
Std Dev	1.346





# B1L002S, U8-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch19, adc0

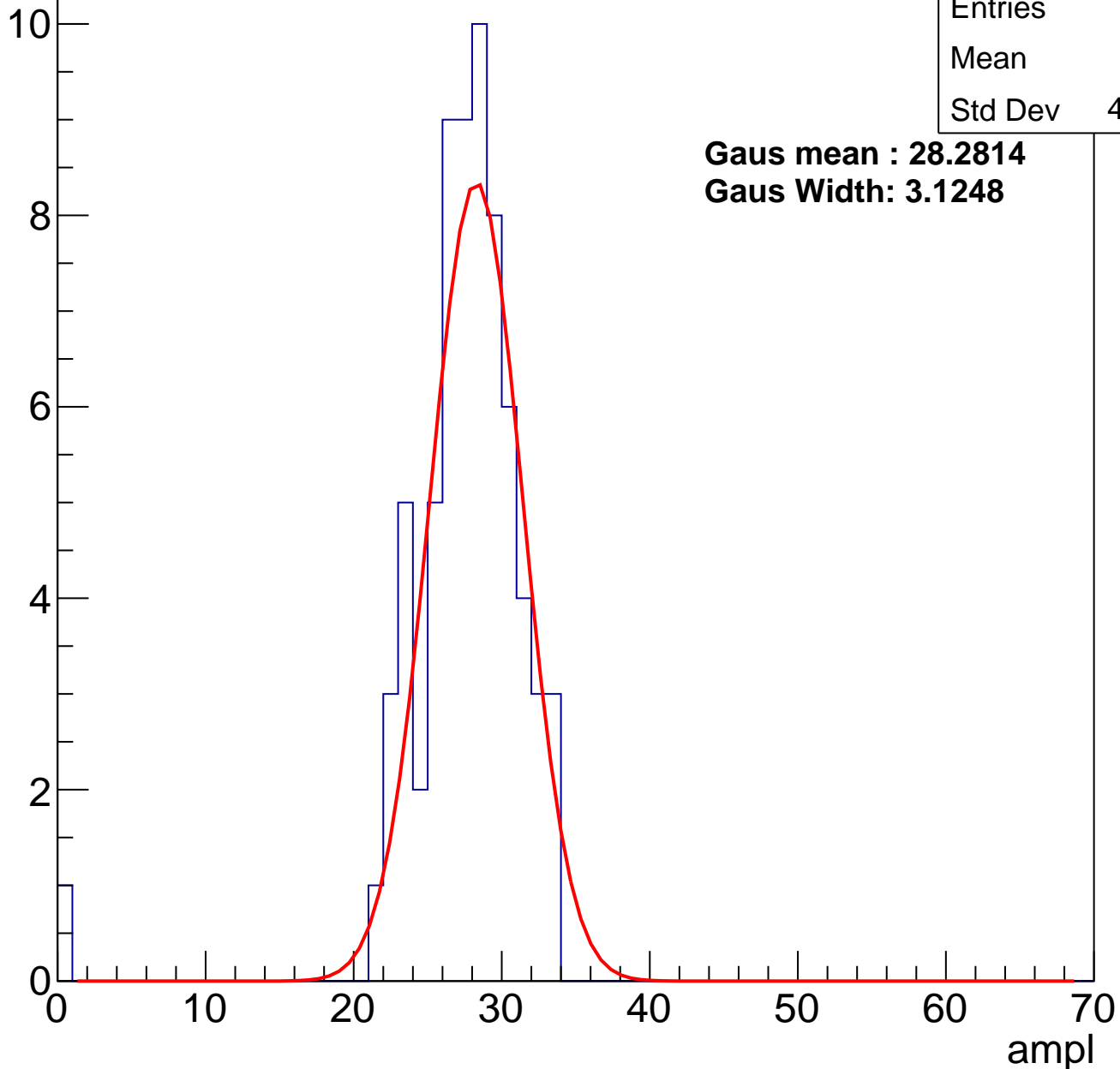
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27
Std Dev	4.357

**Gaus mean : 28.2814**

**Gaus Width: 3.1248**

Entry



# B1L002S, U8-ch19, adc1

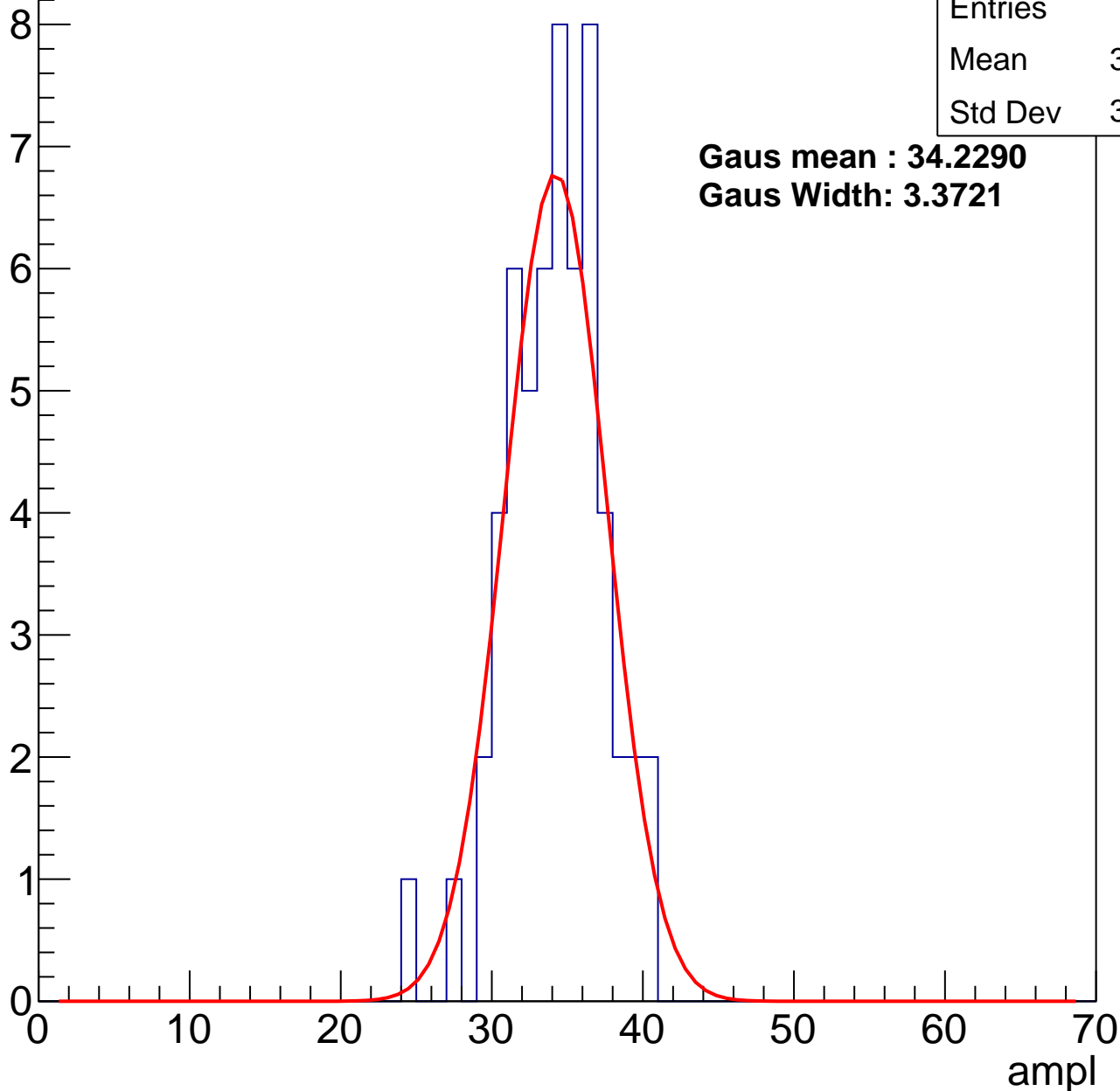
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.77
Std Dev	3.157

**Gaus mean : 34.2290**

**Gaus Width: 3.3721**



# B1L002S, U8-ch19, adc2

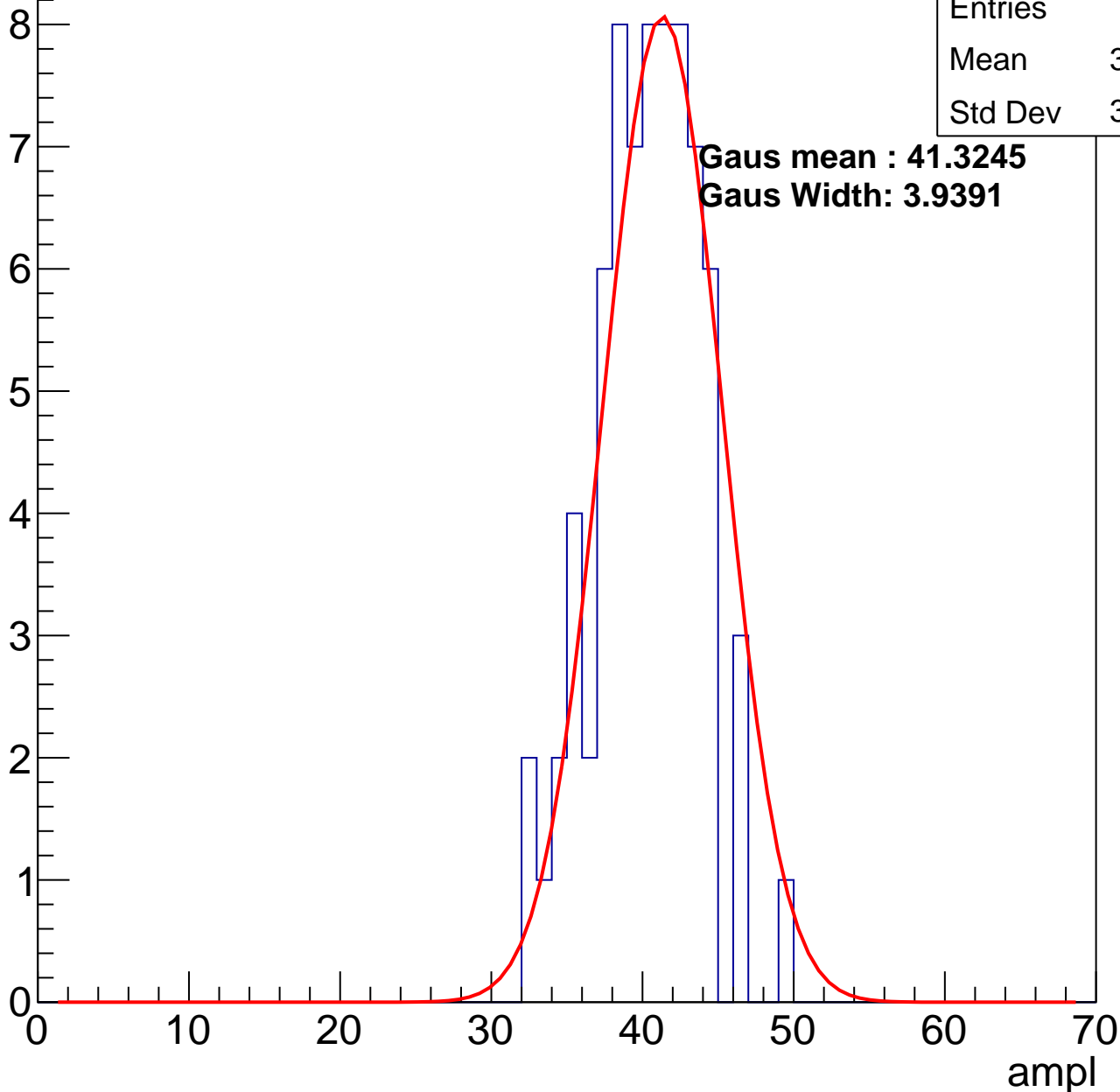
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	39.89
Std Dev	3.435

**Gaus mean : 41.3245**

**Gaus Width: 3.9391**



# B1L002S, U8-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

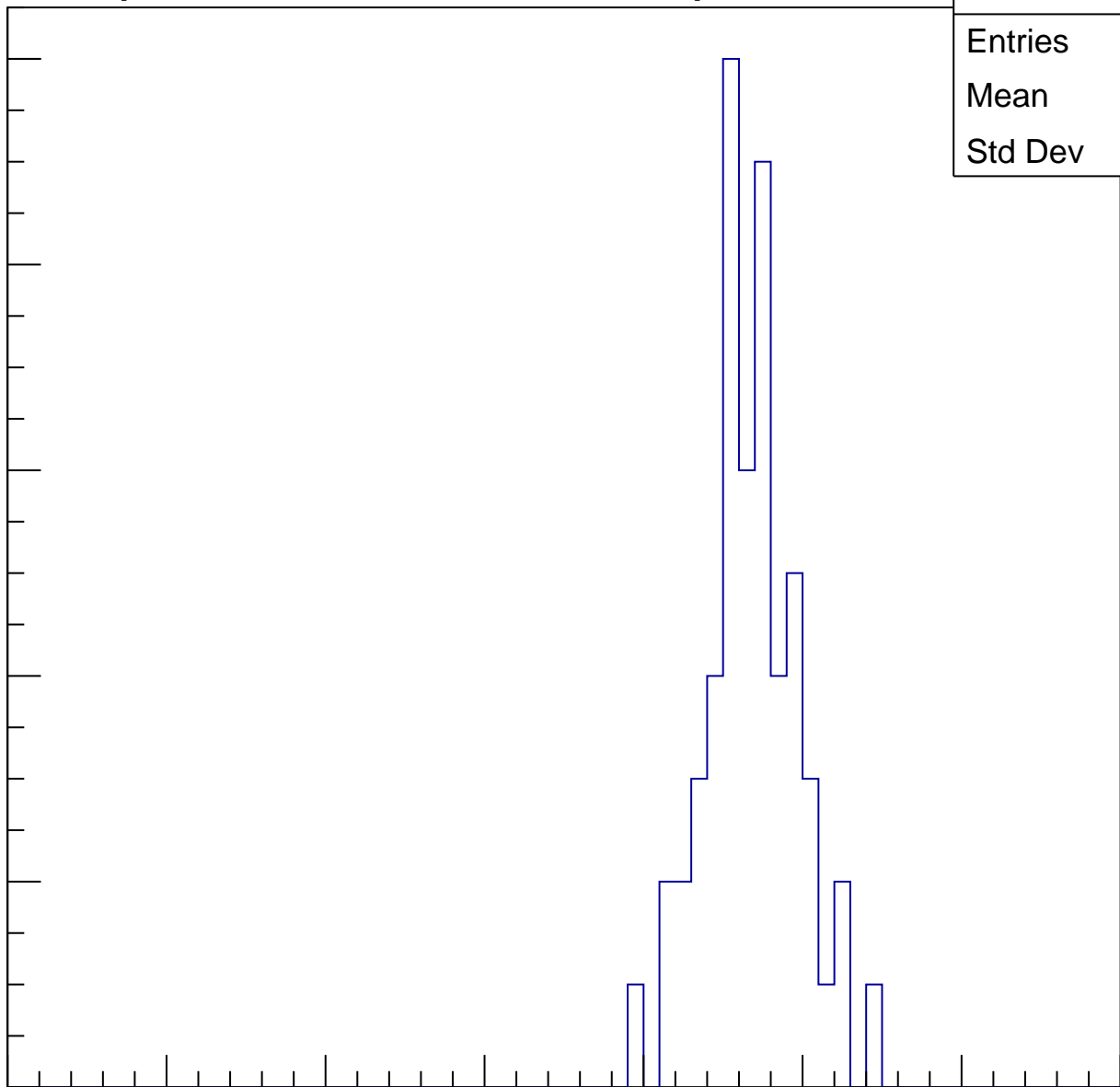
Entries	53
Mean	46.32
Std Dev	2.938

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

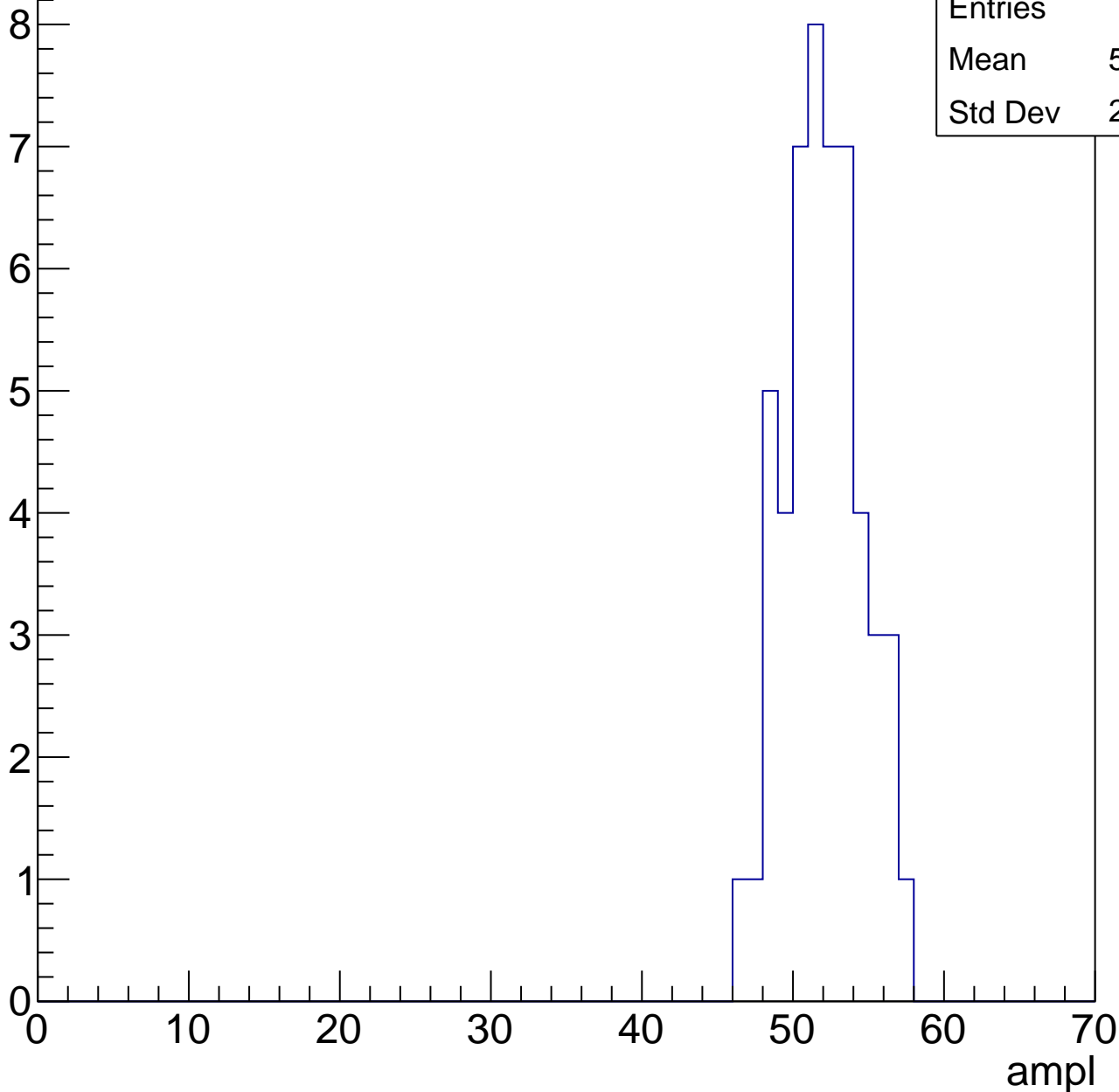


# B1L002S, U8-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	51.53
Std Dev	2.523

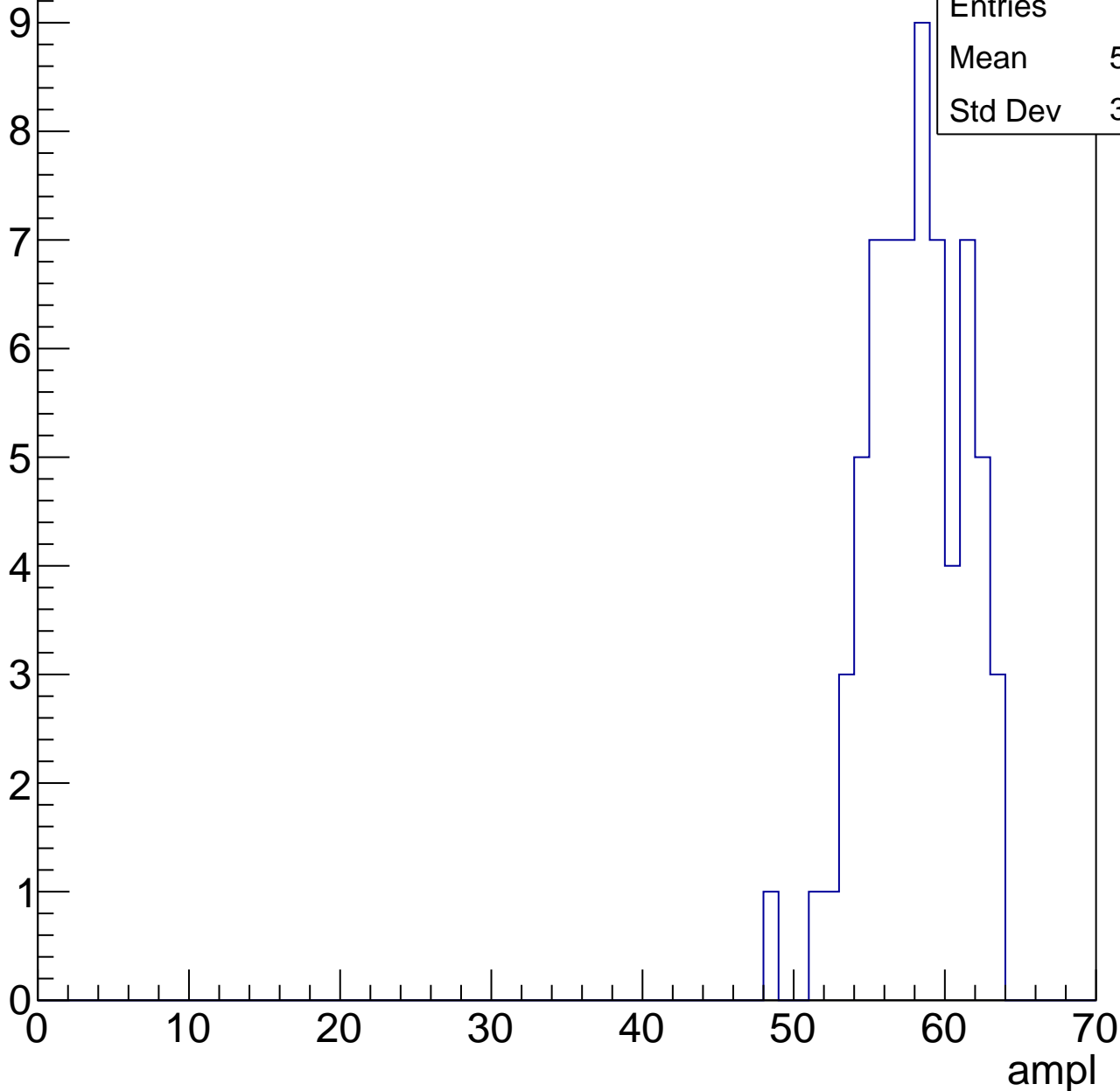


# B1L002S, U8-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	57.57
Std Dev	3.154

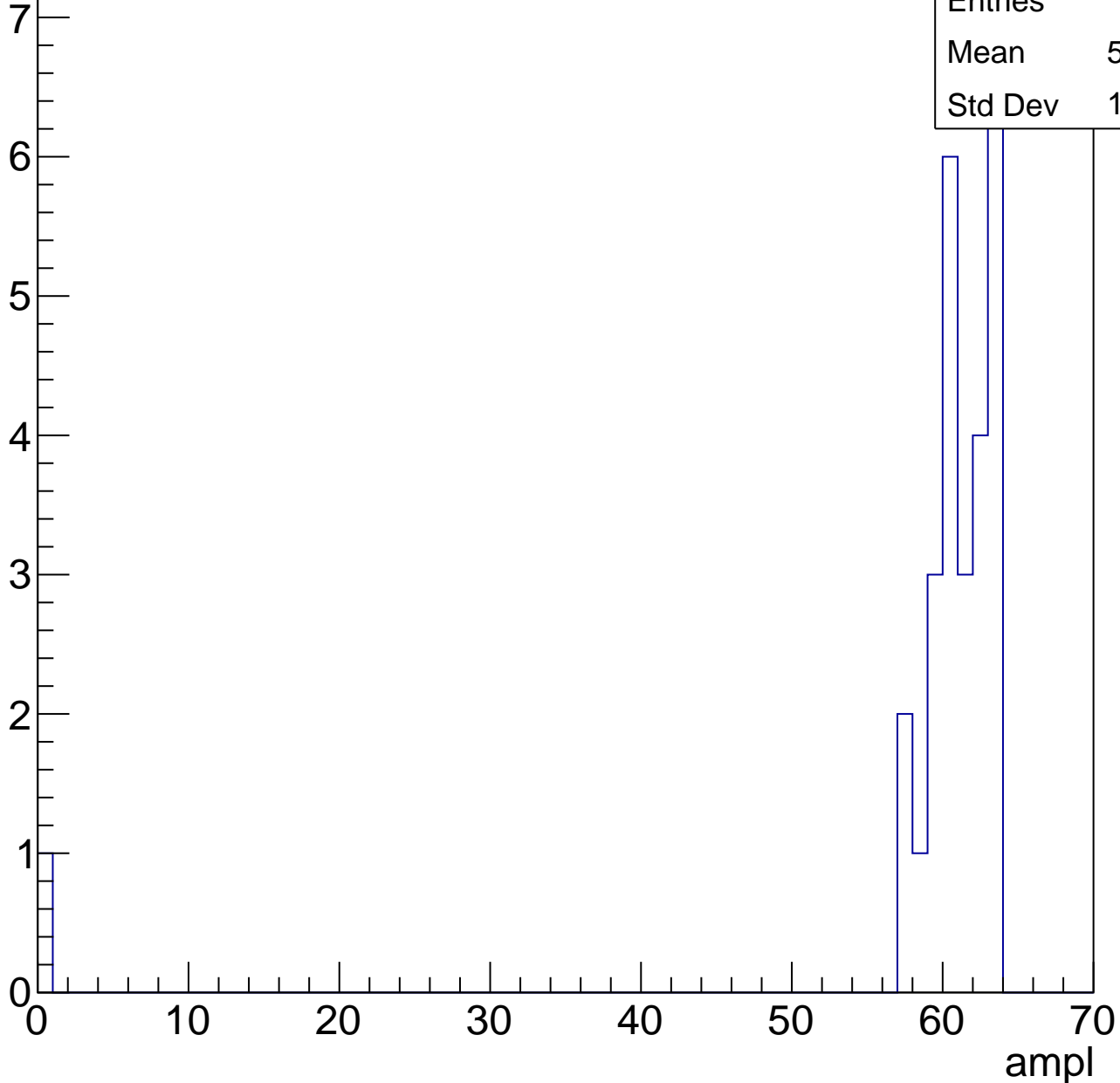


# B1L002S, U8-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.56
Std Dev	11.63

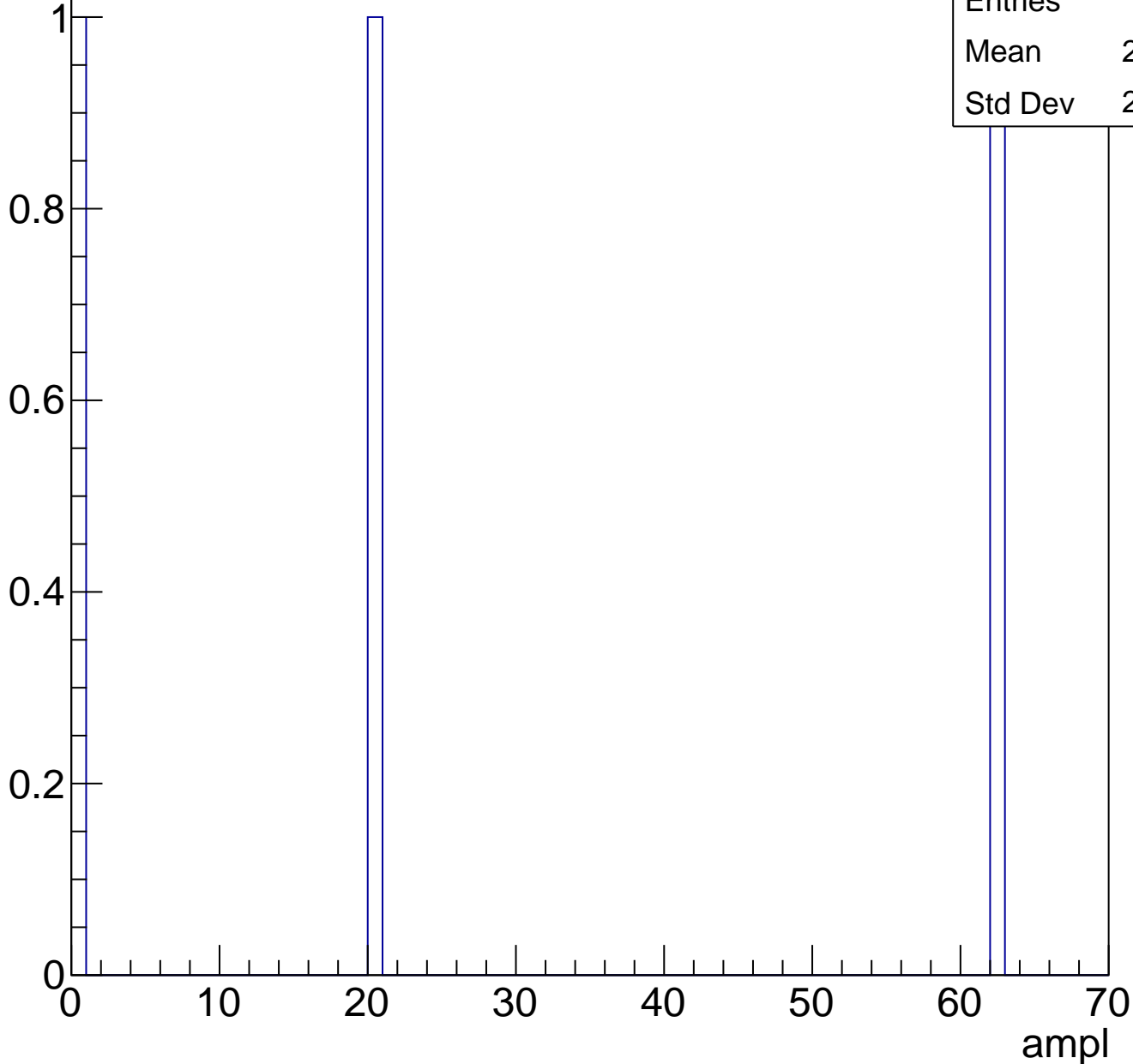




# B1L002S, U8-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.33
Std Dev	25.84

# B1L002S, U8-ch20, adc0

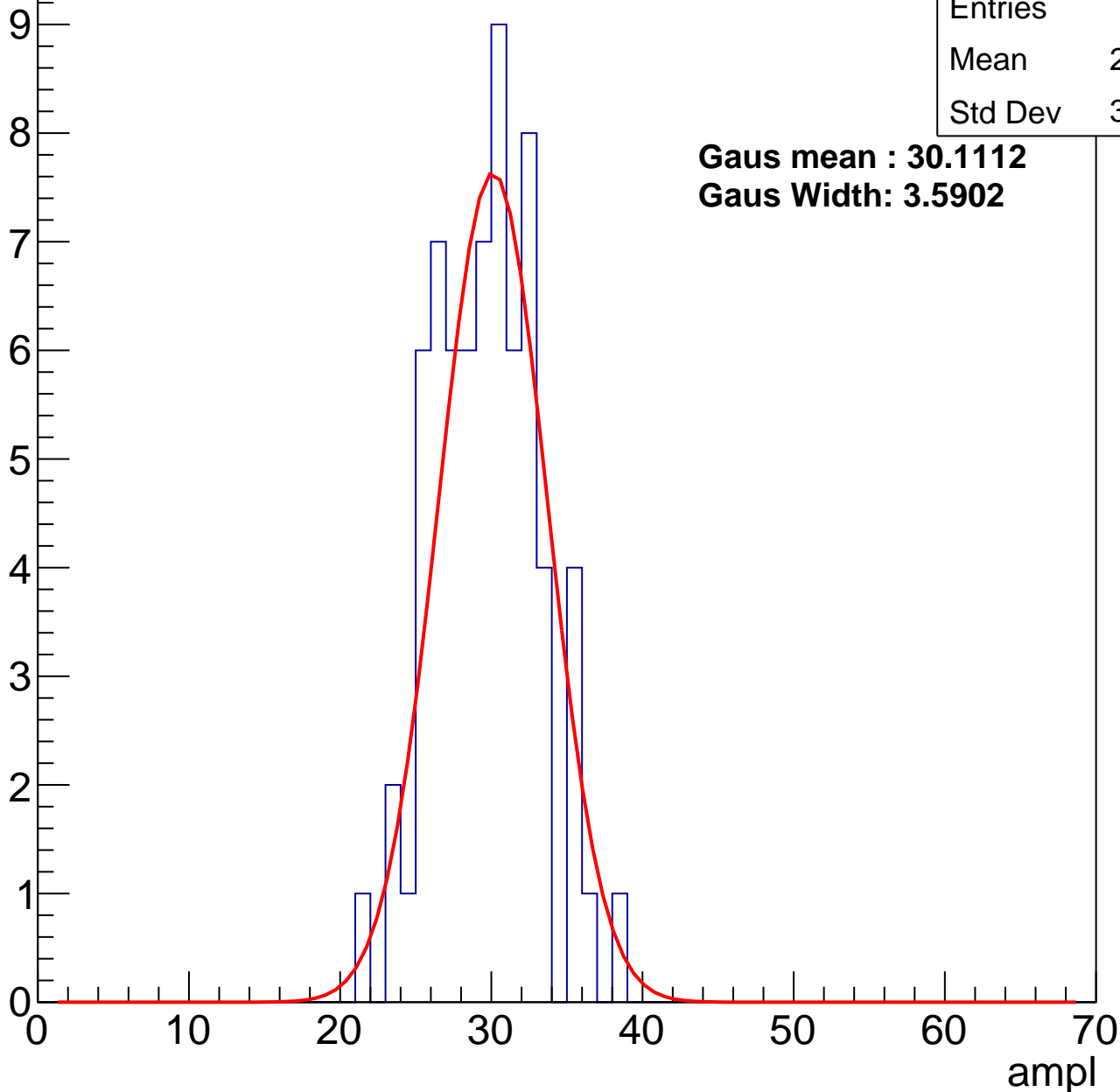
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	29.19
Std Dev	3.385

**Gaus mean : 30.1112**

**Gaus Width: 3.5902**



# B1L002S, U8-ch20, adc1

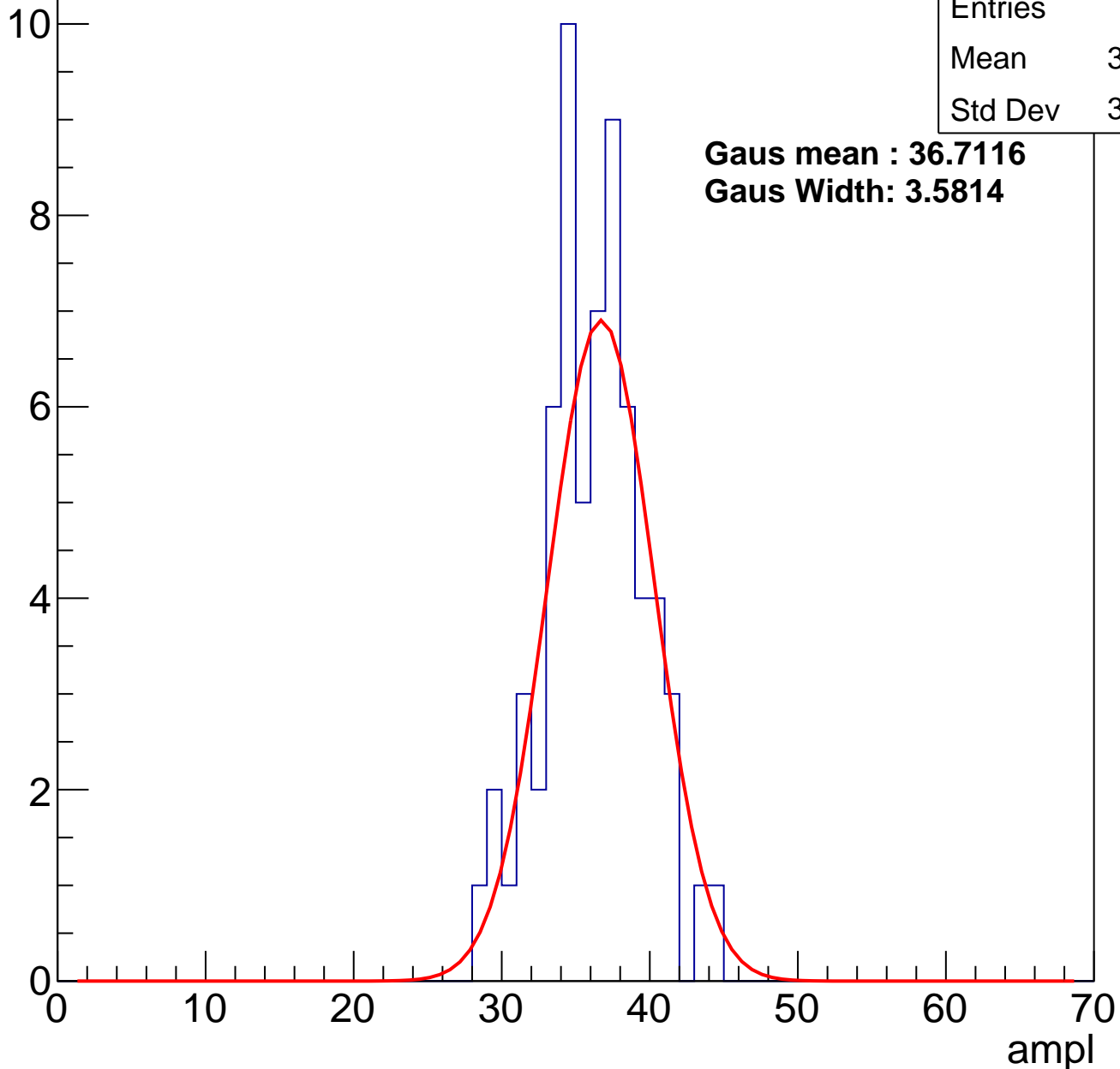
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	35.77
Std Dev	3.332

**Gaus mean : 36.7116**

**Gaus Width: 3.5814**

Entry



# B1L002S, U8-ch20, adc2

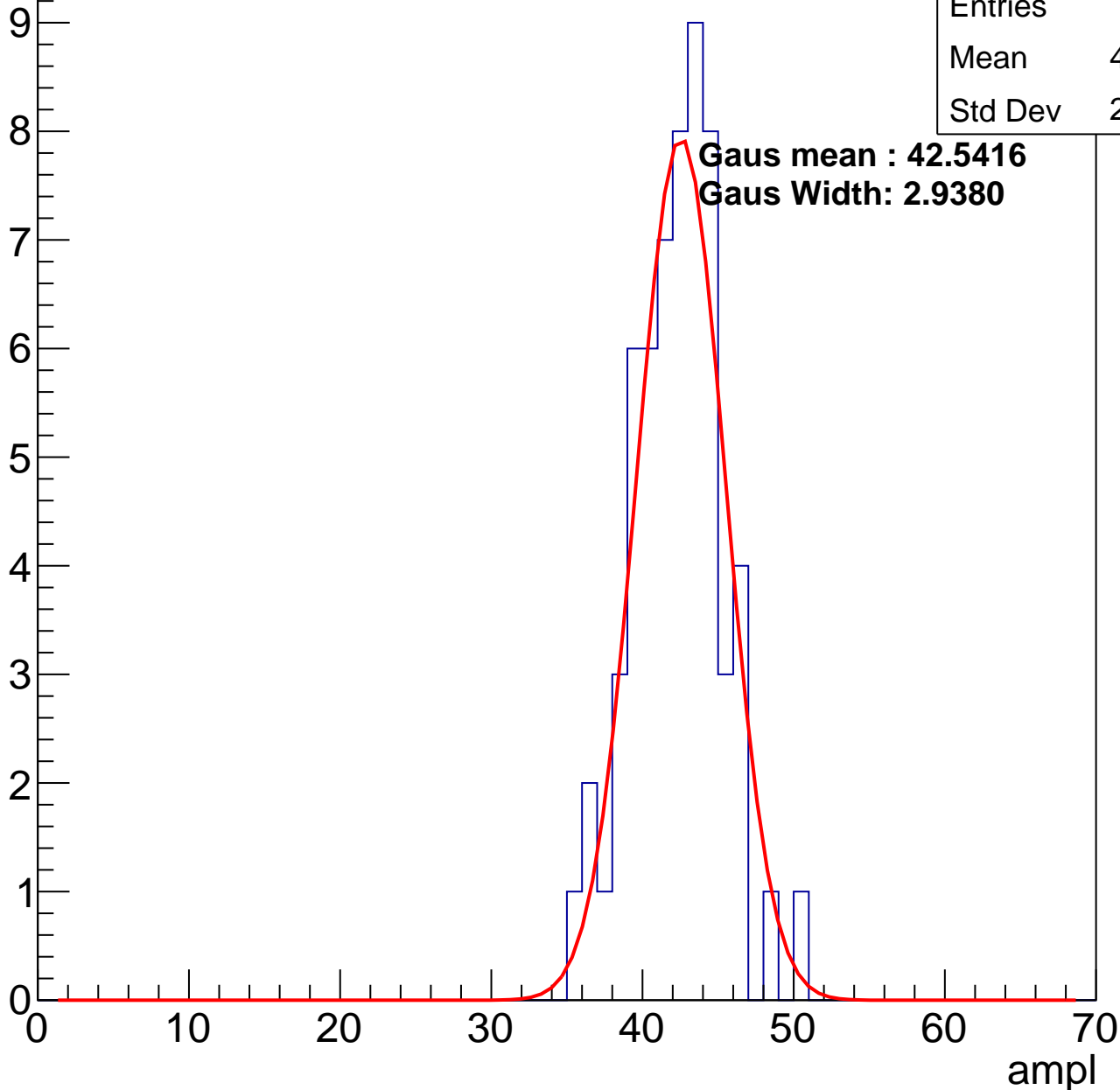
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.85
Std Dev	2.914

**Gaus mean : 42.5416**

**Gaus Width: 2.9380**

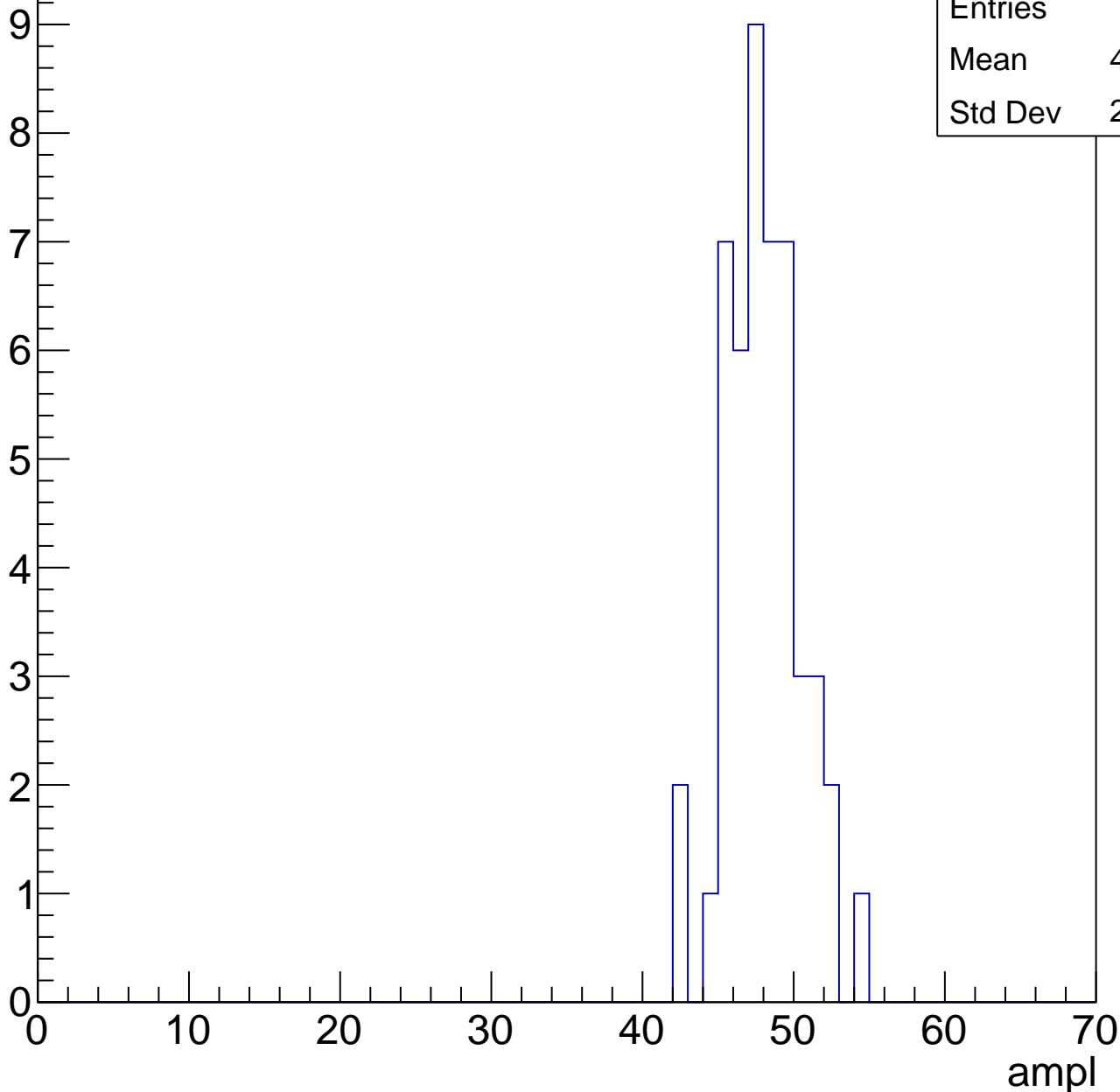


# B1L002S, U8-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

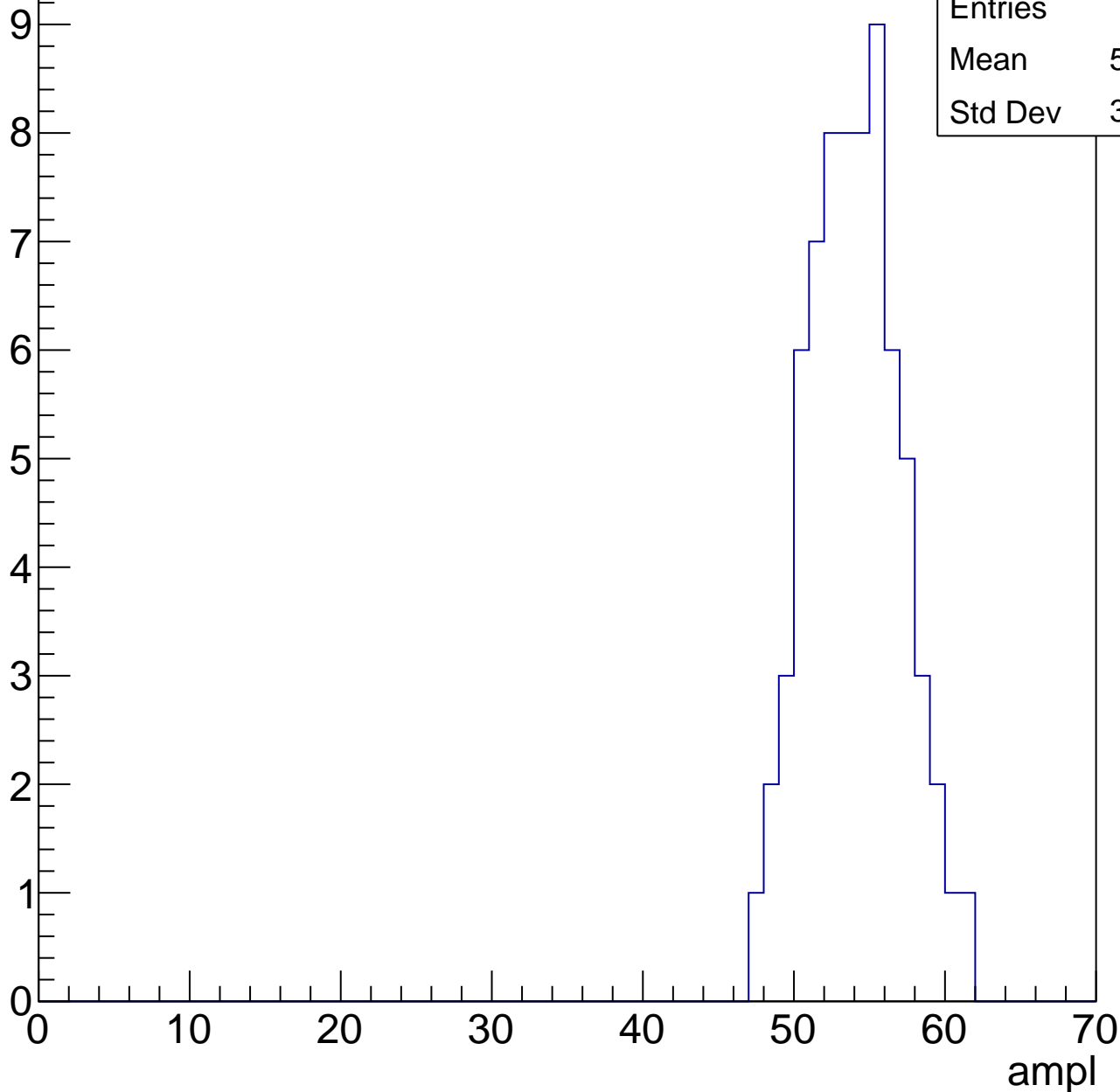
Entries	48
Mean	47.54
Std Dev	2.449



# B1L002S, U8-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



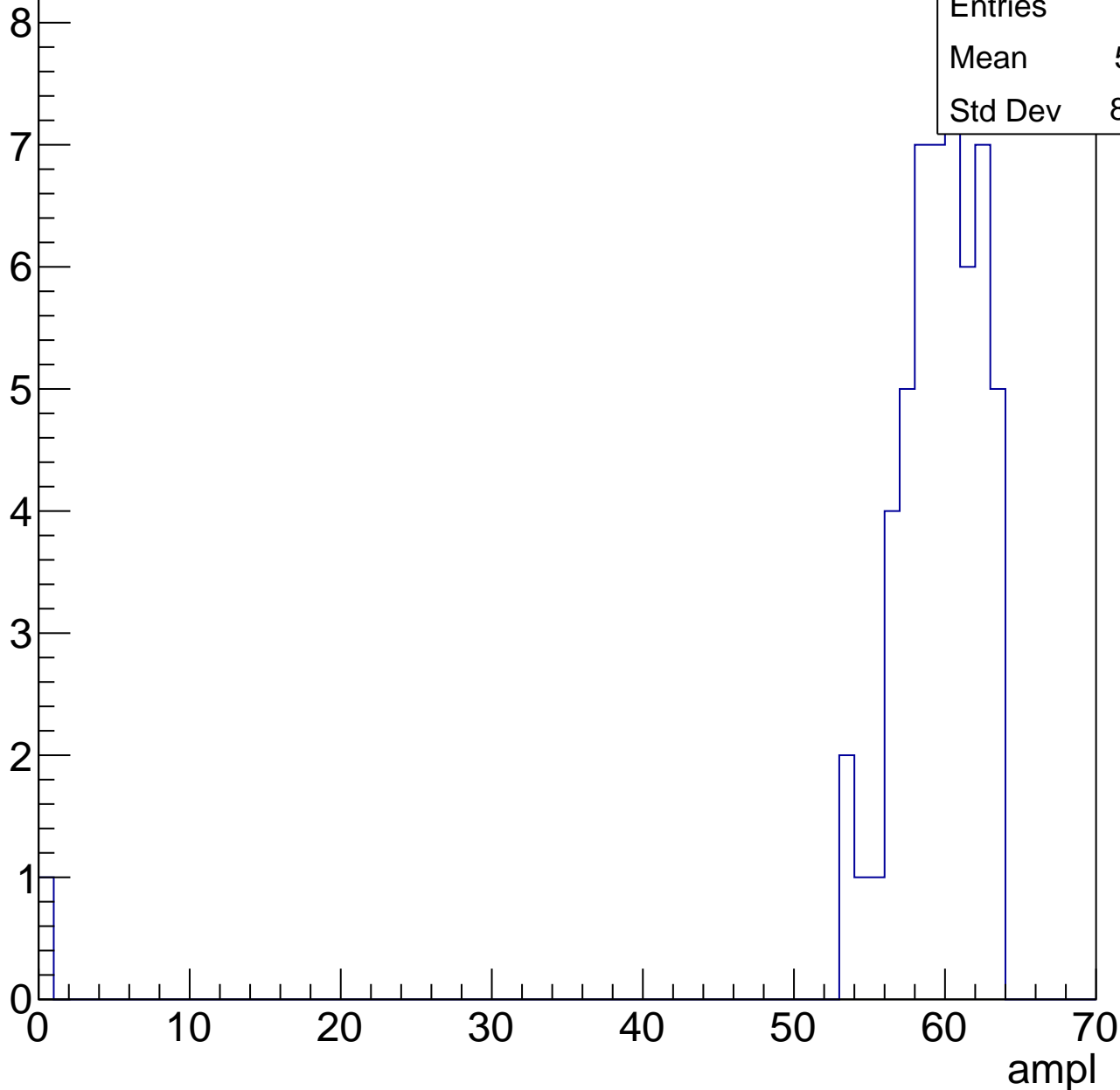
Entries	70
Mean	53.54
Std Dev	3.027

# B1L002S, U8-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.11
Std Dev	8.377

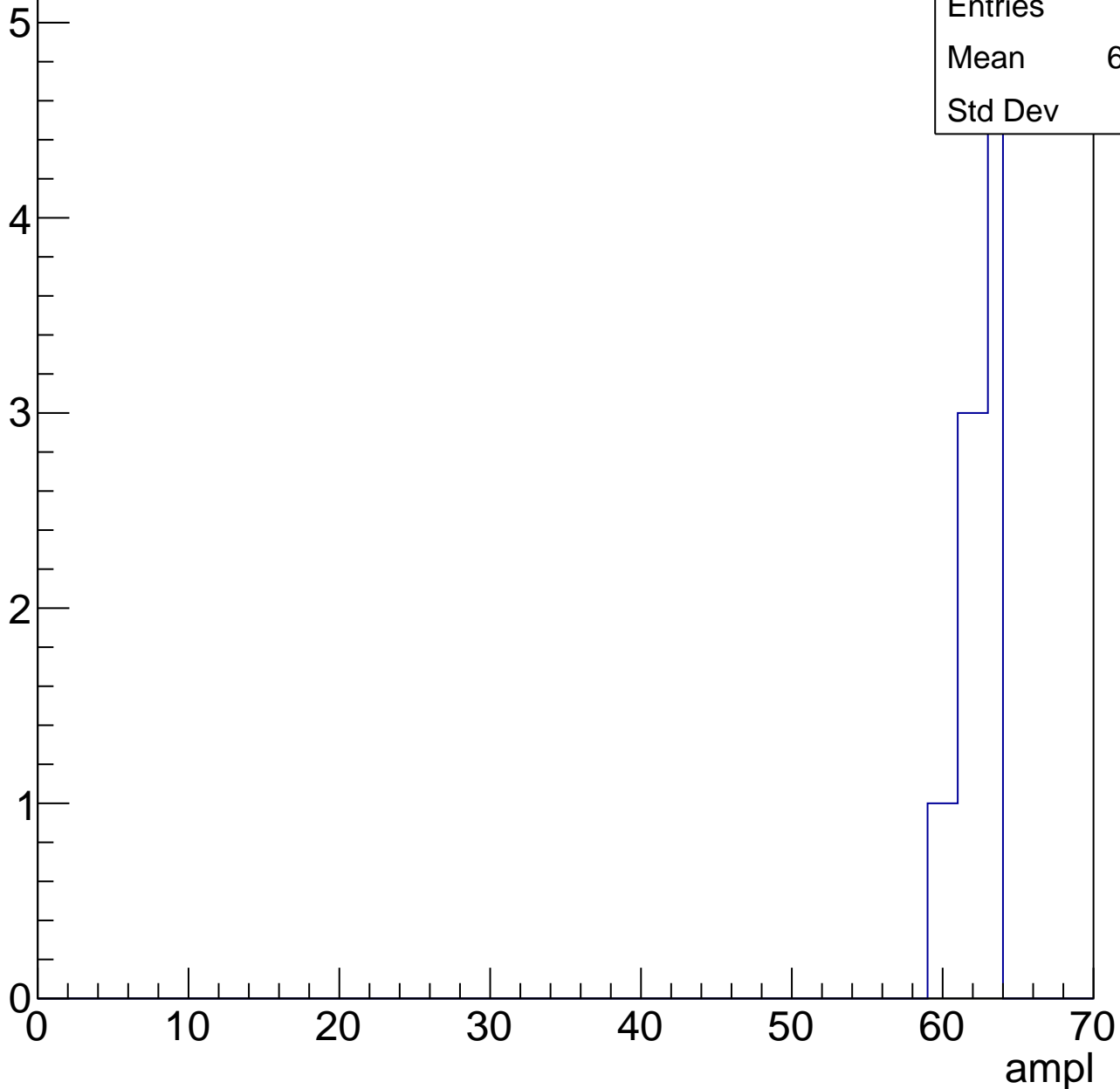


# B1L002S, U8-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.77
Std Dev	1.25





# B1L002S, U8-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch21, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	28.77
Std Dev	2.929

**Gaus mean : 28.9755**

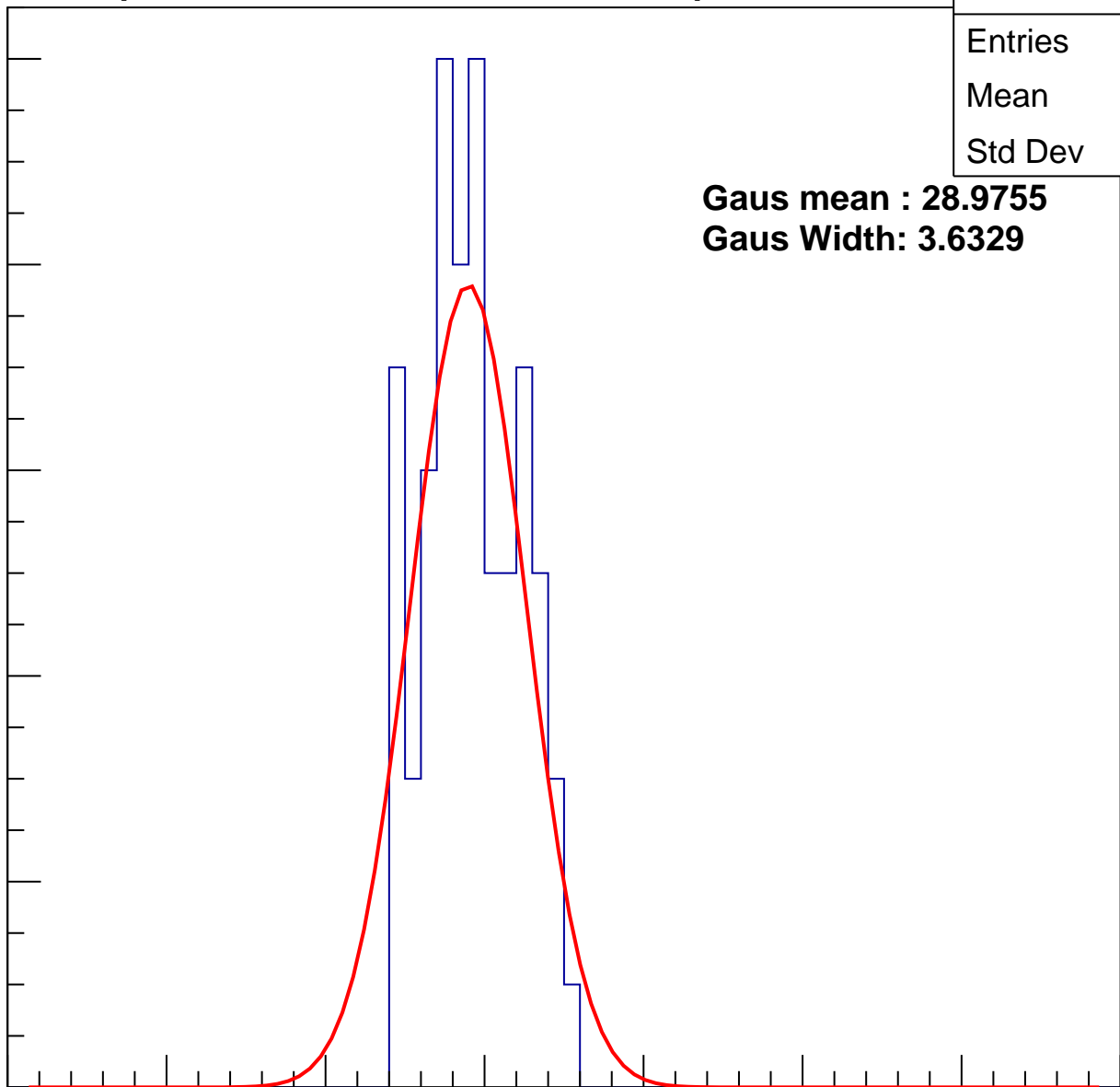
**Gaus Width: 3.6329**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch21, adc1

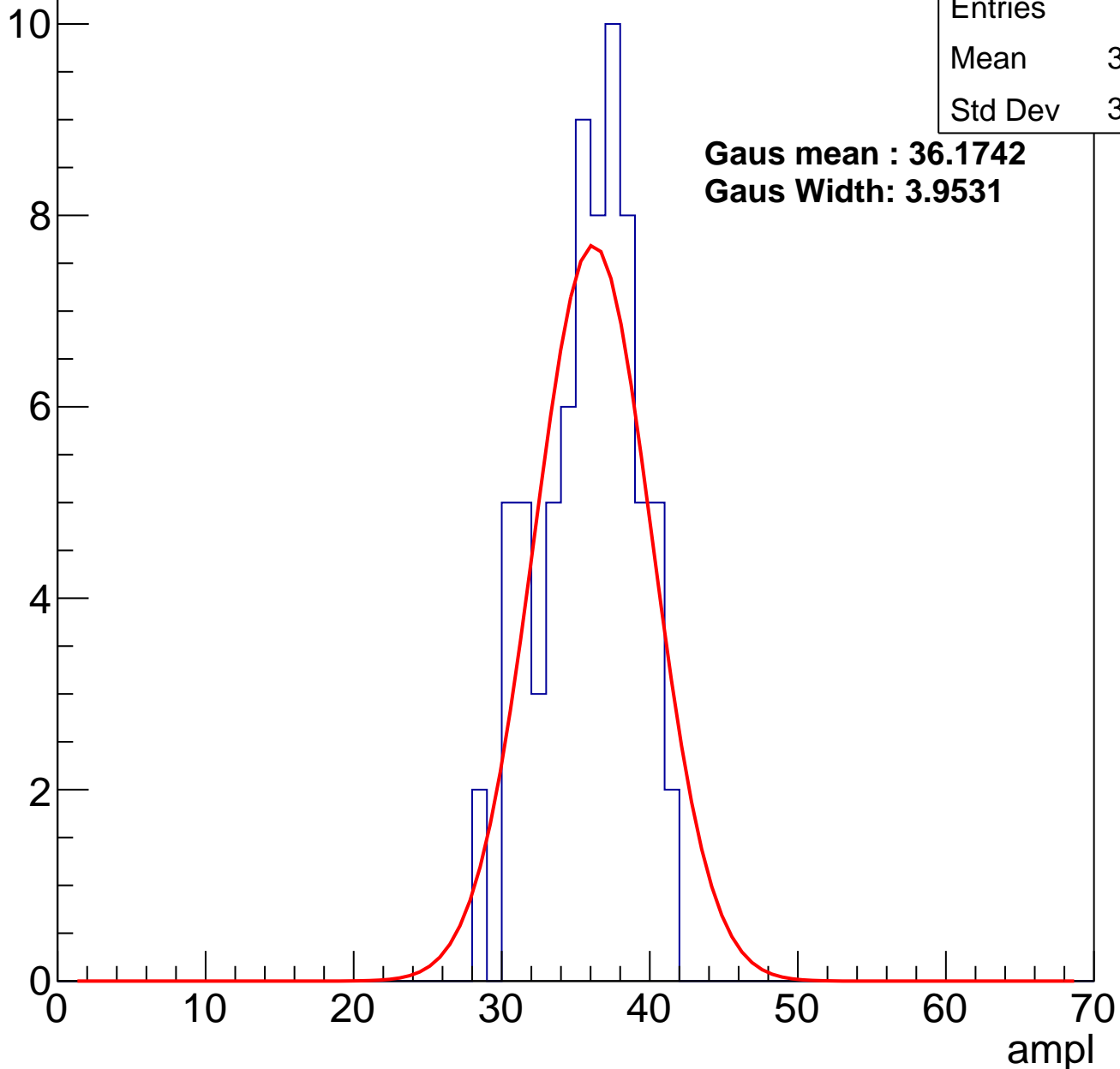
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	35.34
Std Dev	3.193

**Gaus mean : 36.1742**

**Gaus Width: 3.9531**

Entry



# B1L002S, U8-ch21, adc2

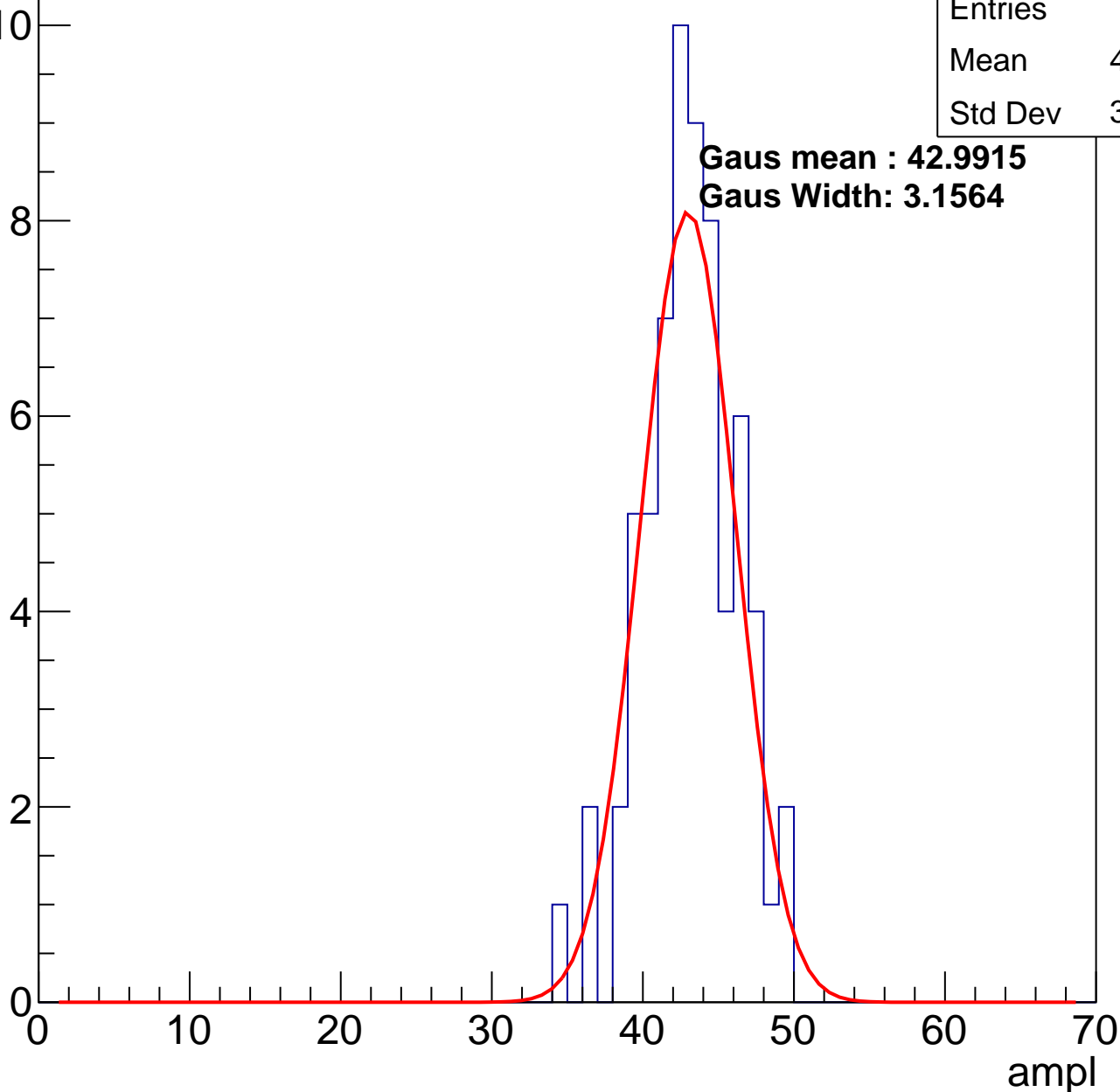
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	42.62
Std Dev	3.069

**Gaus mean : 42.9915**

**Gaus Width: 3.1564**

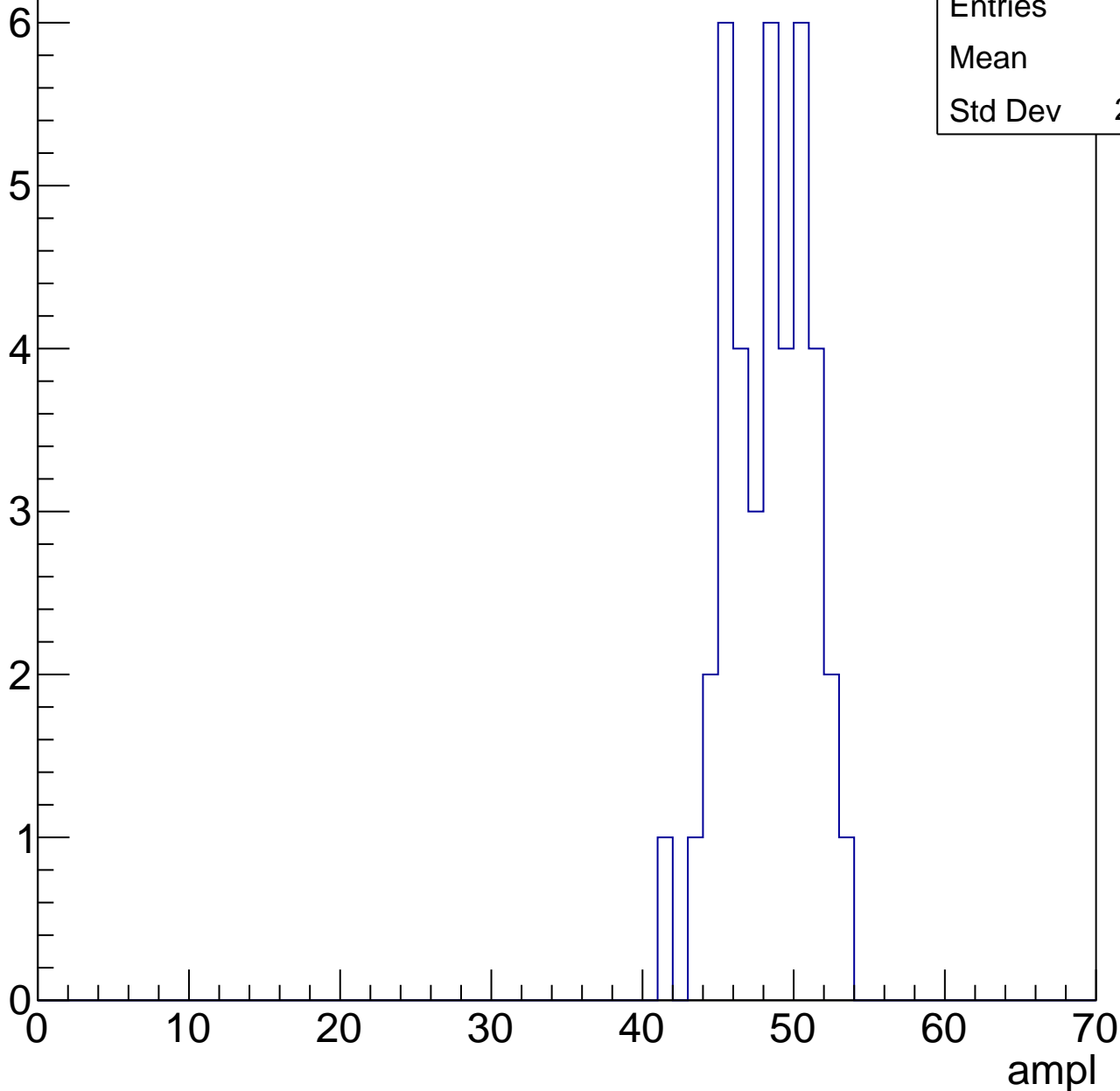


# B1L002S, U8-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	47.8
Std Dev	2.731

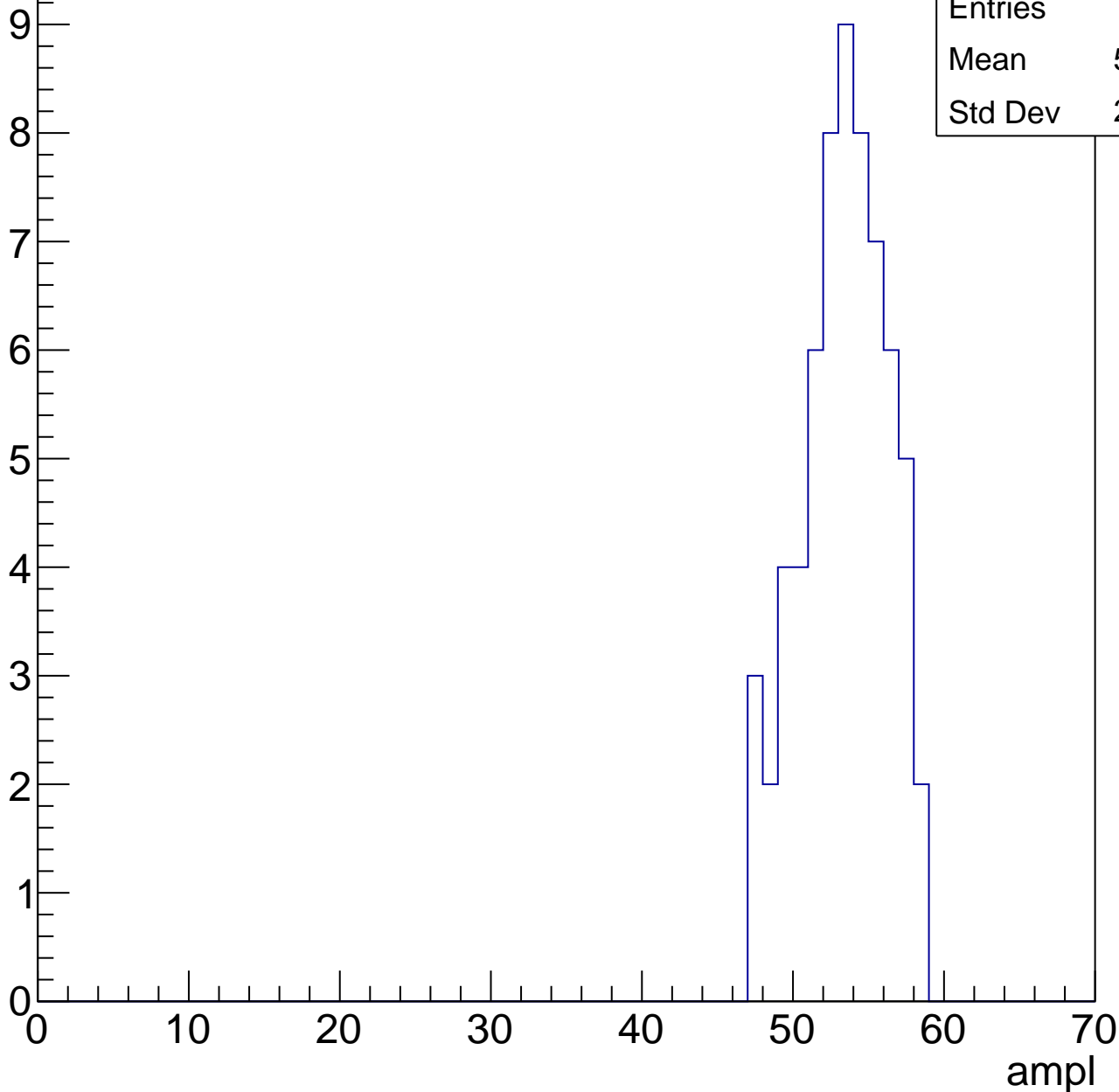


# B1L002S, U8-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

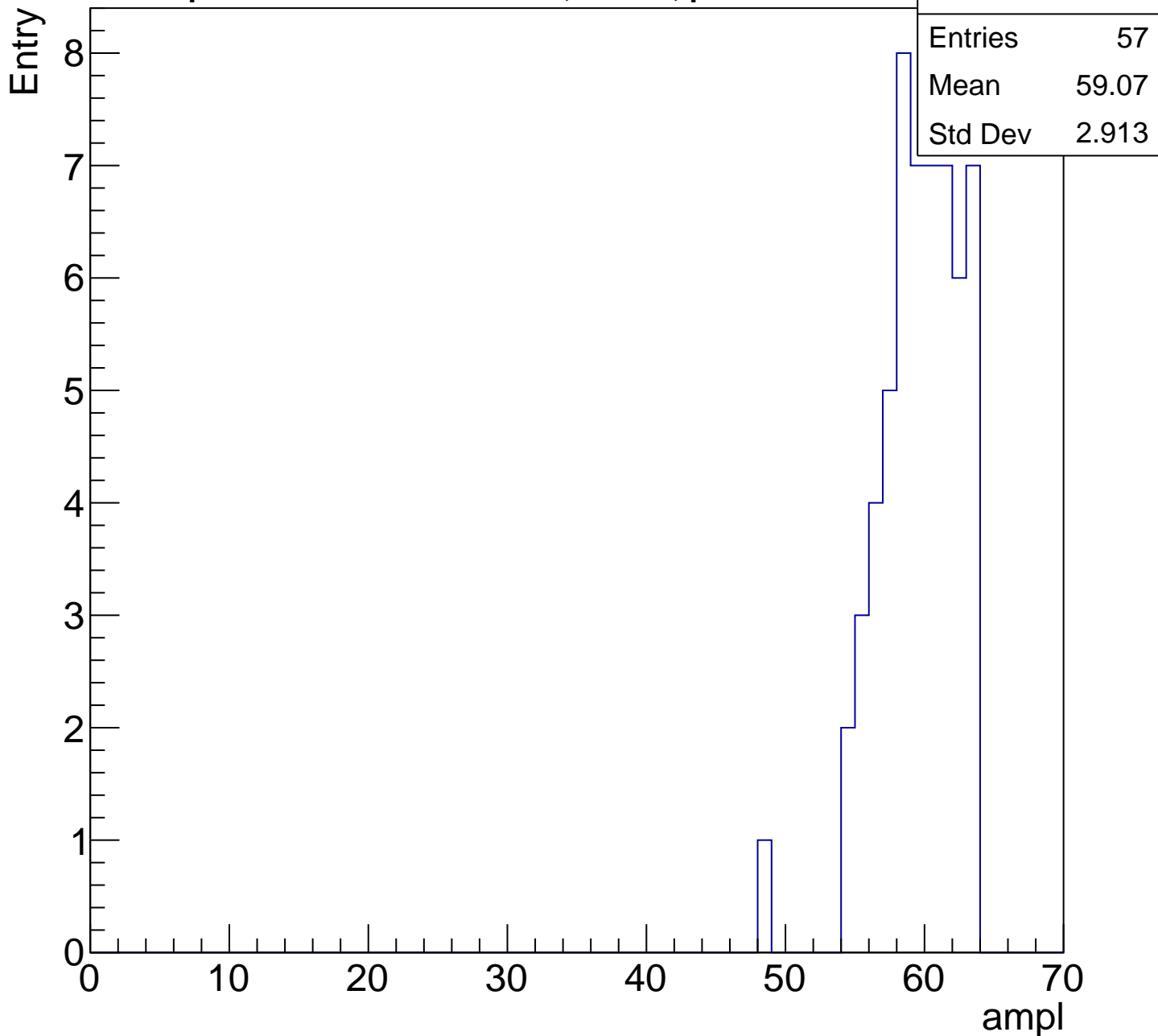
Entry

Entries	64
Mean	52.91
Std Dev	2.821



# B1L002S, U8-ch21, adc5

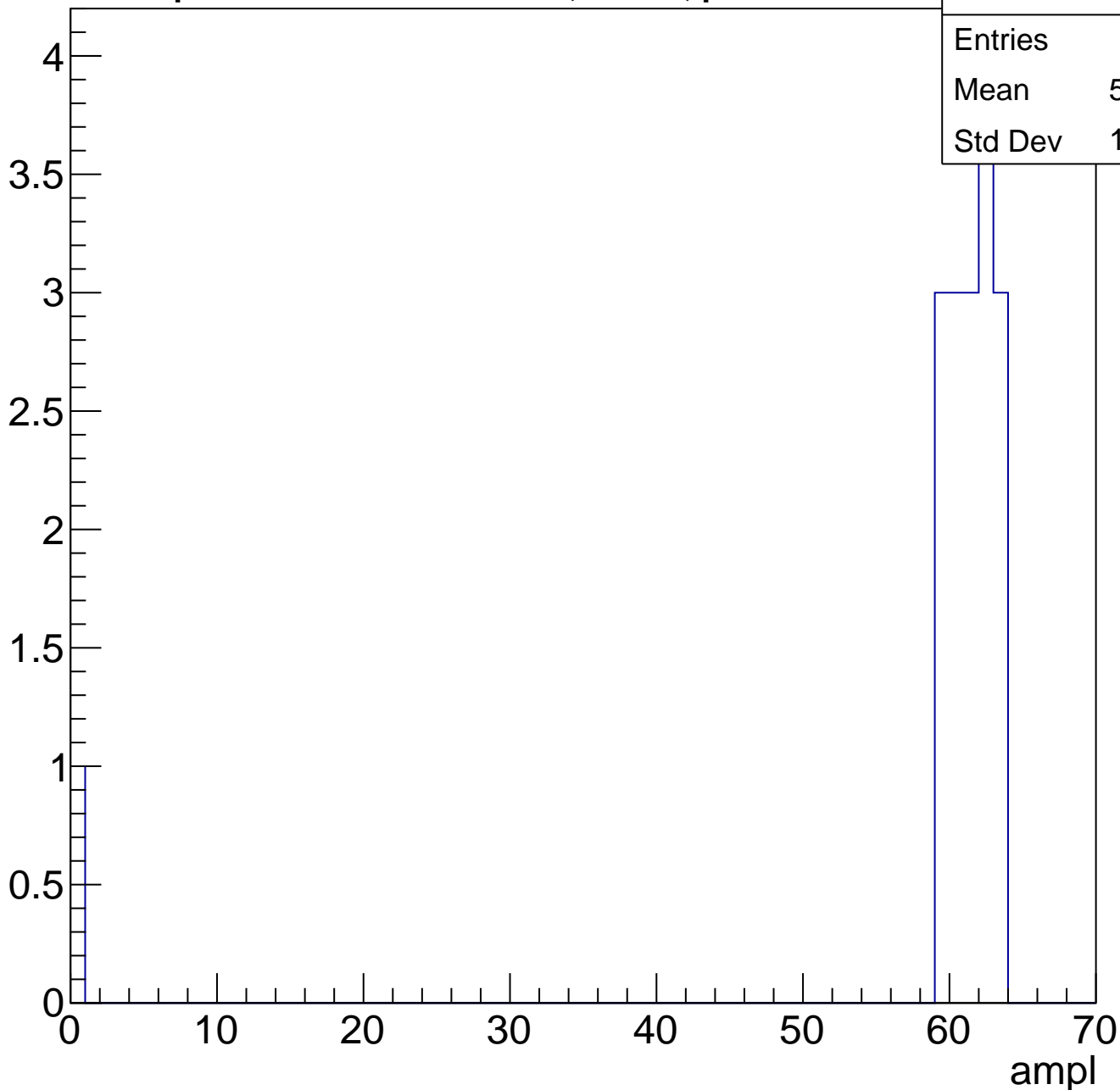
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L002S, U8-ch22, adc0

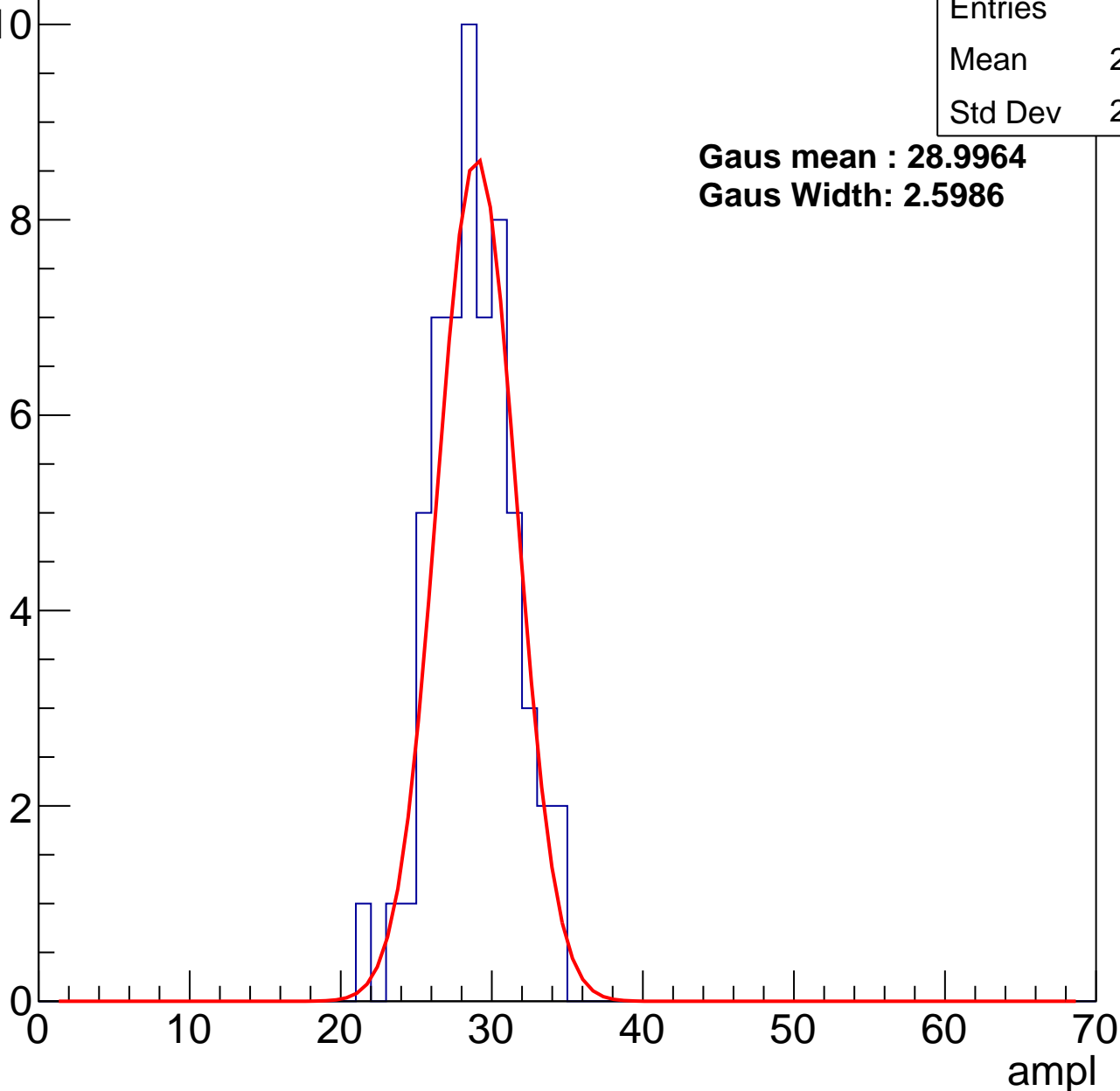
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.34
Std Dev	2.659

**Gaus mean : 28.9964**

**Gaus Width: 2.5986**



# B1L002S, U8-ch22, adc1

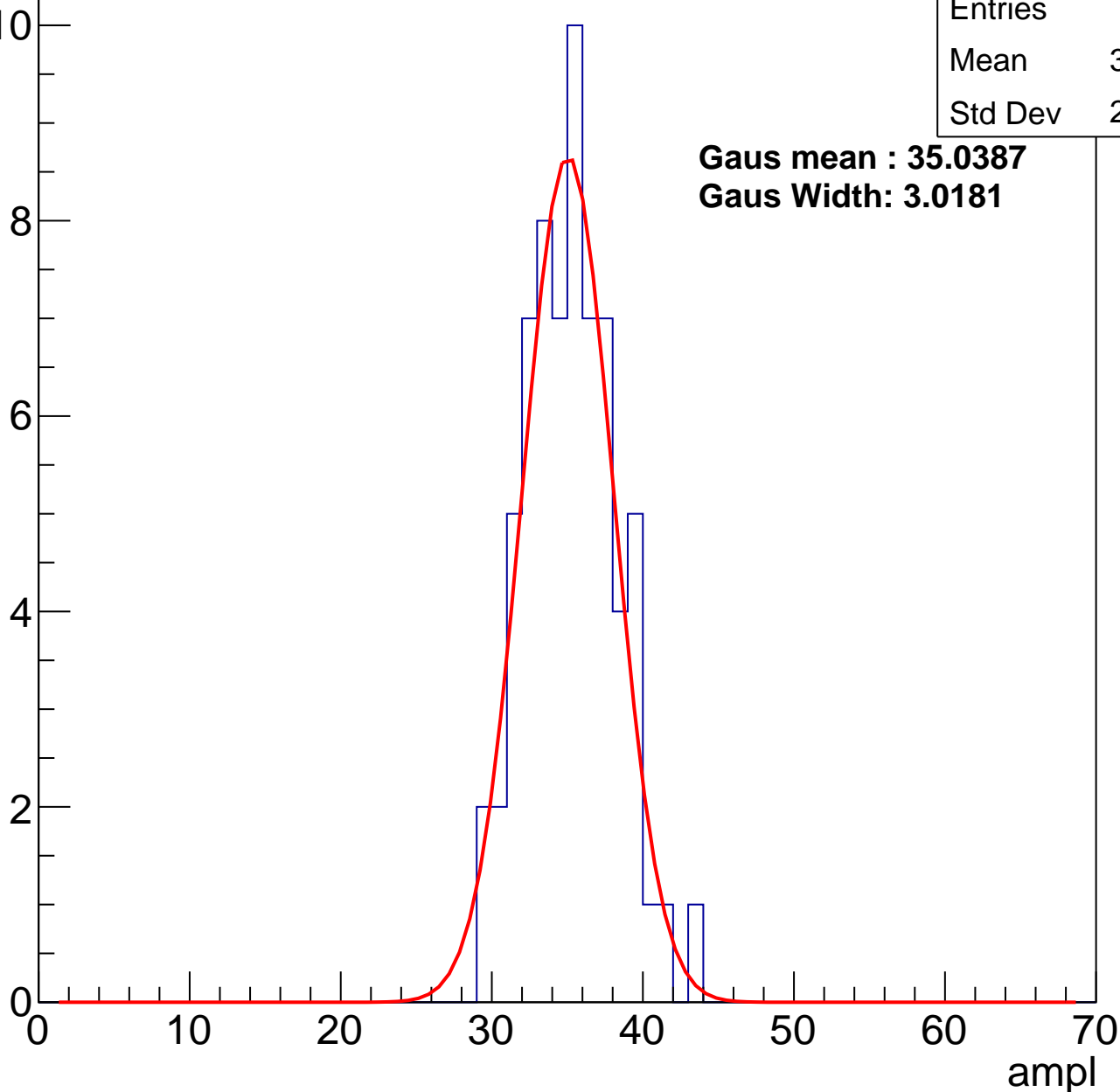
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.79
Std Dev	2.935

**Gaus mean : 35.0387**

**Gaus Width: 3.0181**



# B1L002S, U8-ch22, adc2

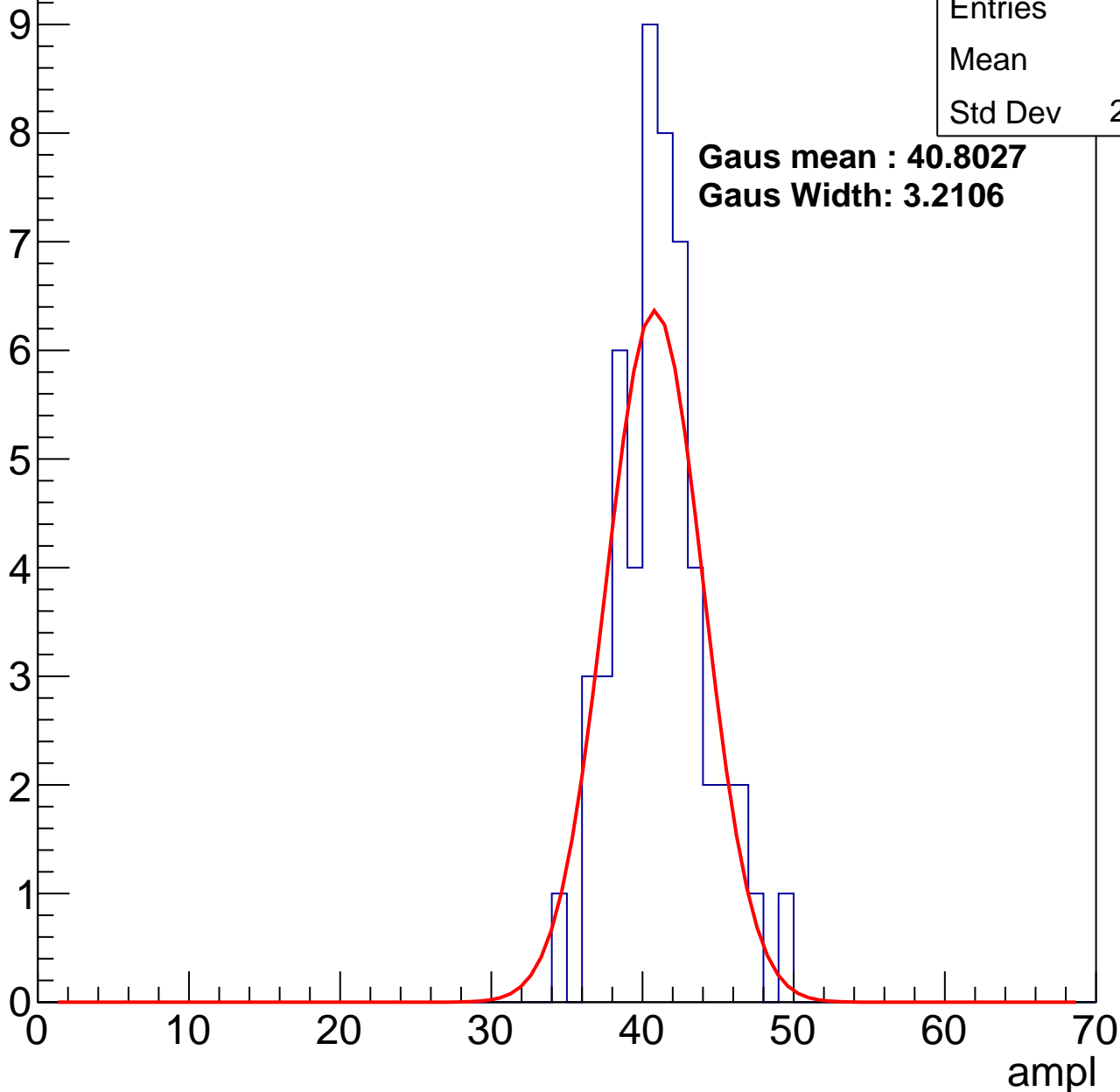
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.7
Std Dev	2.975

**Gaus mean : 40.8027**

**Gaus Width: 3.2106**

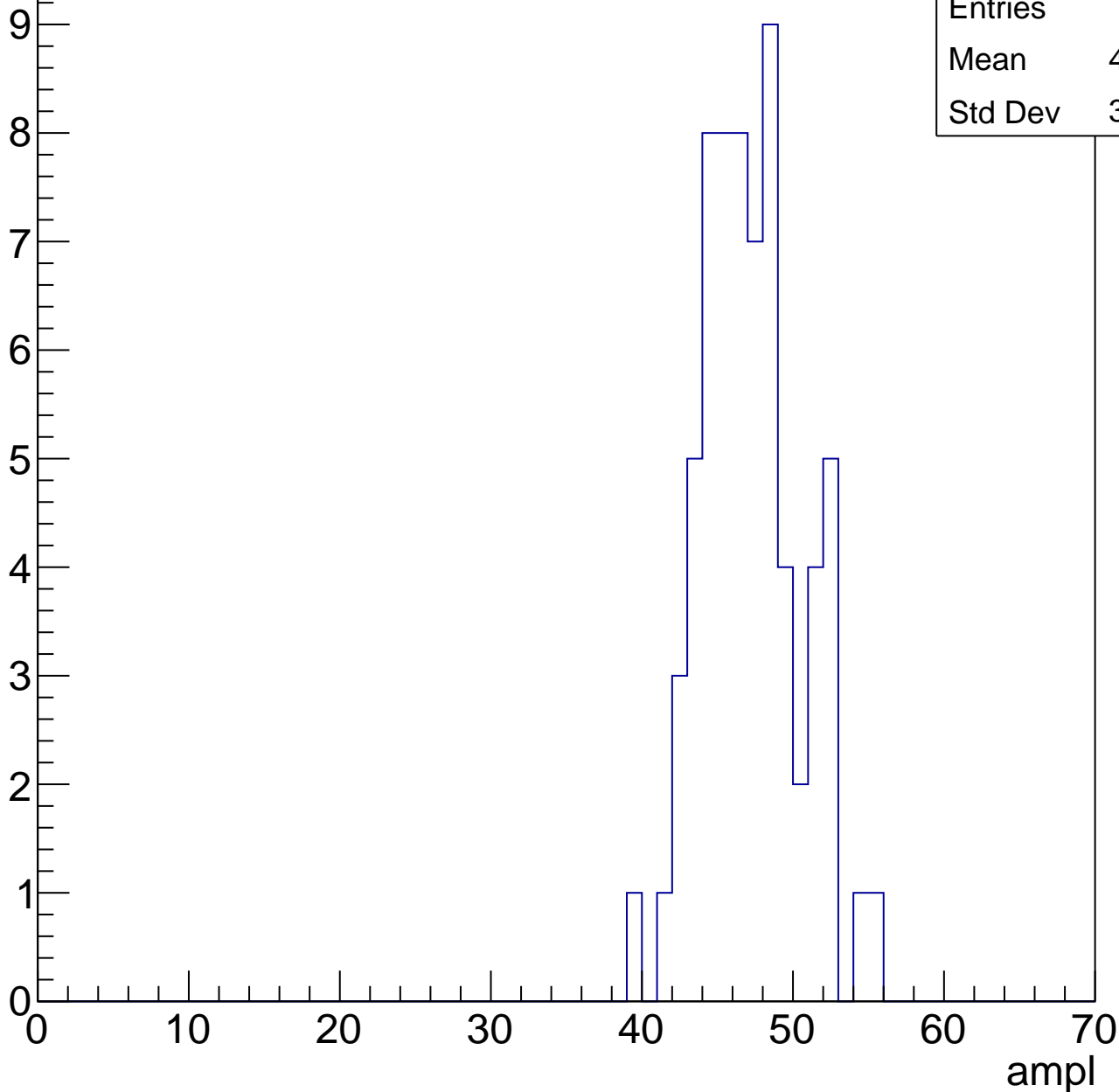


# B1L002S, U8-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	46.73
Std Dev	3.249

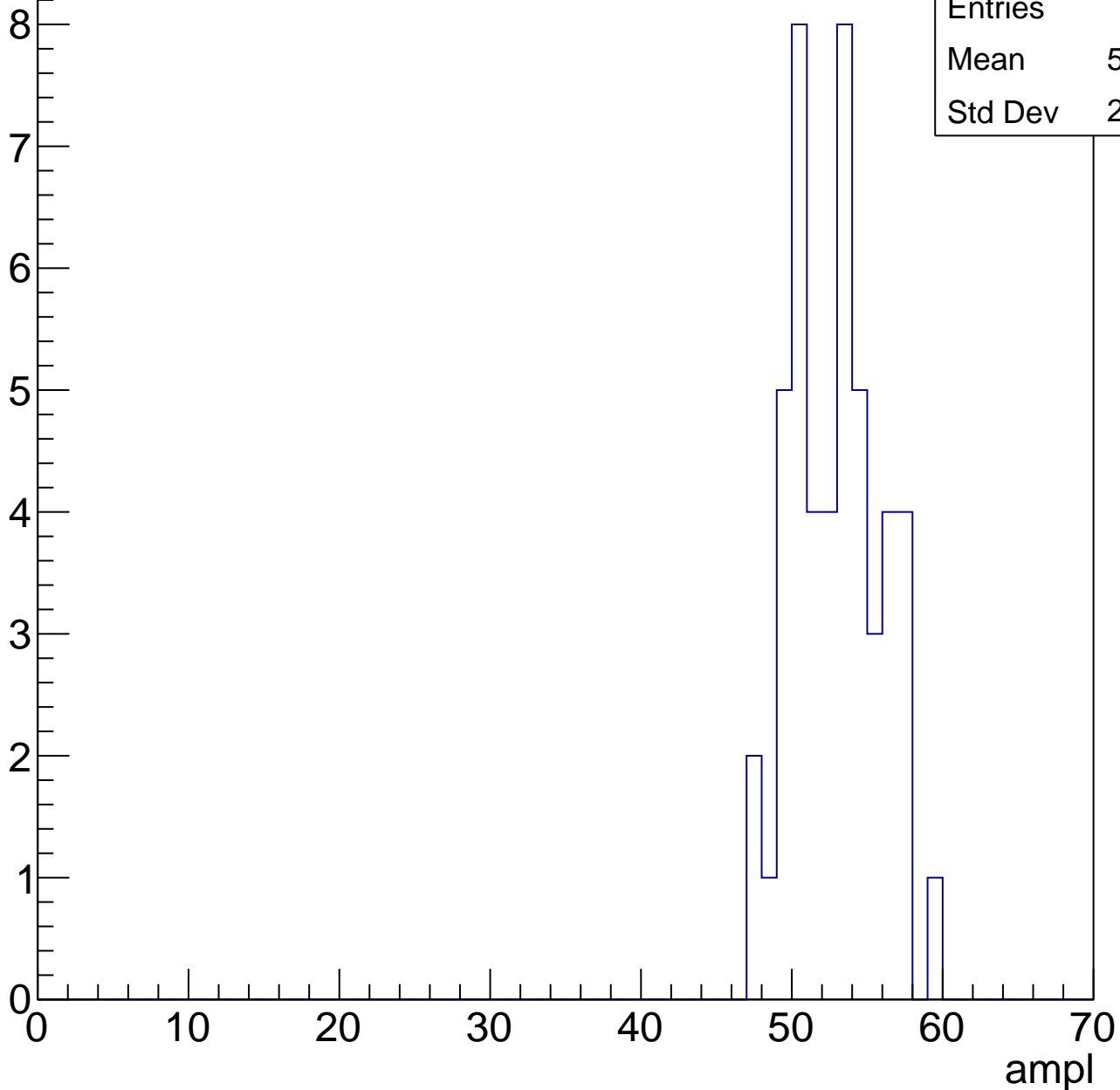


# B1L002S, U8-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.43
Std Dev	2.878

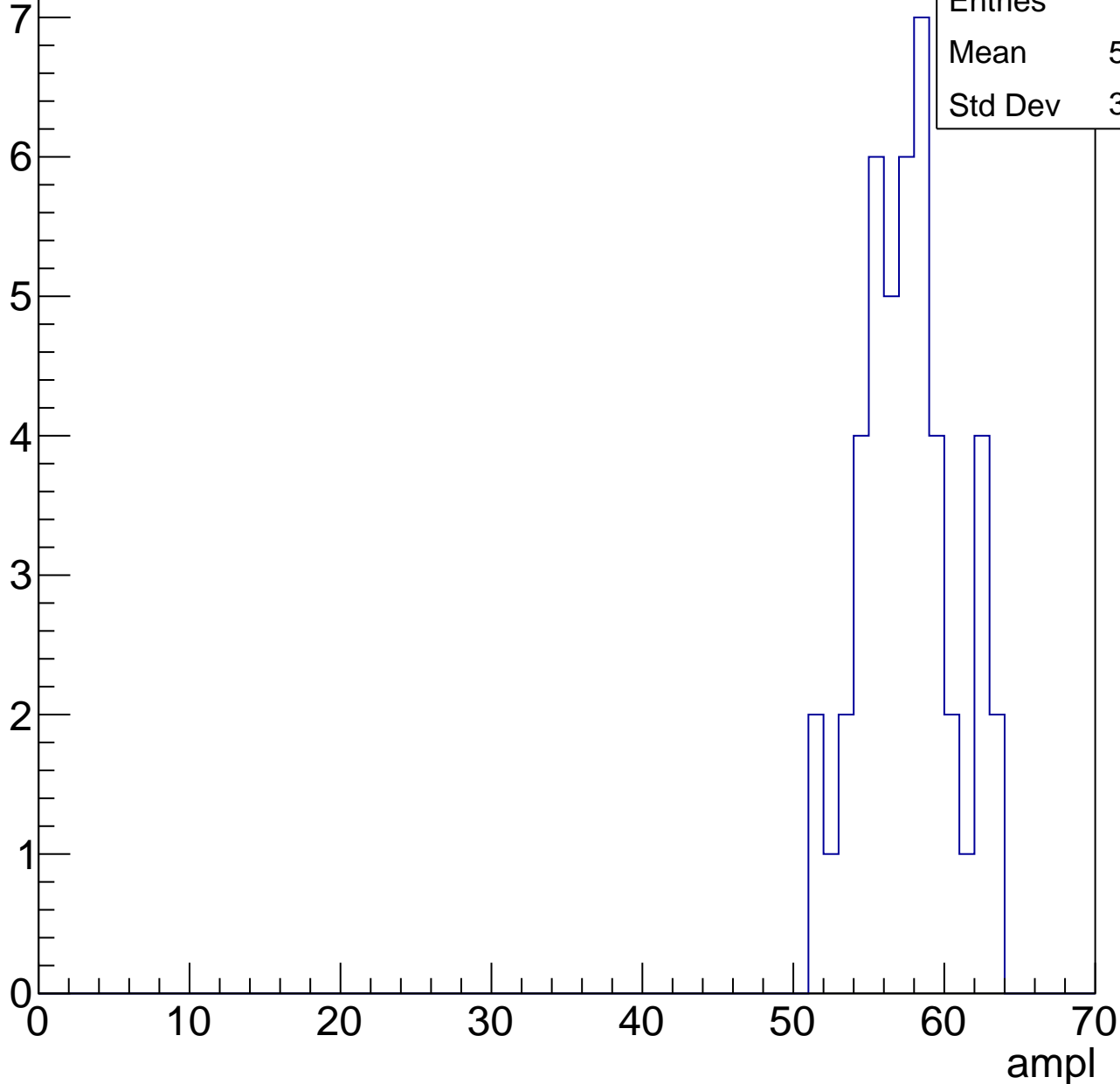


# B1L002S, U8-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	57.07
Std Dev	3.032

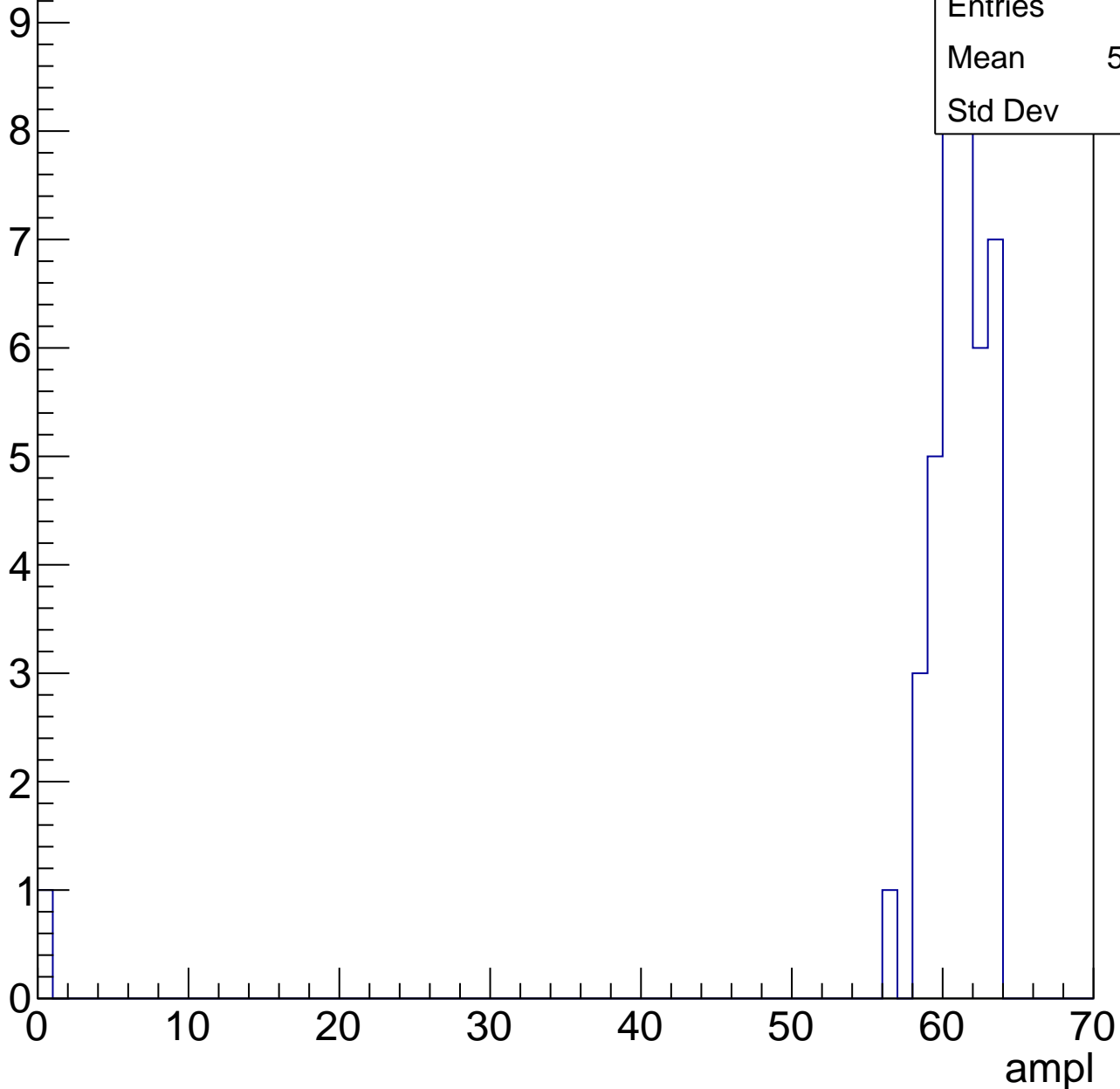


# B1L002S, U8-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	59.17
Std Dev	9.62

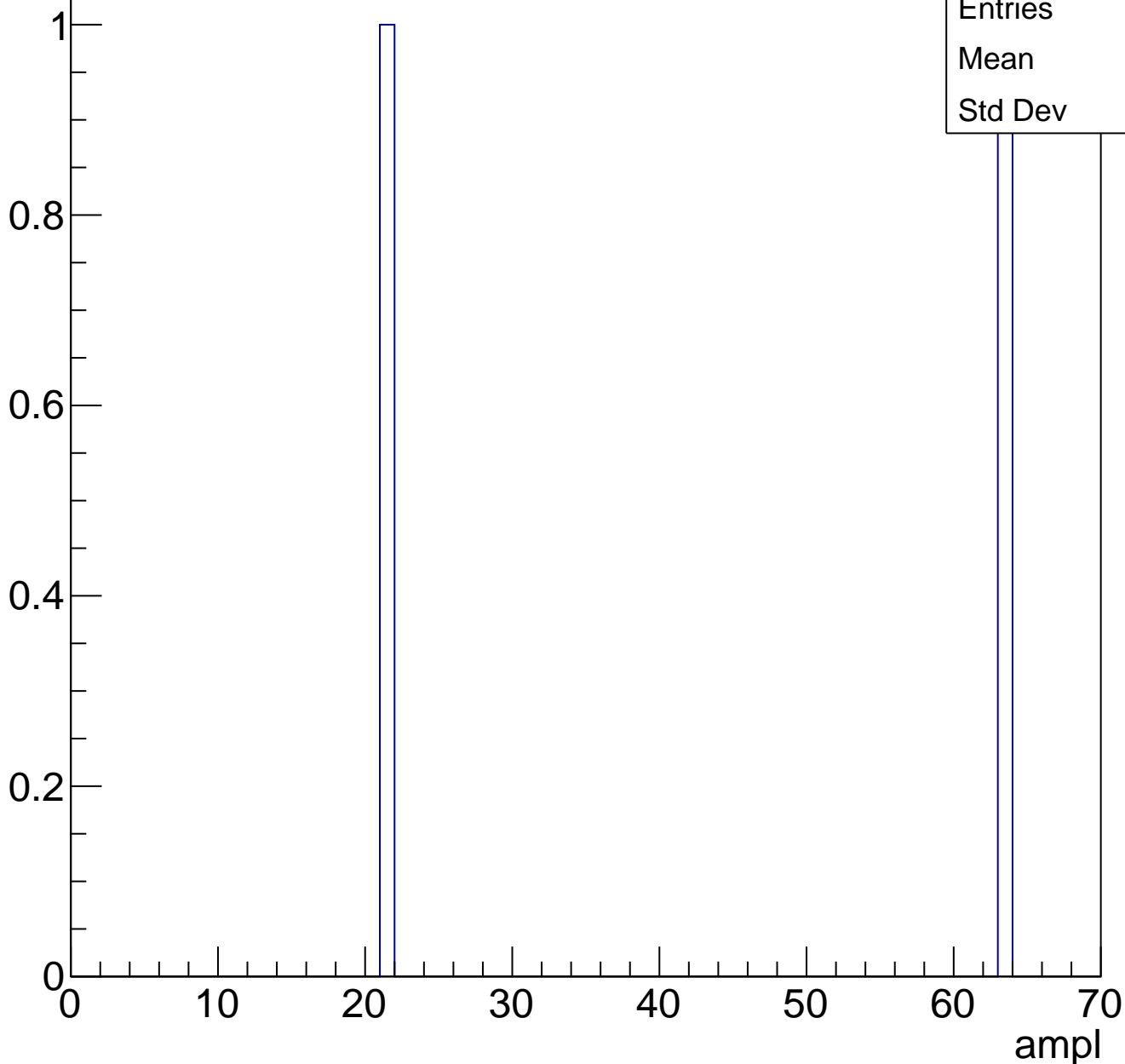




# B1L002S, U8-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	42
Std Dev	21

# B1L002S, U8-ch23, adc0

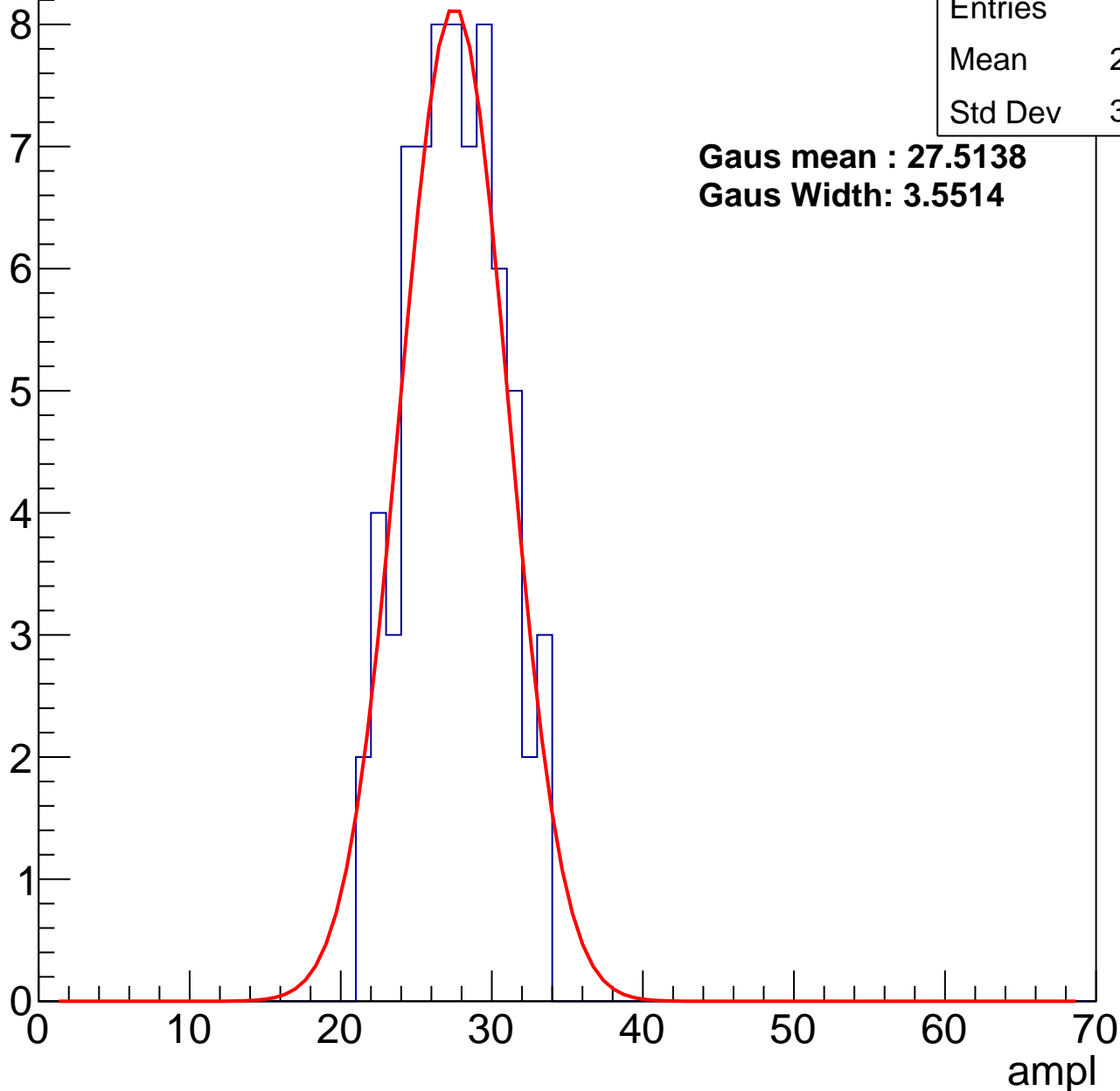
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	27.03
Std Dev	3.047

**Gaus mean : 27.5138**

**Gaus Width: 3.5514**



# B1L002S, U8-ch23, adc1

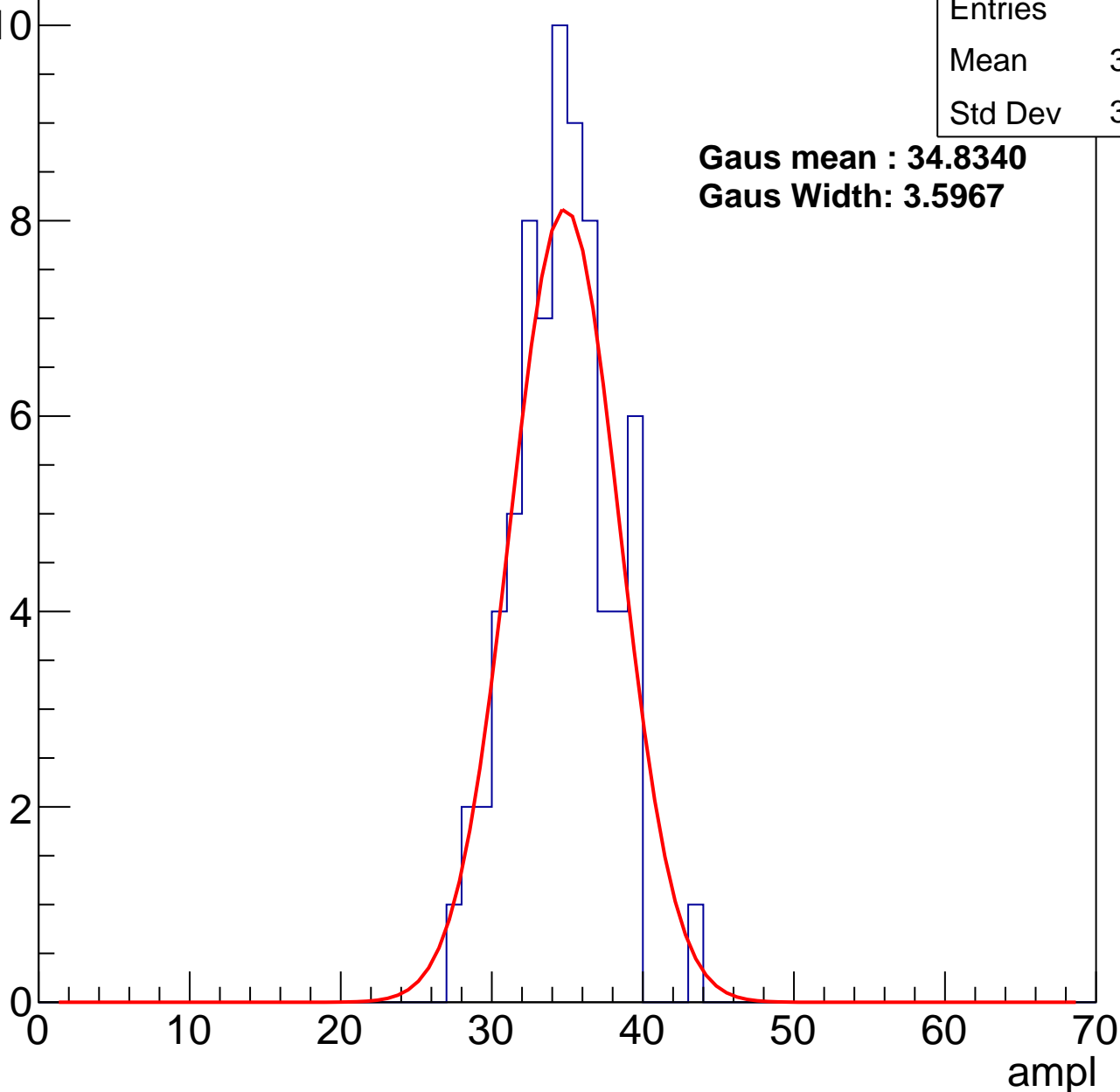
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.13
Std Dev	3.117

**Gaus mean : 34.8340**

**Gaus Width: 3.5967**



# B1L002S, U8-ch23, adc2

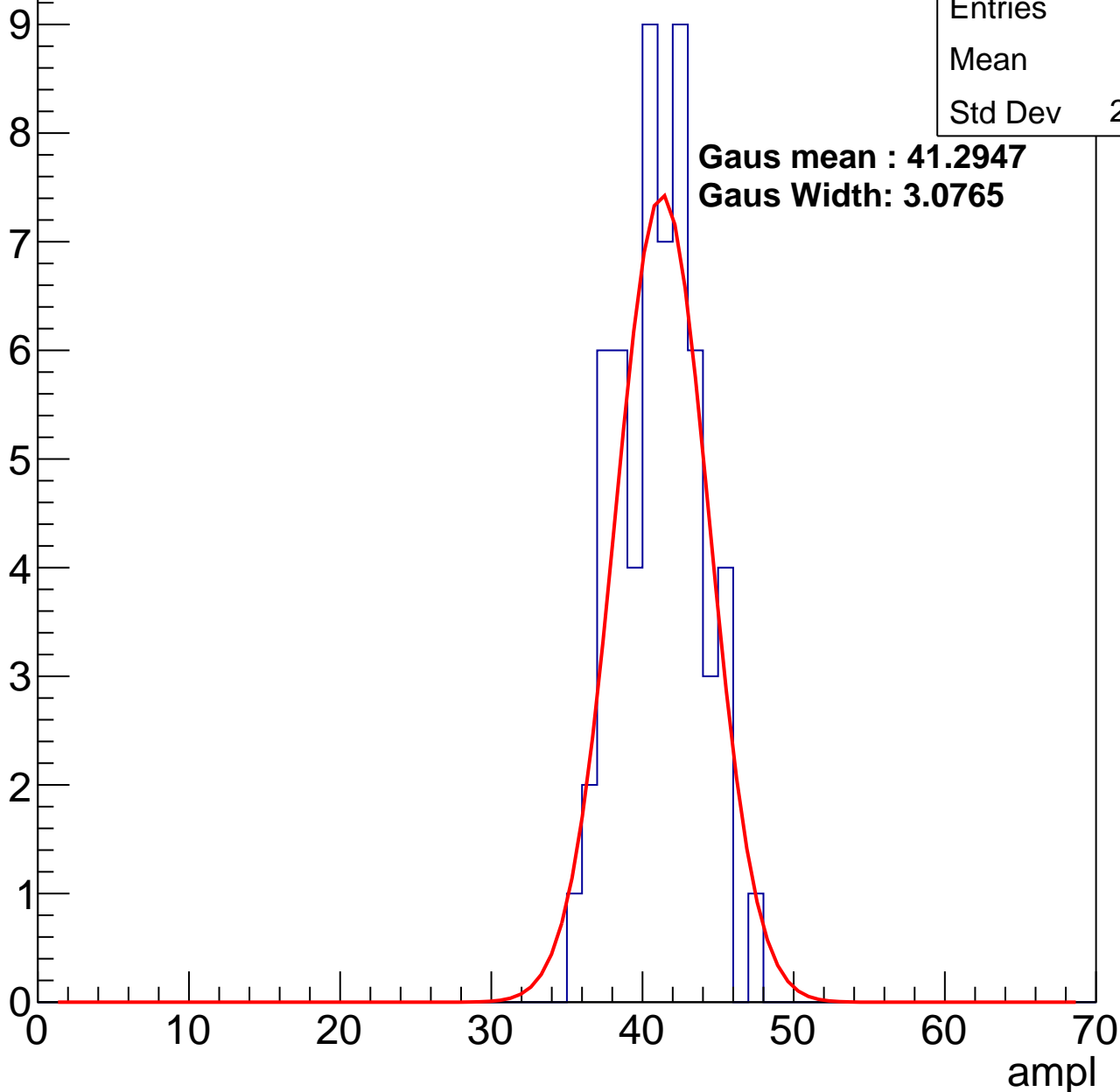
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.6
Std Dev	2.665

**Gaus mean : 41.2947**

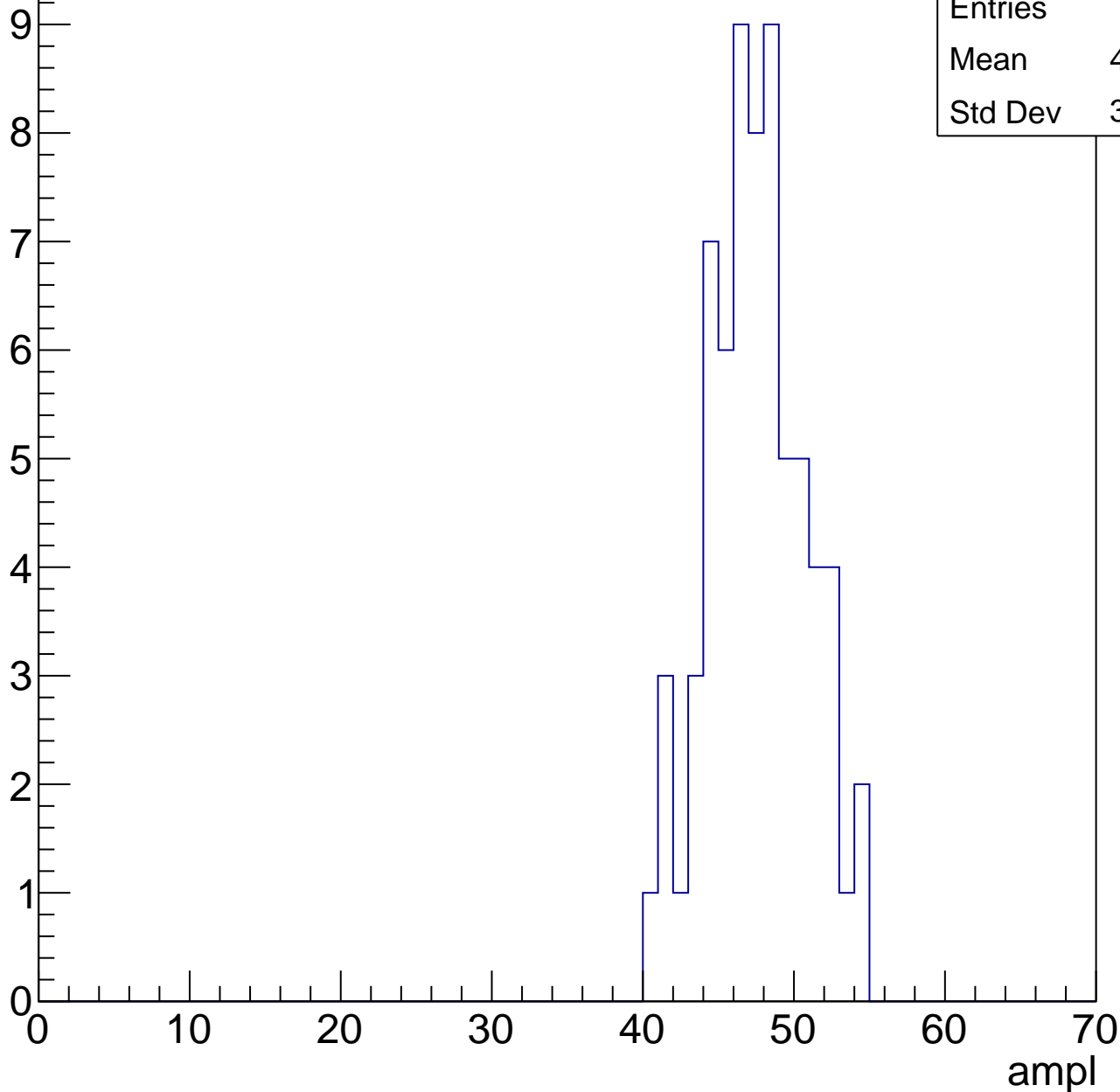
**Gaus Width: 3.0765**



# B1L002S, U8-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

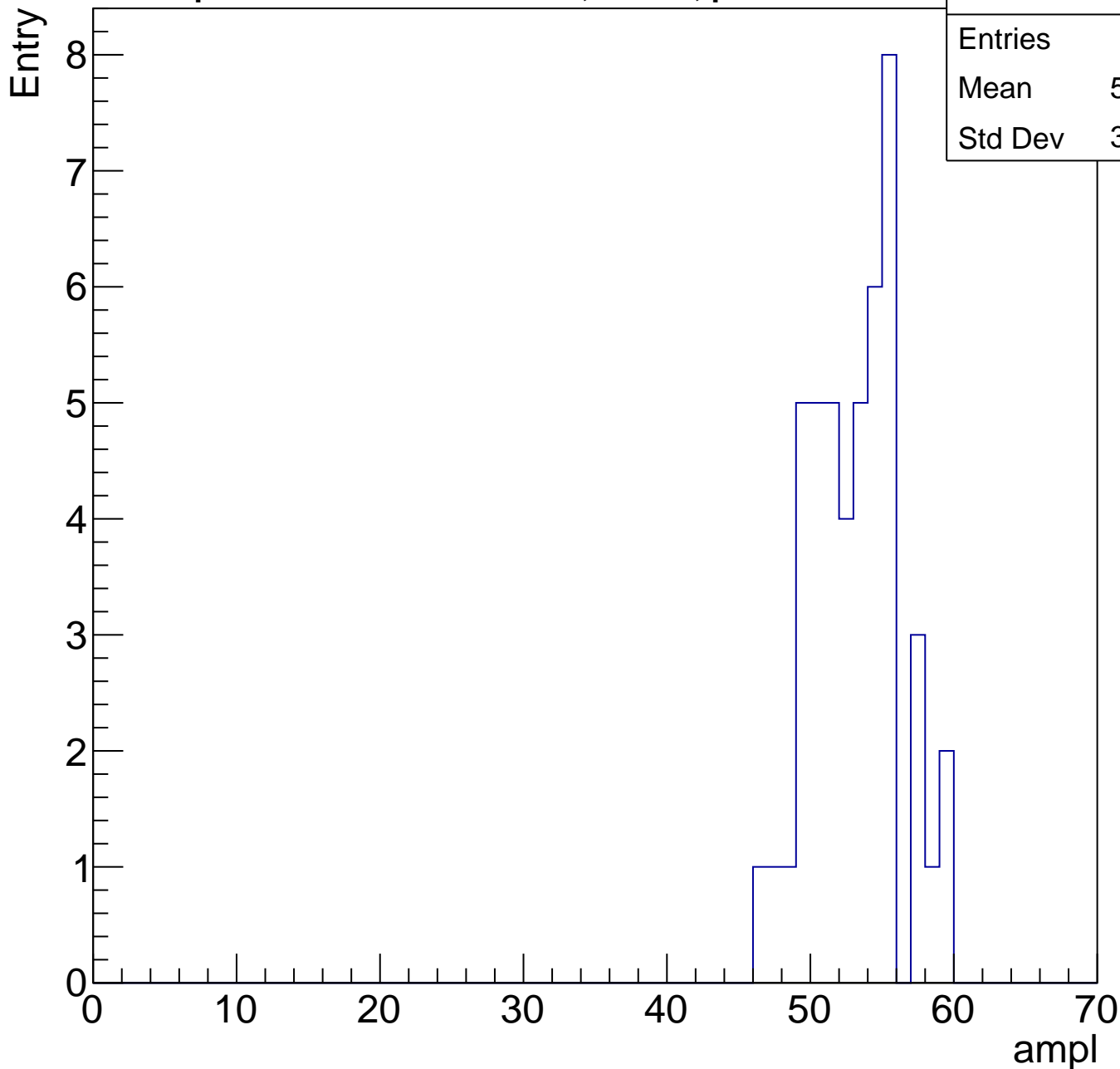
Entry



Entries	68
Mean	47.09
Std Dev	3.203

# B1L002S, U8-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

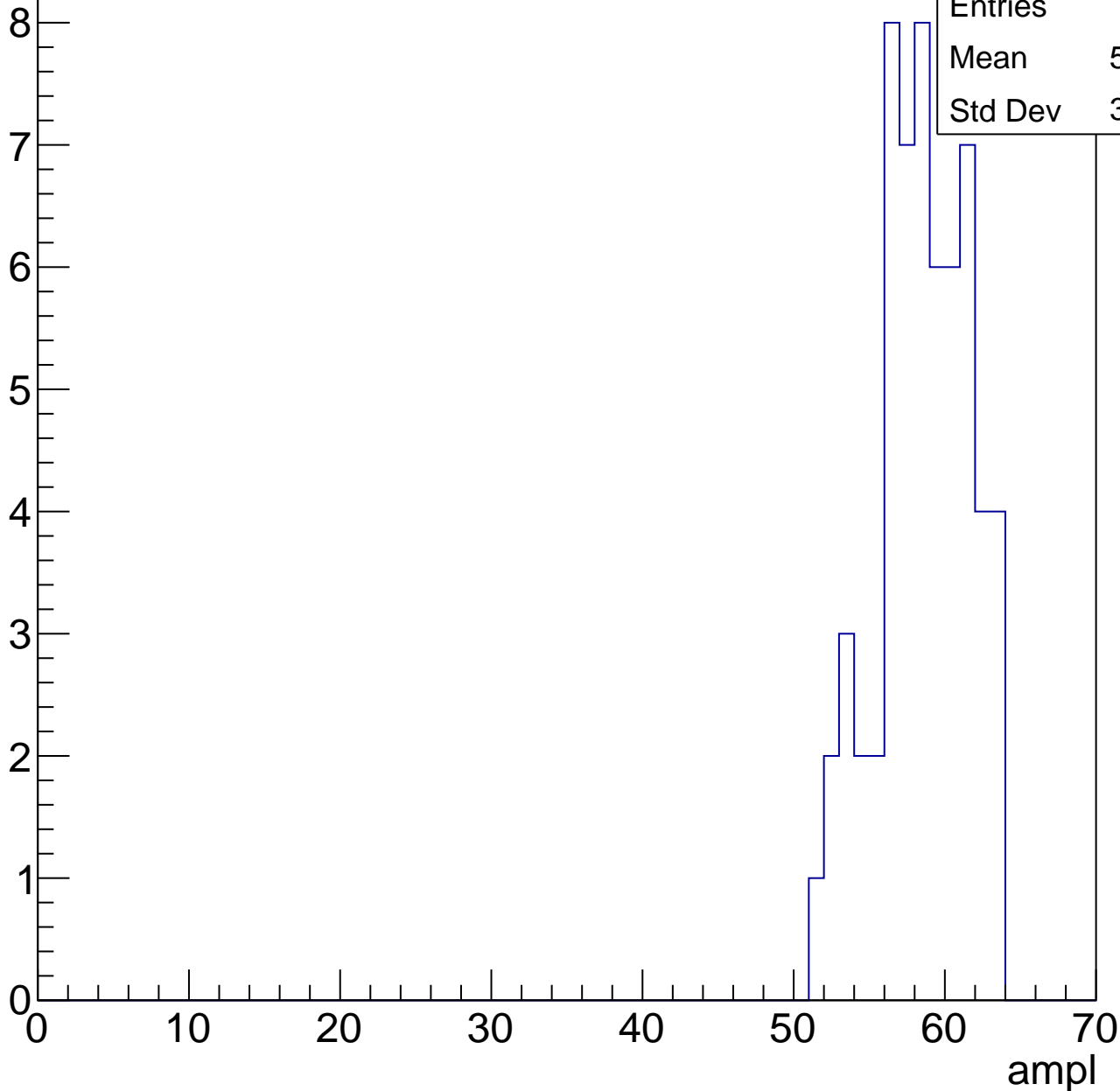


# B1L002S, U8-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.07
Std Dev	3.005

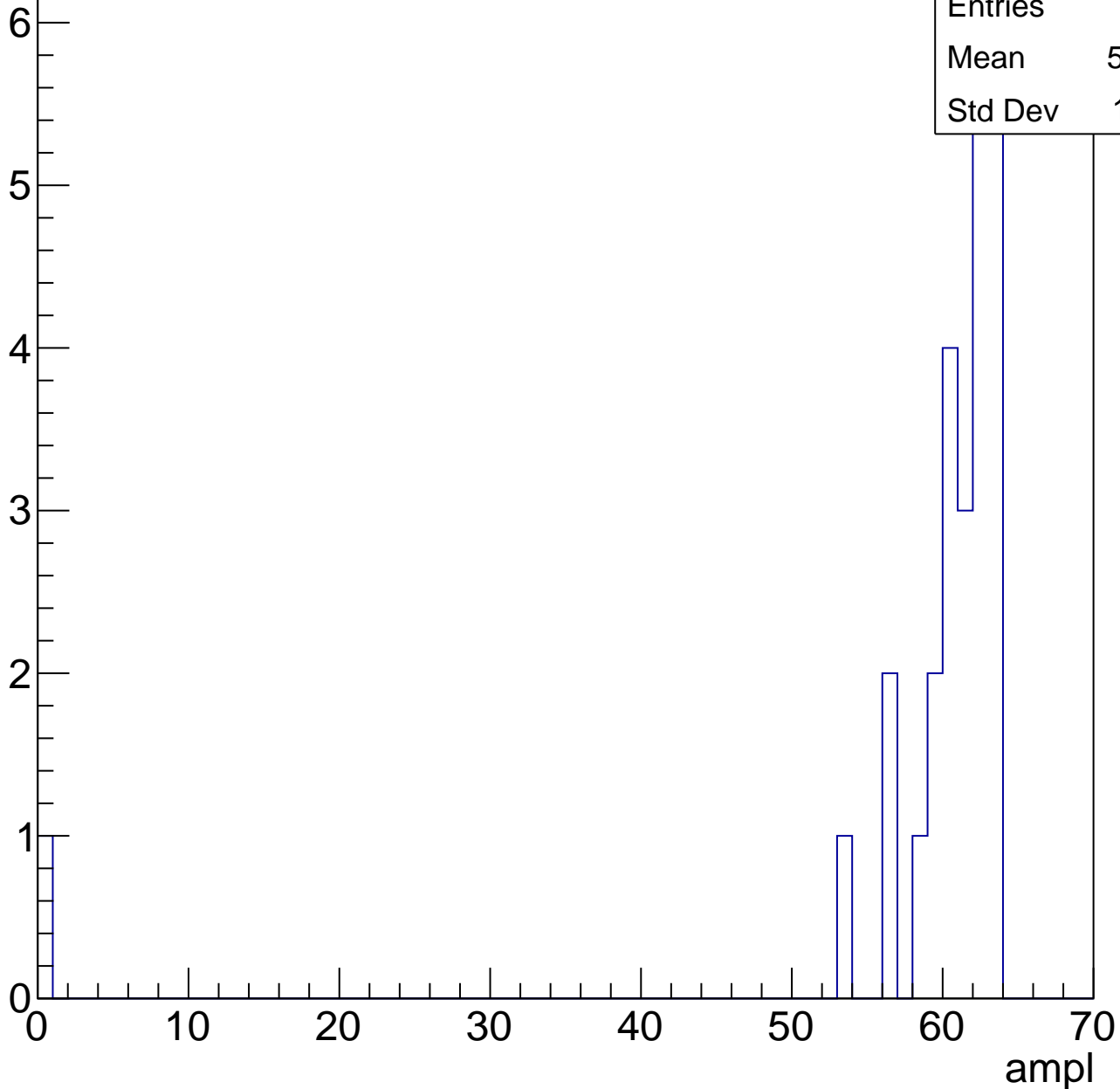


# B1L002S, U8-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.23
Std Dev	11.91

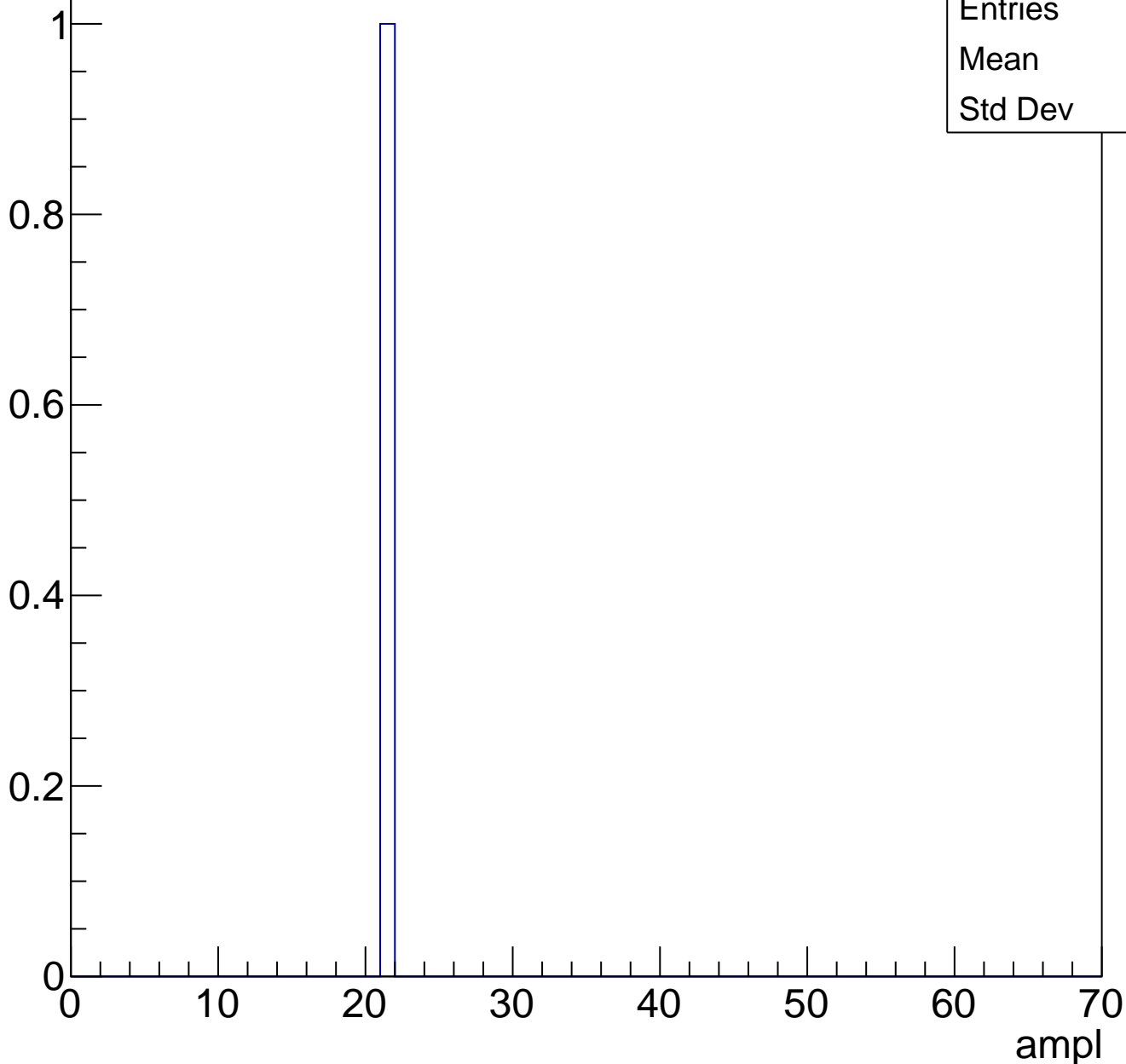




# B1L002S, U8-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	21
Std Dev	0

# B1L002S, U8-ch24, adc0

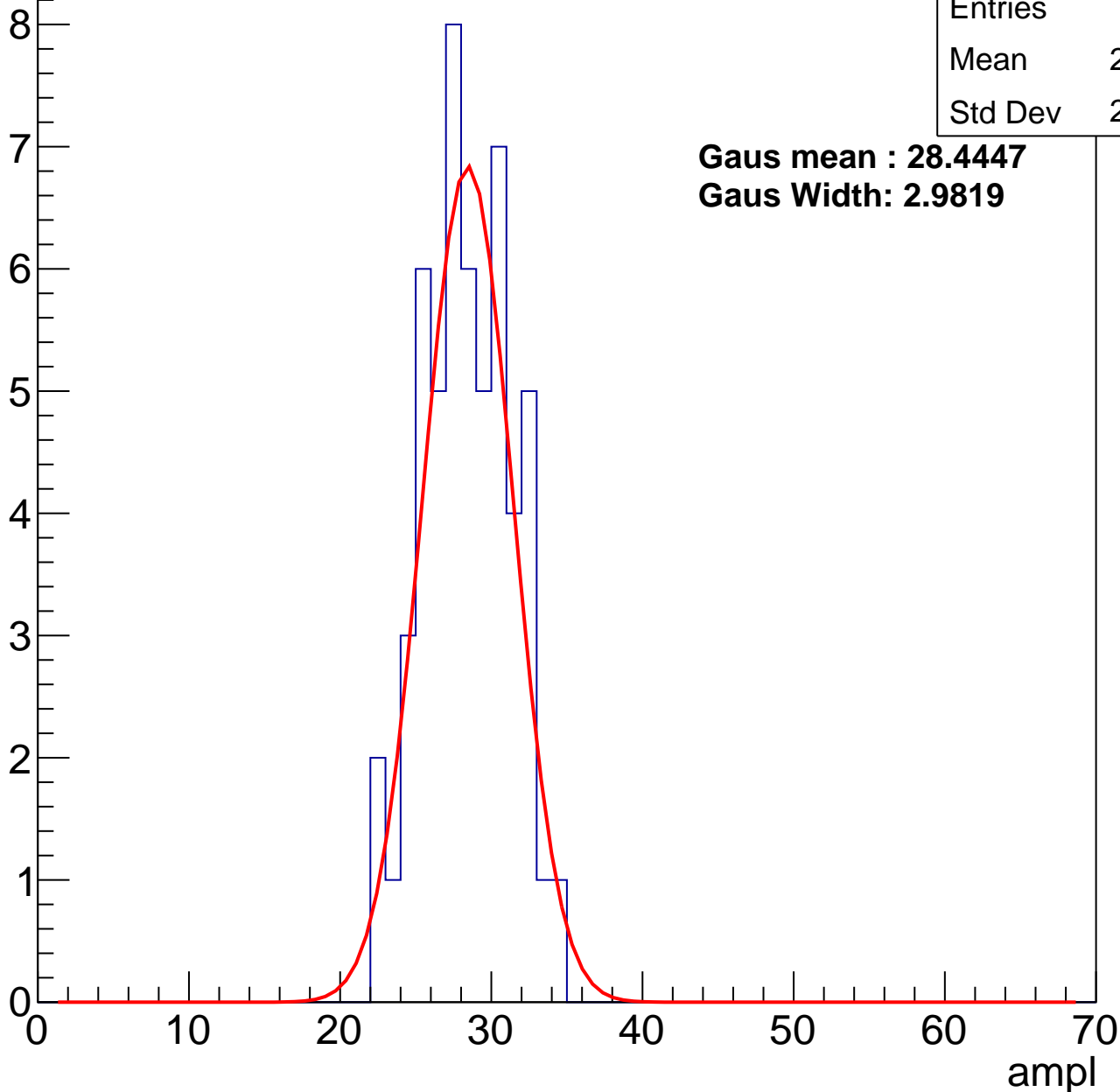
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	27.94
Std Dev	2.844

**Gaus mean : 28.4447**

**Gaus Width: 2.9819**



# B1L002S, U8-ch24, adc1

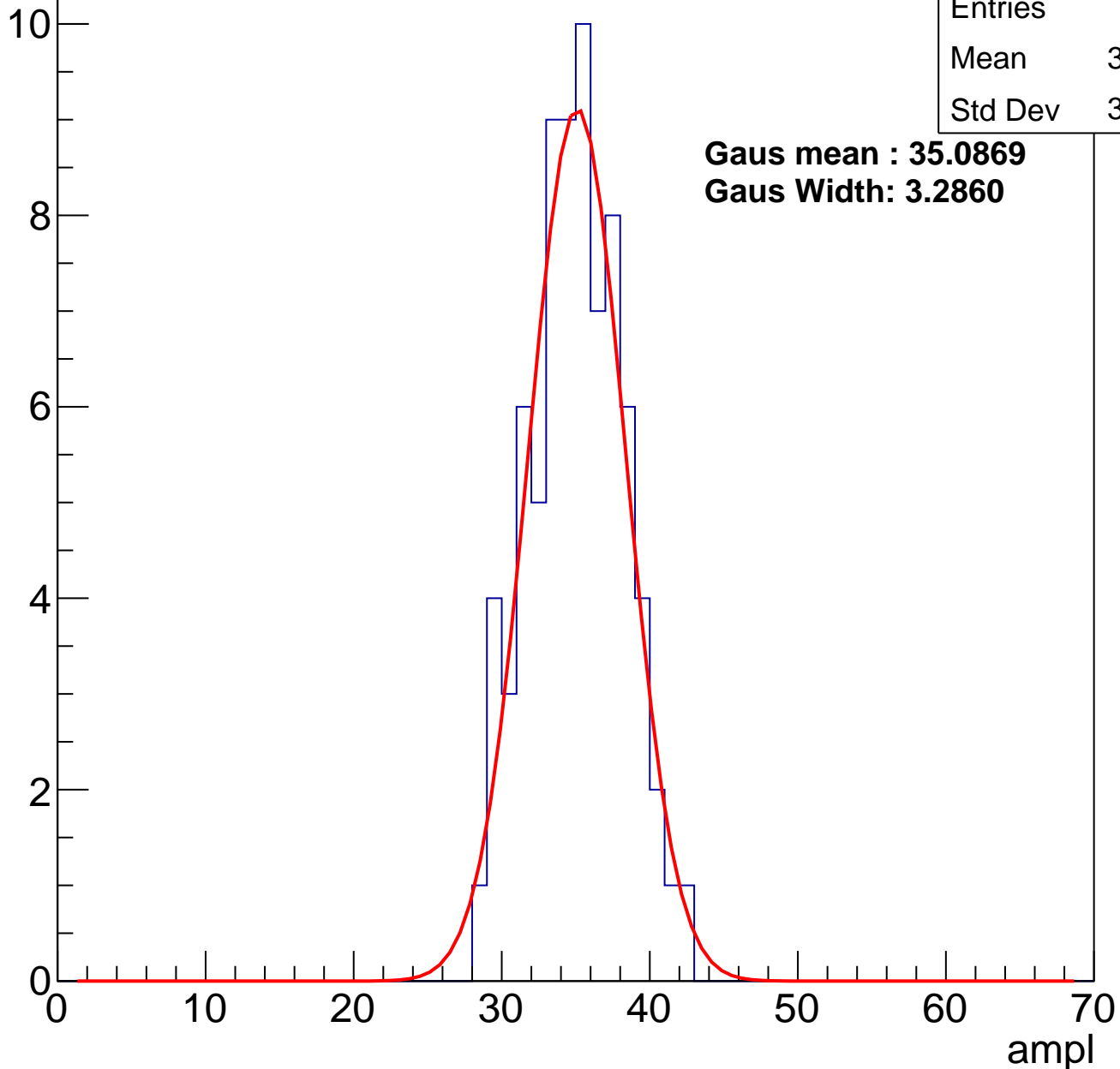
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	34.58
Std Dev	3.105

**Gaus mean : 35.0869**

**Gaus Width: 3.2860**

Entry



# B1L002S, U8-ch24, adc2

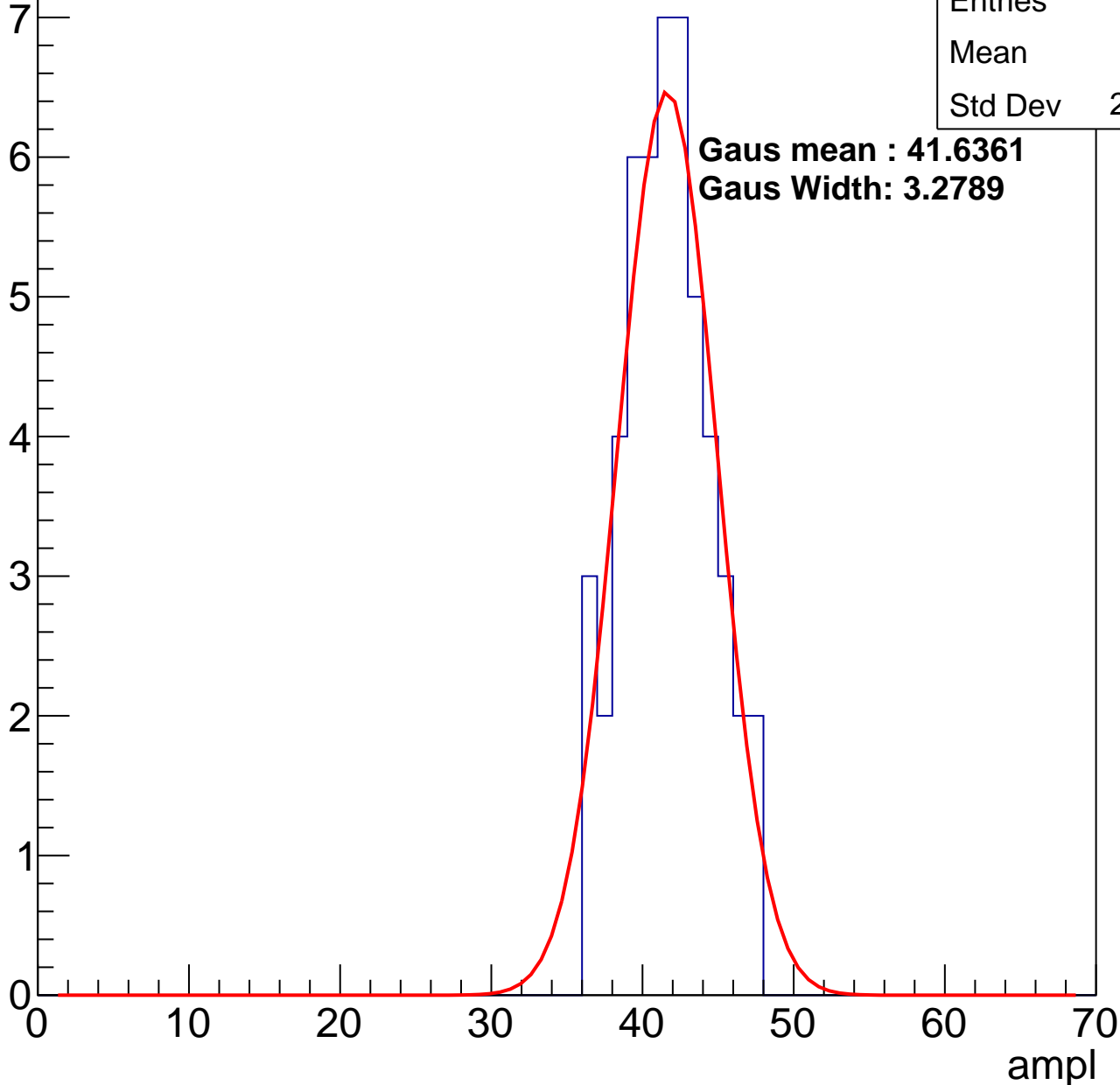
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.2
Std Dev	2.815

**Gaus mean : 41.6361**

**Gaus Width: 3.2789**

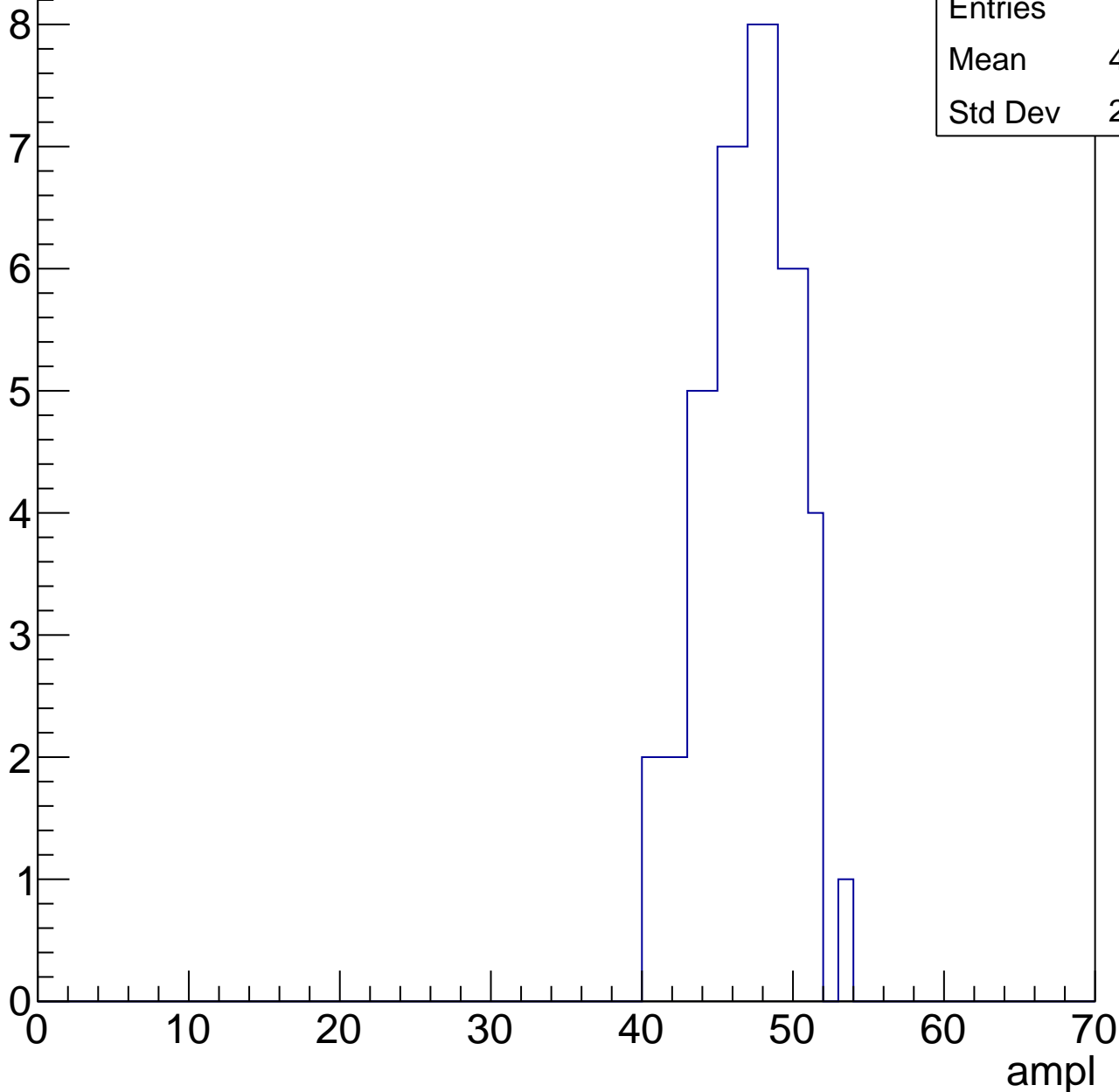


# B1L002S, U8-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.49
Std Dev	2.954

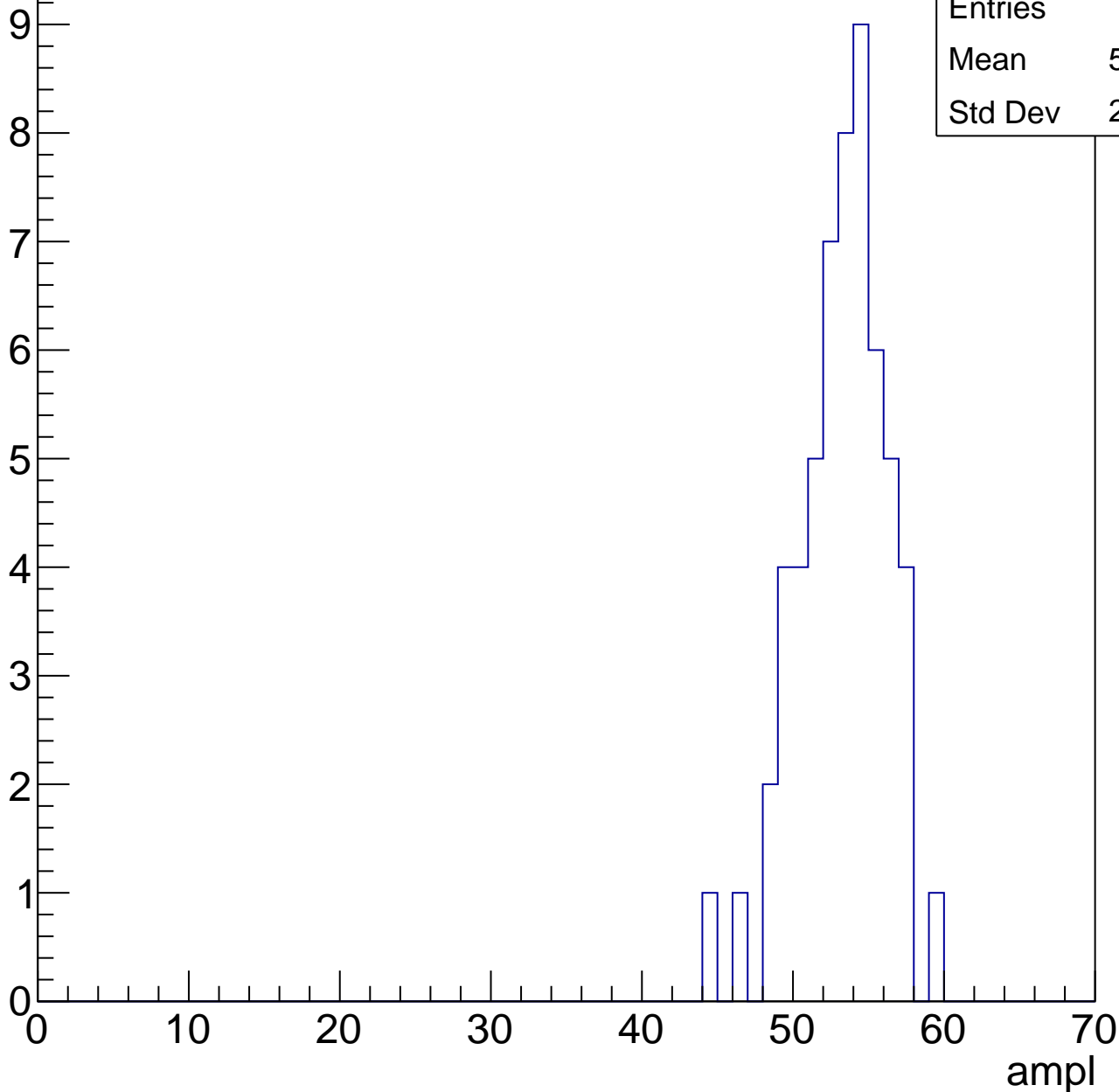


# B1L002S, U8-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.77
Std Dev	2.908

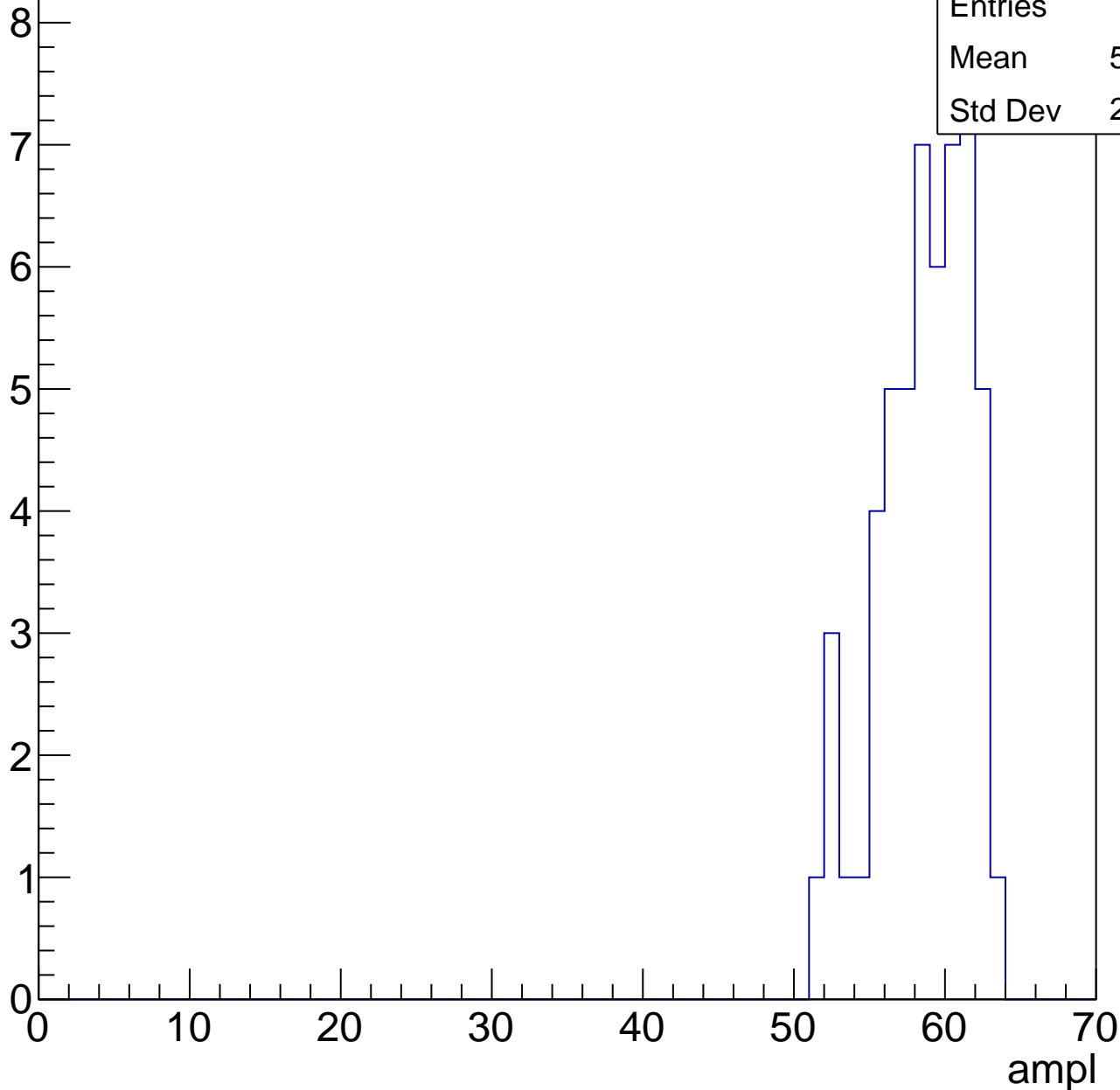


# B1L002S, U8-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.15
Std Dev	2.946

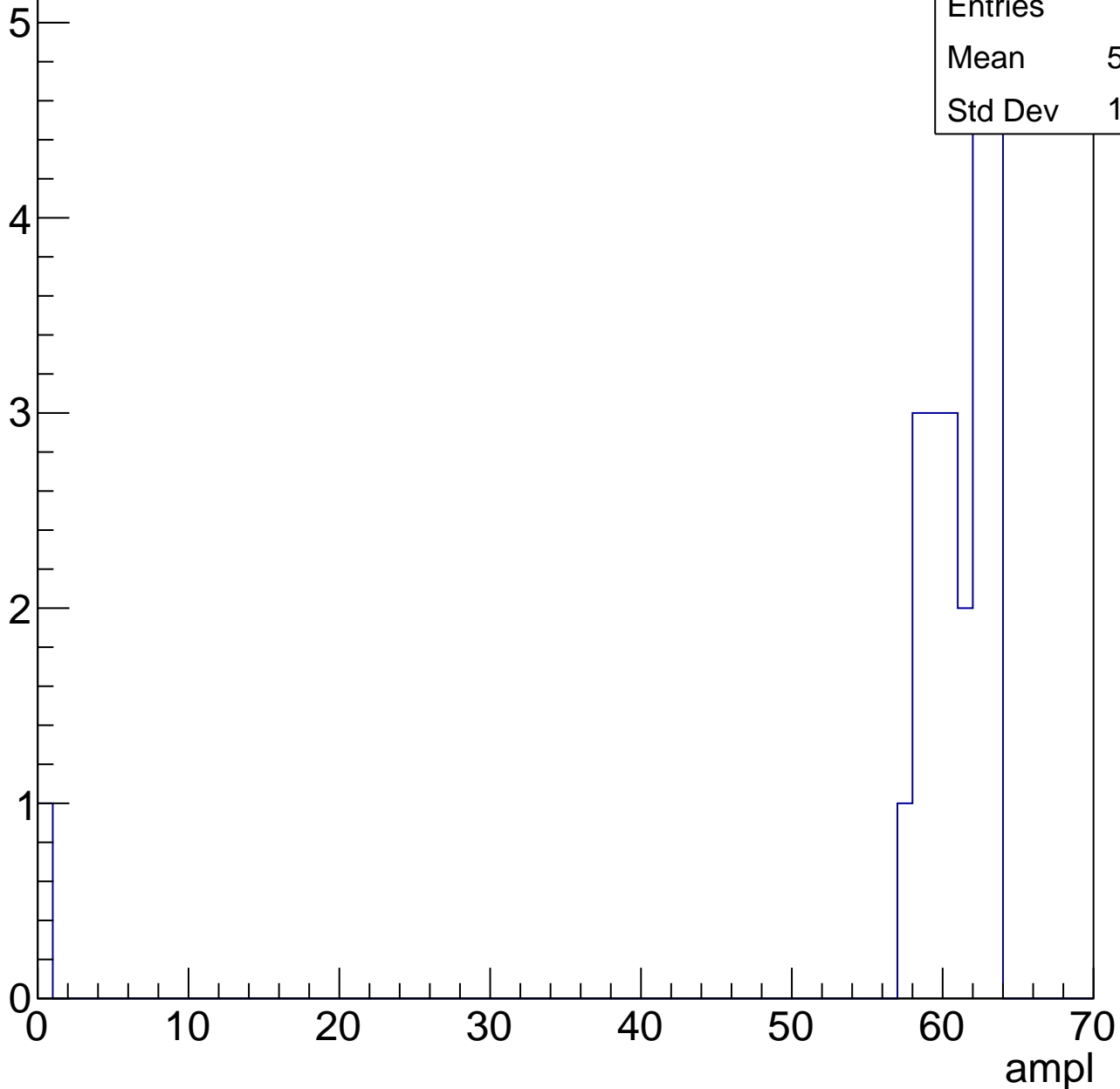


# B1L002S, U8-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.04
Std Dev	12.52

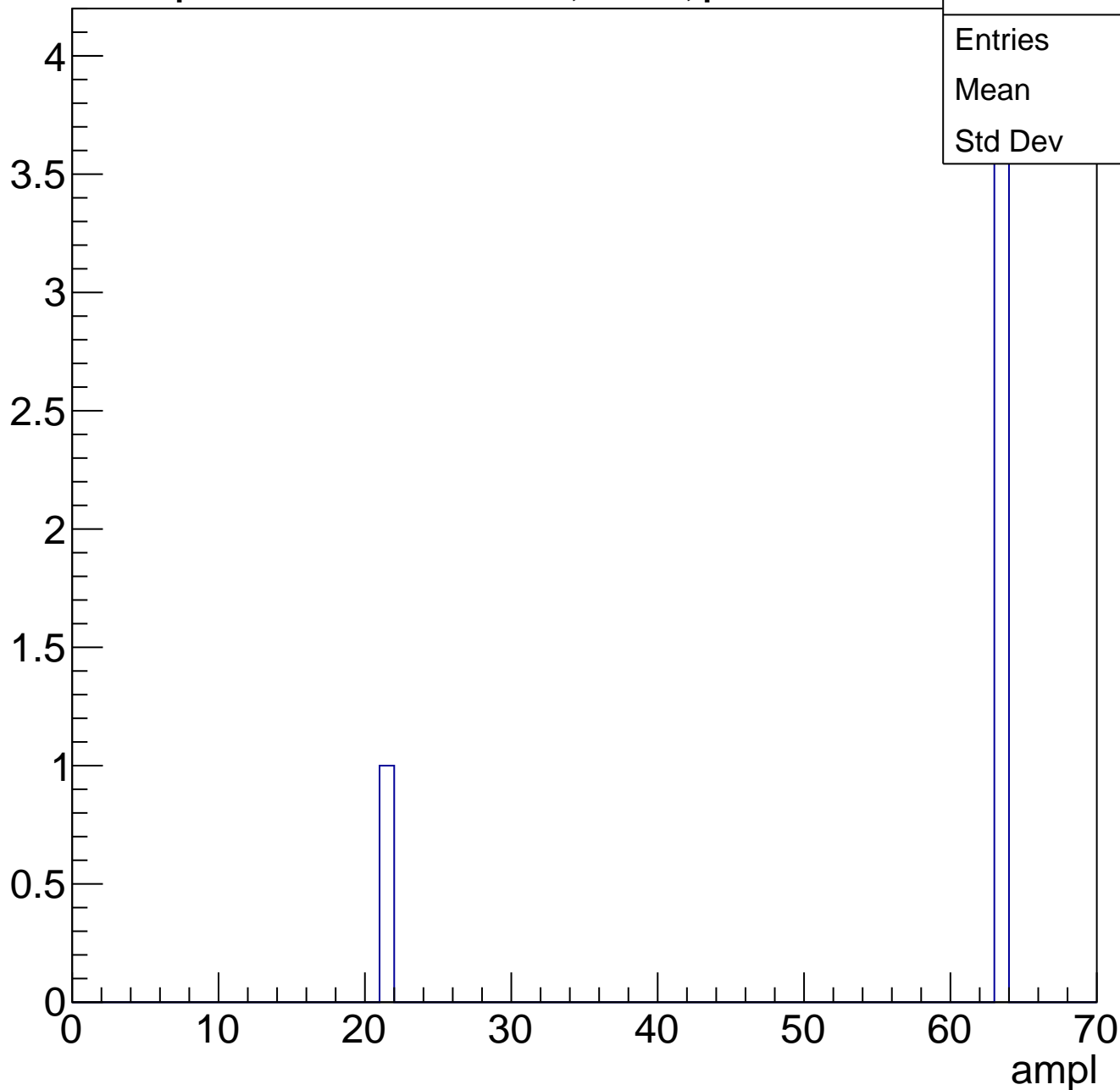




# B1L002S, U8-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch25, adc0

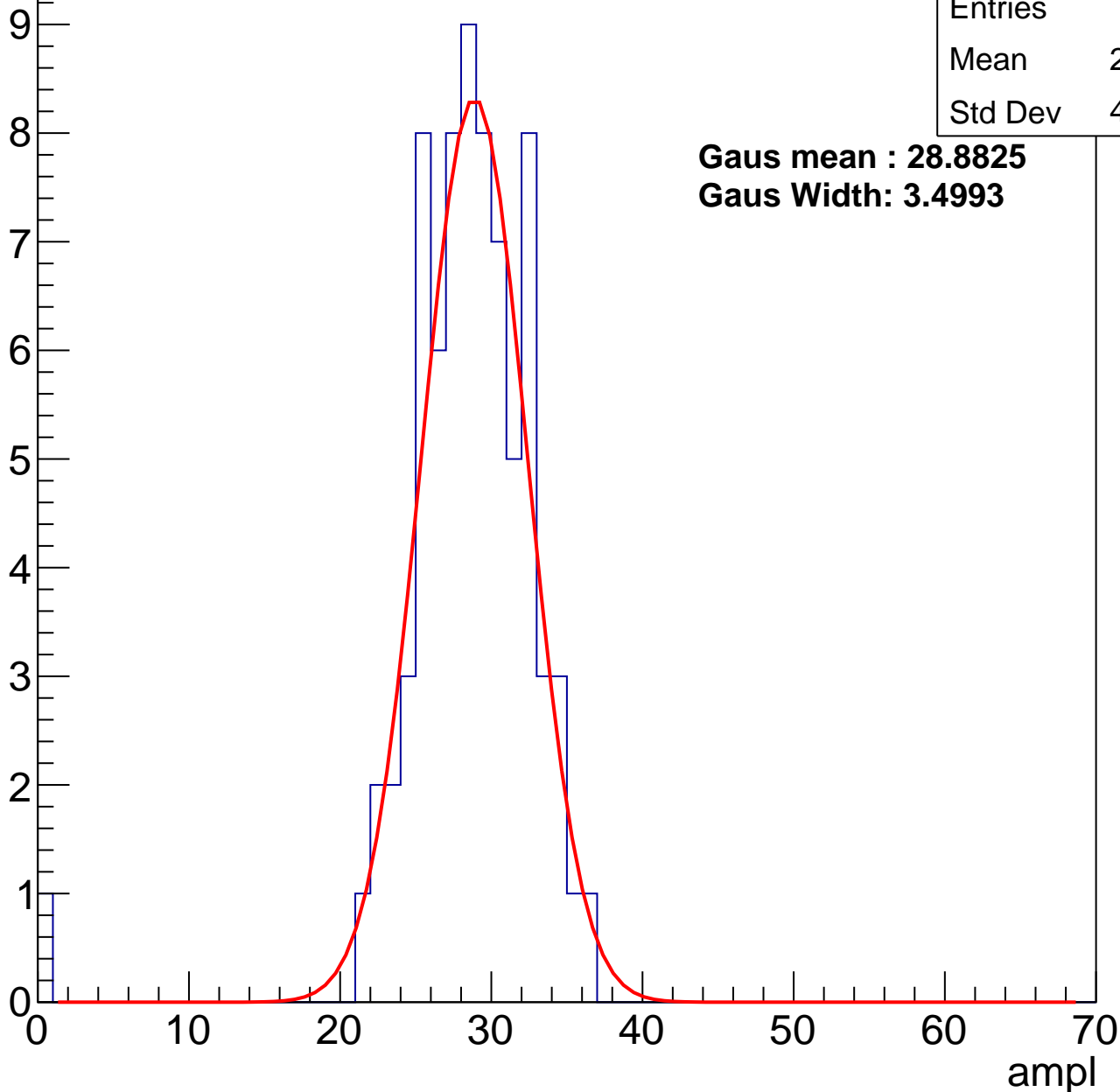
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	28.05
Std Dev	4.605

**Gaus mean : 28.8825**

**Gaus Width: 3.4993**



# B1L002S, U8-ch25, adc1

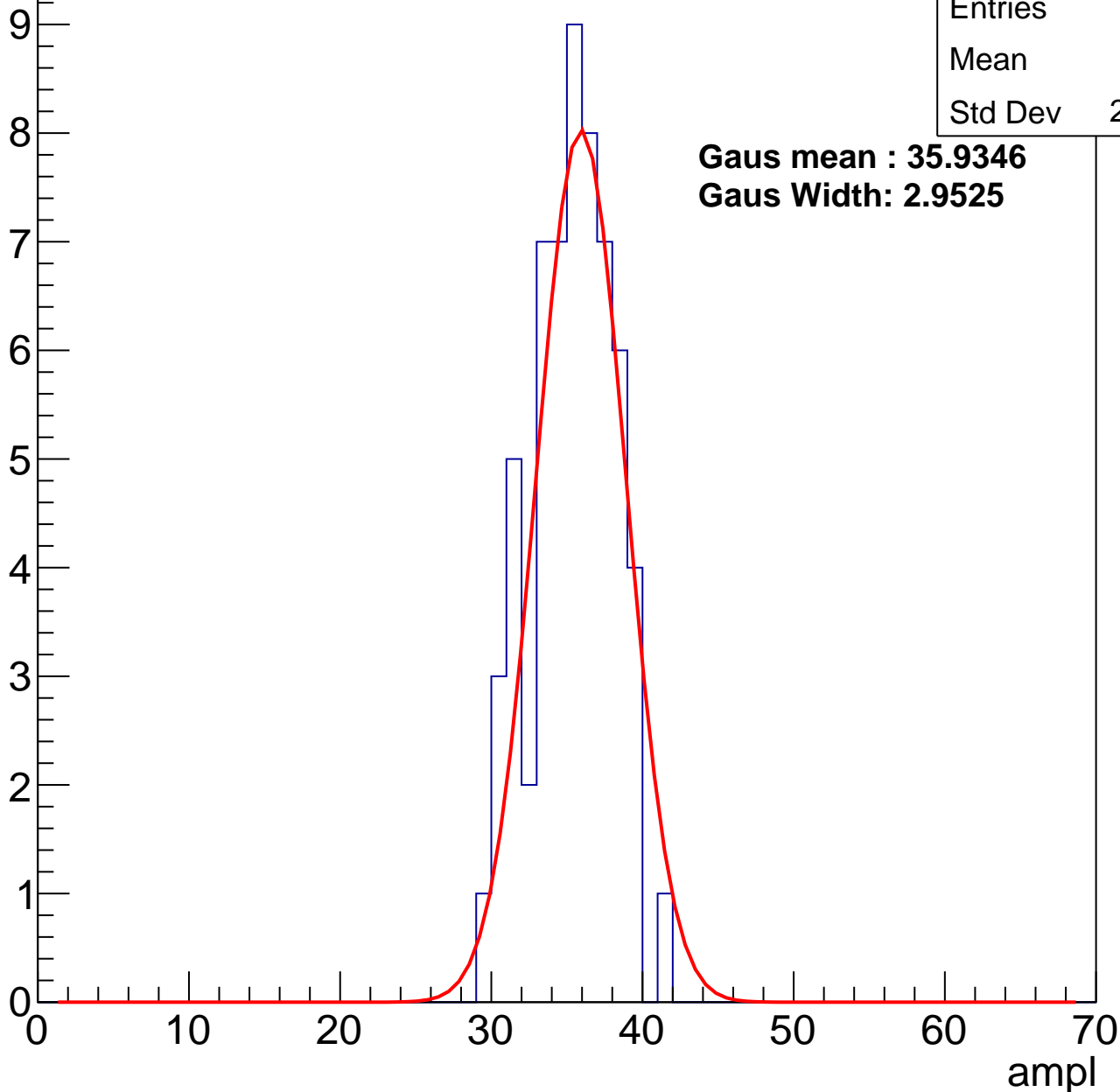
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.9
Std Dev	2.688

**Gaus mean : 35.9346**

**Gaus Width: 2.9525**



# B1L002S, U8-ch25, adc2

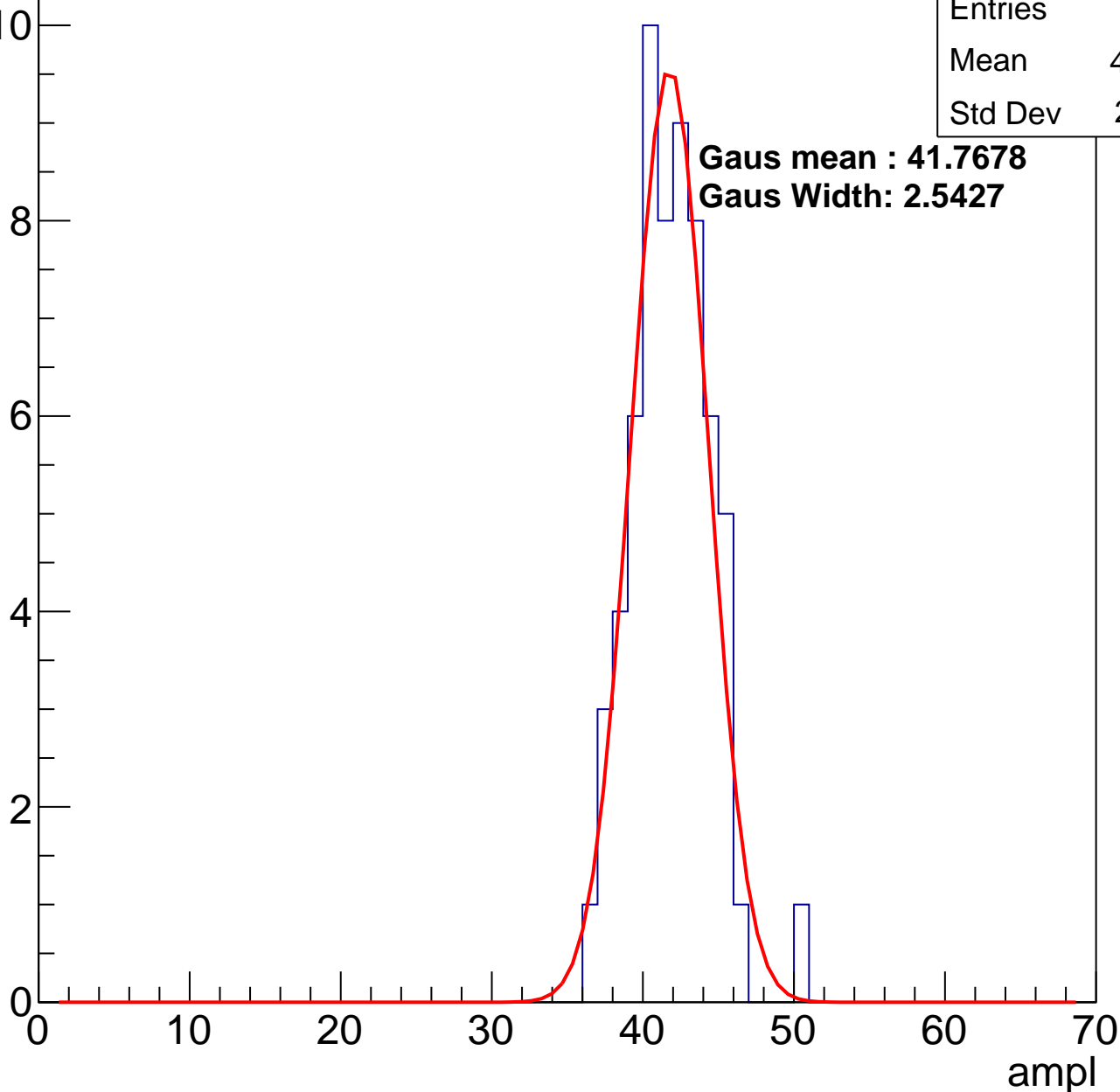
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.42
Std Dev	2.581

**Gaus mean : 41.7678**

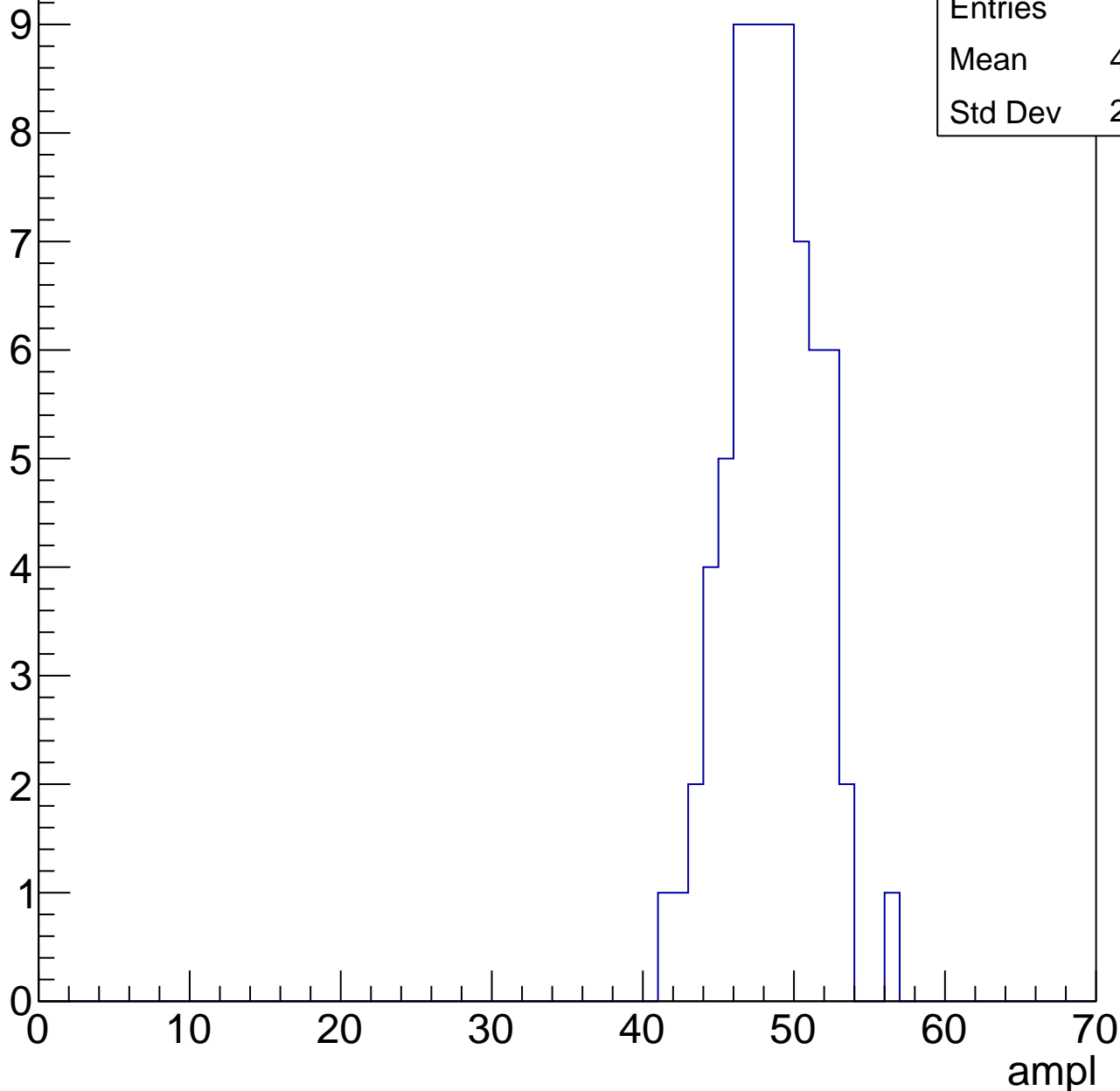
**Gaus Width: 2.5427**



# B1L002S, U8-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

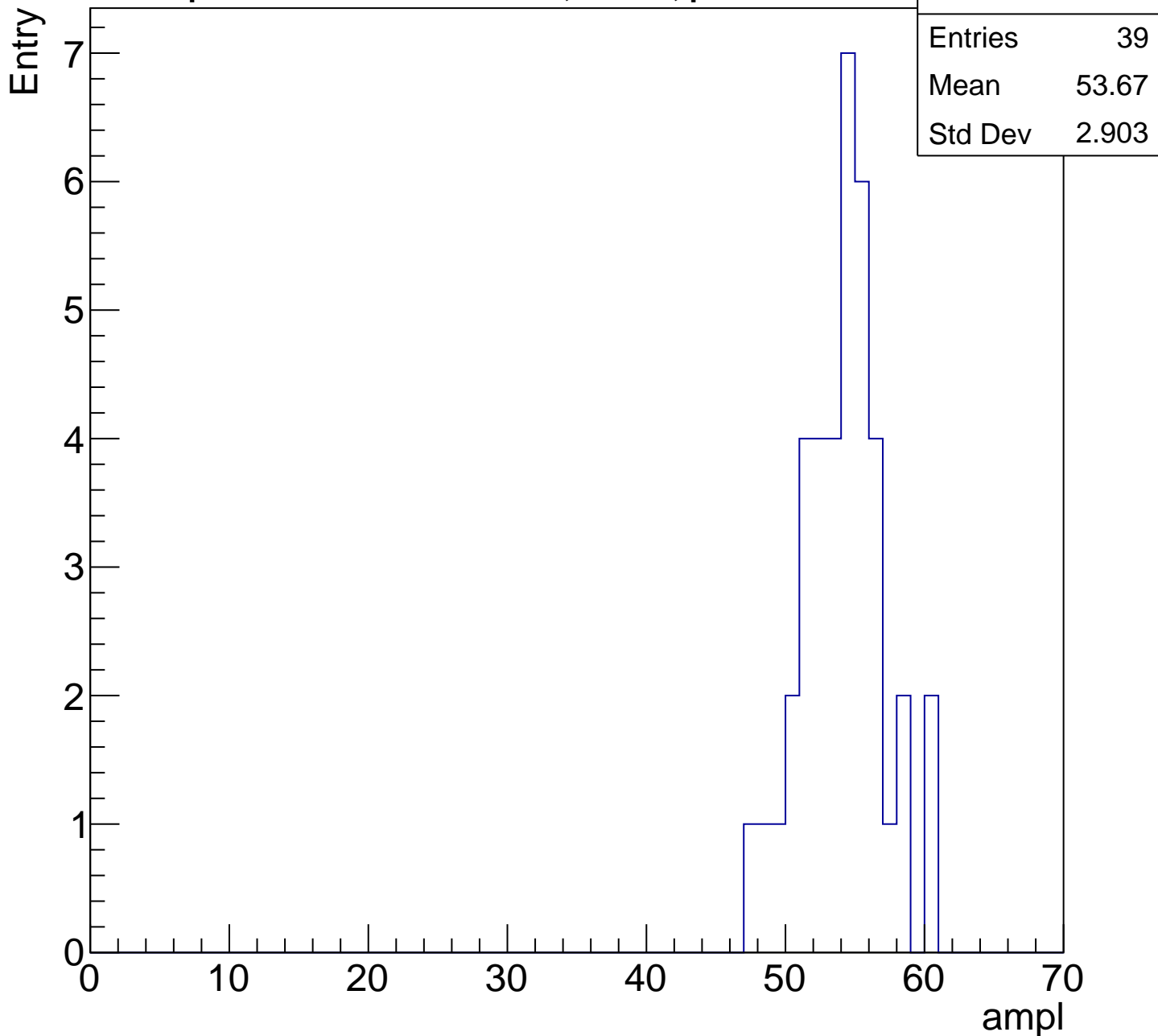
Entry



Entries	71
Mean	48.03
Std Dev	2.883

# B1L002S, U8-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch25, adc5

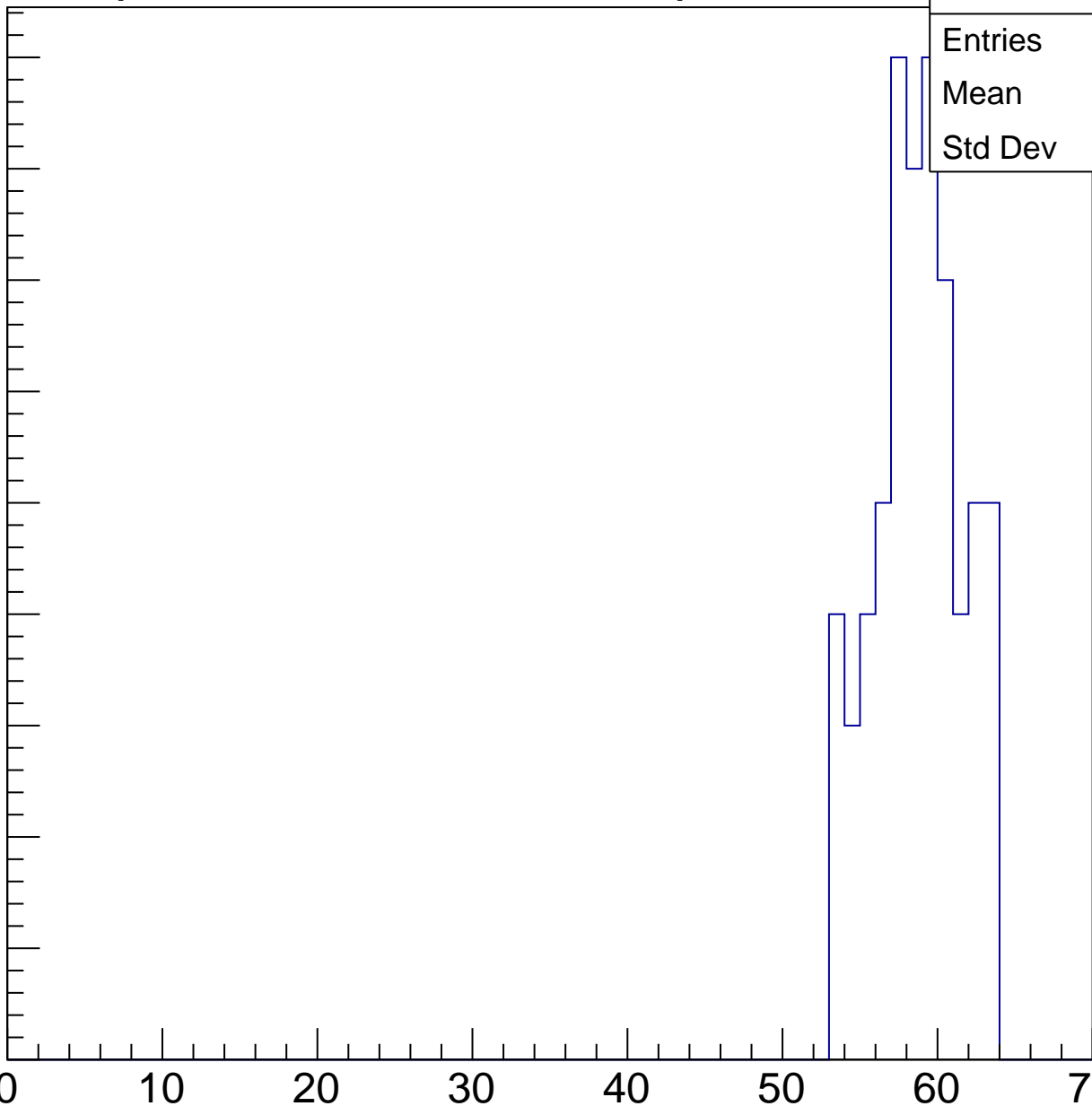
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.27
Std Dev	2.779

ampl

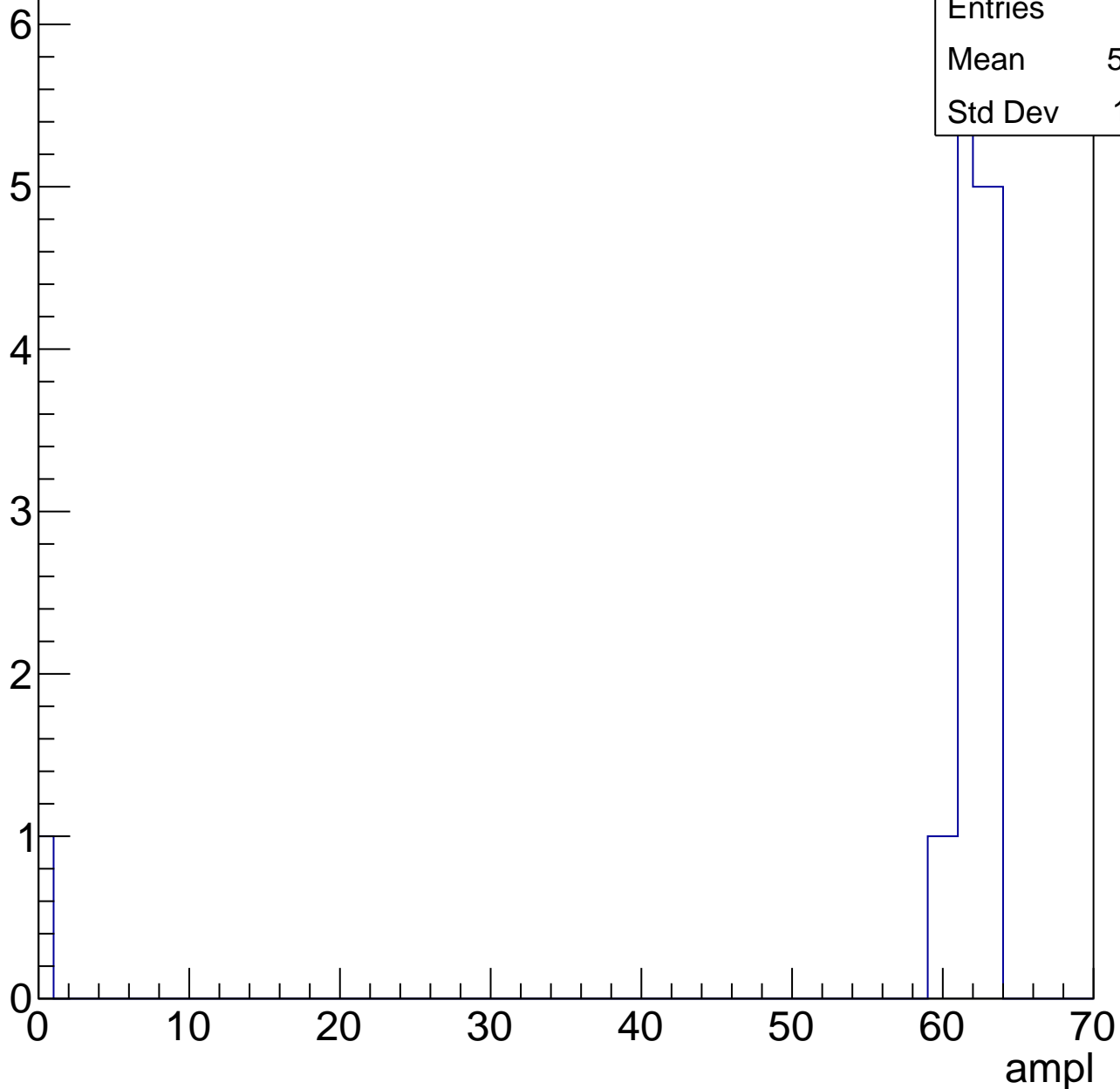


# B1L002S, U8-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.42
Std Dev	13.81





# B1L002S, U8-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch26, adc0

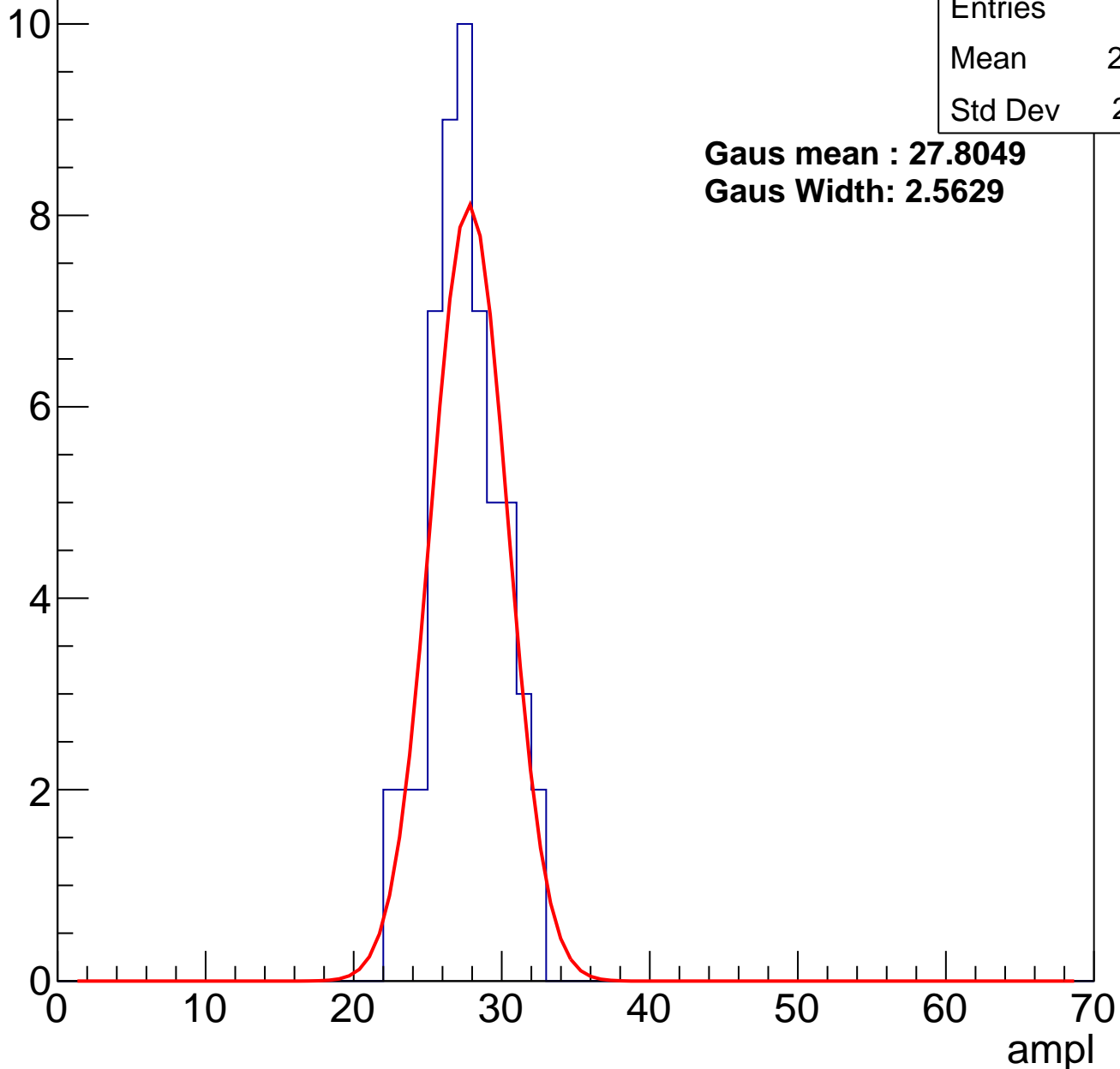
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	54
Mean	27.13
Std Dev	2.381

**Gaus mean : 27.8049**

**Gaus Width: 2.5629**

Entry



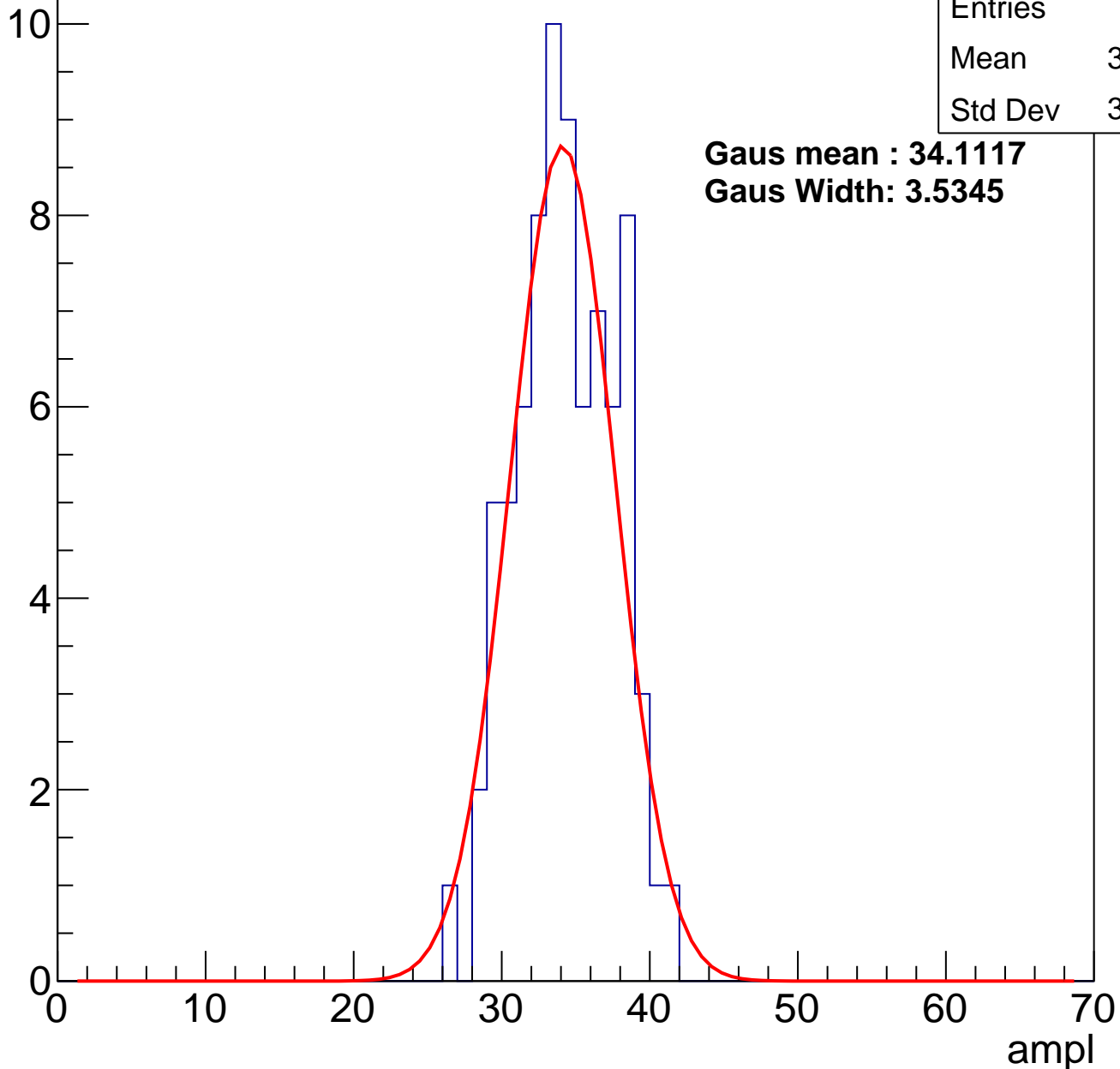
# B1L002S, U8-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	33.86
Std Dev	3.225

**Gaus mean : 34.1117**  
**Gaus Width: 3.5345**

Entry



# B1L002S, U8-ch26, adc2

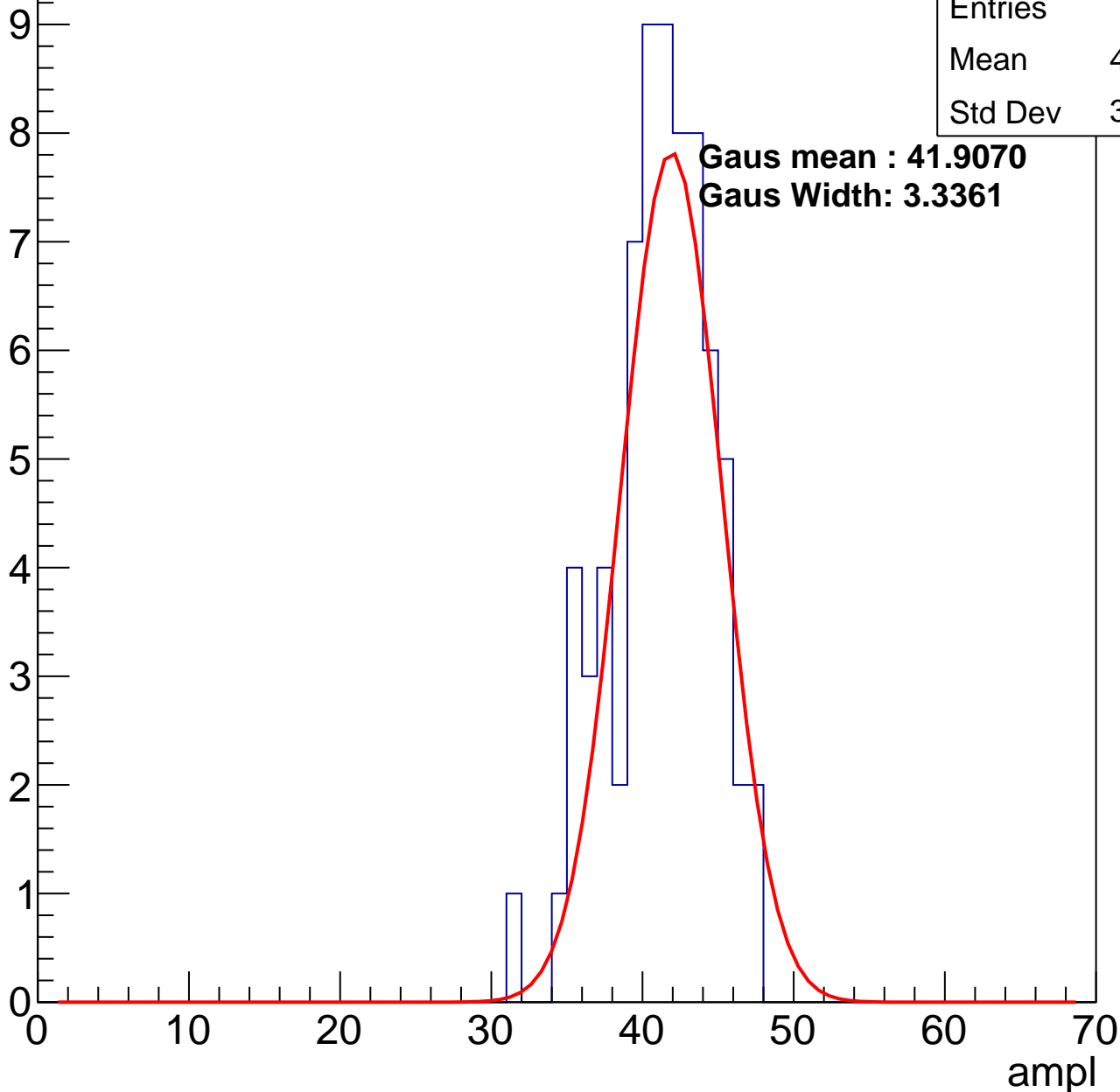
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.76
Std Dev	3.312

**Gaus mean : 41.9070**

**Gaus Width: 3.3361**

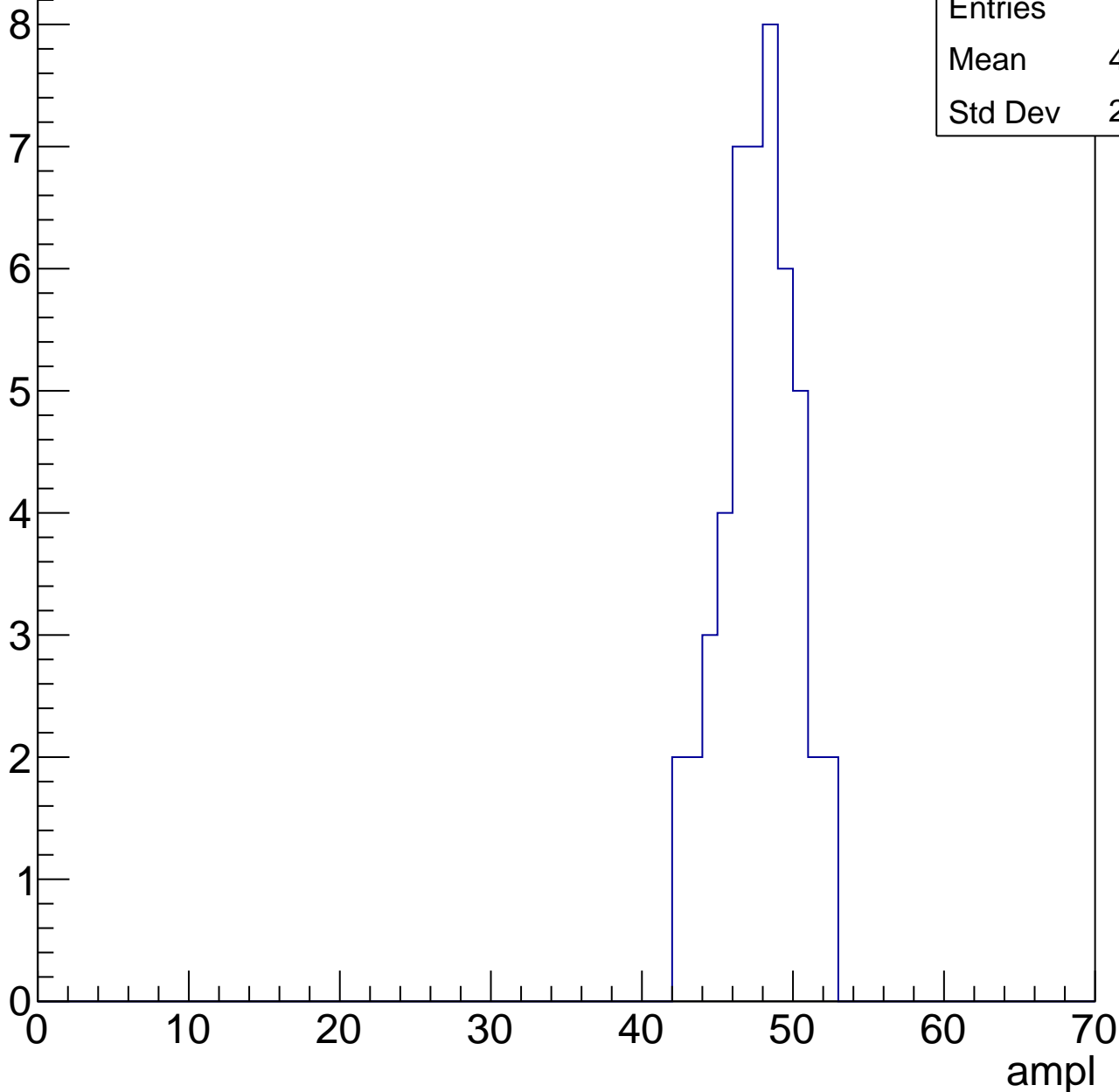


# B1L002S, U8-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	47.23
Std Dev	2.452

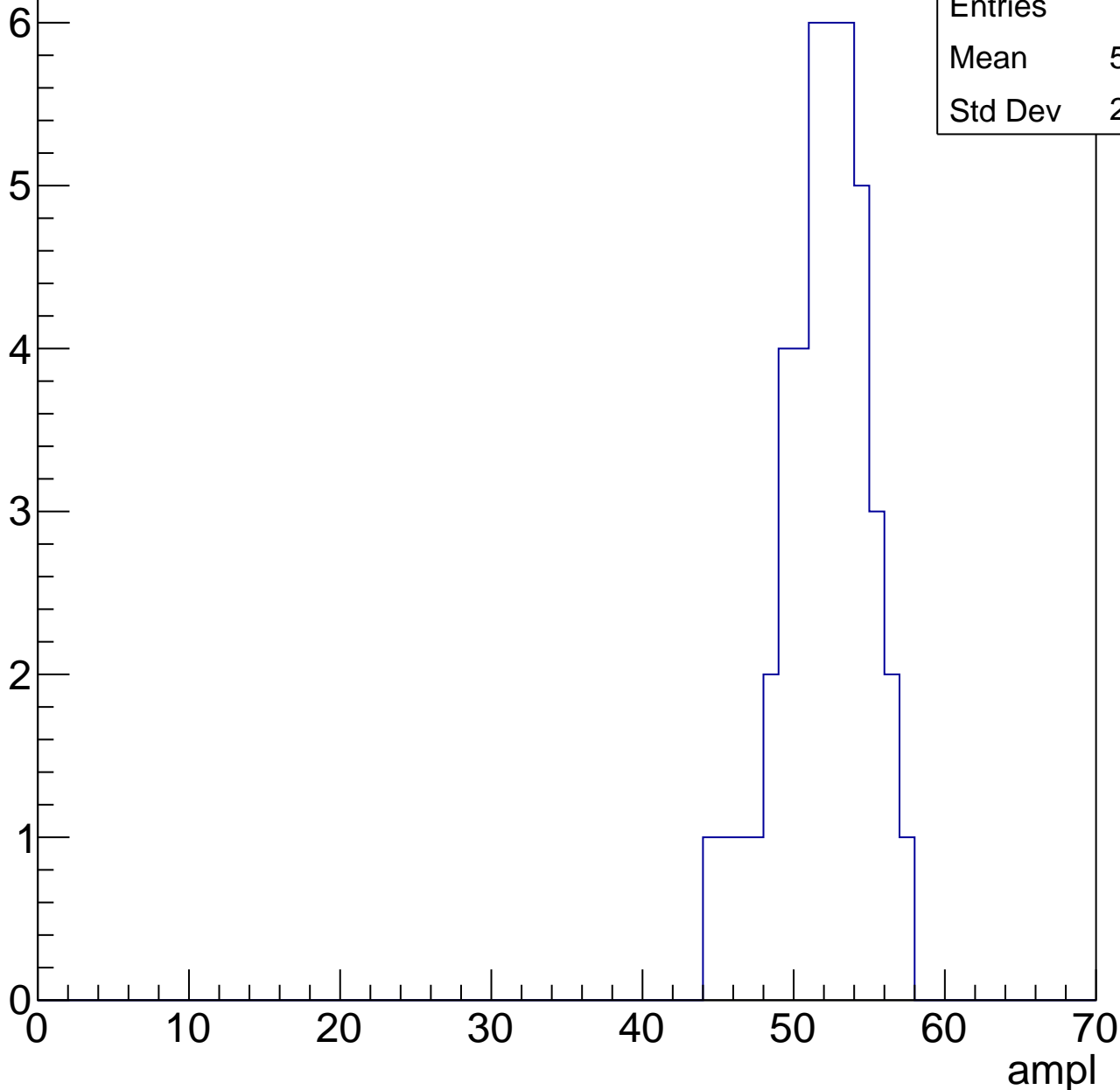


# B1L002S, U8-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	51.49
Std Dev	2.904



# B1L002S, U8-ch26, adc5

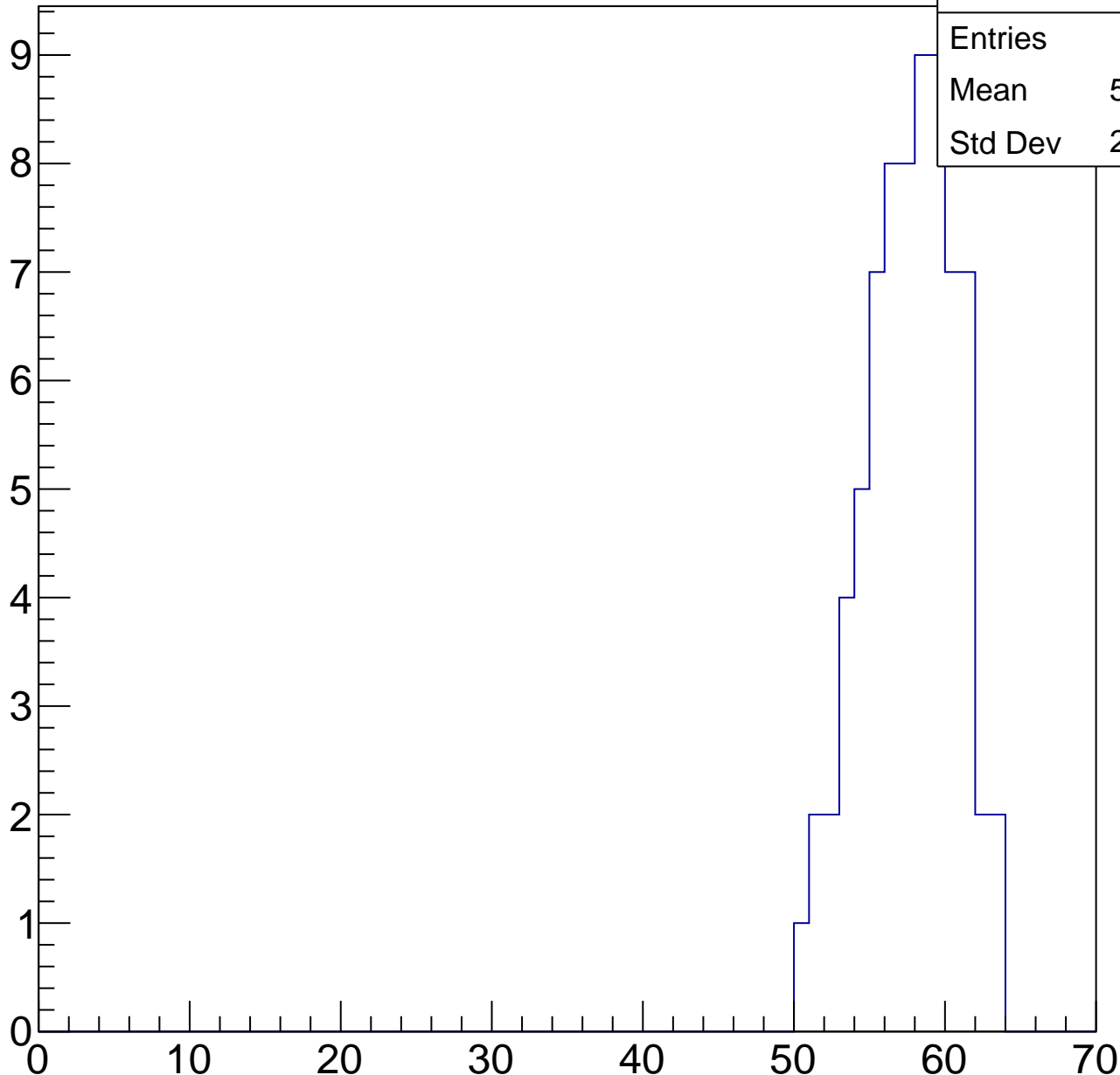
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	73
Mean	57.22
Std Dev	2.994

ampl

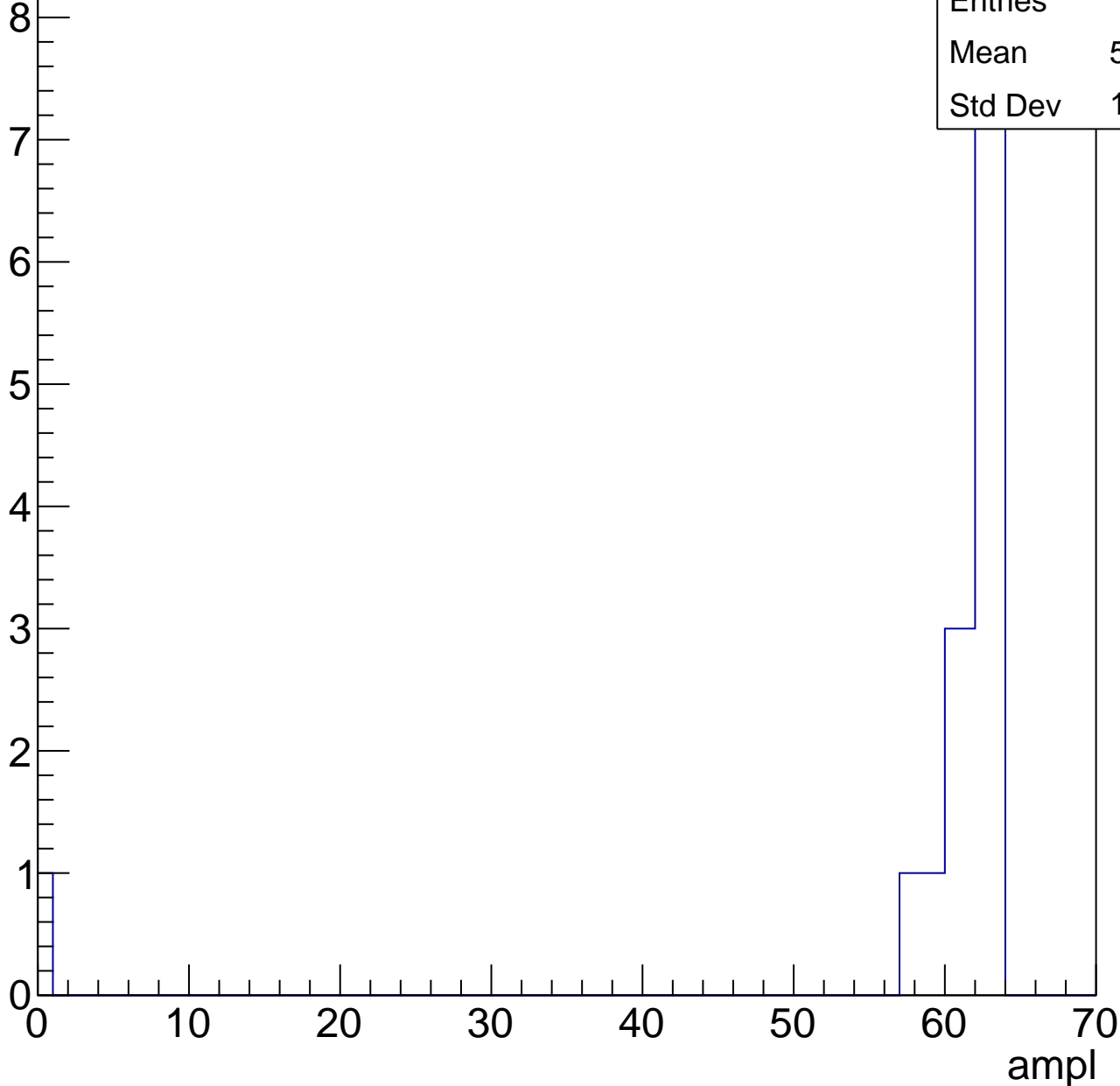


# B1L002S, U8-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59.12
Std Dev	11.93





# B1L002S, U8-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch27, adc0

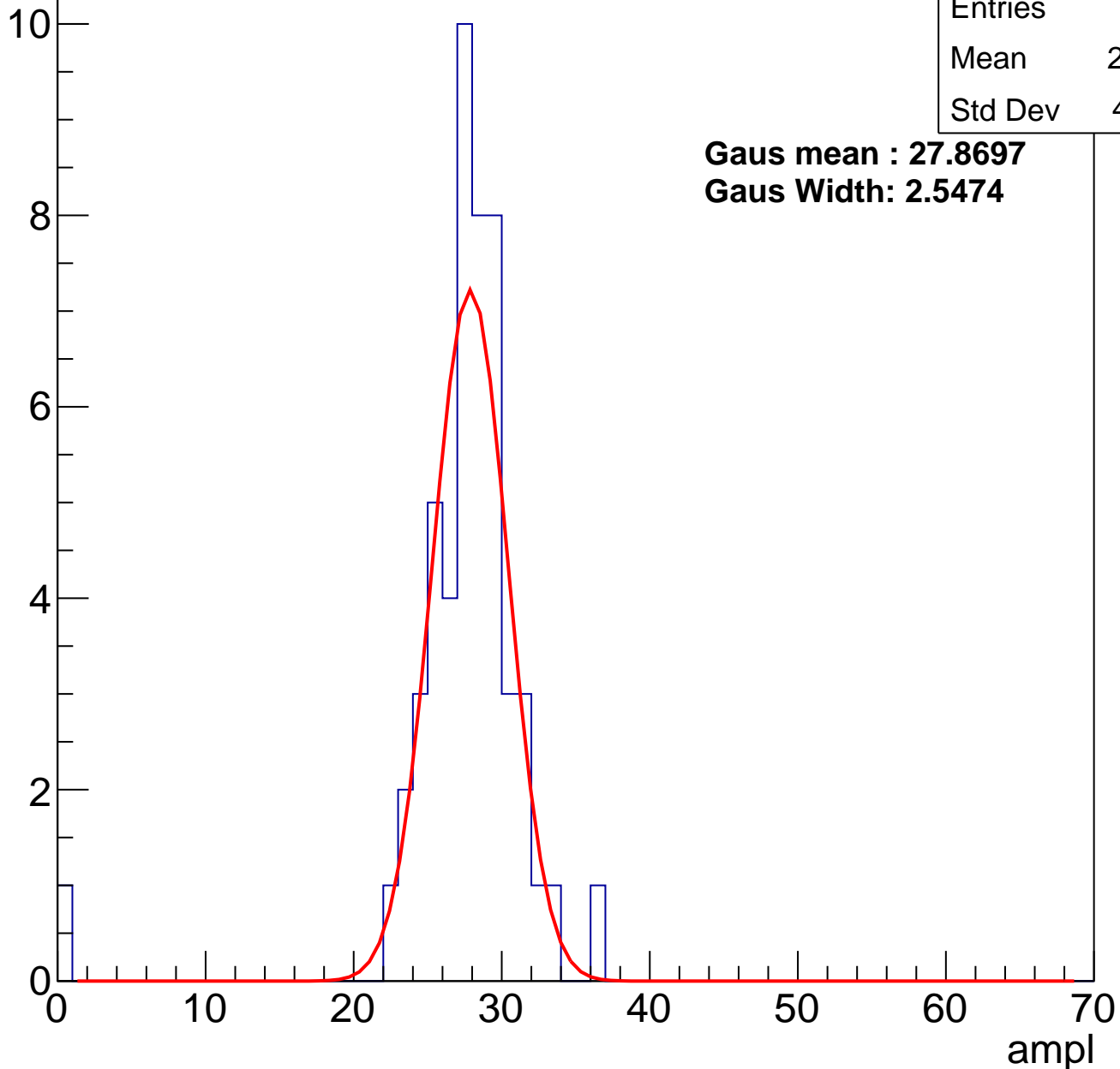
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	51
Mean	27.04
Std Dev	4.631

**Gaus mean : 27.8697**

**Gaus Width: 2.5474**

Entry



# B1L002S, U8-ch27, adc1

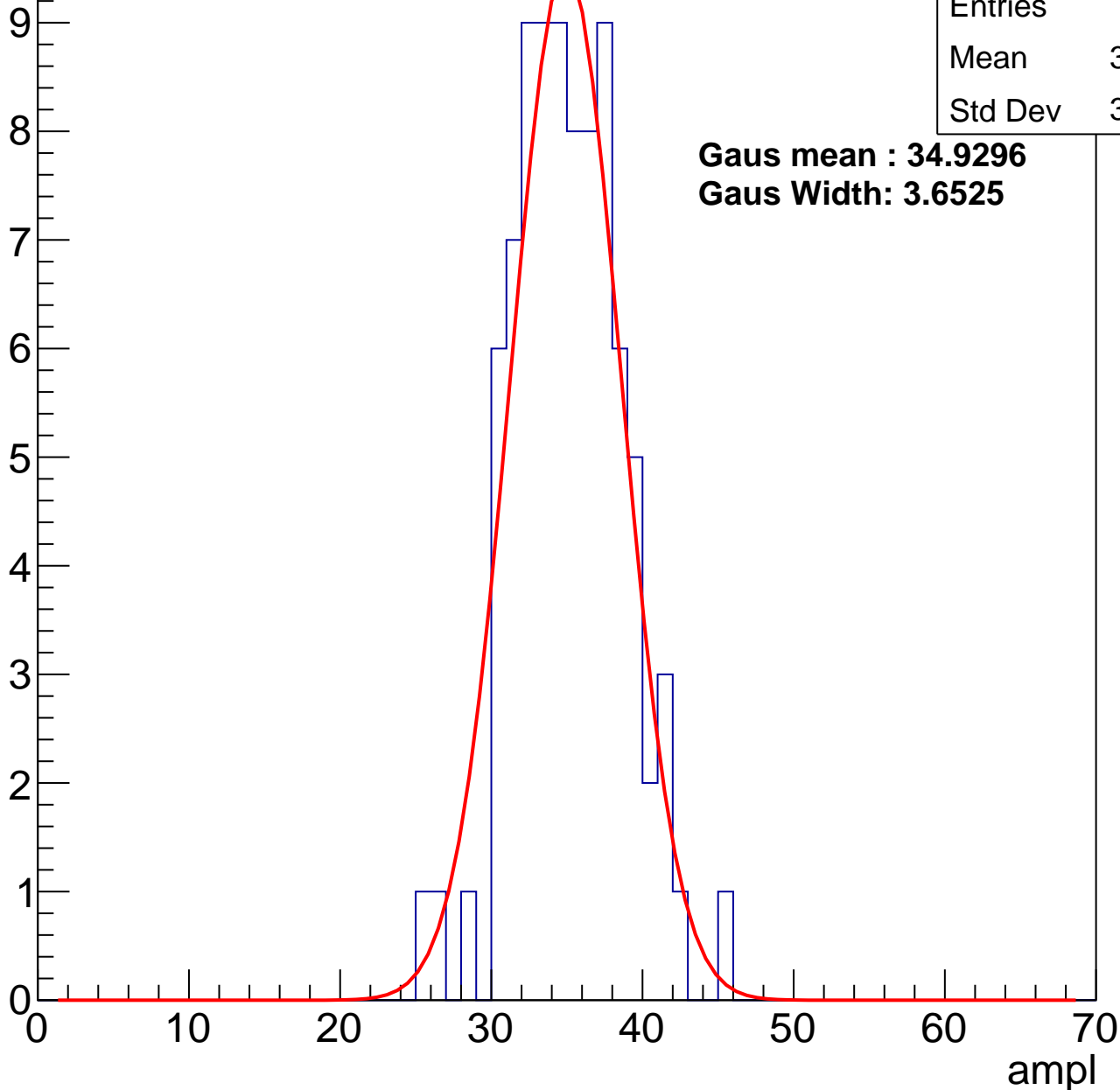
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	86
Mean	34.66
Std Dev	3.565

**Gaus mean : 34.9296**

**Gaus Width: 3.6525**



# B1L002S, U8-ch27, adc2

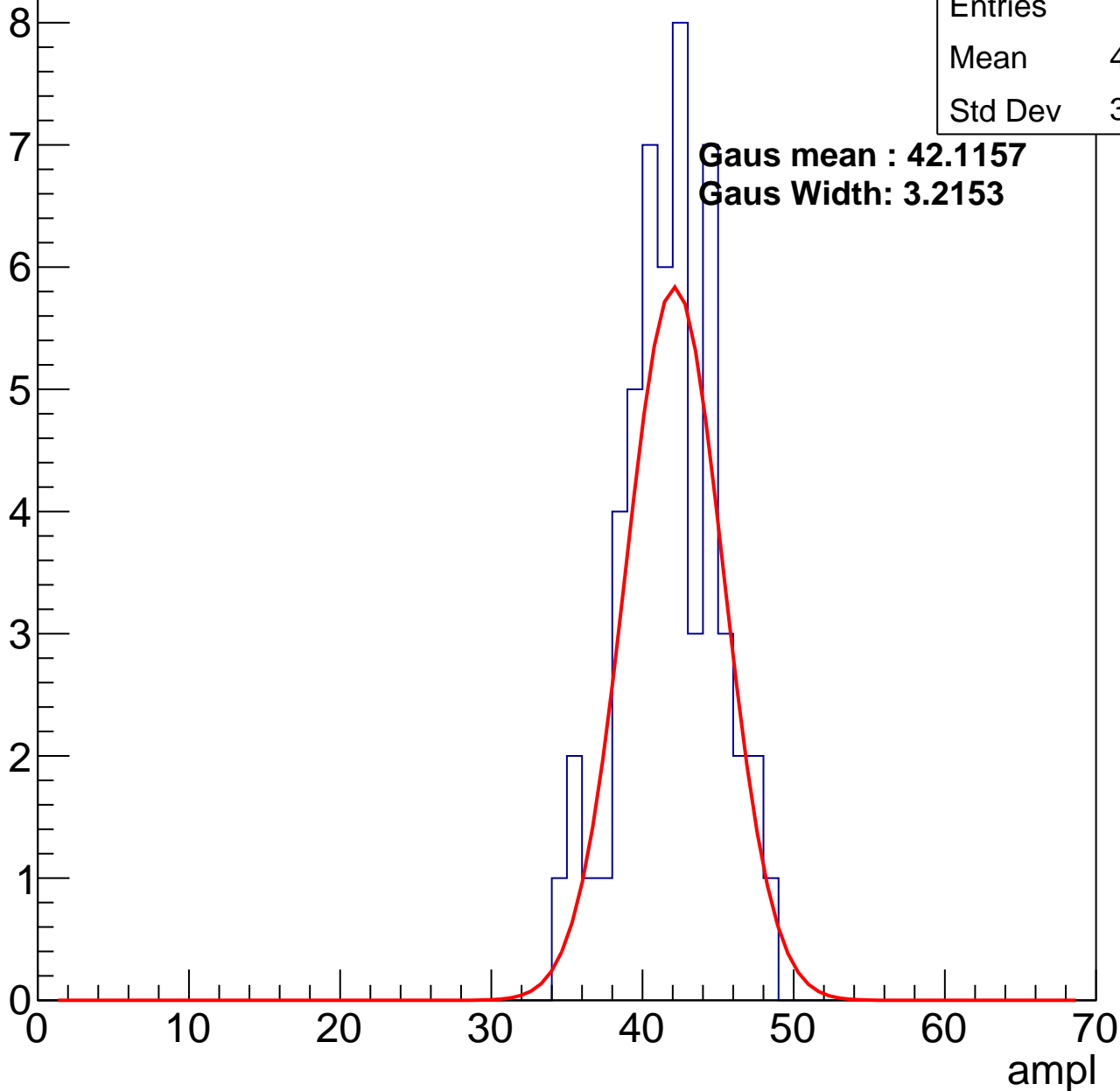
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	41.36
Std Dev	3.133

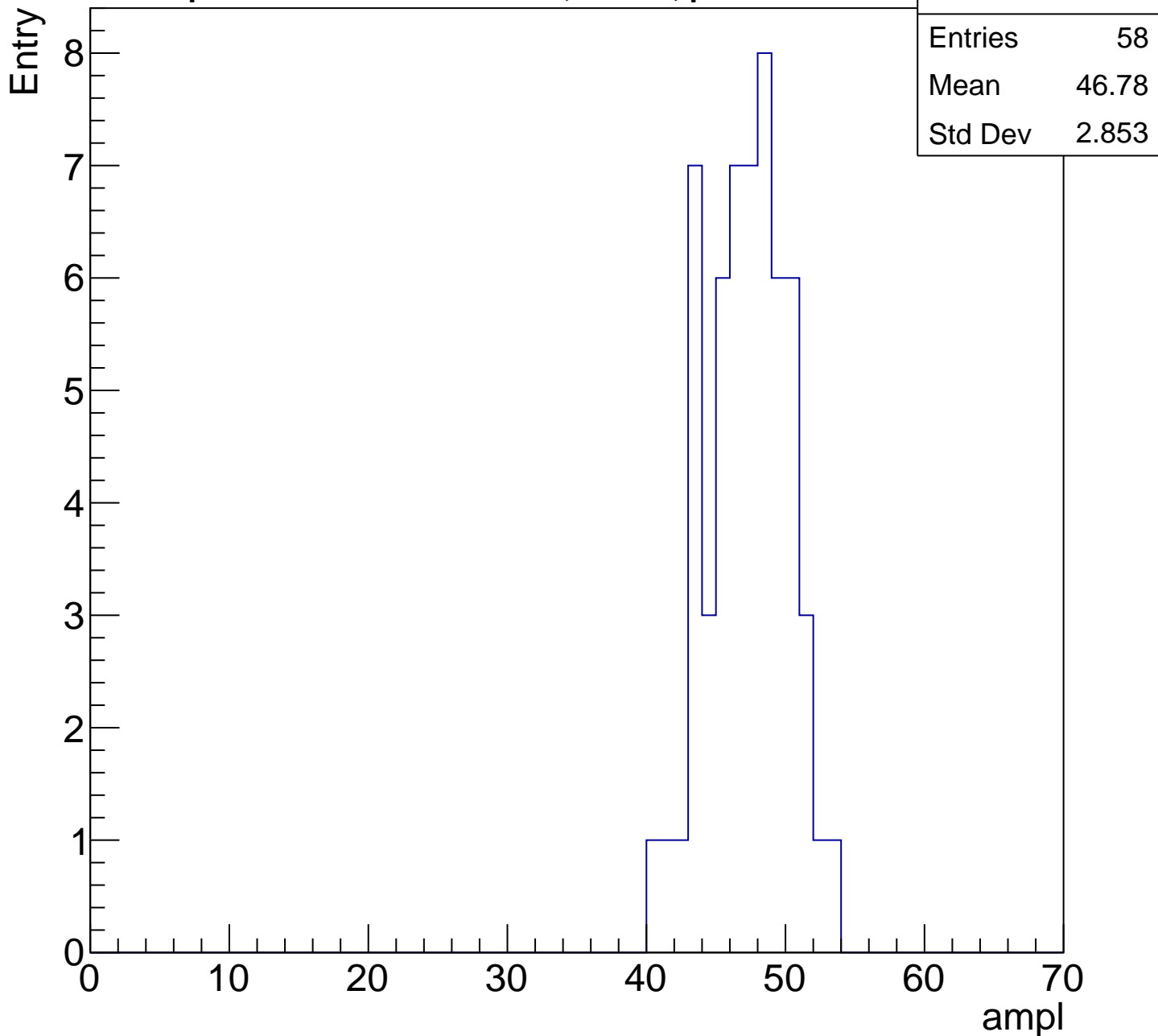
**Gaus mean : 42.1157**

**Gaus Width: 3.2153**



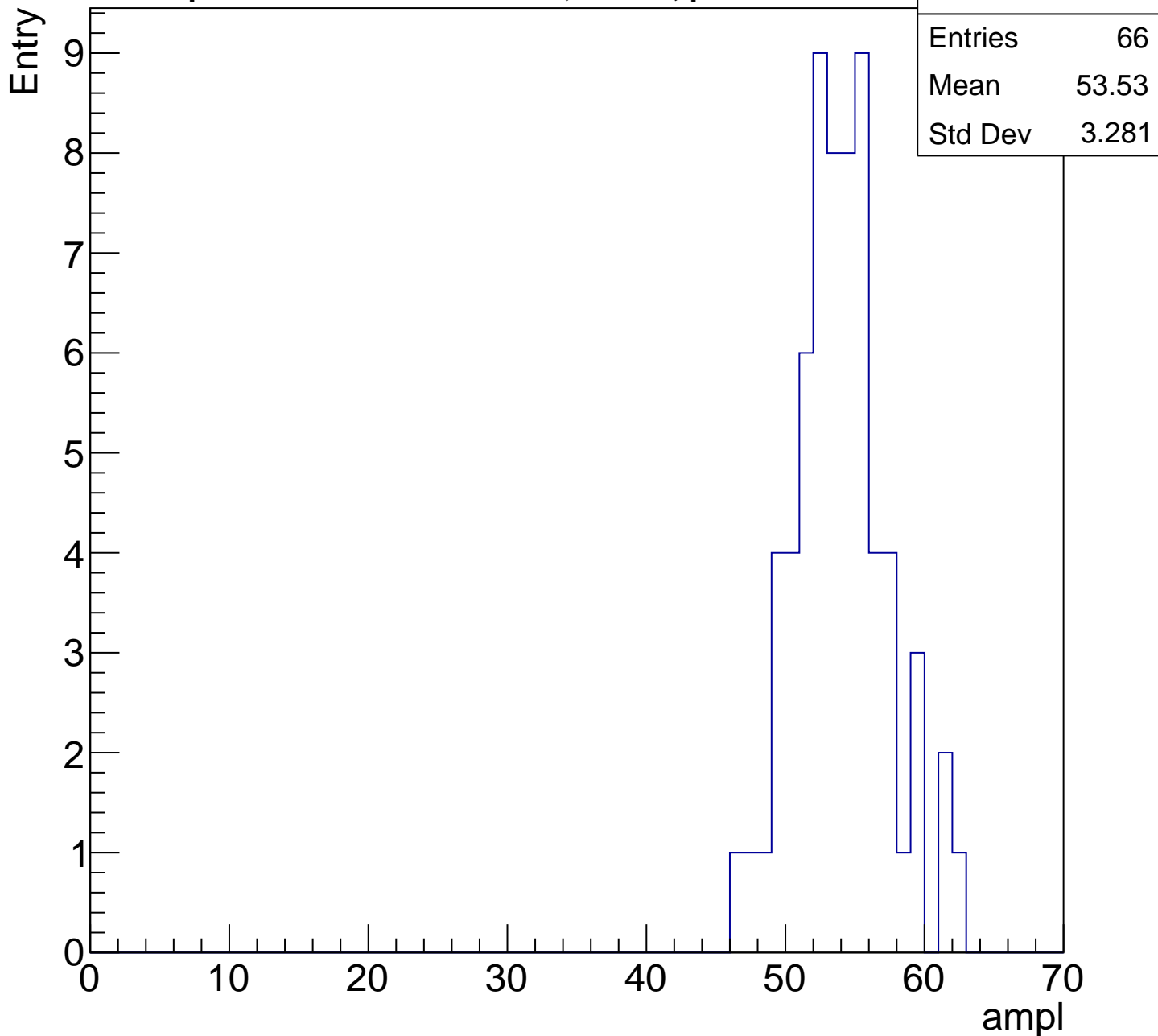
# B1L002S, U8-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

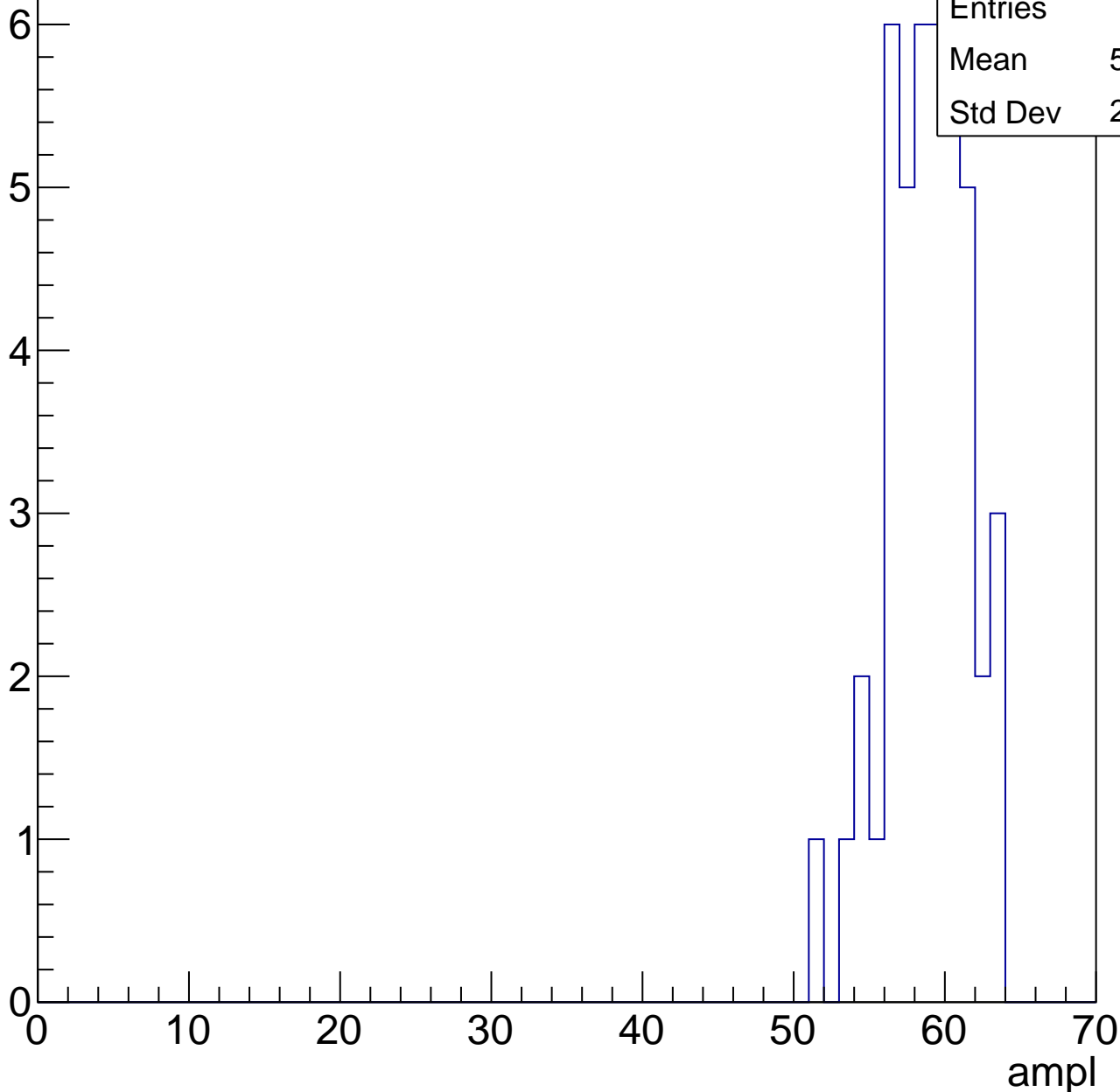


# B1L002S, U8-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.36
Std Dev	2.698

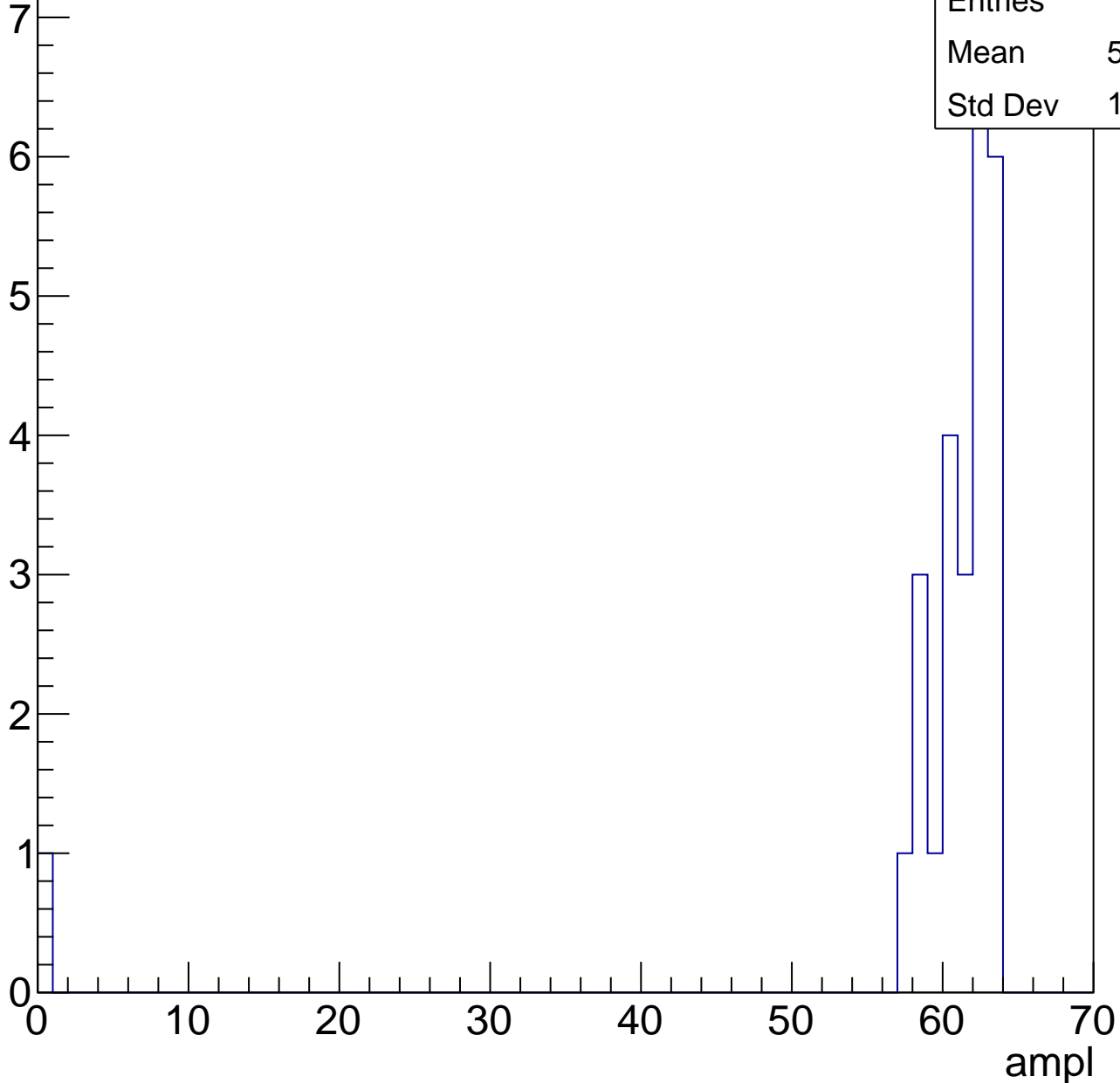


# B1L002S, U8-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.65
Std Dev	11.86





# B1L002S, U8-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L002S, U8-ch28, adc0

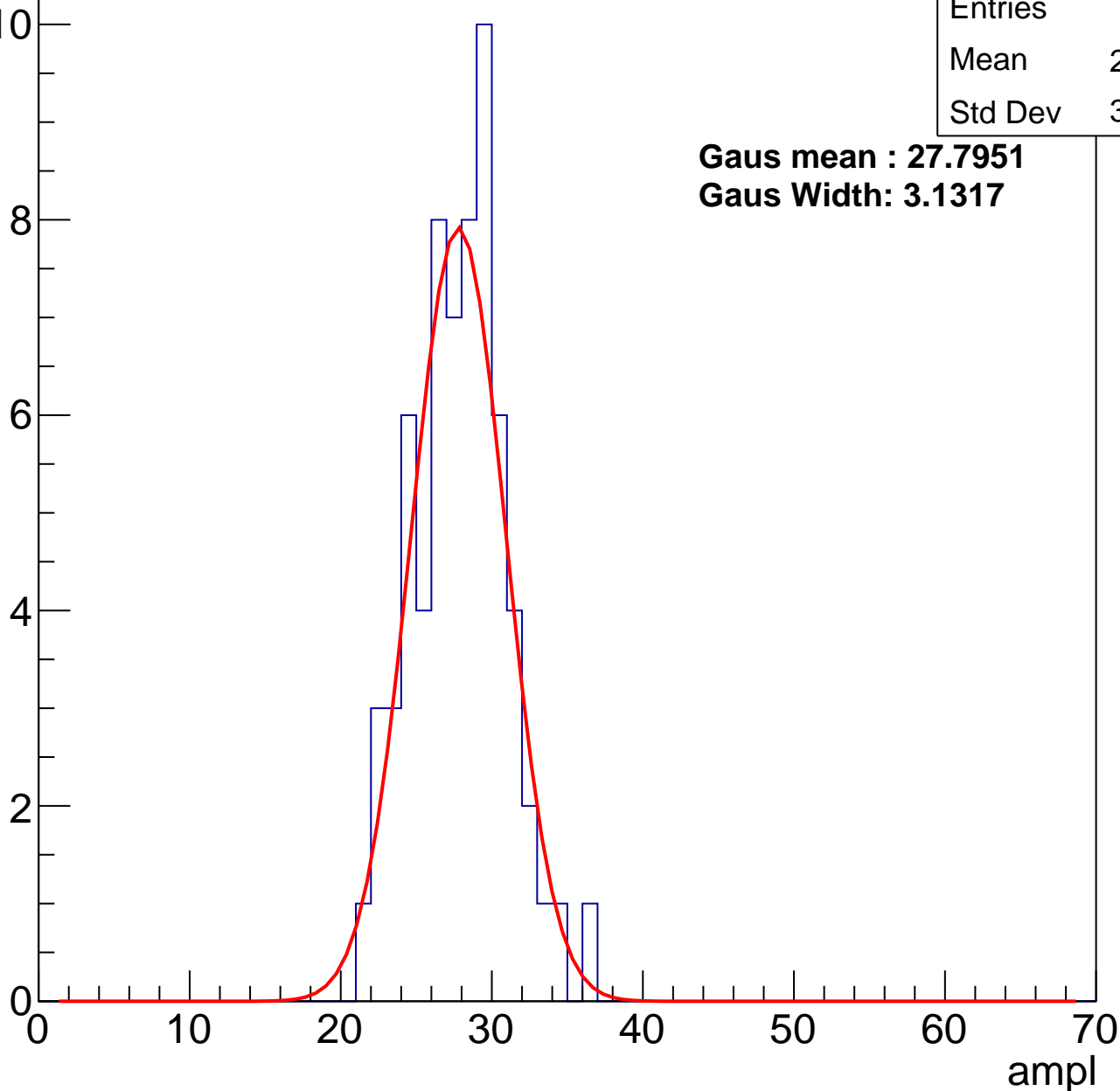
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.42
Std Dev	3.058

**Gaus mean : 27.7951**

**Gaus Width: 3.1317**



# B1L002S, U8-ch28, adc1

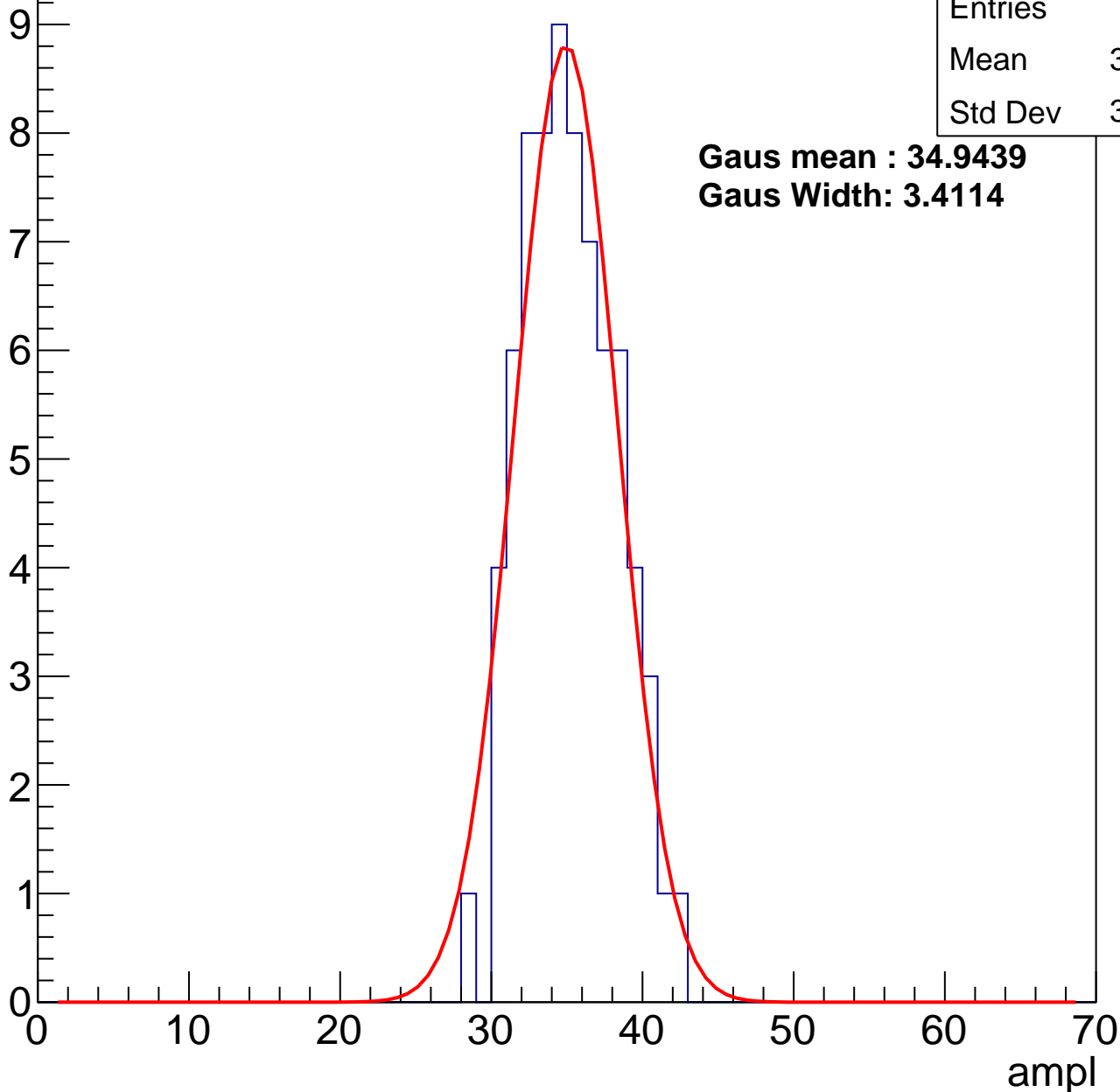
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.74
Std Dev	3.032

**Gaus mean : 34.9439**

**Gaus Width: 3.4114**



# B1L002S, U8-ch28, adc2

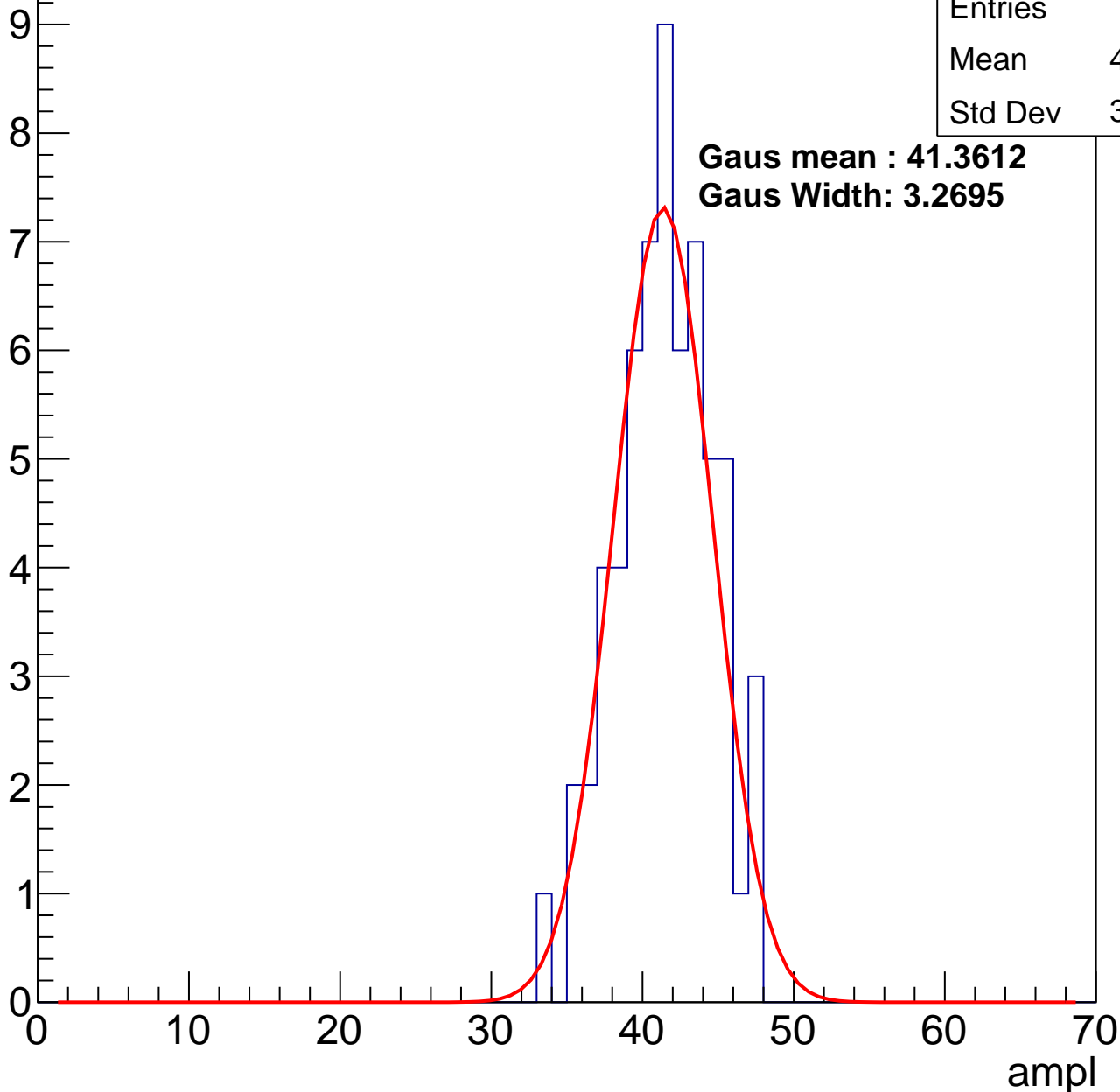
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.02
Std Dev	3.134

**Gaus mean : 41.3612**

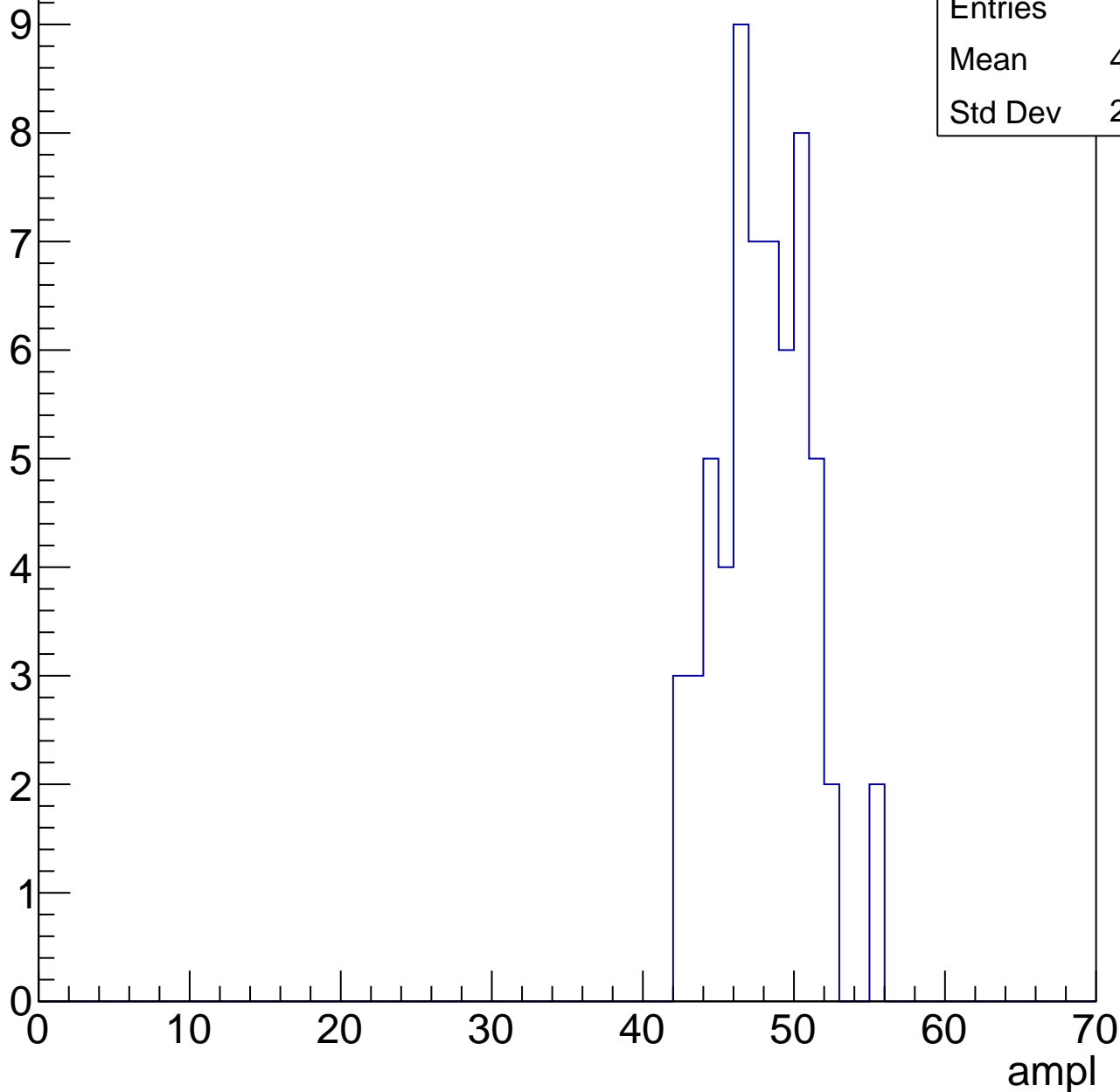
**Gaus Width: 3.2695**



# B1L002S, U8-ch28, adc3

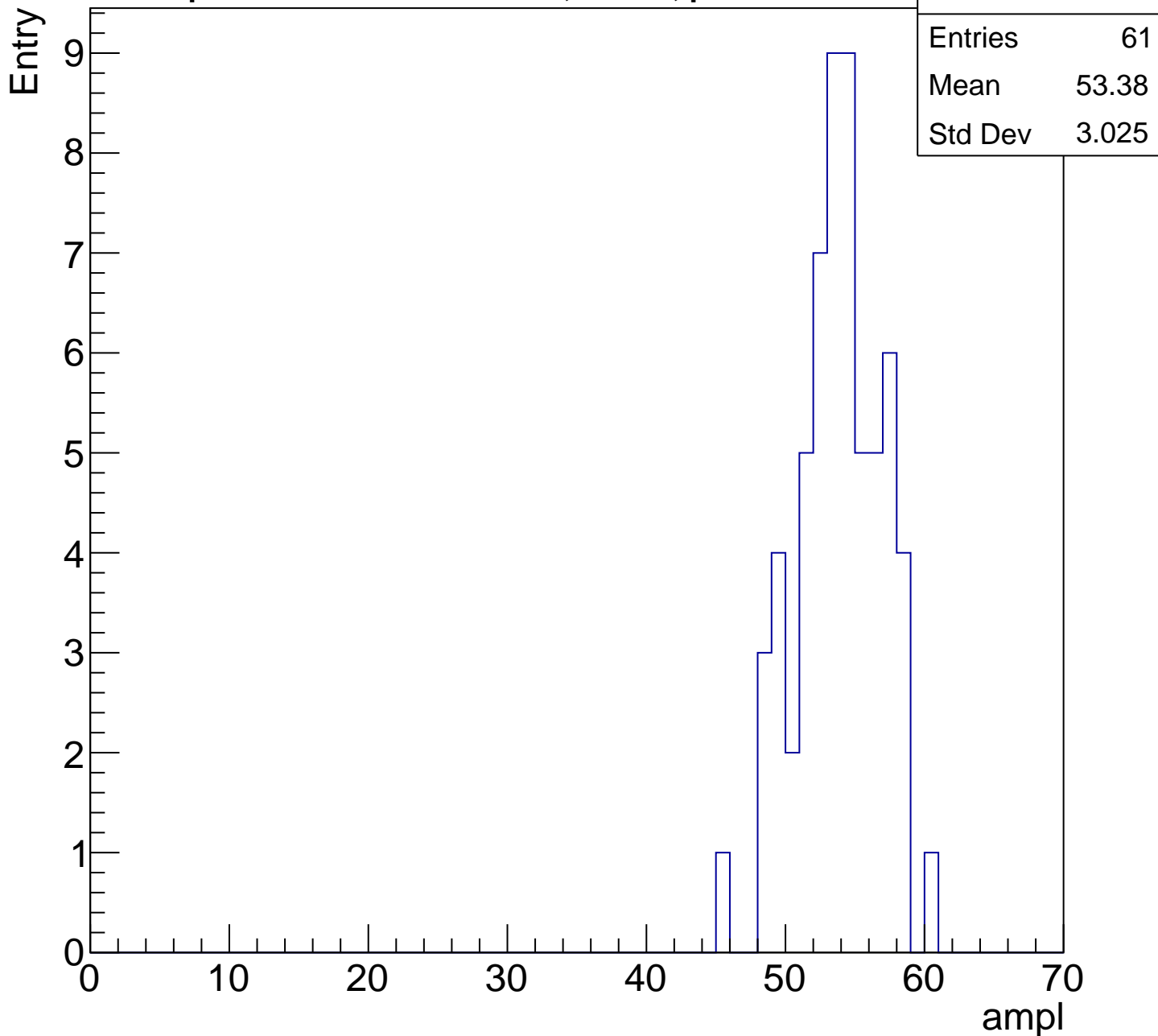
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



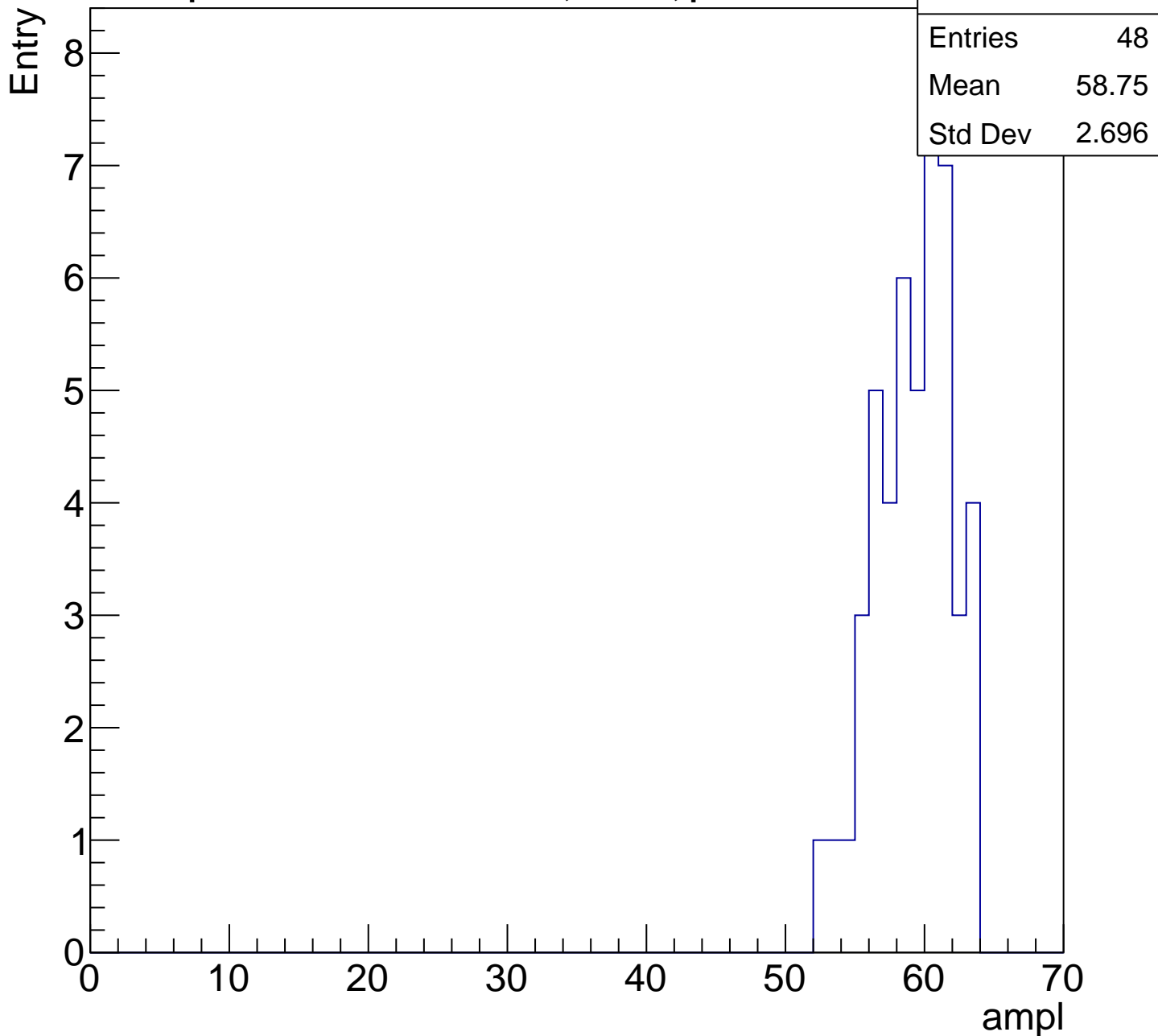
# B1L002S, U8-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

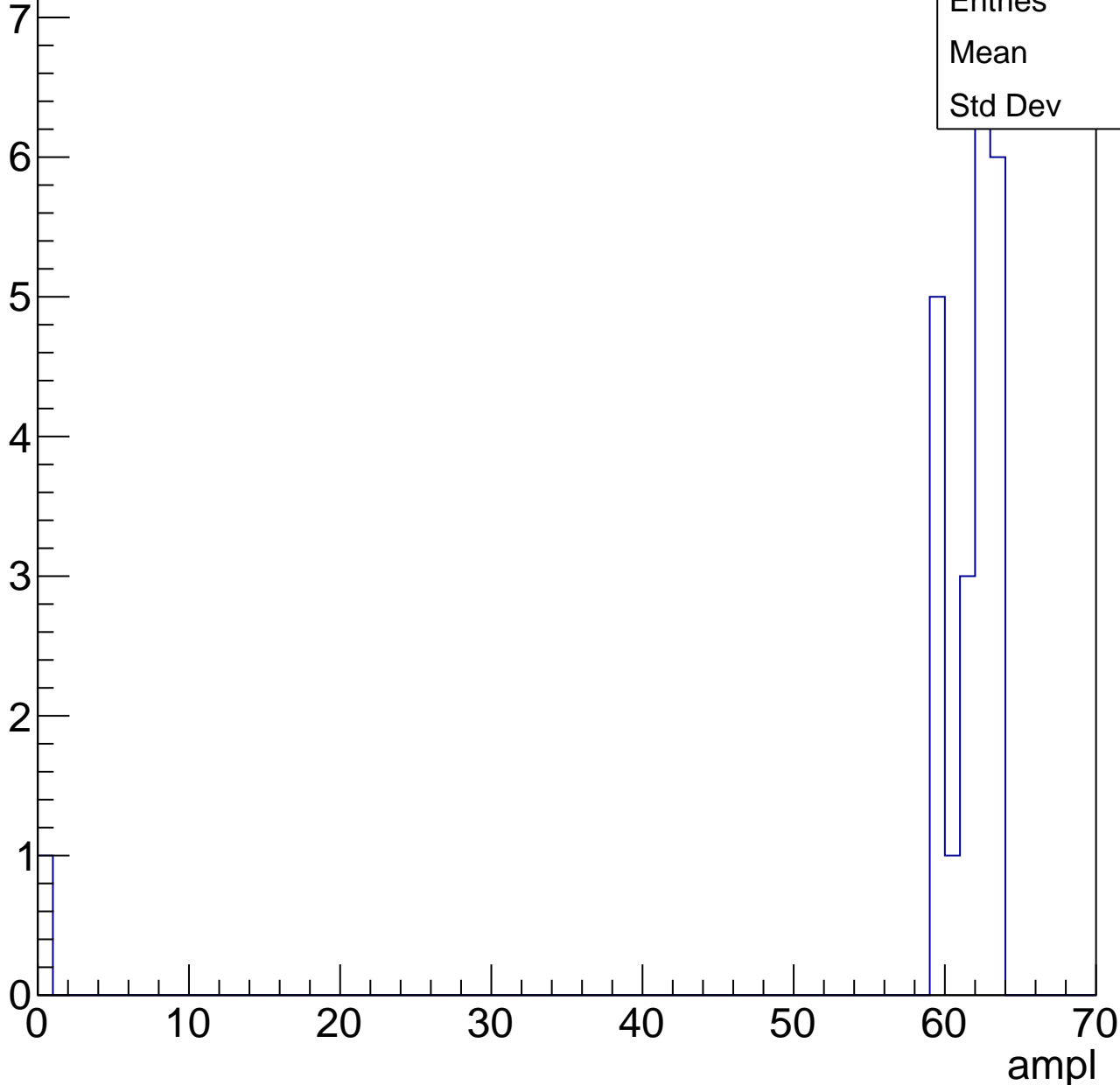


# B1L002S, U8-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.7
Std Dev	12.6

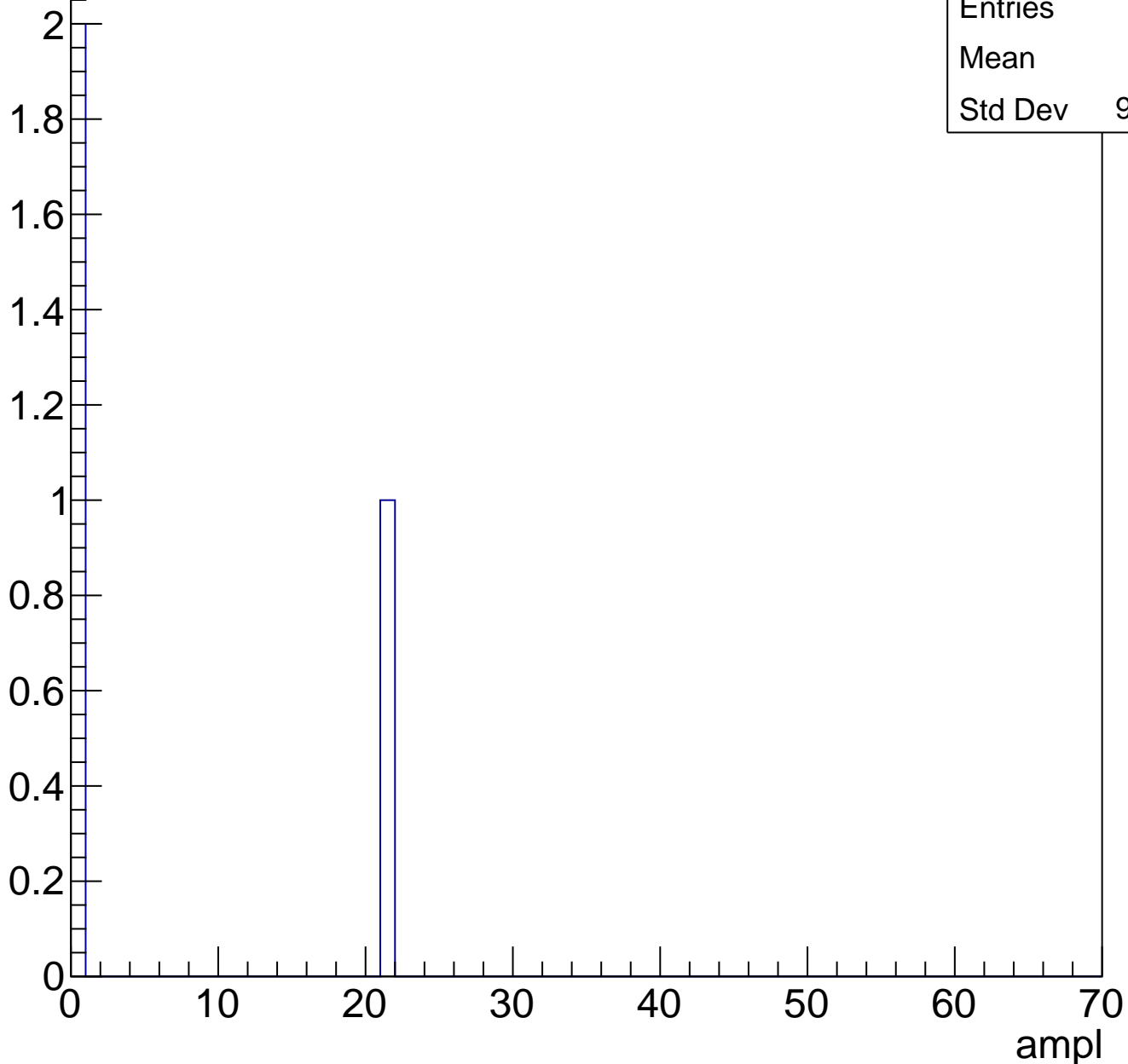




# B1L002S, U8-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L002S, U8-ch29, adc0

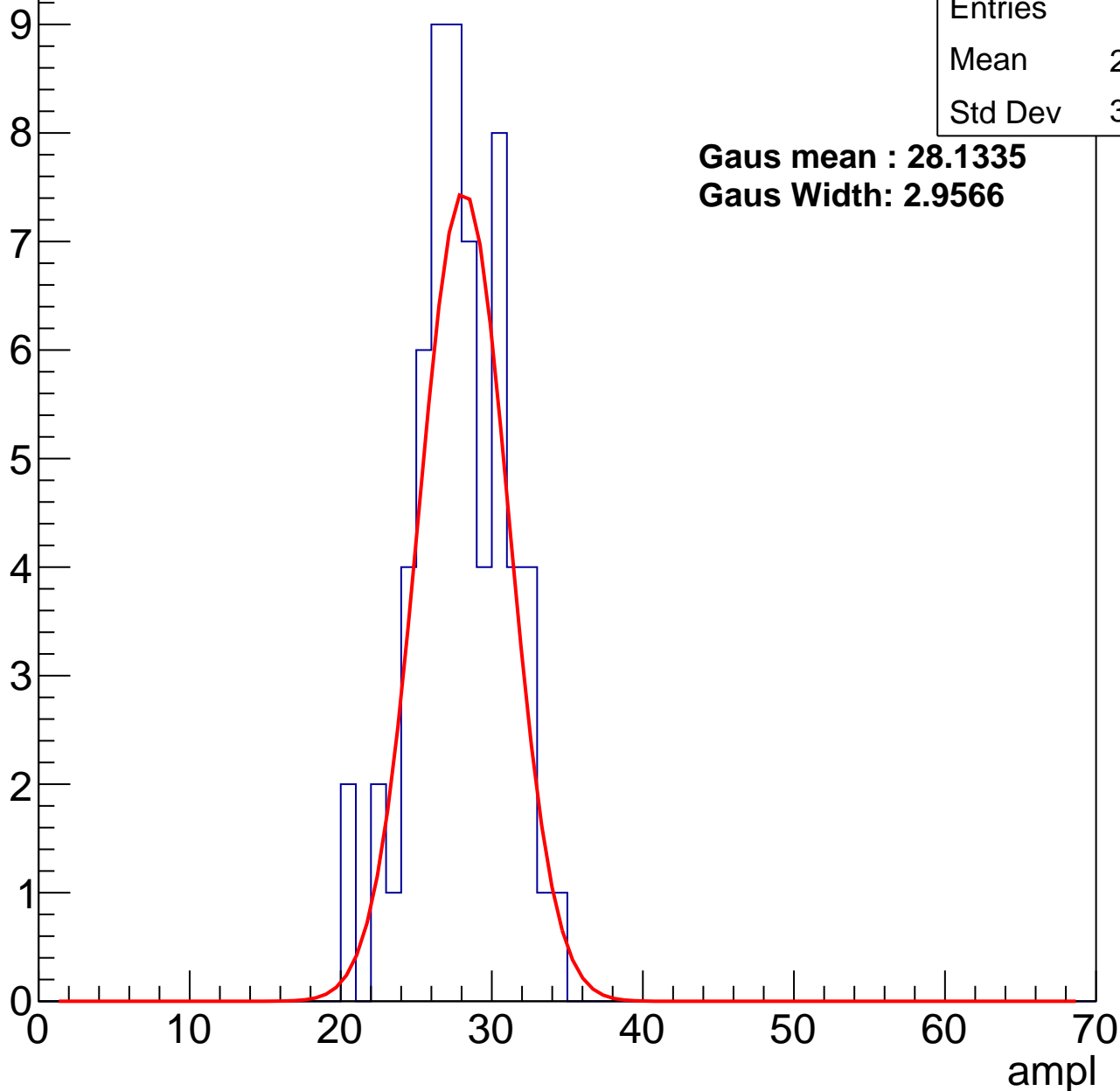
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.44
Std Dev	3.019

**Gaus mean : 28.1335**

**Gaus Width: 2.9566**



# B1L002S, U8-ch29, adc1

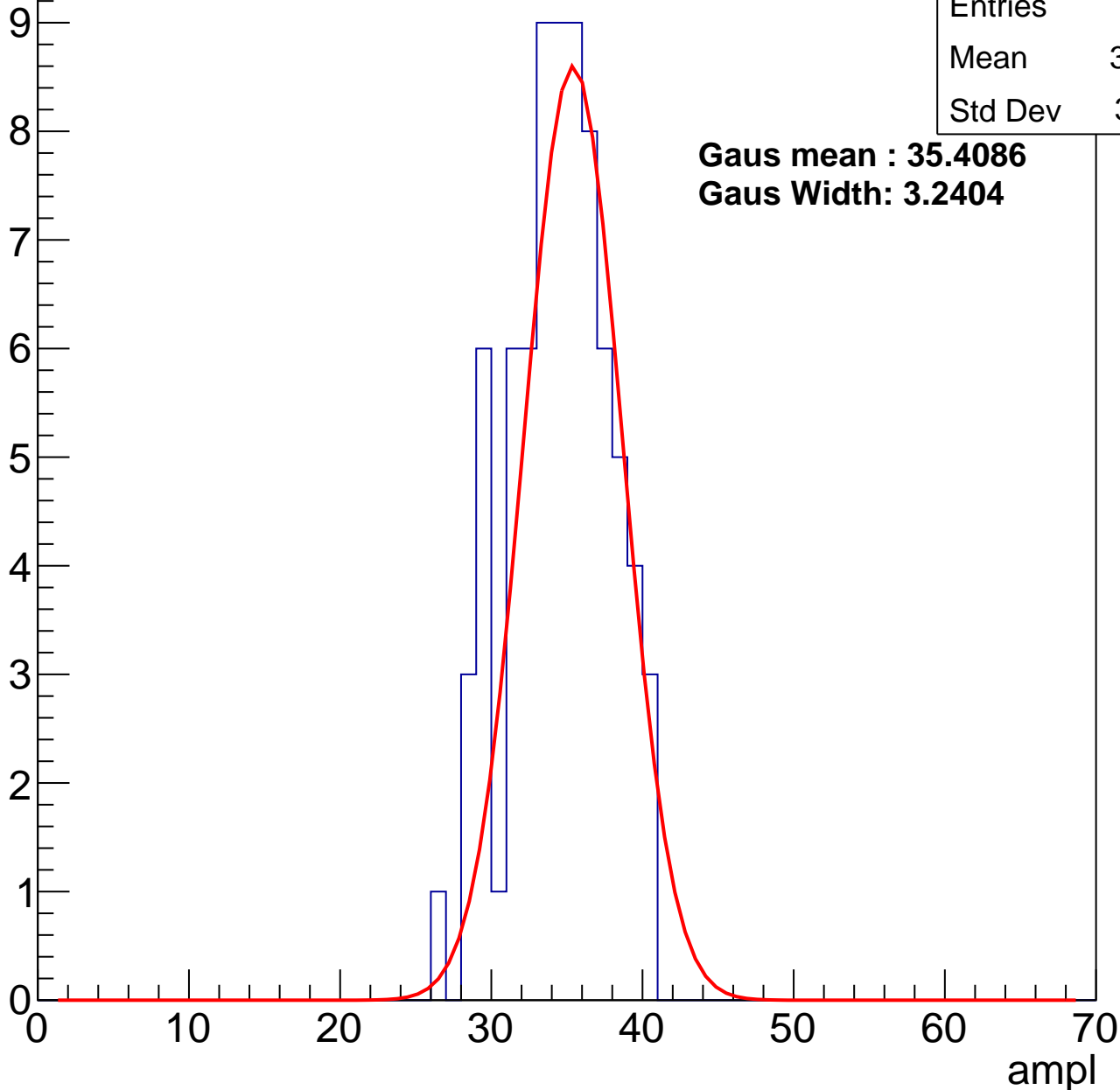
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	34.03
Std Dev	3.261

**Gaus mean : 35.4086**

**Gaus Width: 3.2404**



# B1L002S, U8-ch29, adc2

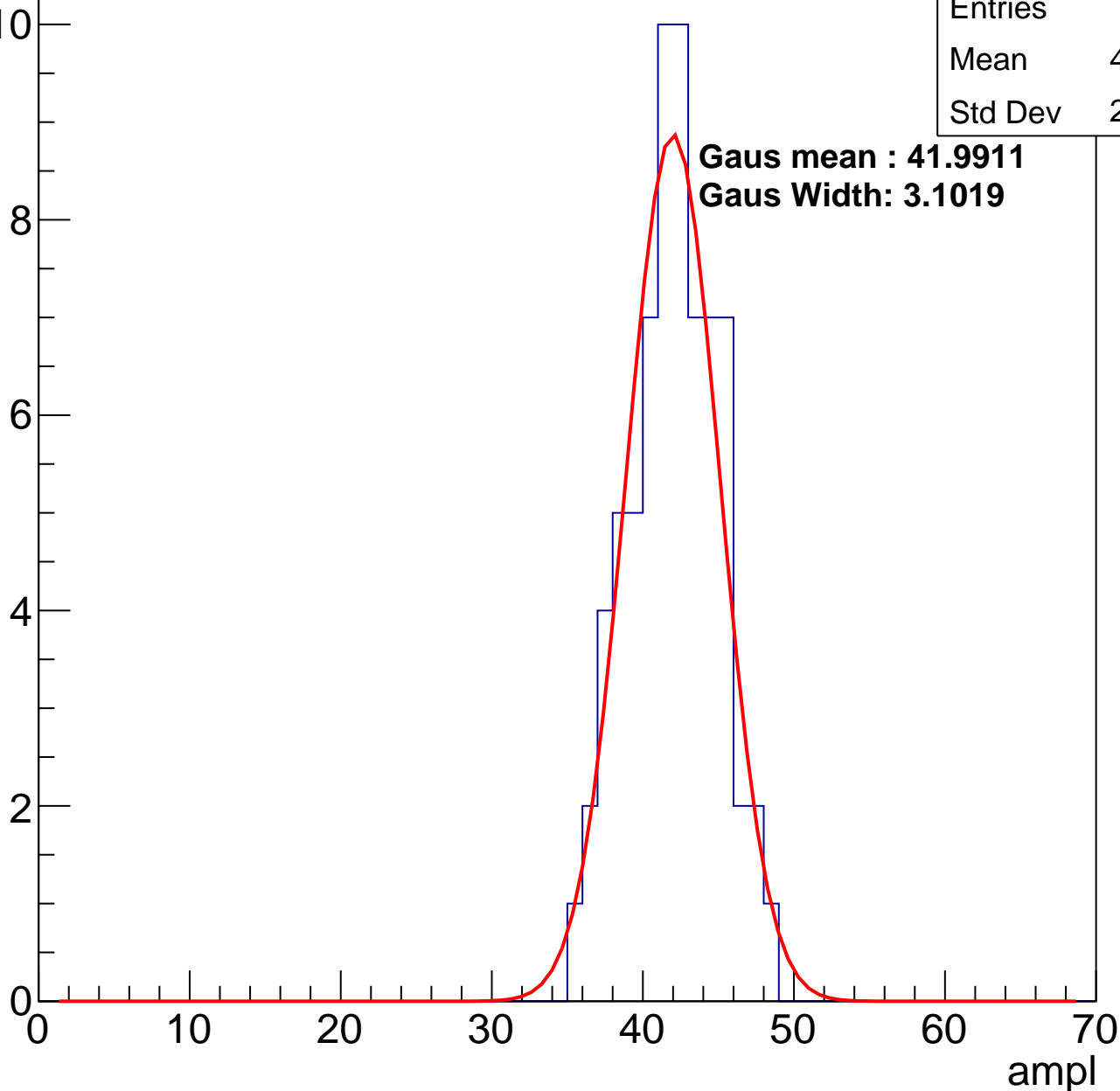
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	41.54
Std Dev	2.892

**Gaus mean : 41.9911**

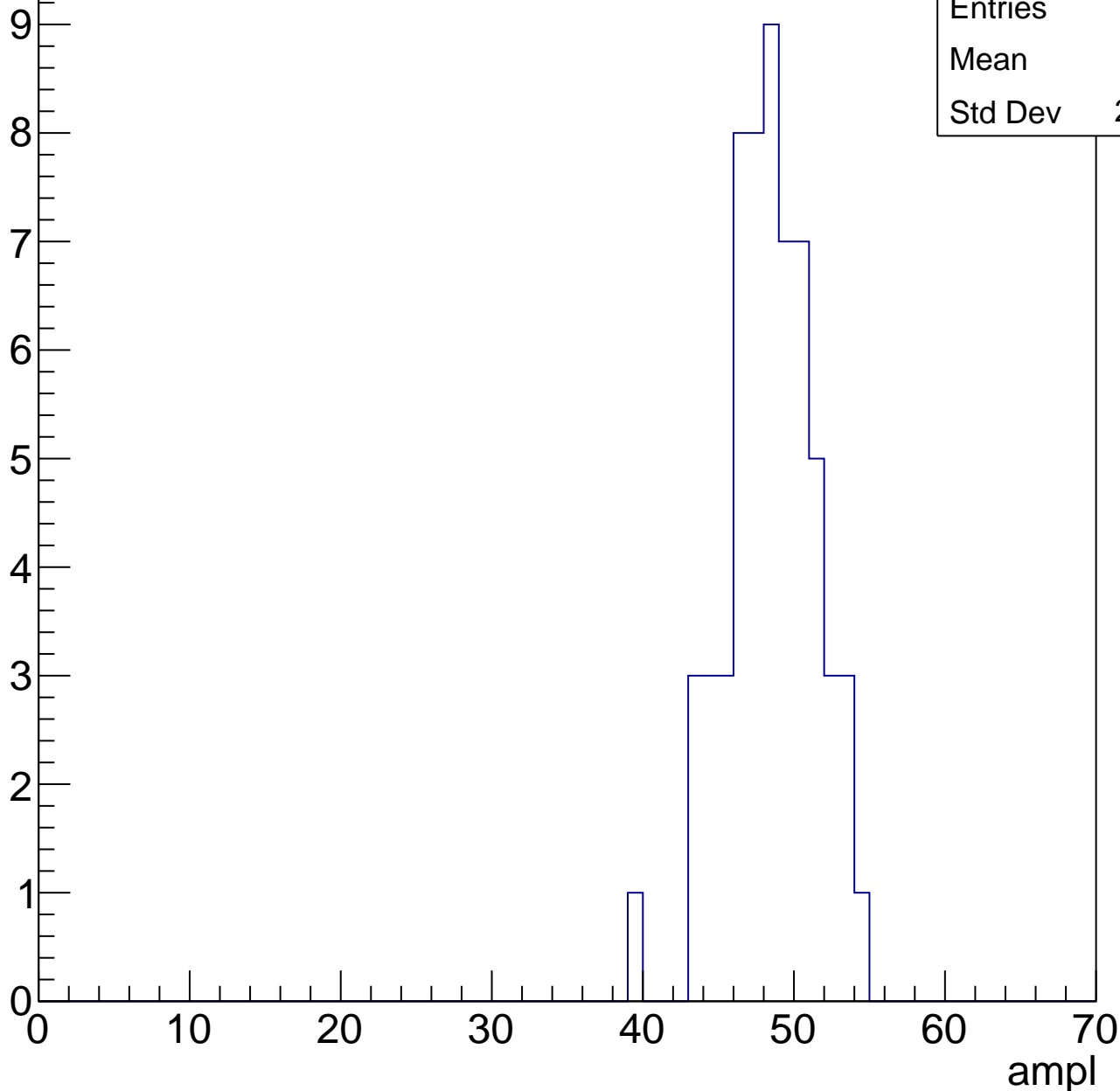
**Gaus Width: 3.1019**



# B1L002S, U8-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



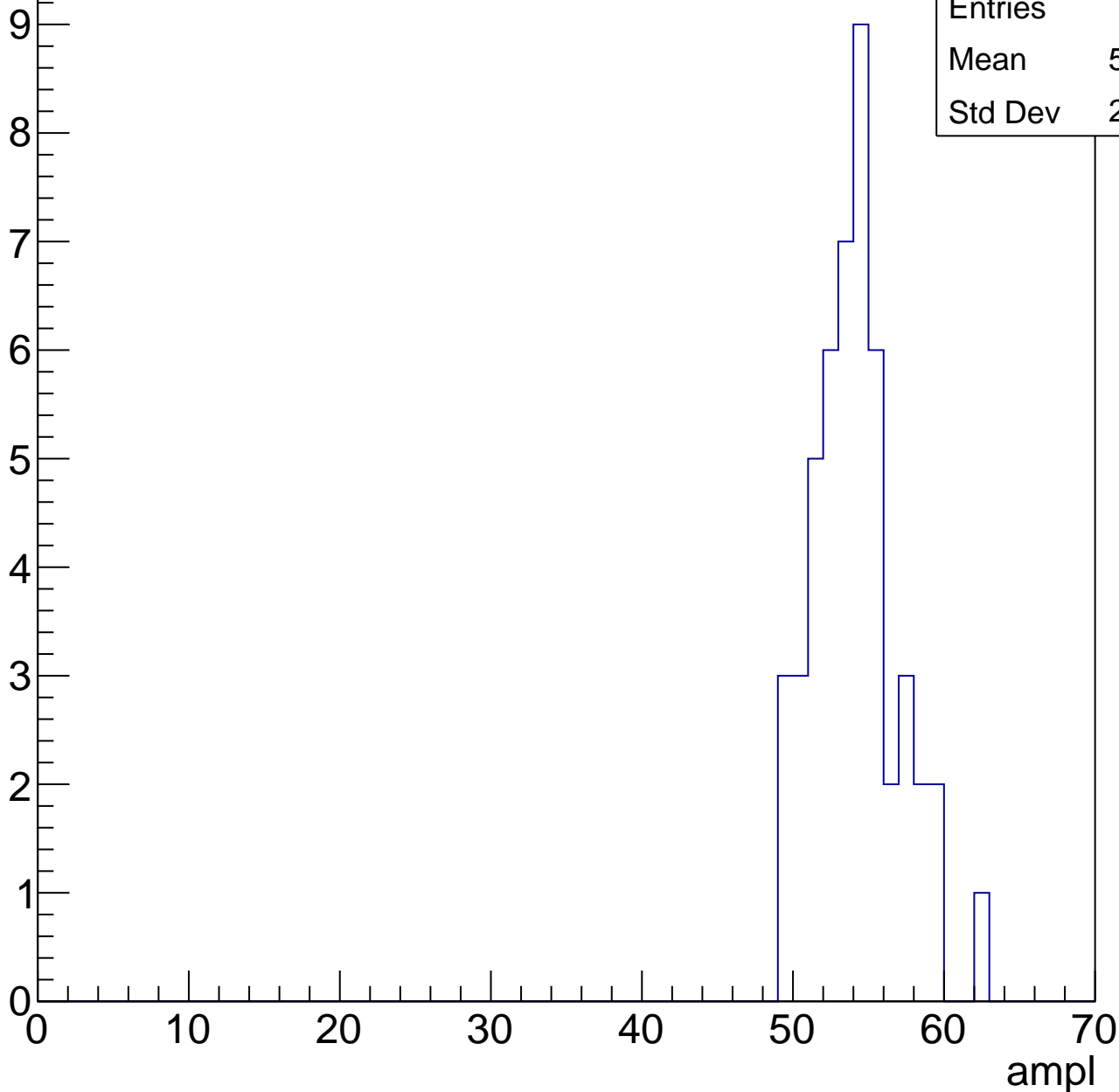
Entries	61
Mean	48
Std Dev	2.891

# B1L002S, U8-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	53.67
Std Dev	2.788

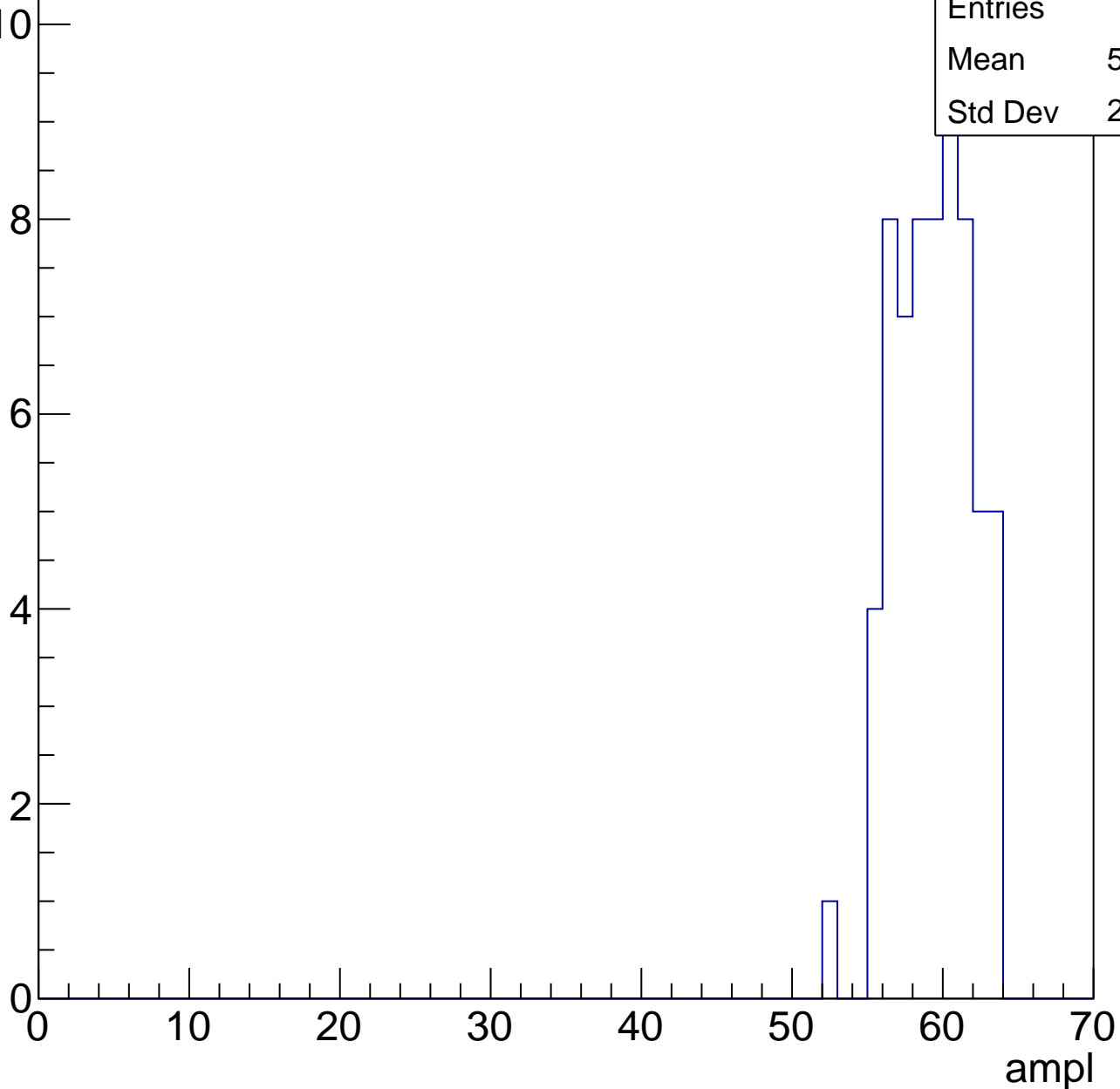


# B1L002S, U8-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	58.88
Std Dev	2.459

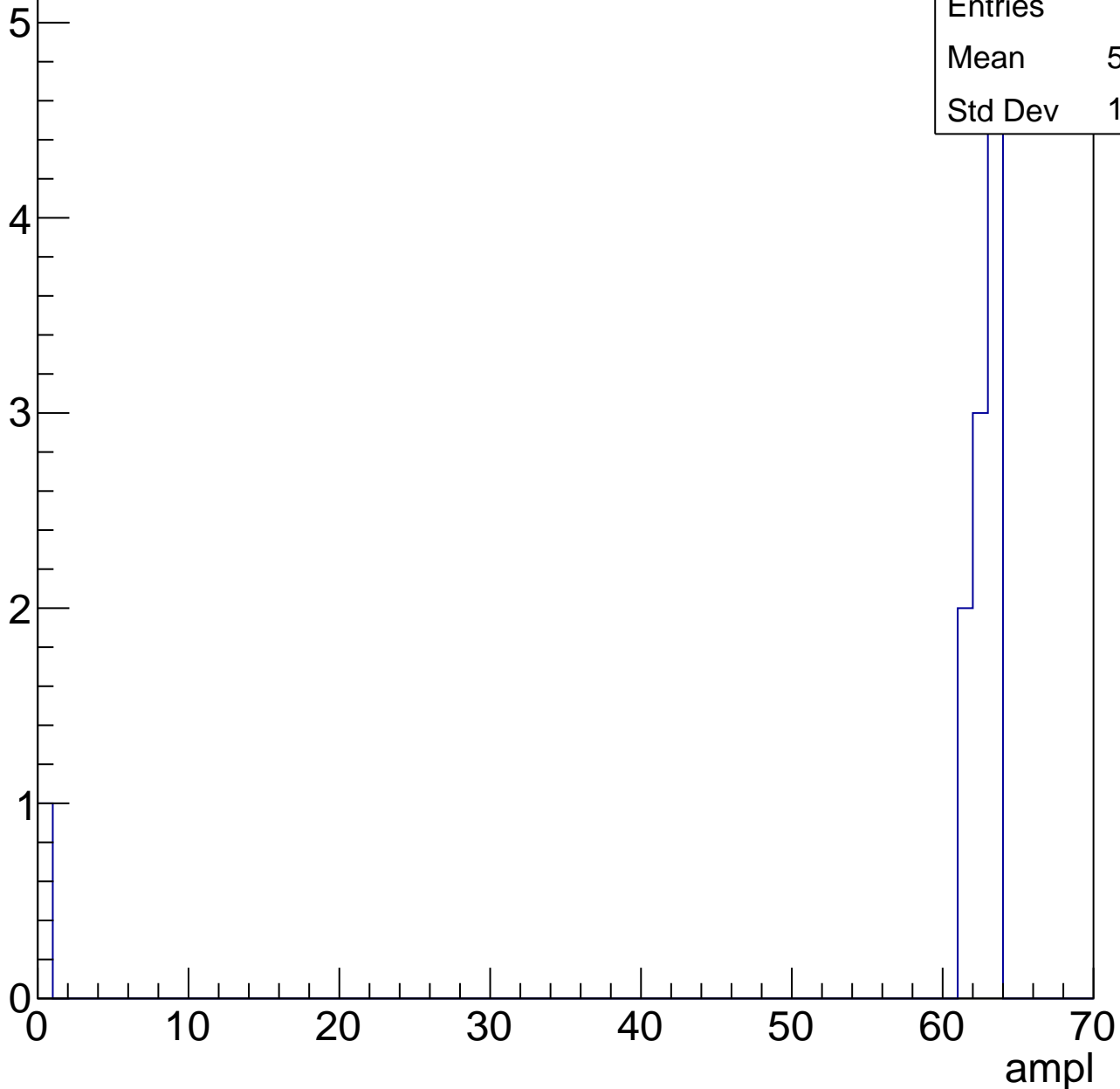


# B1L002S, U8-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	56.64
Std Dev	17.93





# B1L002S, U8-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch30, adc0

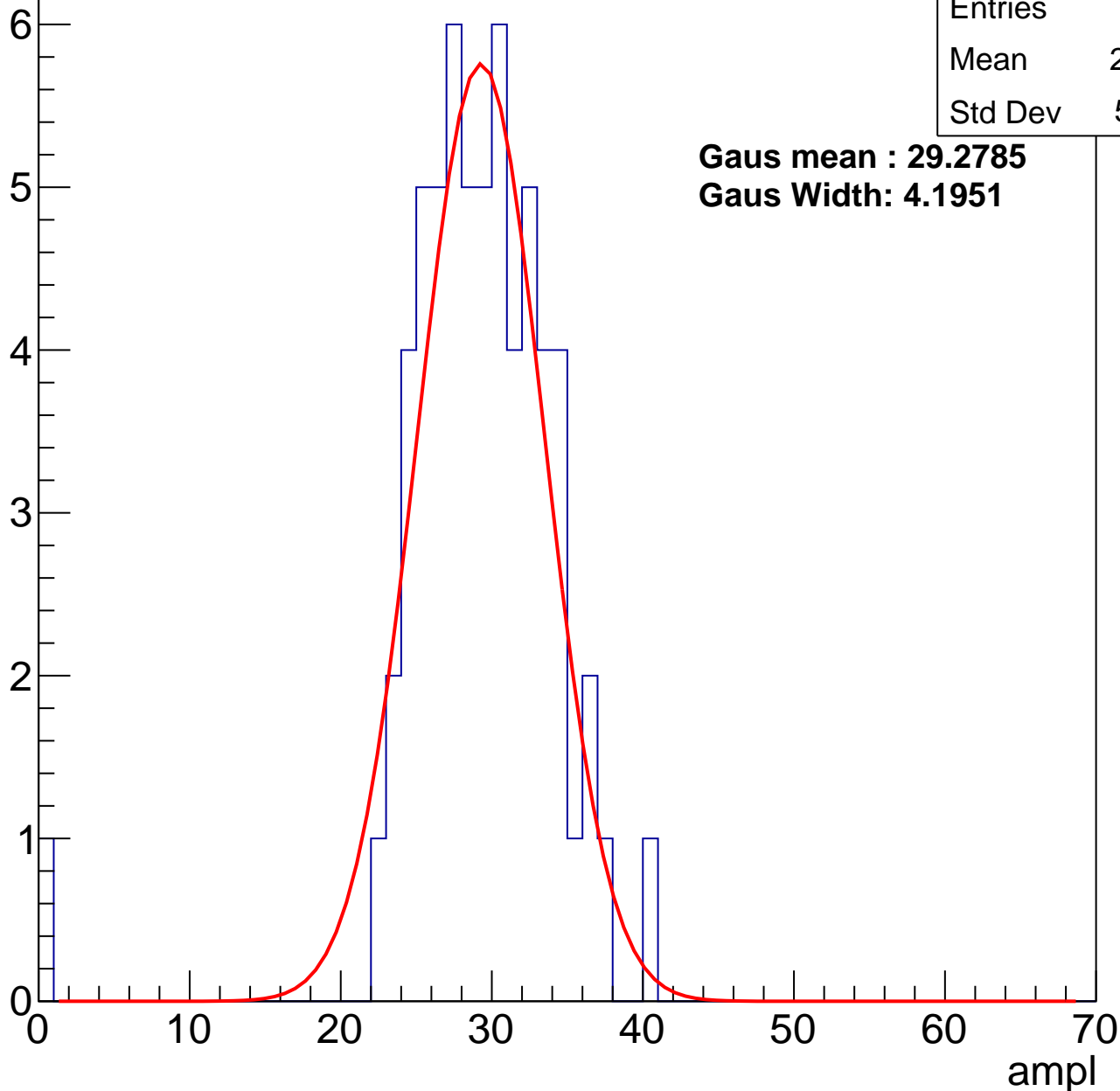
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.74
Std Dev	5.331

**Gaus mean : 29.2785**

**Gaus Width: 4.1951**



# B1L002S, U8-ch30, adc1

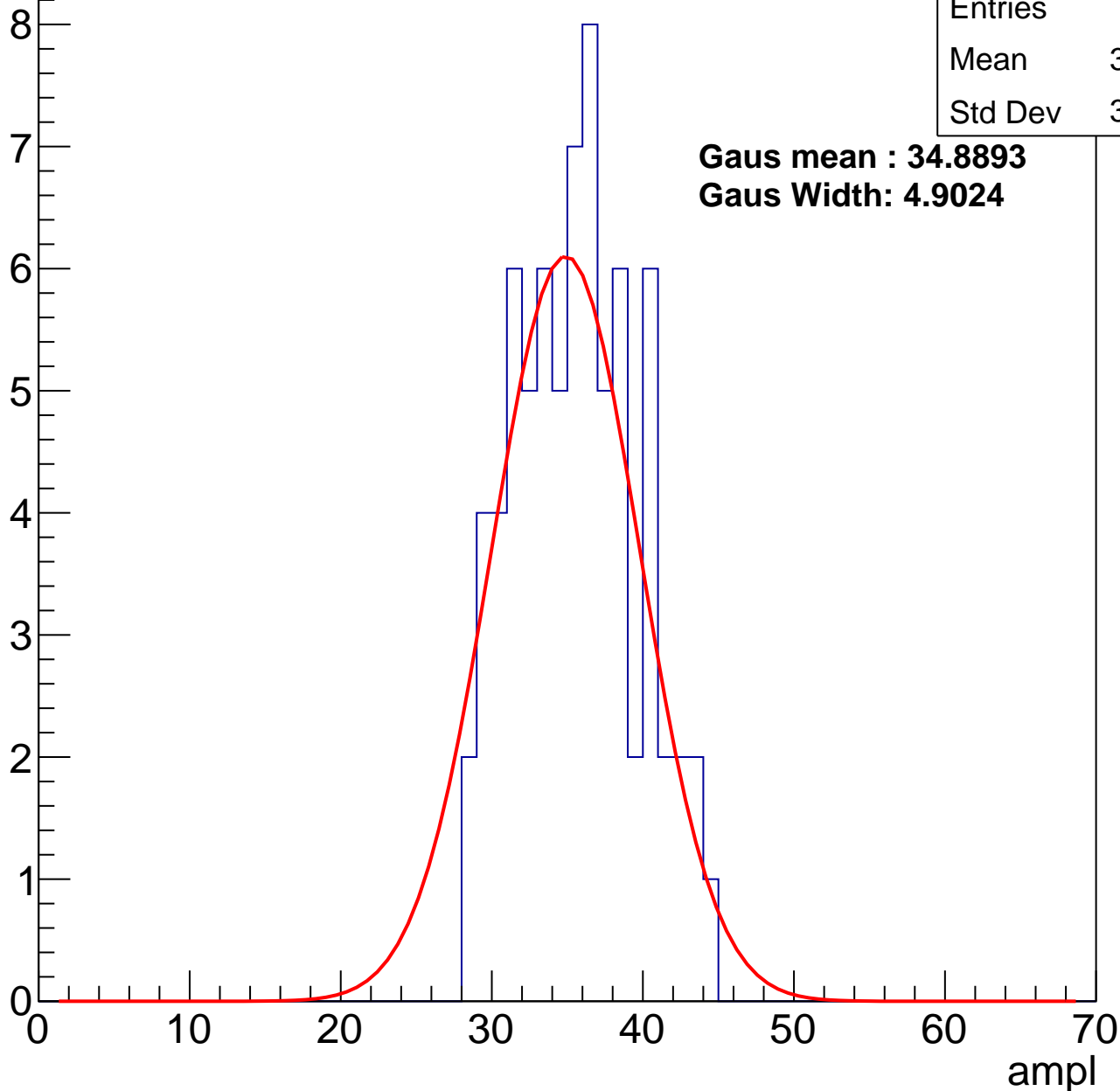
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	35.15
Std Dev	3.975

**Gaus mean : 34.8893**

**Gaus Width: 4.9024**



# B1L002S, U8-ch30, adc2

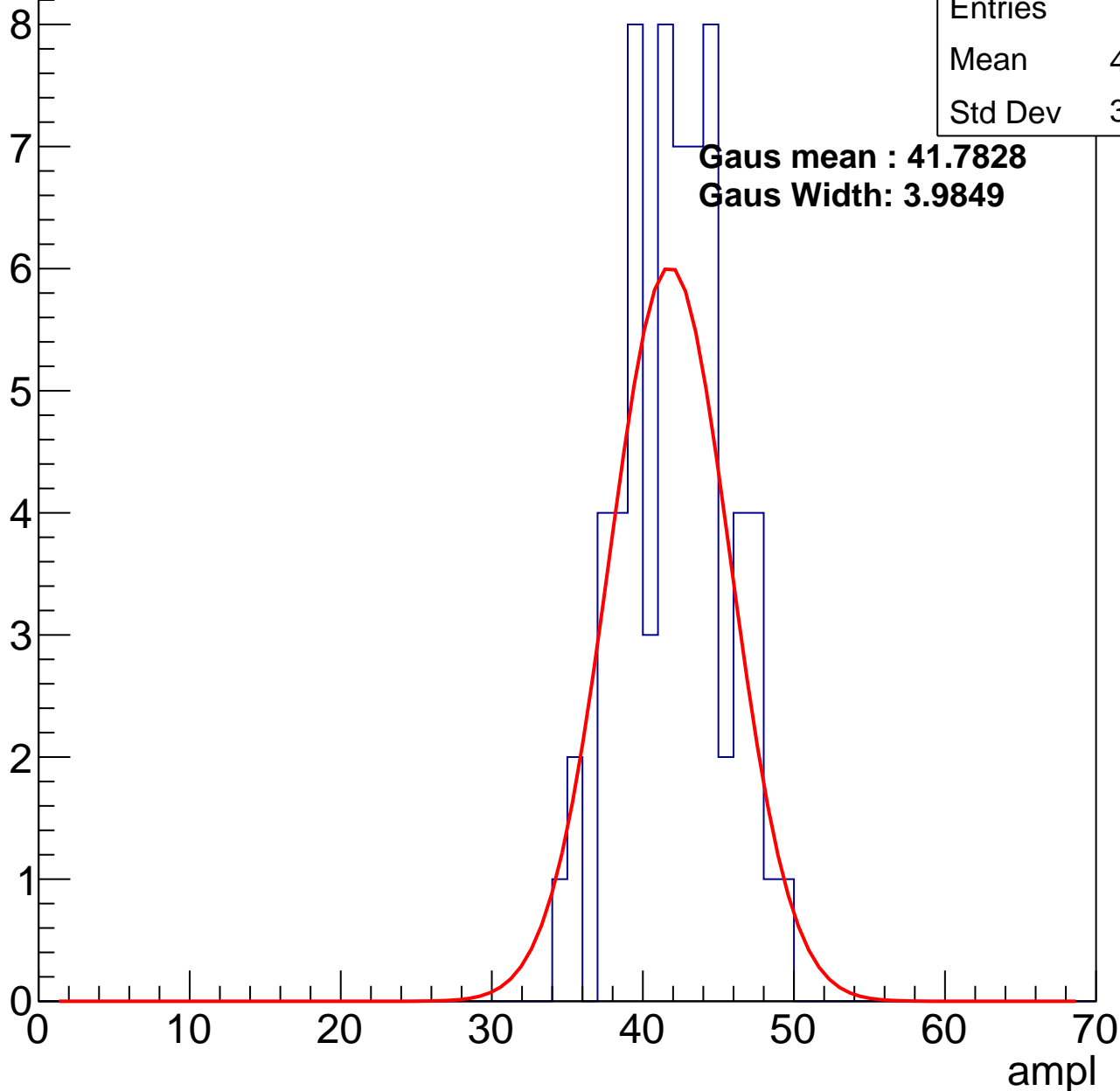
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.72
Std Dev	3.352

**Gaus mean : 41.7828**

**Gaus Width: 3.9849**

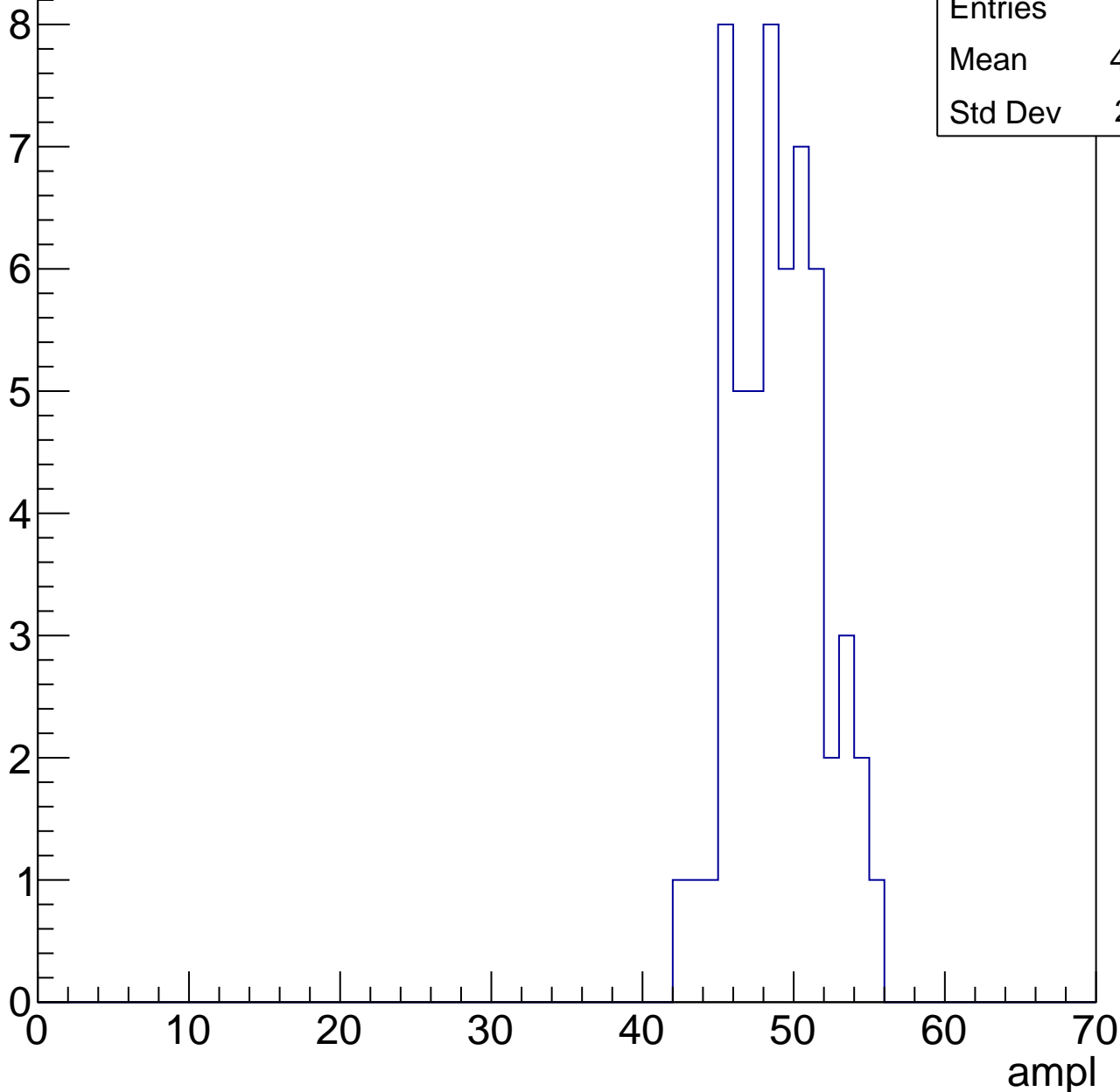


# B1L002S, U8-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

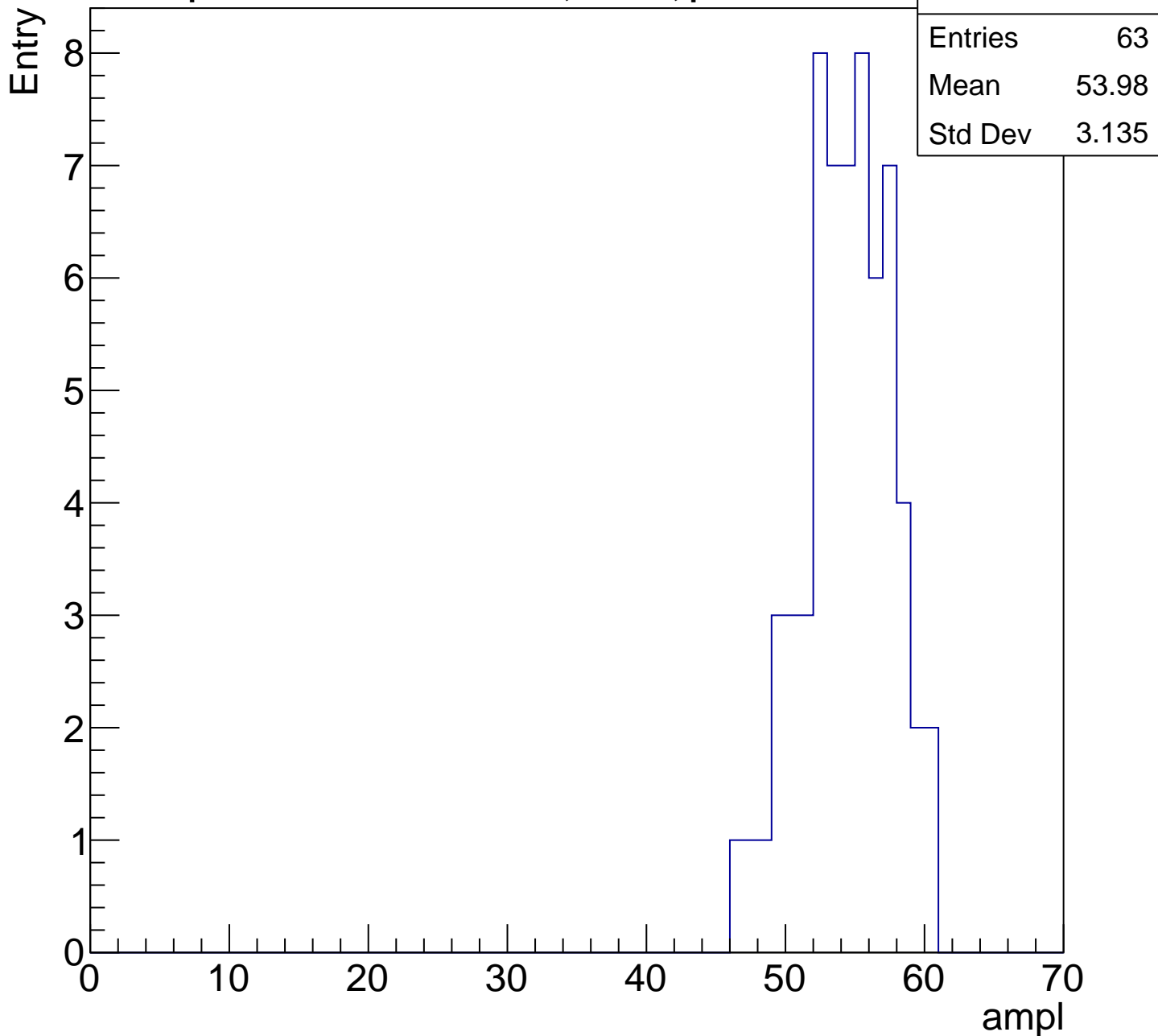
Entry

Entries	56
Mean	48.46
Std Dev	2.921



# B1L002S, U8-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

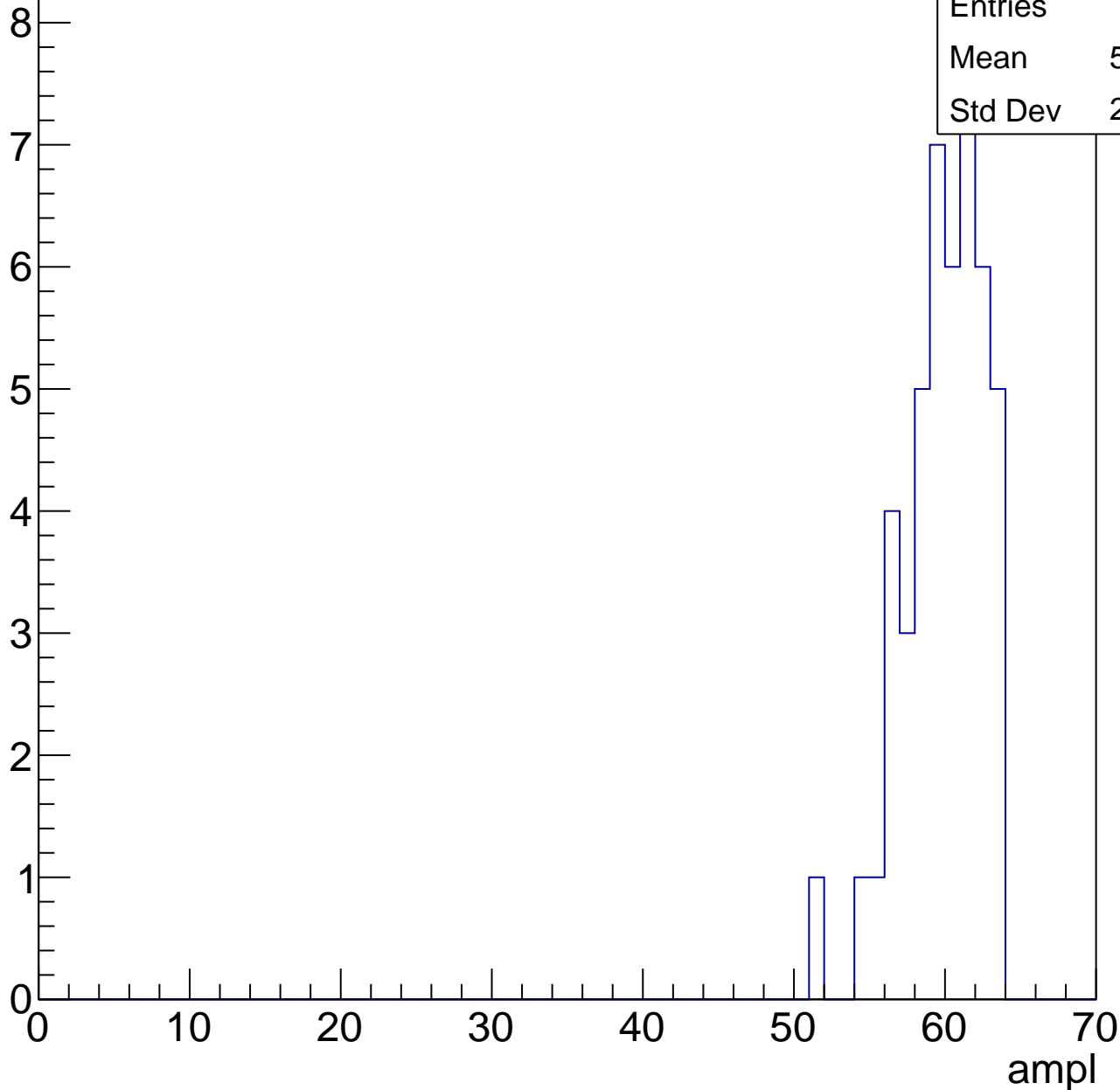


# B1L002S, U8-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	59.43
Std Dev	2.615

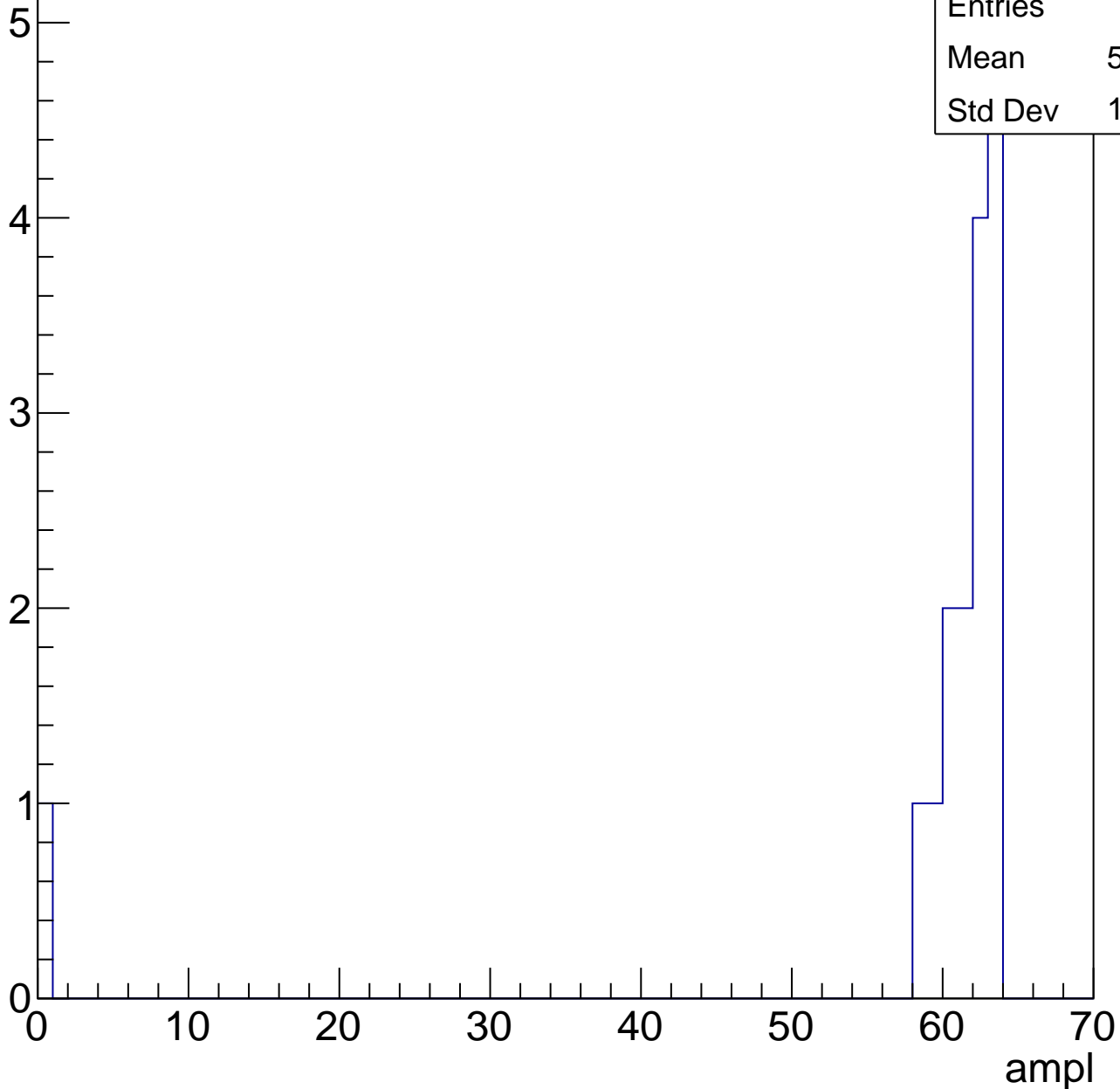


# B1L002S, U8-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.62
Std Dev	14.95





# B1L002S, U8-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch31, adc0

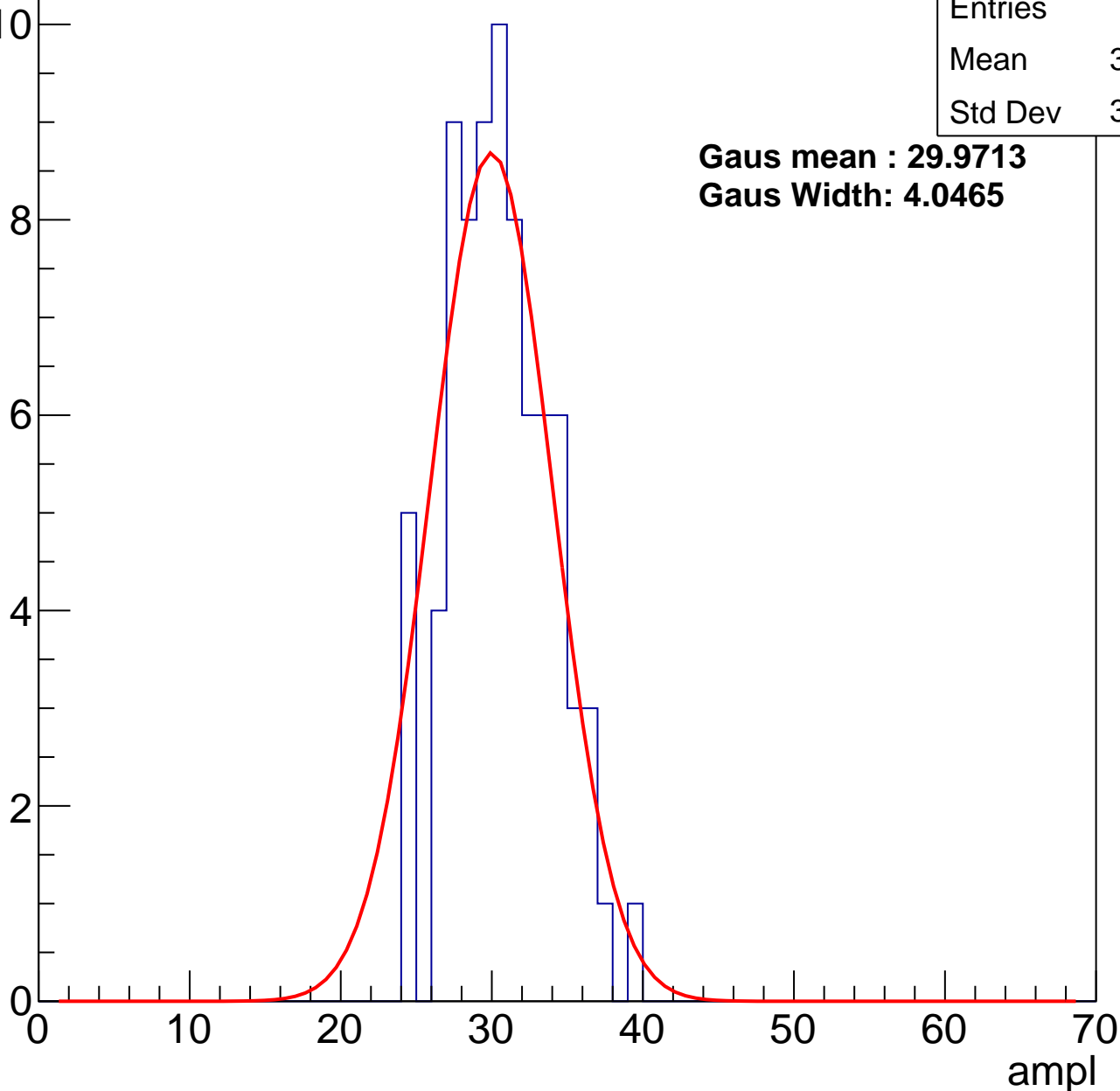
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	30.16
Std Dev	3.297

**Gaus mean : 29.9713**

**Gaus Width: 4.0465**



# B1L002S, U8-ch31, adc1

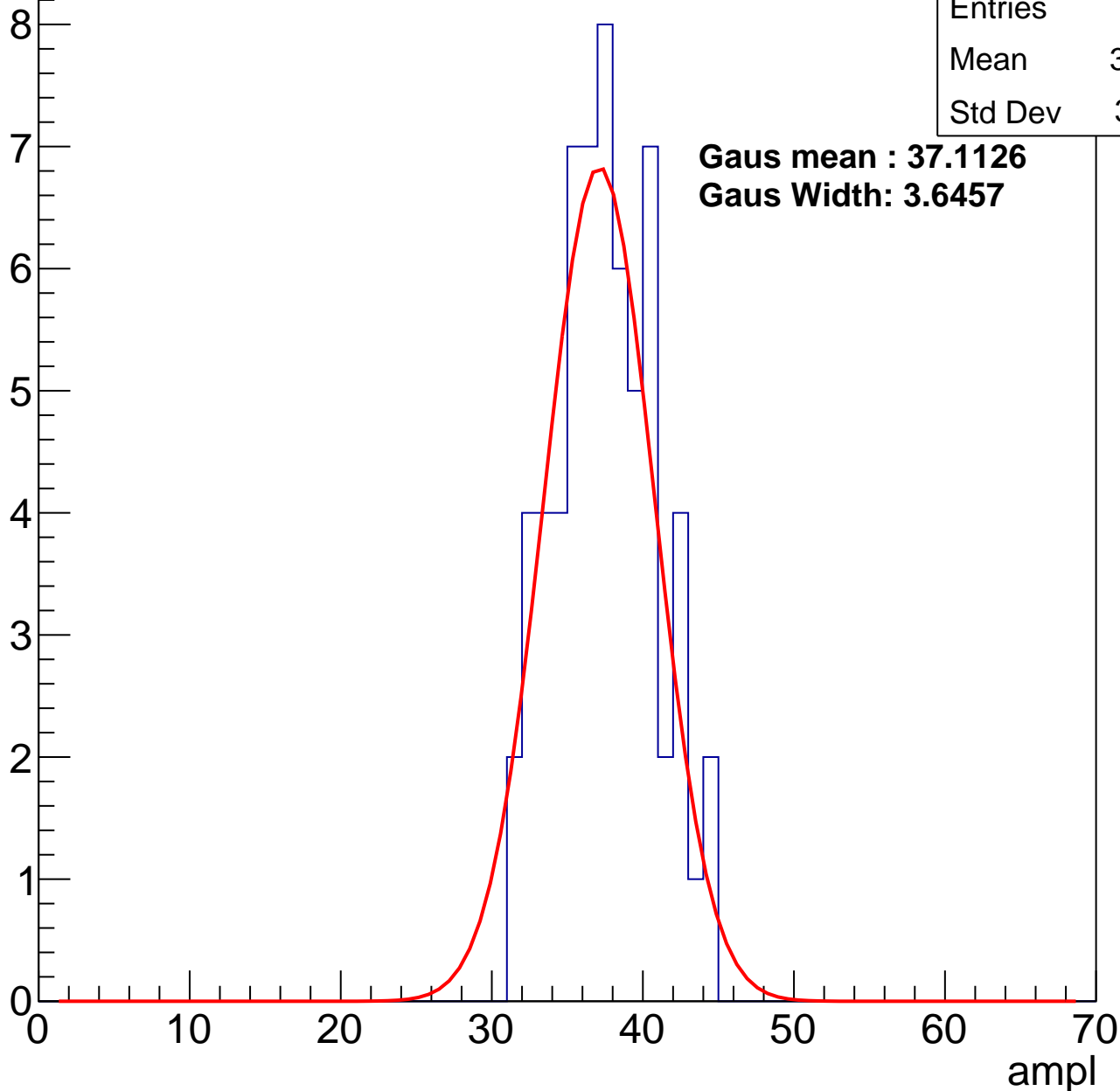
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	37.06
Std Dev	3.241

**Gaus mean : 37.1126**

**Gaus Width: 3.6457**



# B1L002S, U8-ch31, adc2

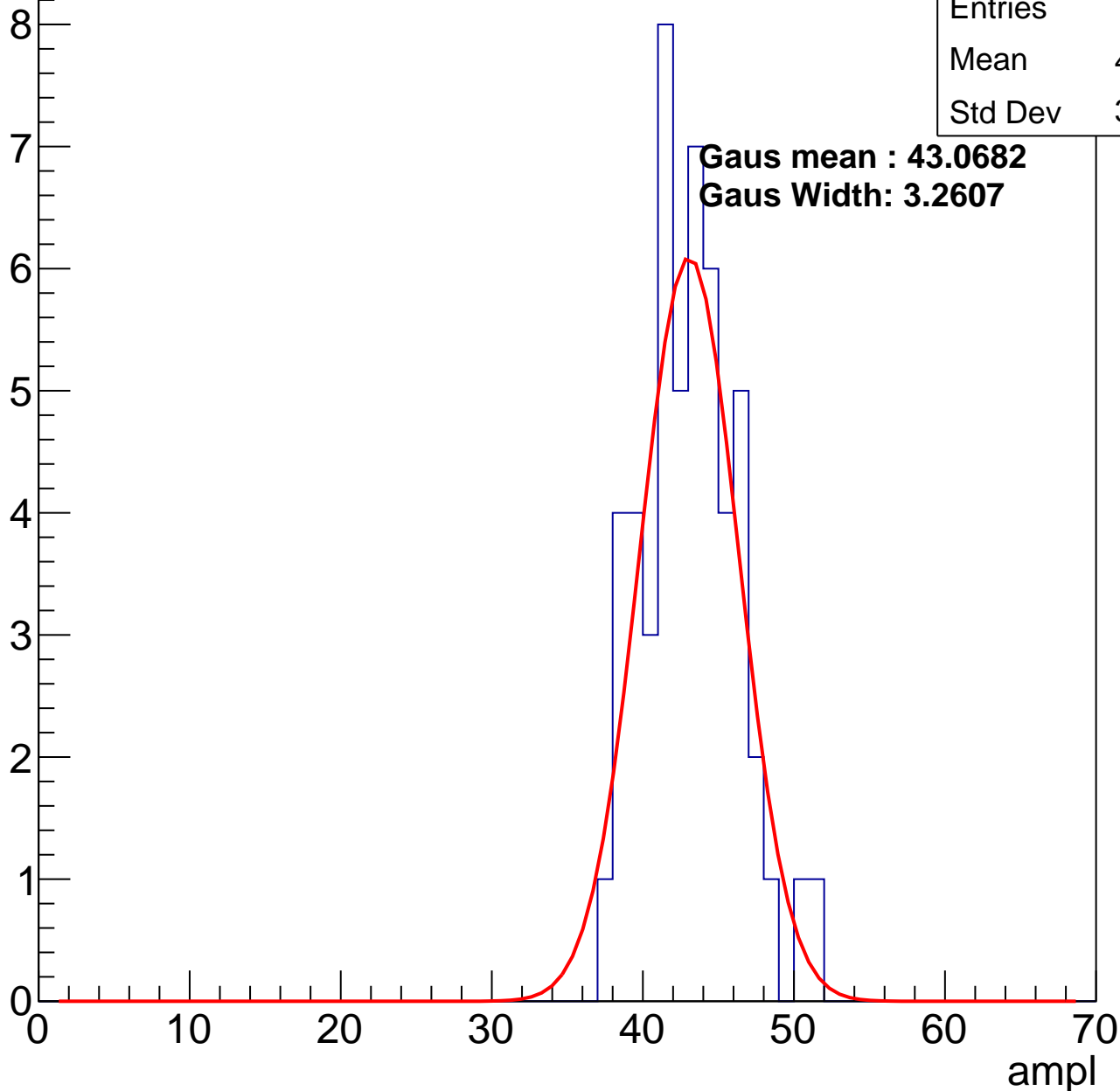
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	42.71
Std Dev	3.091

**Gaus mean : 43.0682**

**Gaus Width: 3.2607**

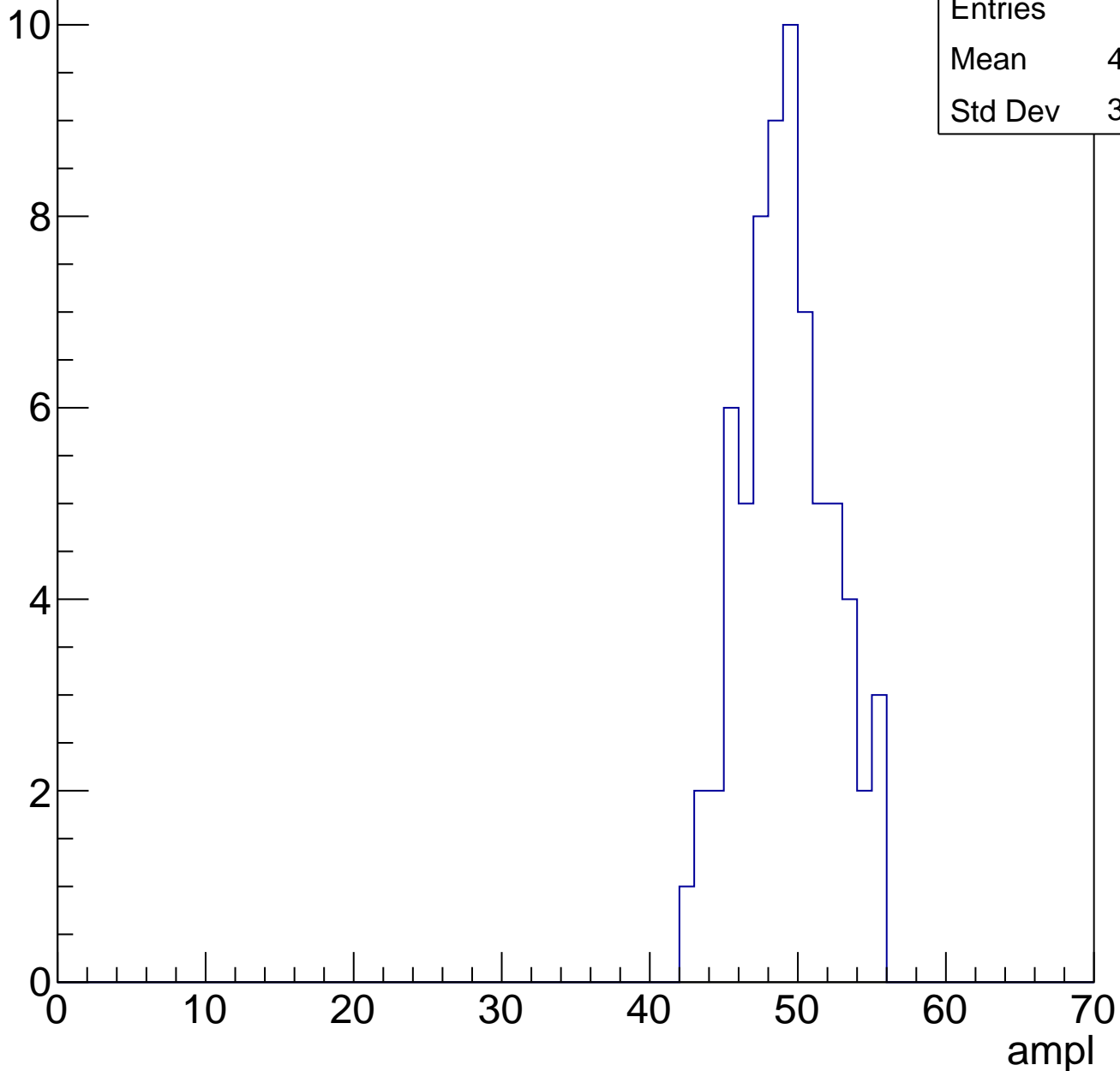


# B1L002S, U8-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

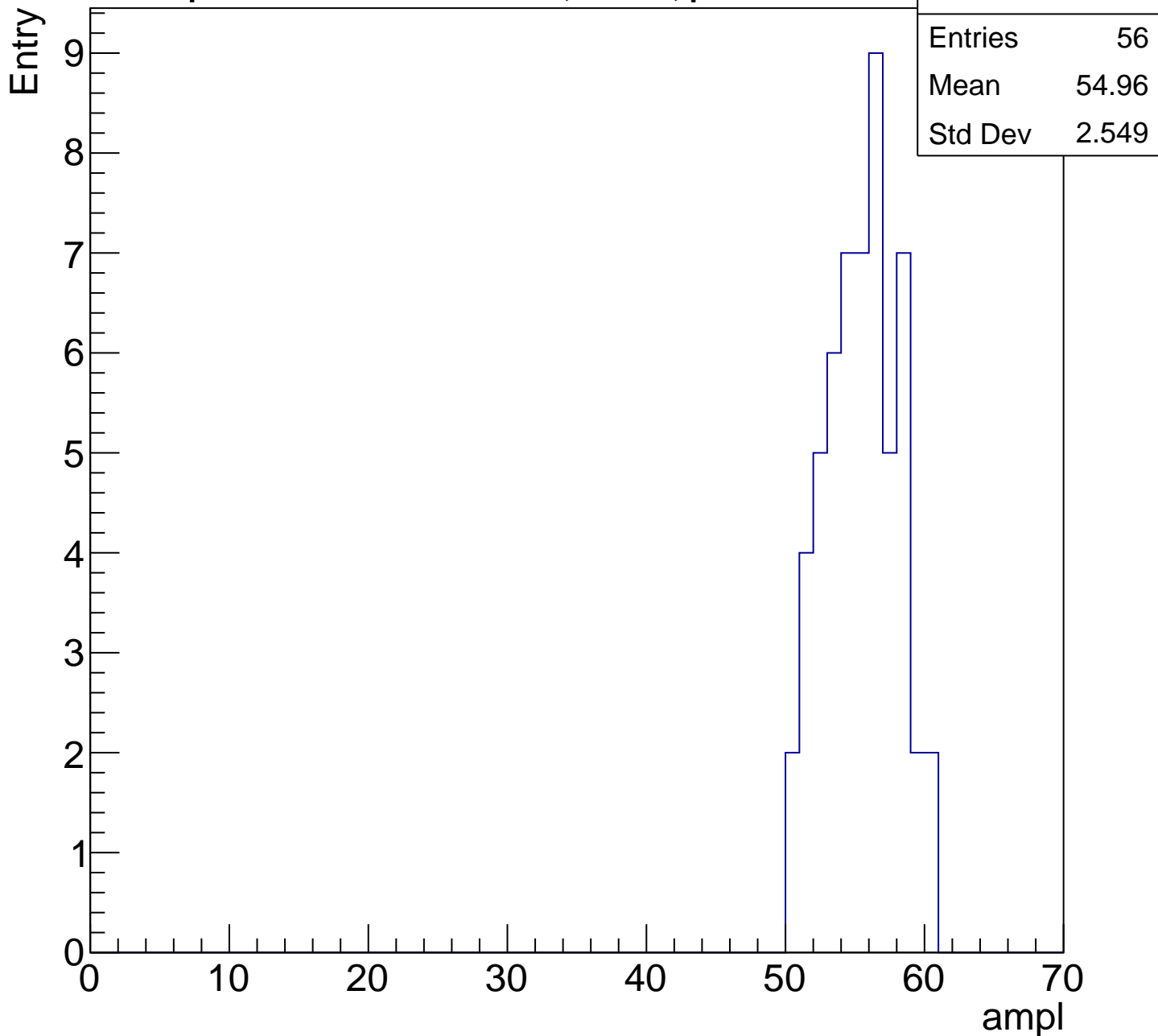
Entries	69
Mean	48.75
Std Dev	3.052

Entry



# B1L002S, U8-ch31, adc4

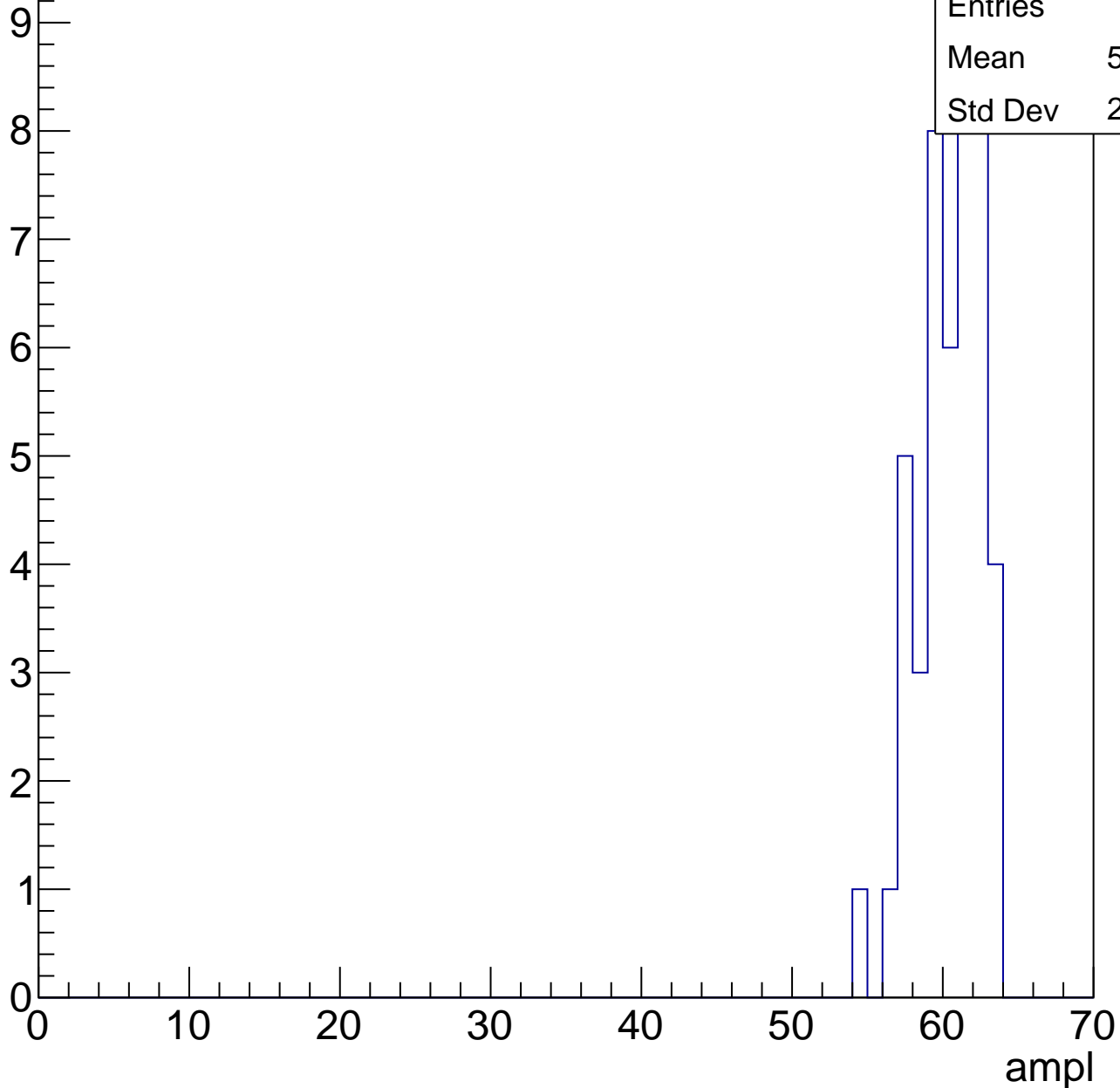
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

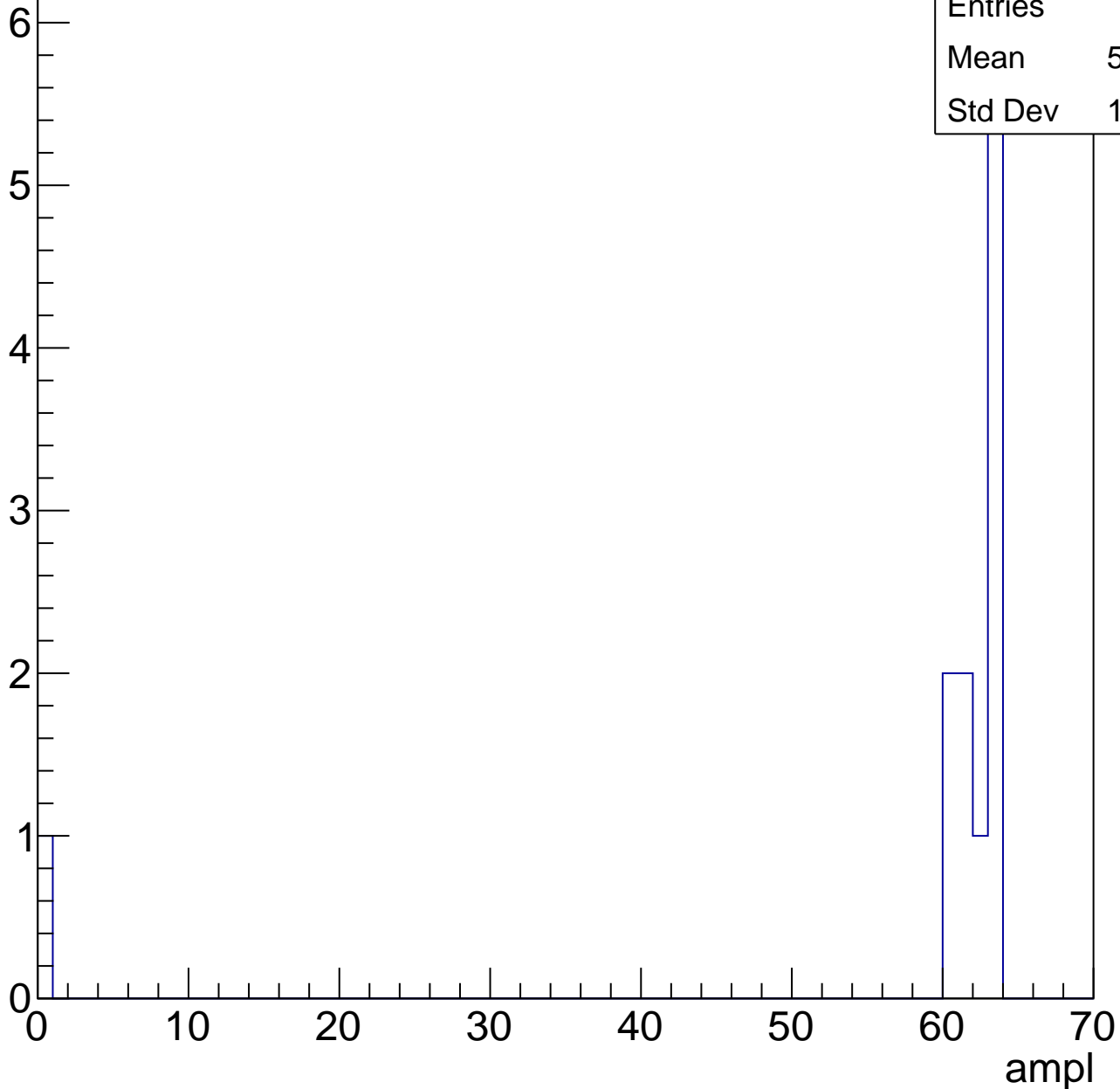


# B1L002S, U8-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	12
Mean	56.83
Std Dev	17.17





# B1L002S, U8-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch32, adc0

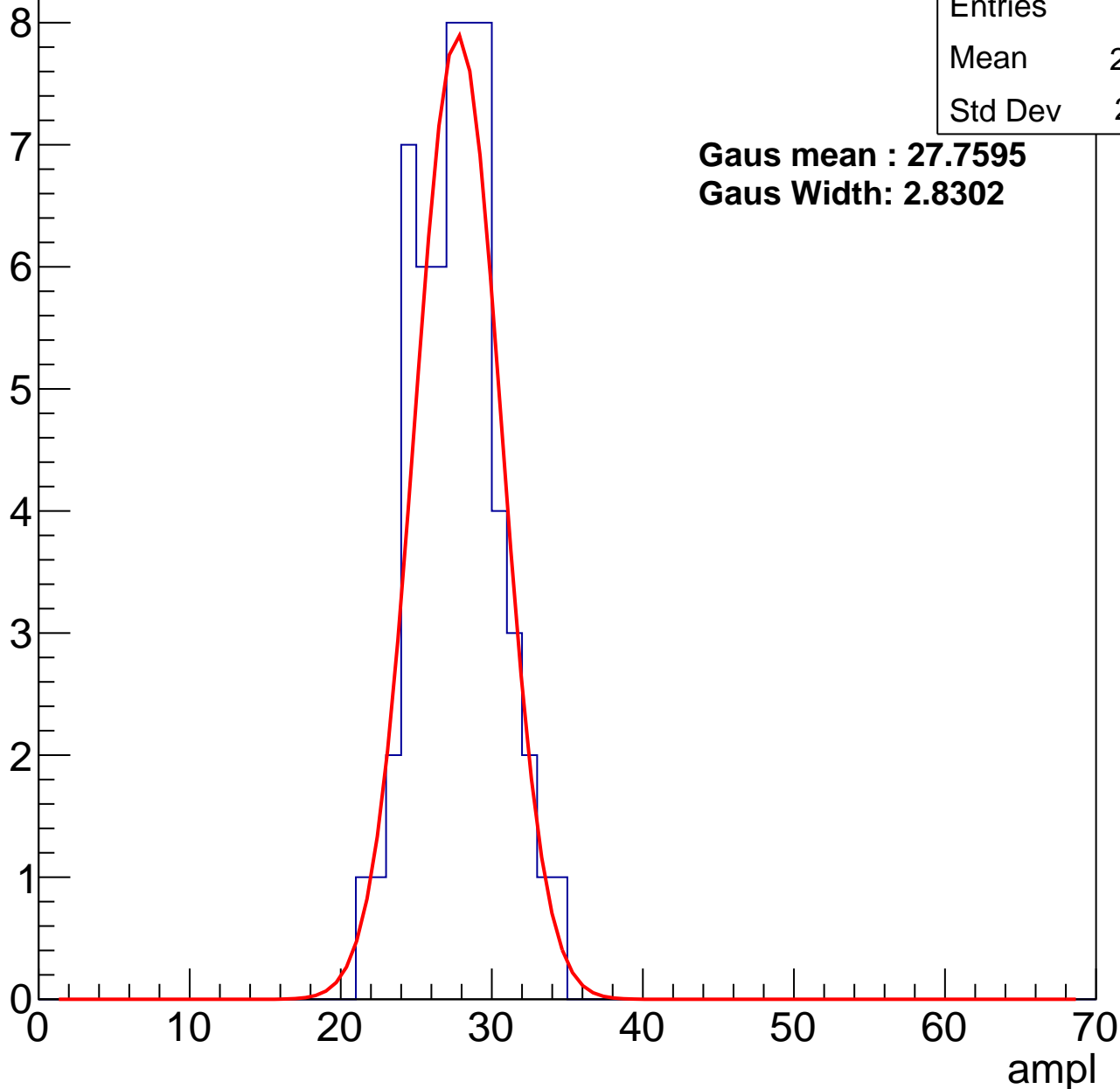
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.22
Std Dev	2.761

**Gaus mean : 27.7595**

**Gaus Width: 2.8302**



# B1L002S, U8-ch32, adc1

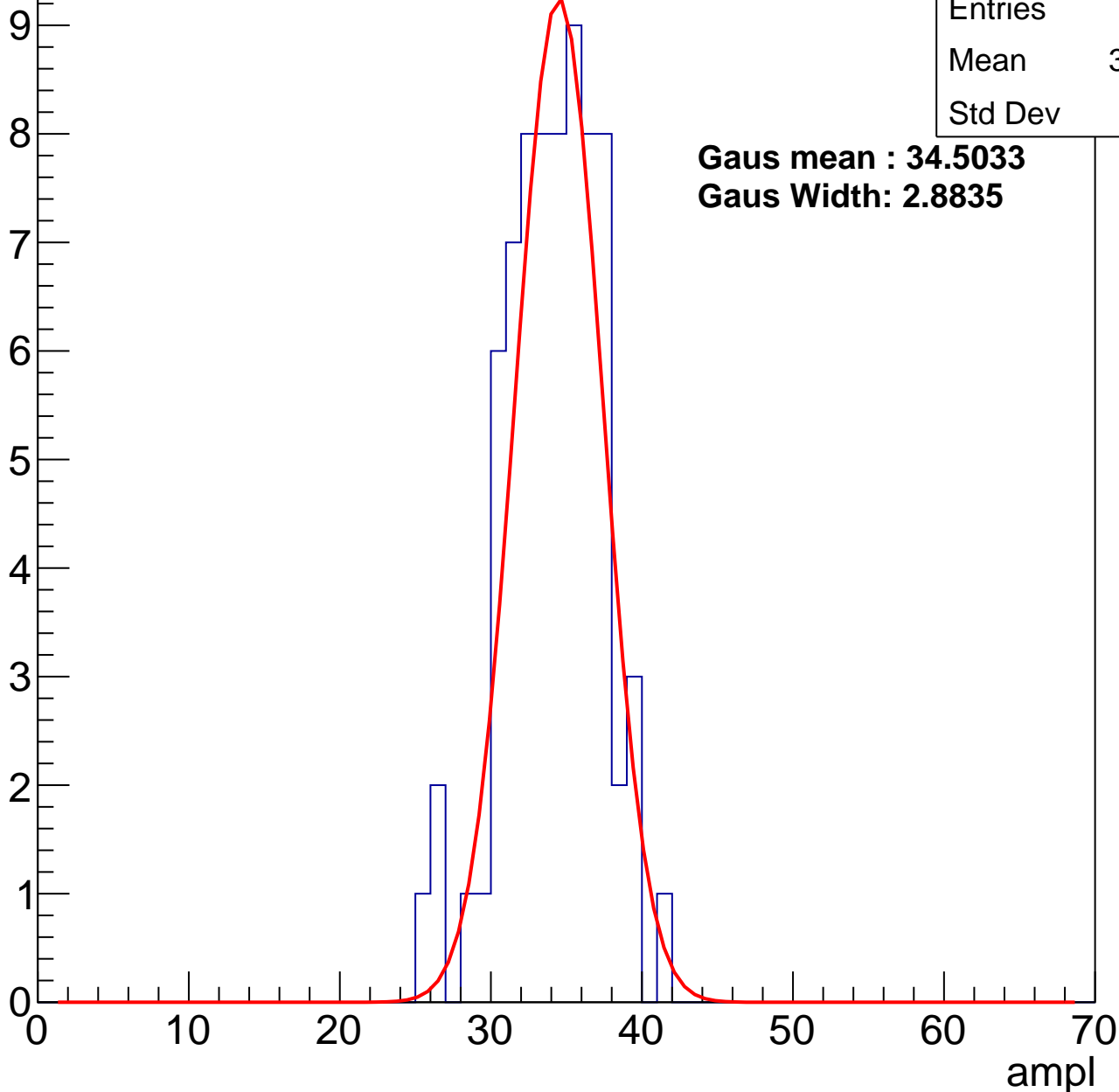
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	33.64
Std Dev	3.16

**Gaus mean : 34.5033**

**Gaus Width: 2.8835**



# B1L002S, U8-ch32, adc2

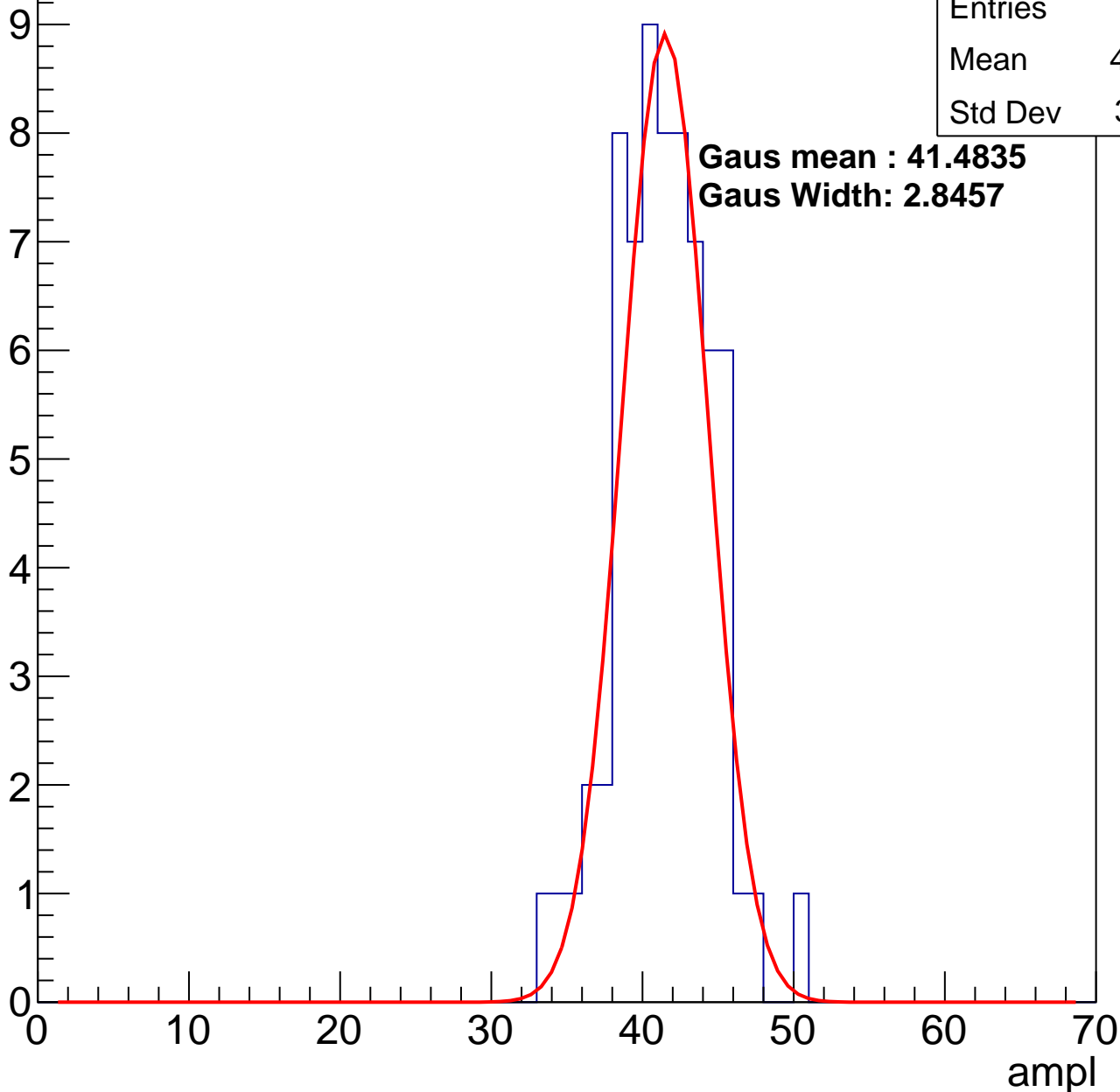
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.97
Std Dev	3.111

**Gaus mean : 41.4835**

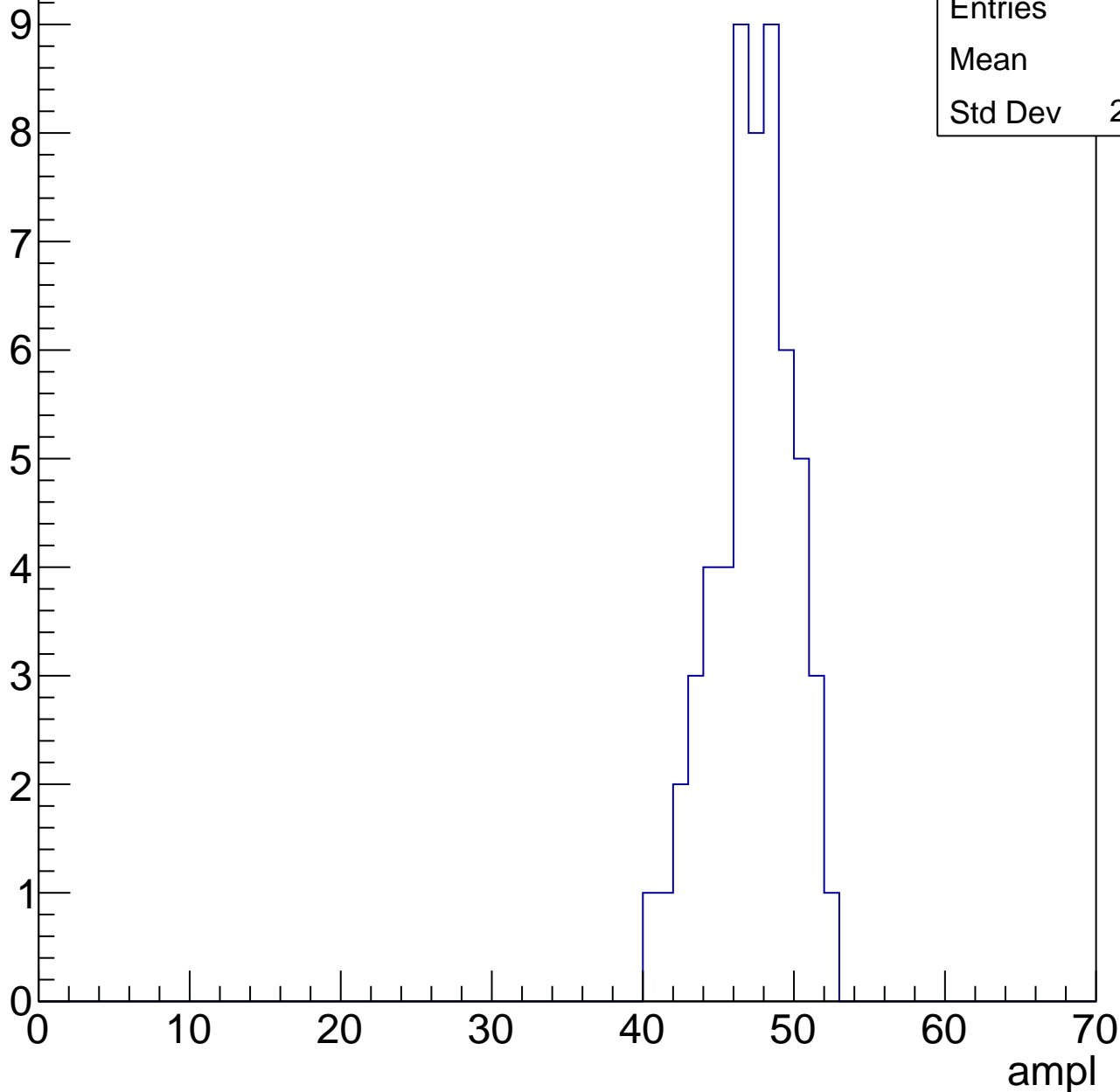
**Gaus Width: 2.8457**



# B1L002S, U8-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

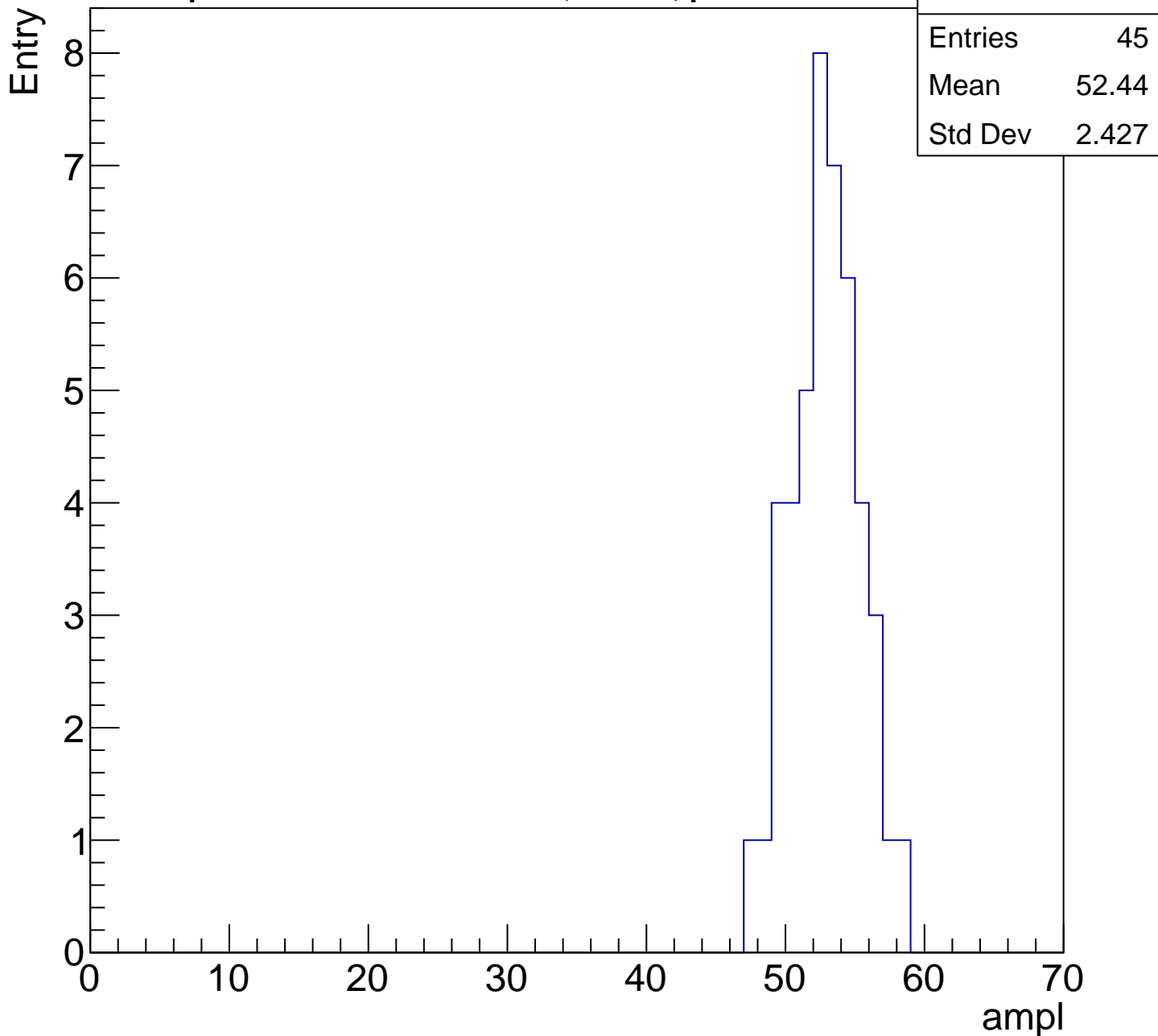
Entry



Entries	56
Mean	46.8
Std Dev	2.649

# B1L002S, U8-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch32, adc5

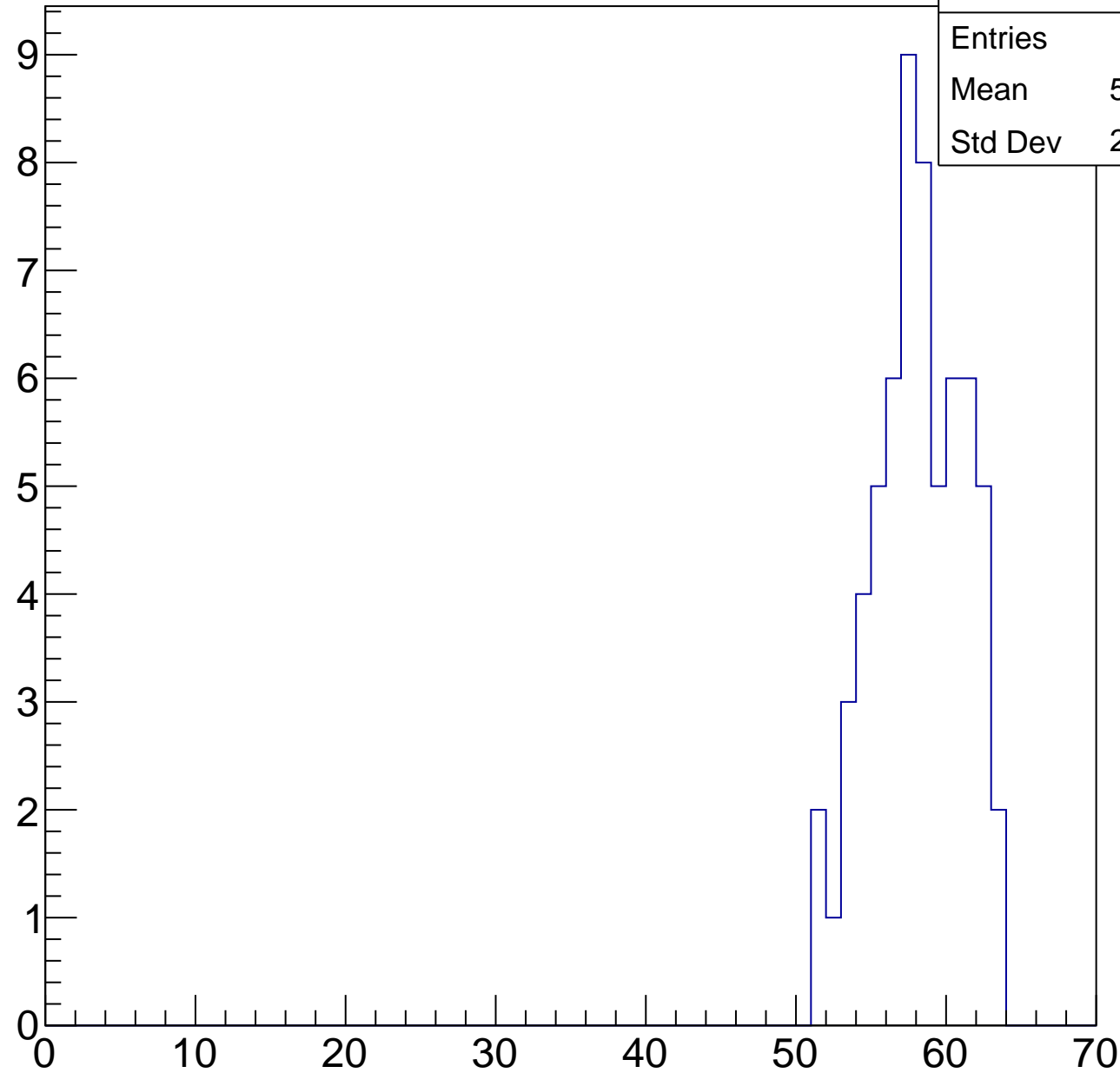
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	57.65
Std Dev	2.995

ampl

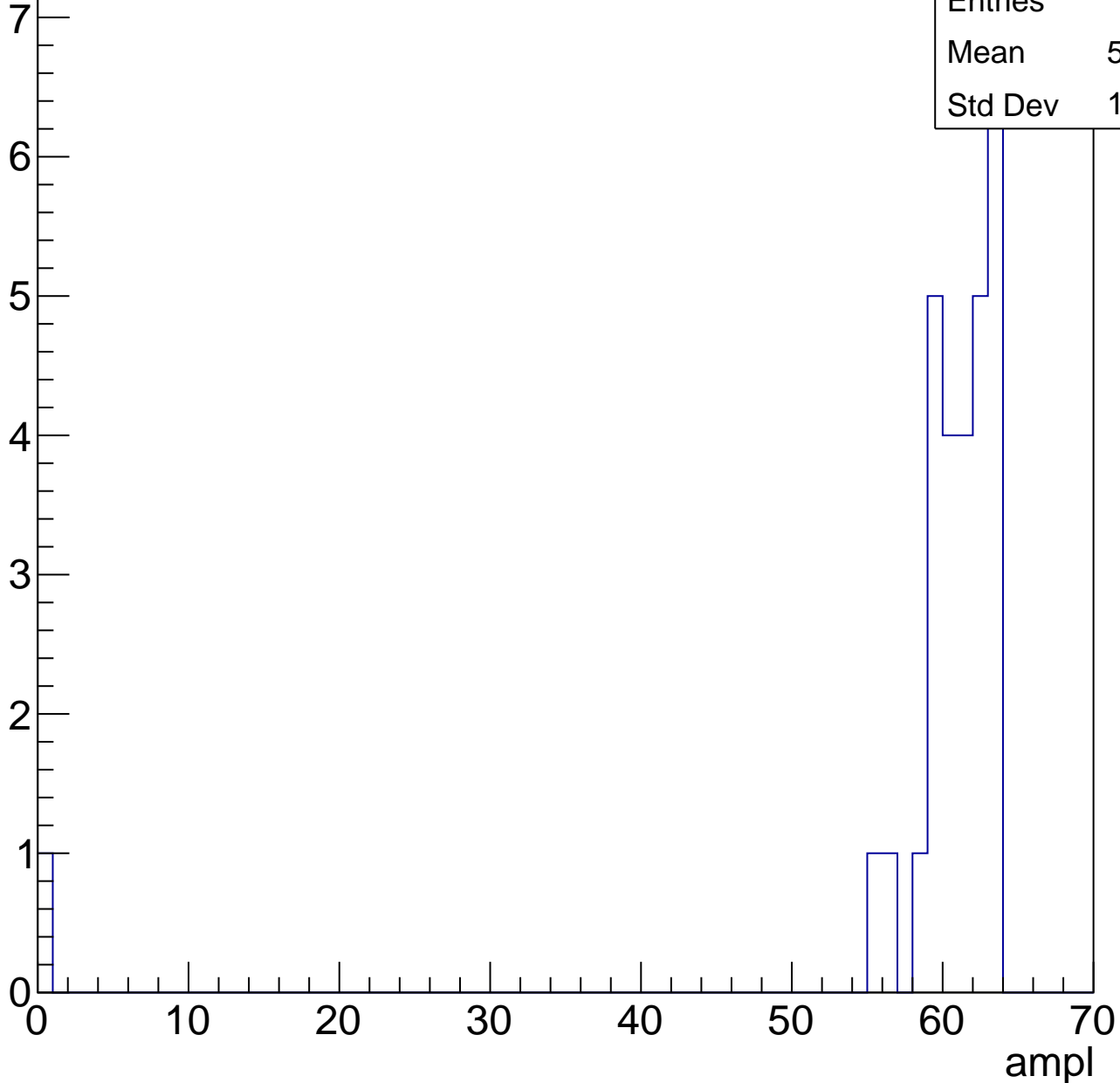


# B1L002S, U8-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.59
Std Dev	11.26

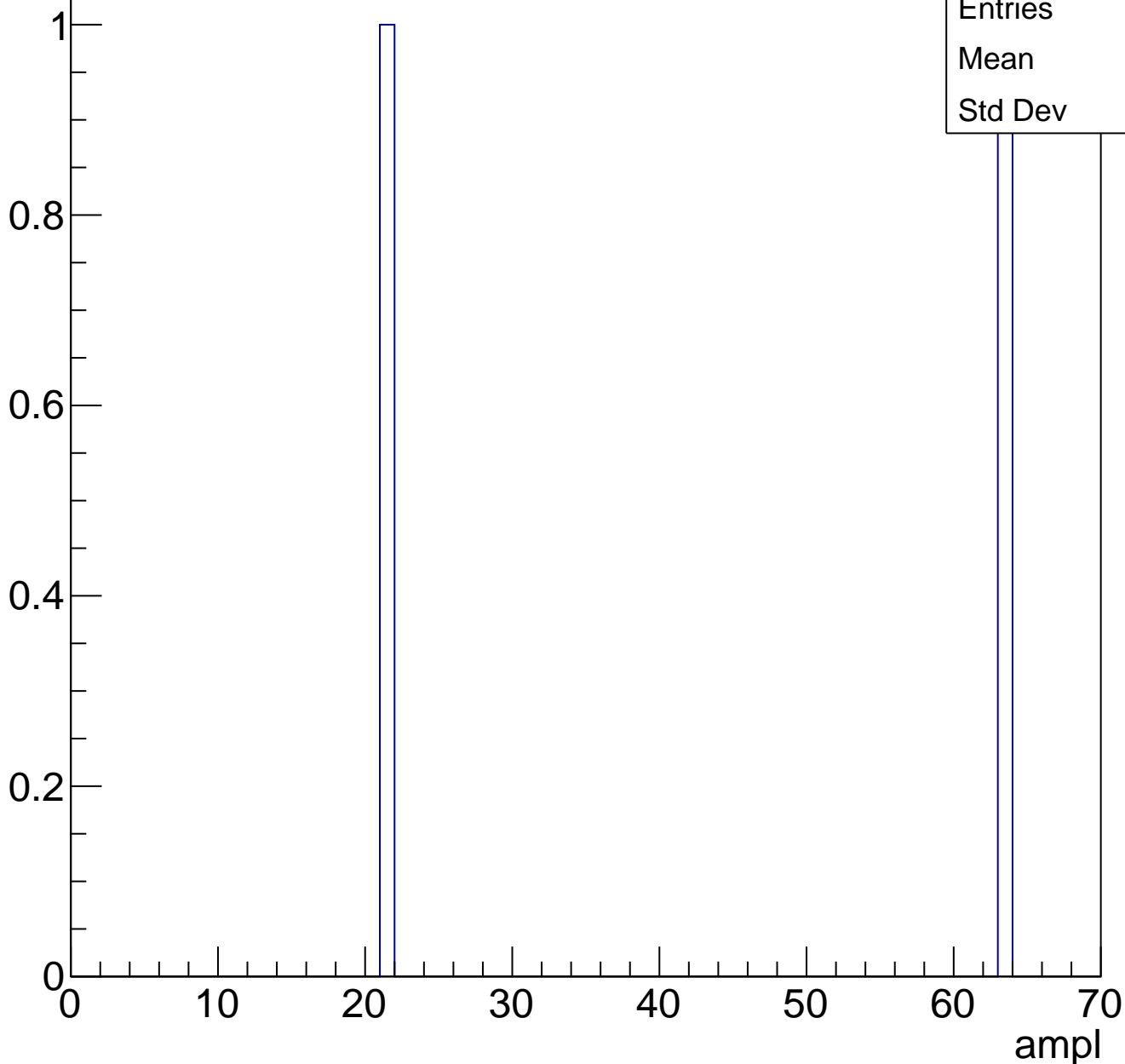




# B1L002S, U8-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	42
Std Dev	21

# B1L002S, U8-ch33, adc0

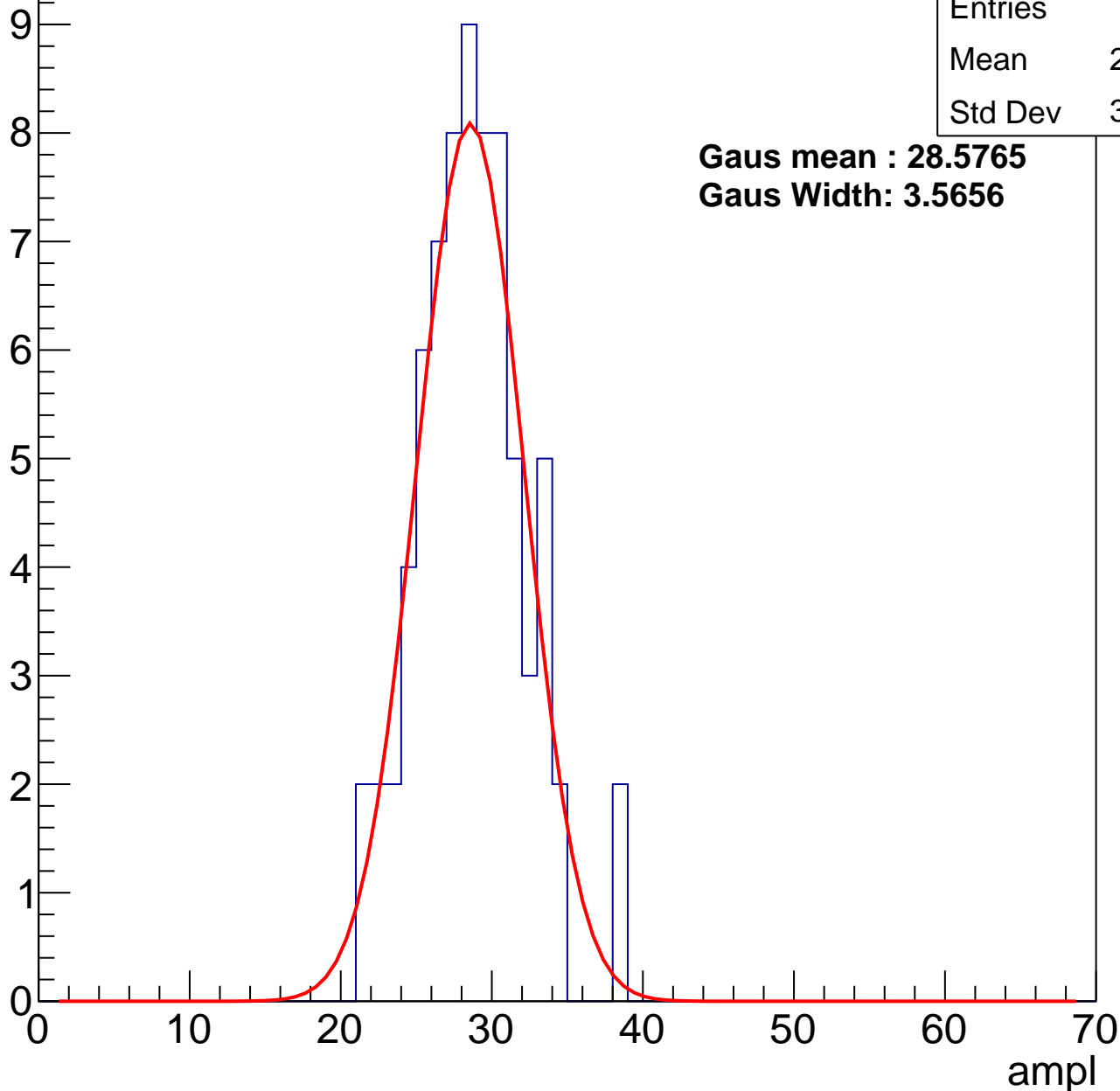
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	28.22
Std Dev	3.512

**Gaus mean : 28.5765**

**Gaus Width: 3.5656**



# B1L002S, U8-ch33, adc1

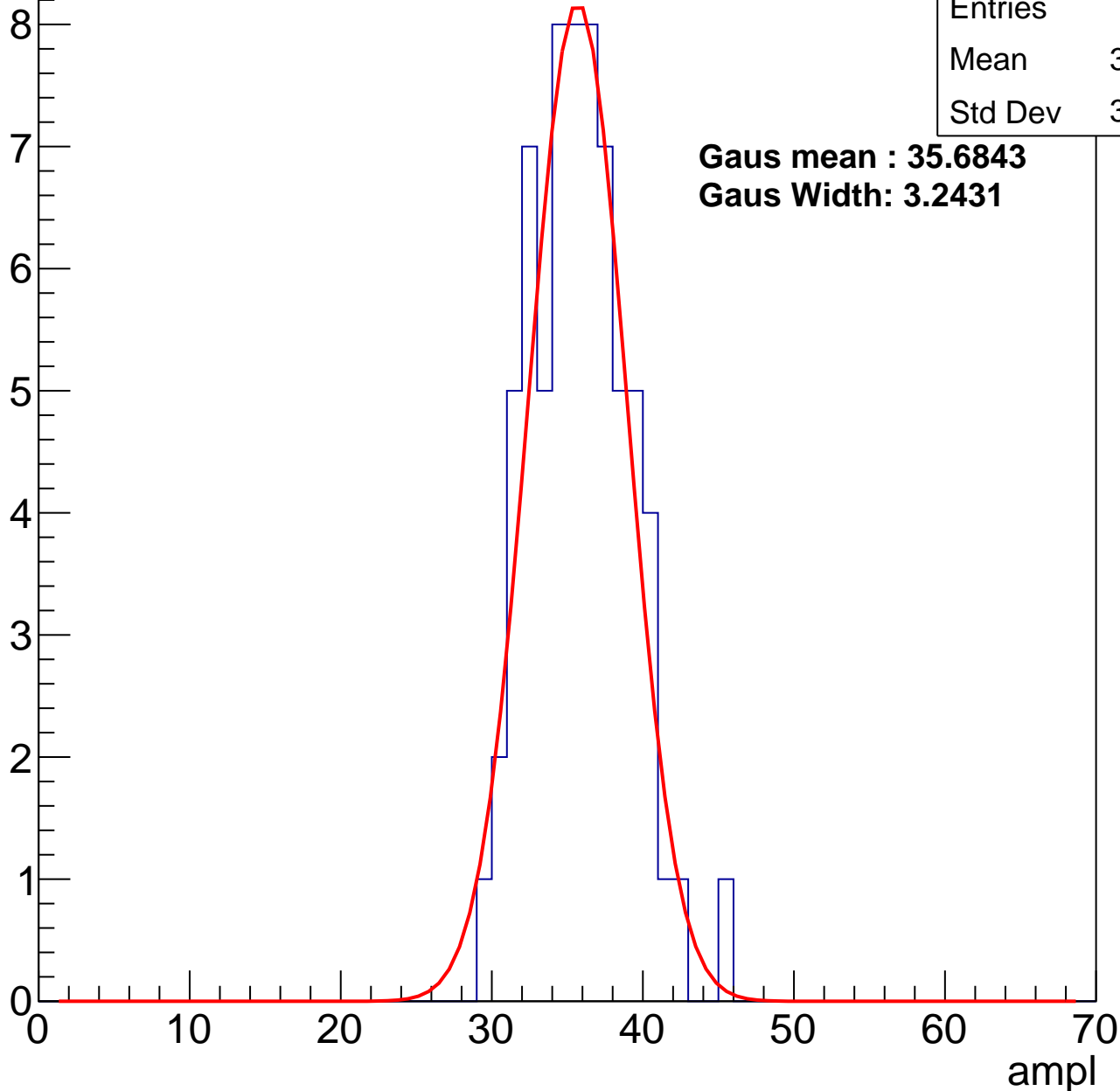
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.37
Std Dev	3.194

**Gaus mean : 35.6843**

**Gaus Width: 3.2431**



# B1L002S, U8-ch33, adc2

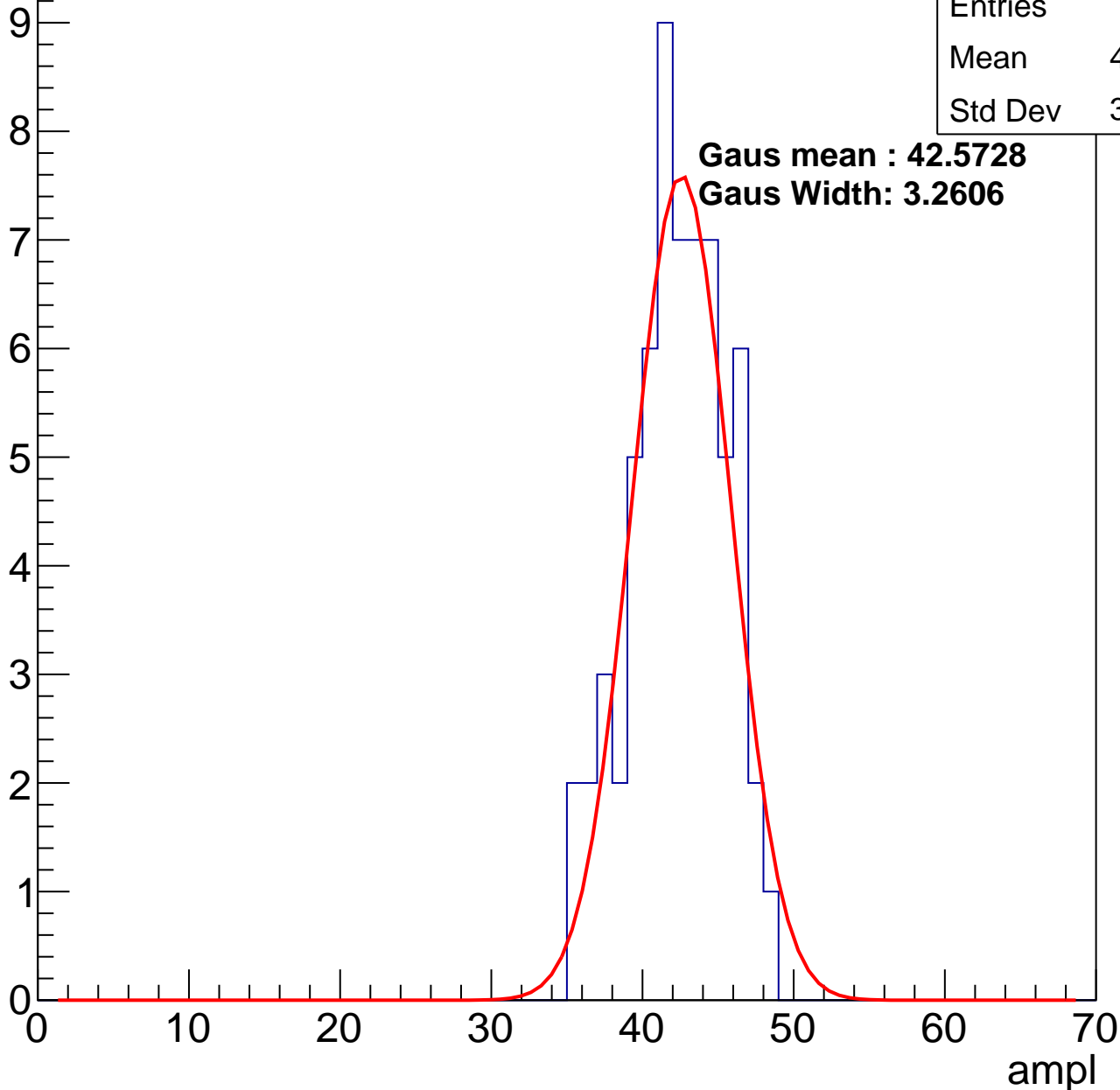
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.86
Std Dev	3.102

**Gaus mean : 42.5728**

**Gaus Width: 3.2606**

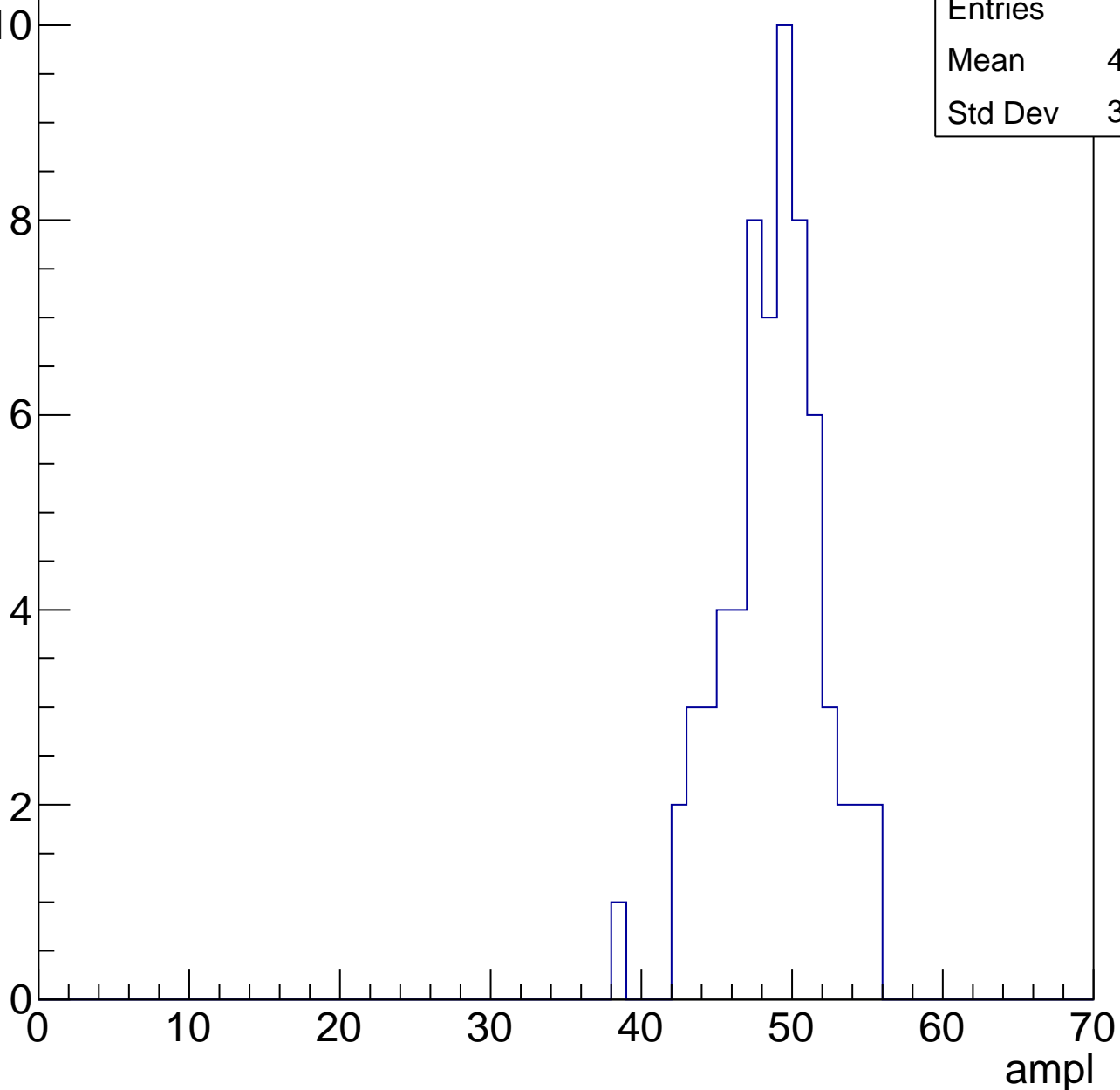


# B1L002S, U8-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	48.23
Std Dev	3.318

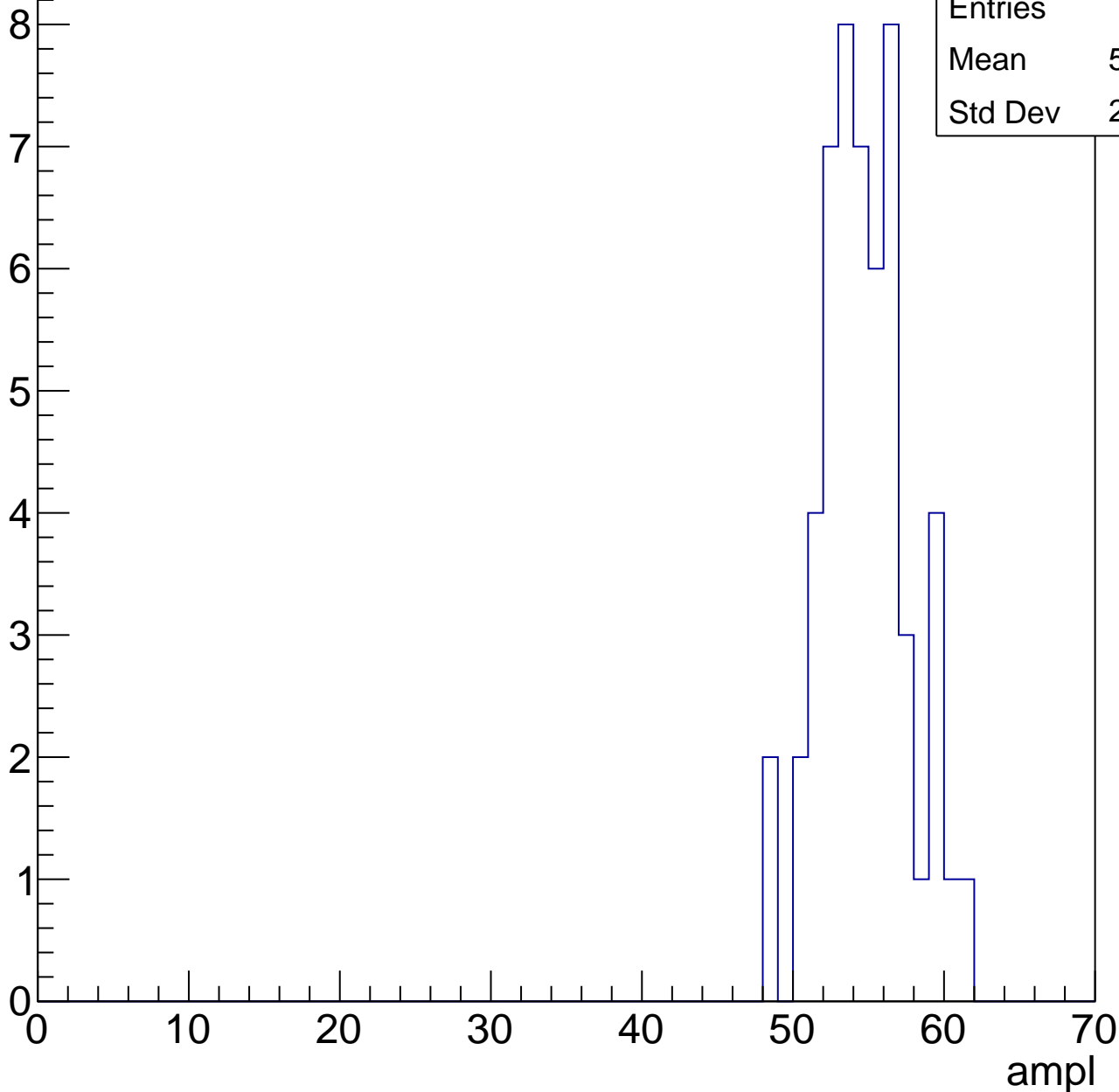


# B1L002S, U8-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

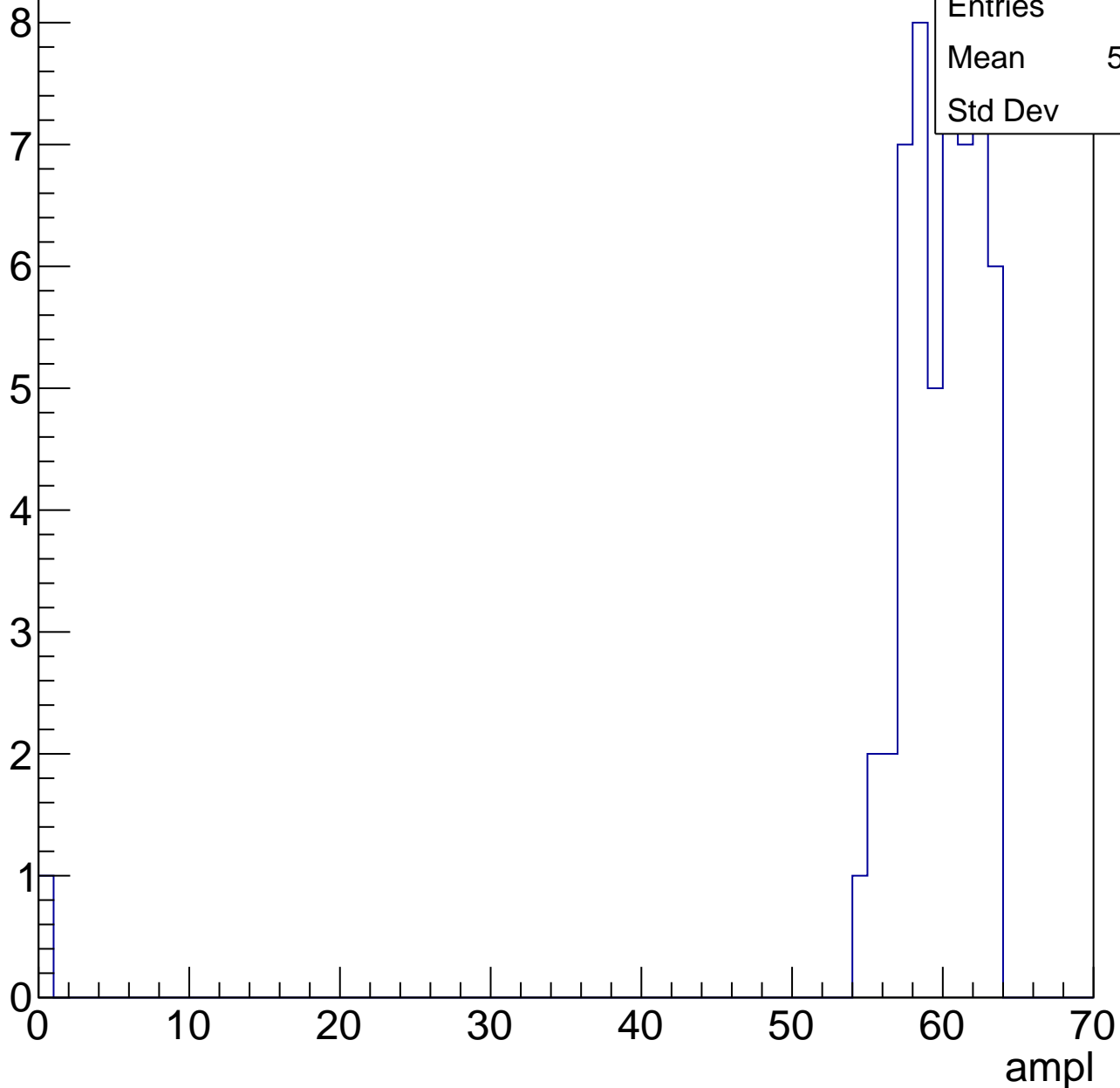
Entries	54
Mean	54.26
Std Dev	2.849



# B1L002S, U8-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

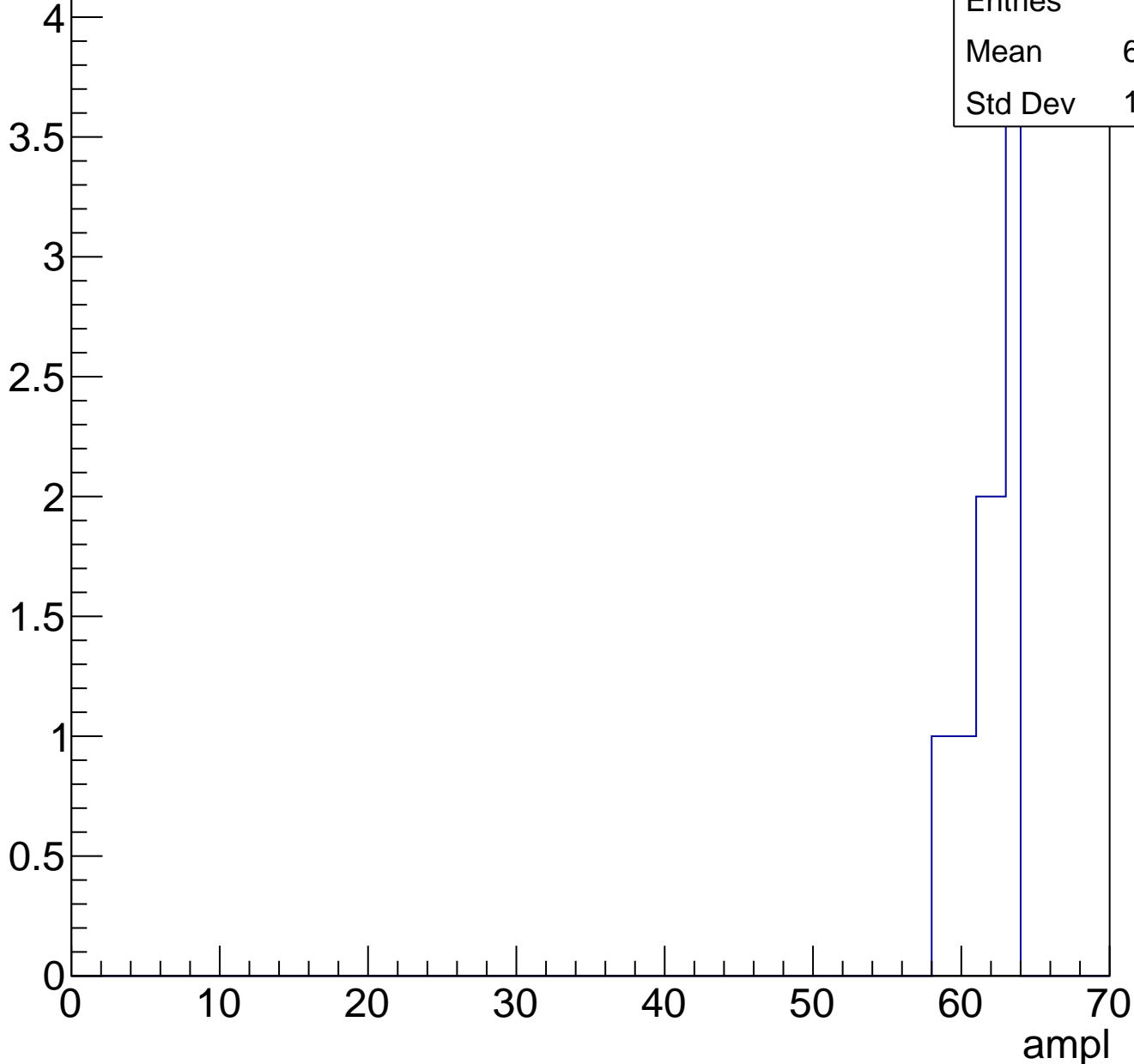
Entry



# B1L002S, U8-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch34, adc0

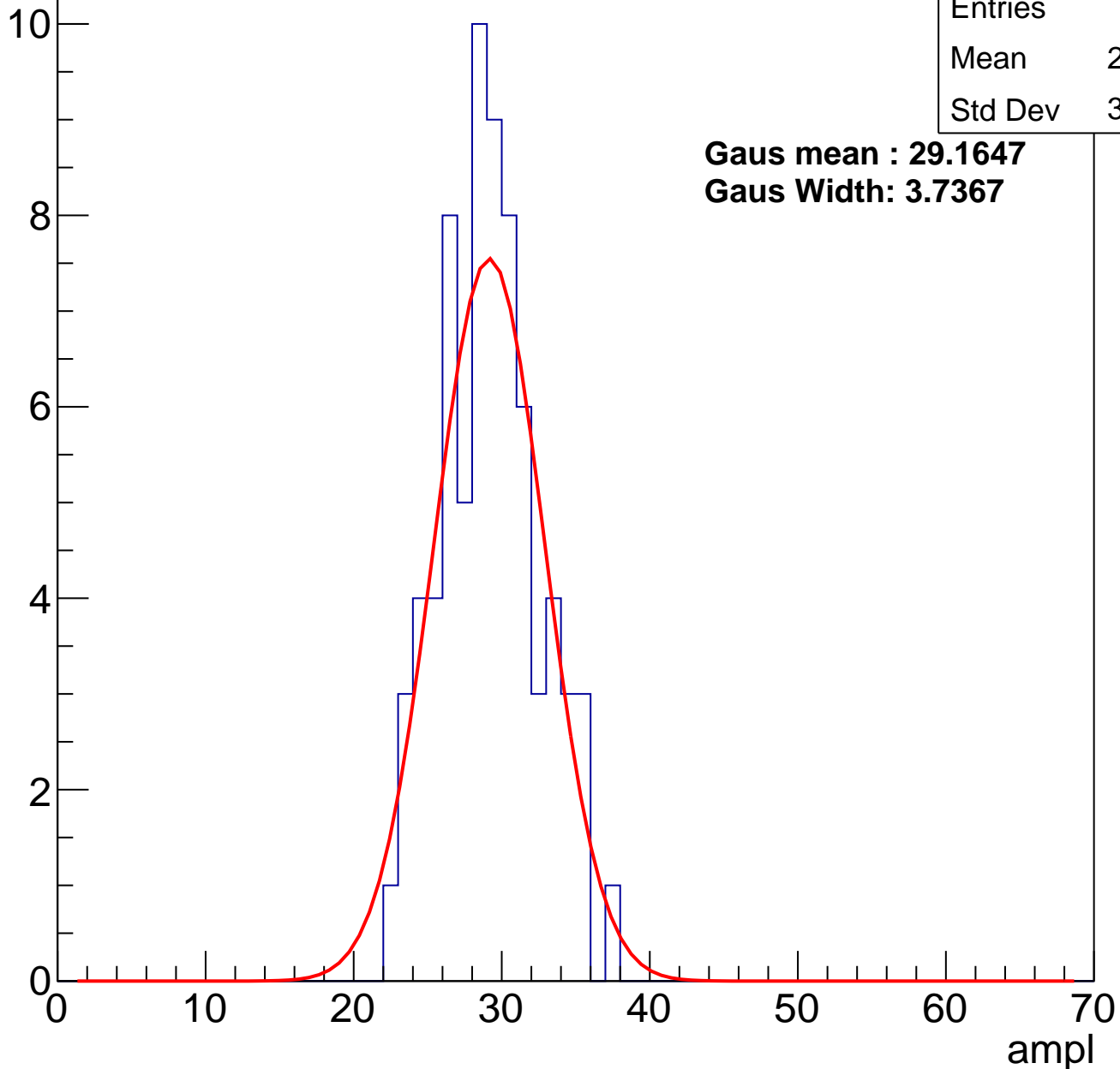
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	28.74
Std Dev	3.308

**Gaus mean : 29.1647**

**Gaus Width: 3.7367**

Entry



# B1L002S, U8-ch34, adc1

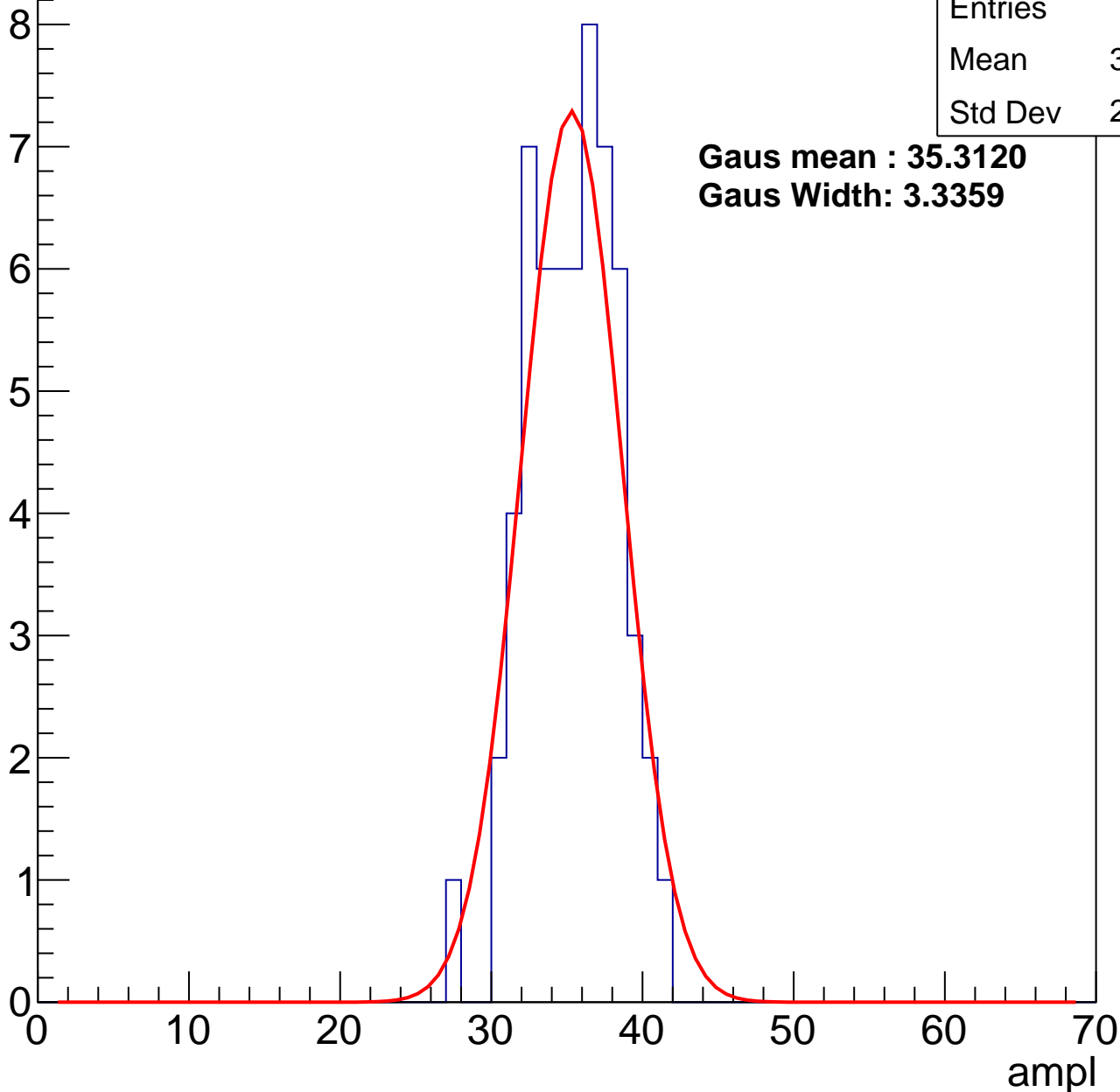
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.92
Std Dev	2.895

**Gaus mean : 35.3120**

**Gaus Width: 3.3359**



# B1L002S, U8-ch34, adc2

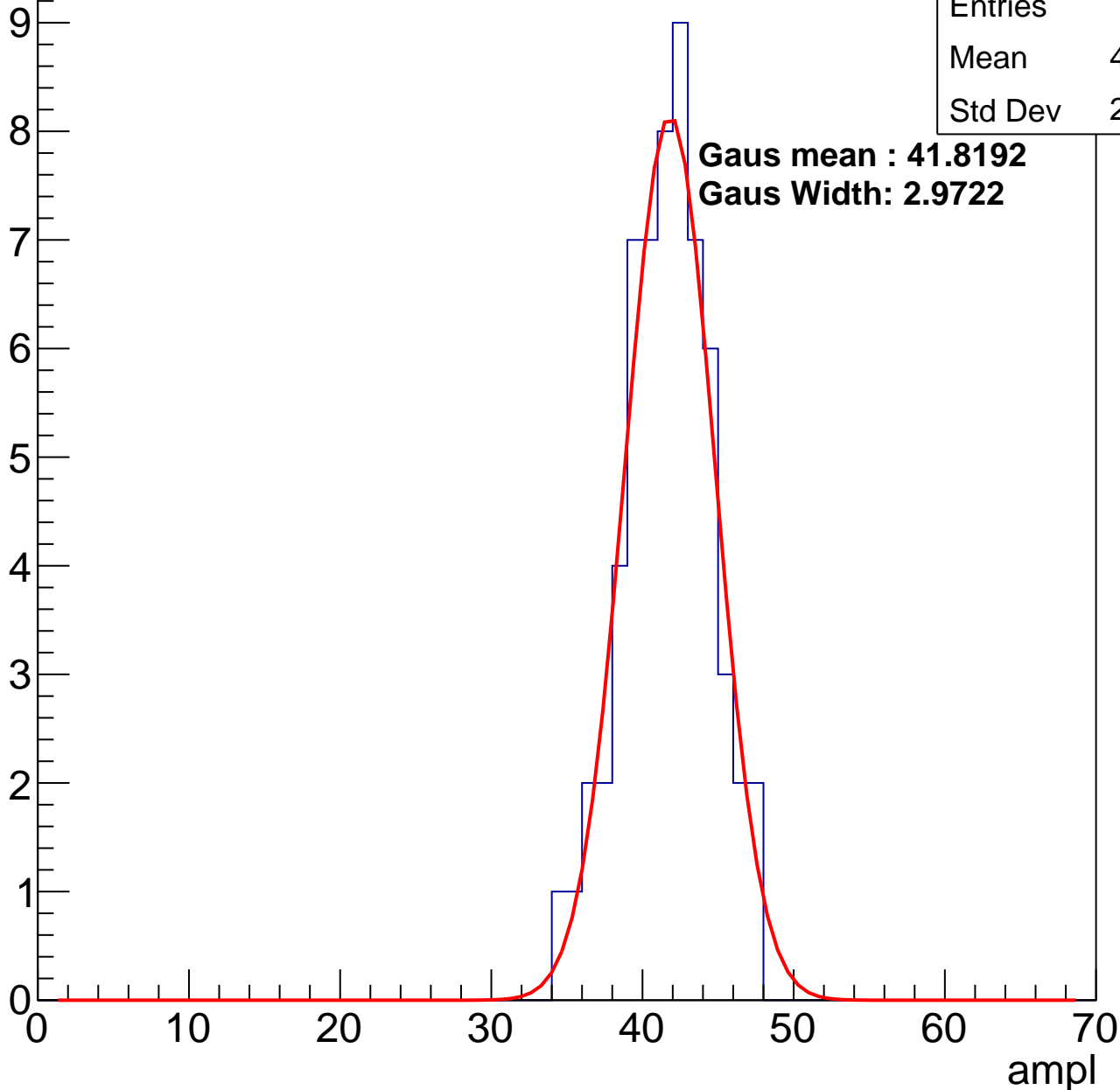
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.18
Std Dev	2.854

**Gaus mean : 41.8192**

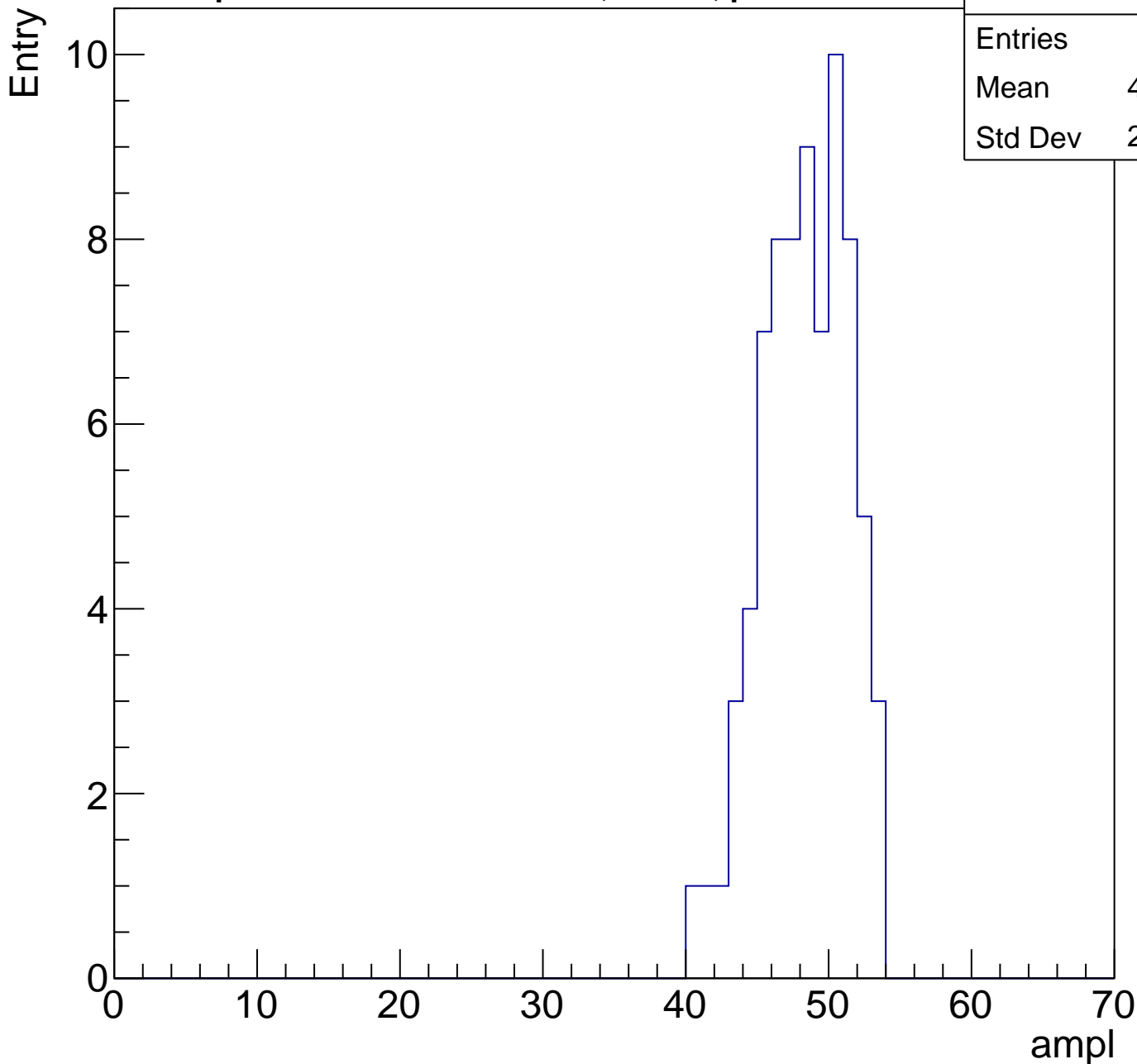
**Gaus Width: 2.9722**



# B1L002S, U8-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

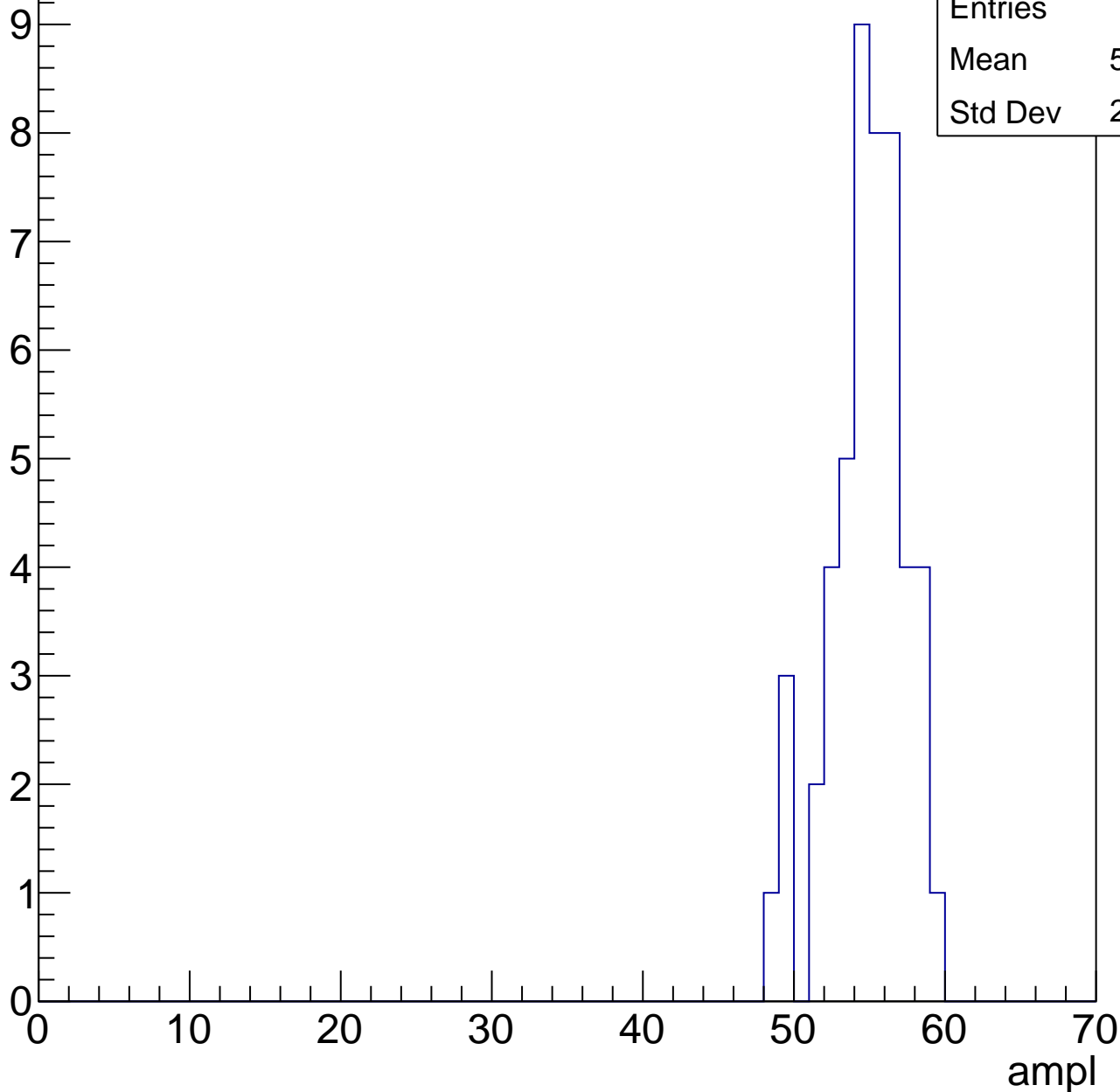
Entries	75
Mean	47.85
Std Dev	2.974



# B1L002S, U8-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

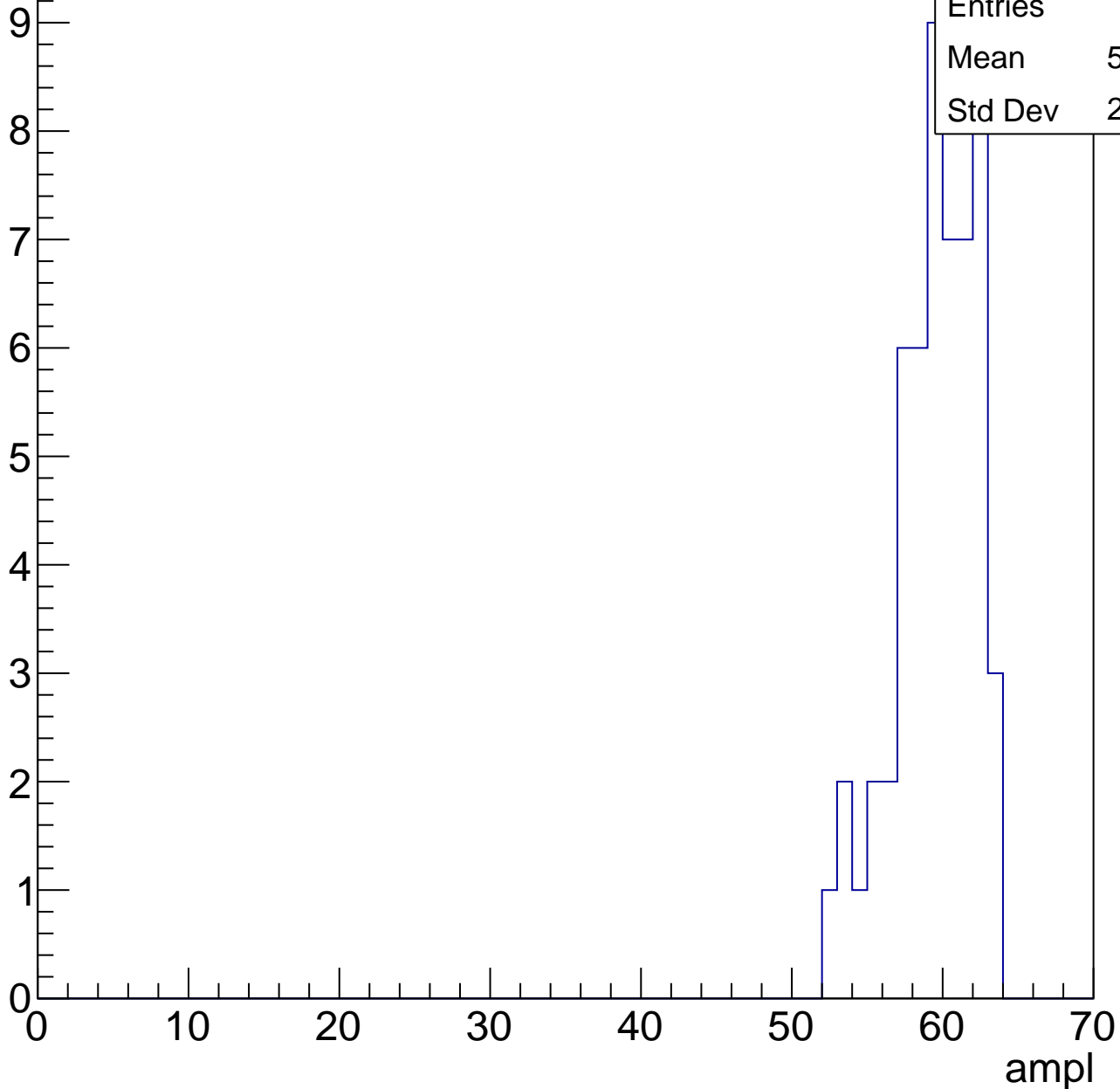
Entry



# B1L002S, U8-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



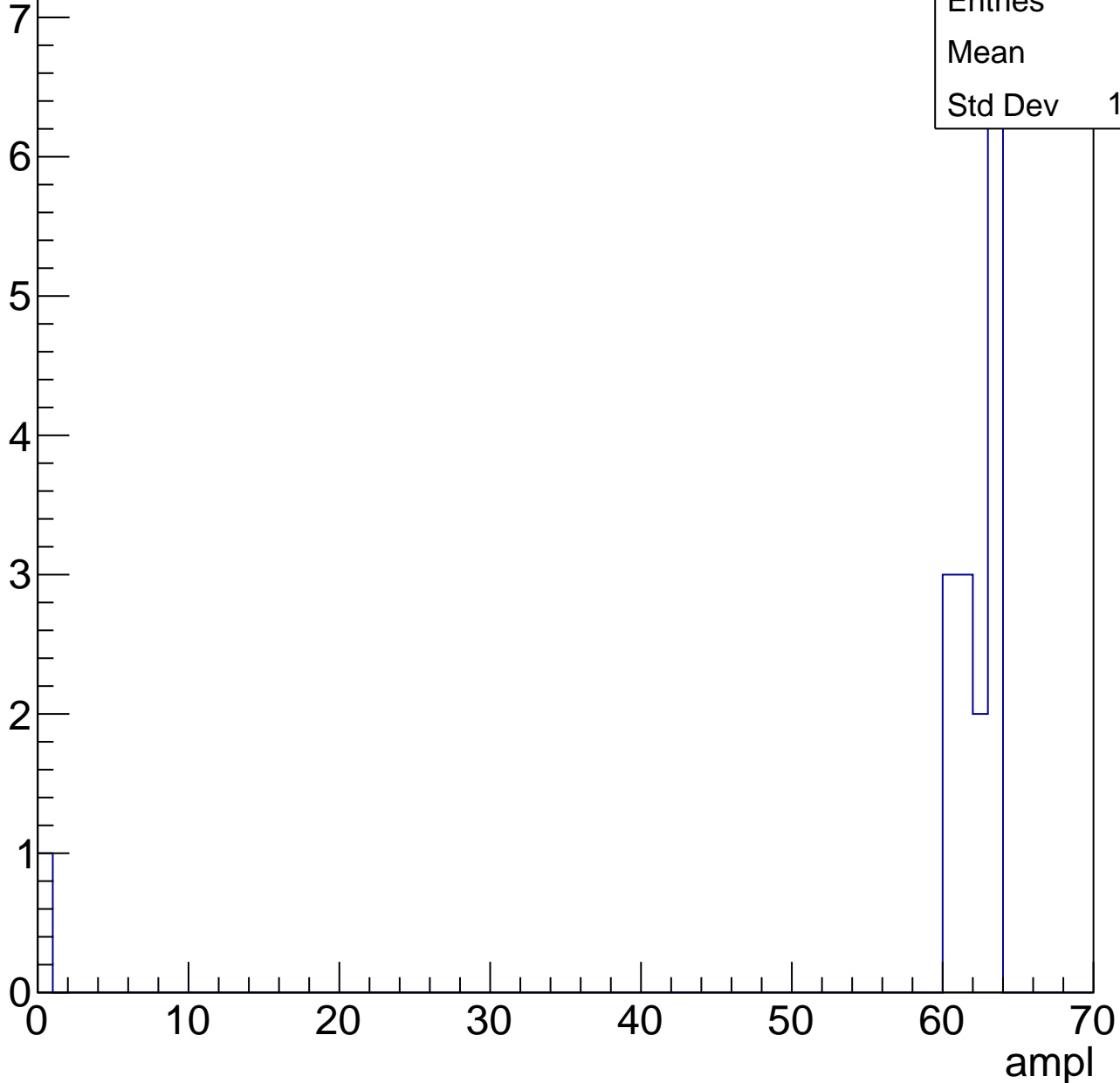
Entries	54
Mean	59.02
Std Dev	2.656

# B1L002S, U8-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	58
Std Dev	15.02





# B1L002S, U8-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L002S, U8-ch35, adc0

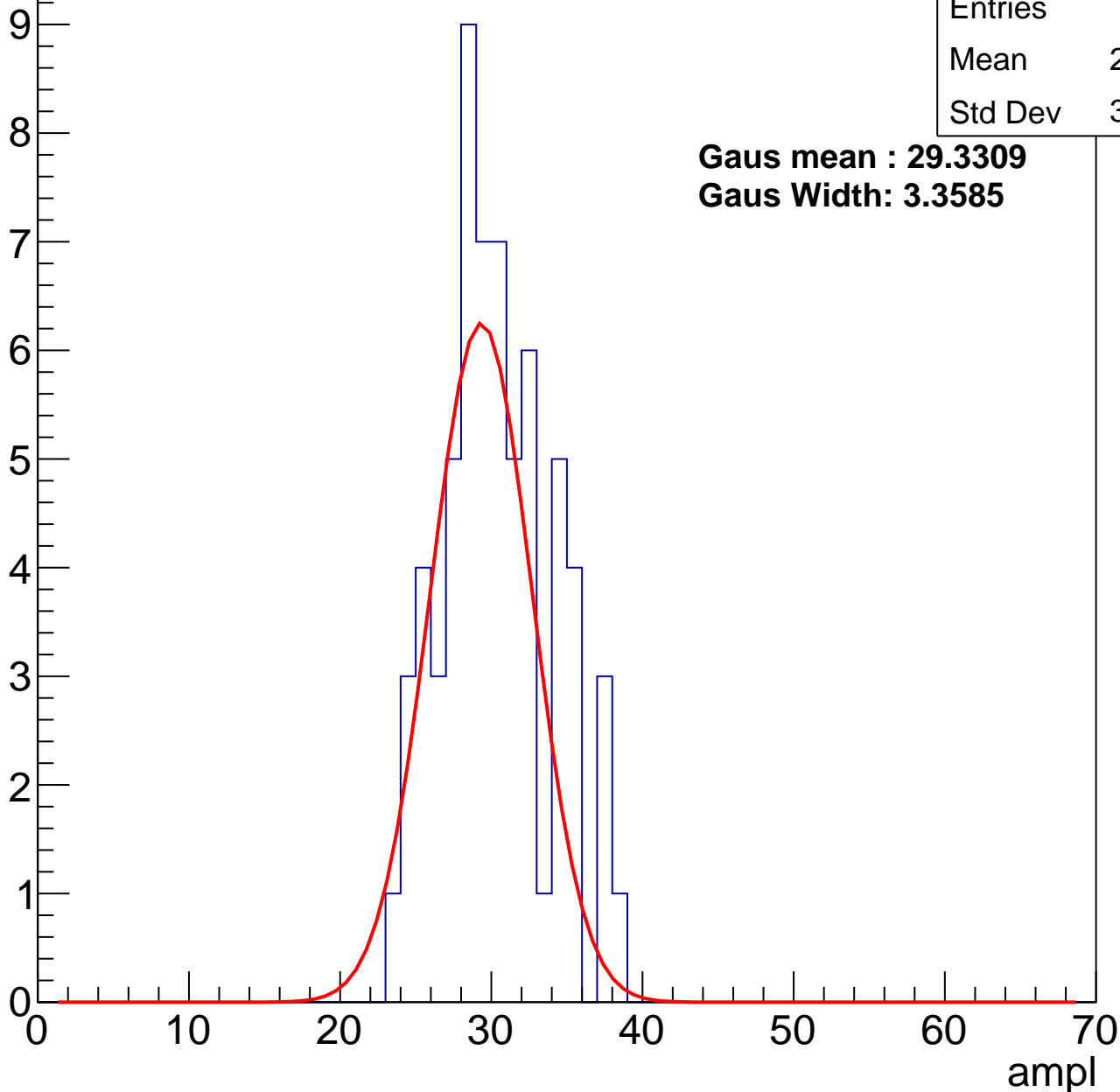
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	29.88
Std Dev	3.582

**Gaus mean : 29.3309**

**Gaus Width: 3.3585**



# B1L002S, U8-ch35, adc1

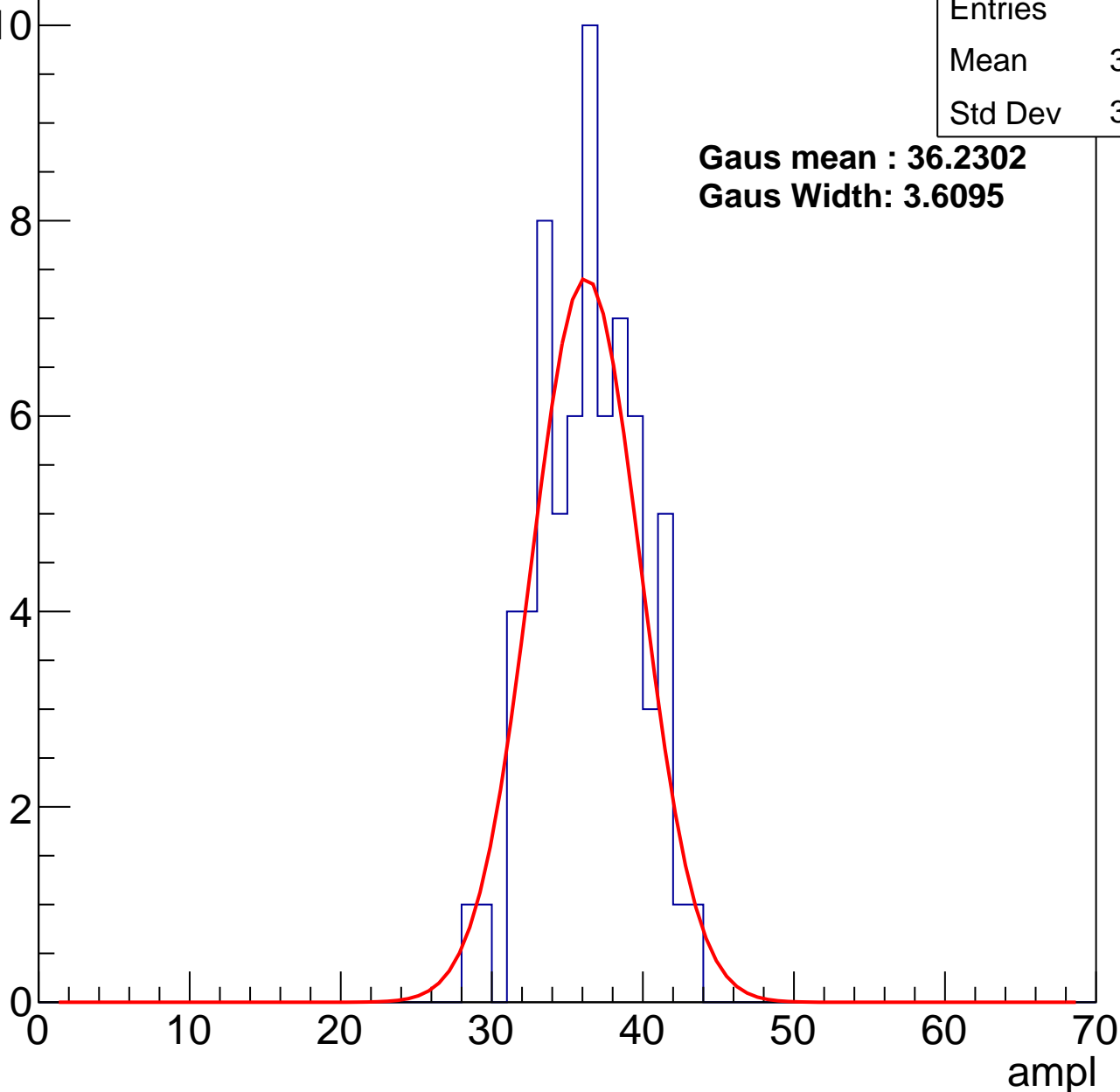
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.96
Std Dev	3.256

**Gaus mean : 36.2302**

**Gaus Width: 3.6095**



# B1L002S, U8-ch35, adc2

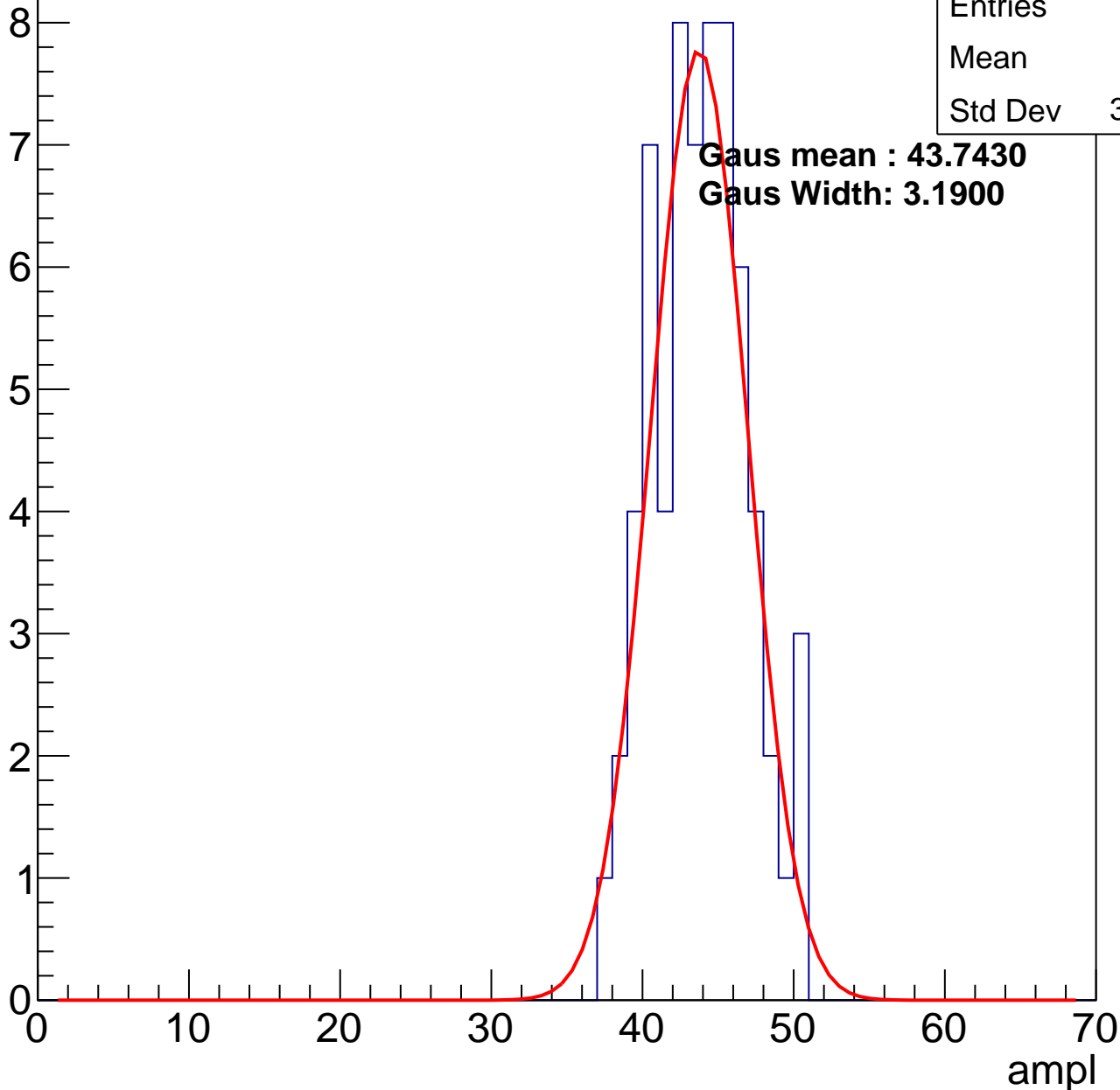
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	43.4
Std Dev	3.082

**Gaus mean : 43.7430**

**Gaus Width: 3.1900**

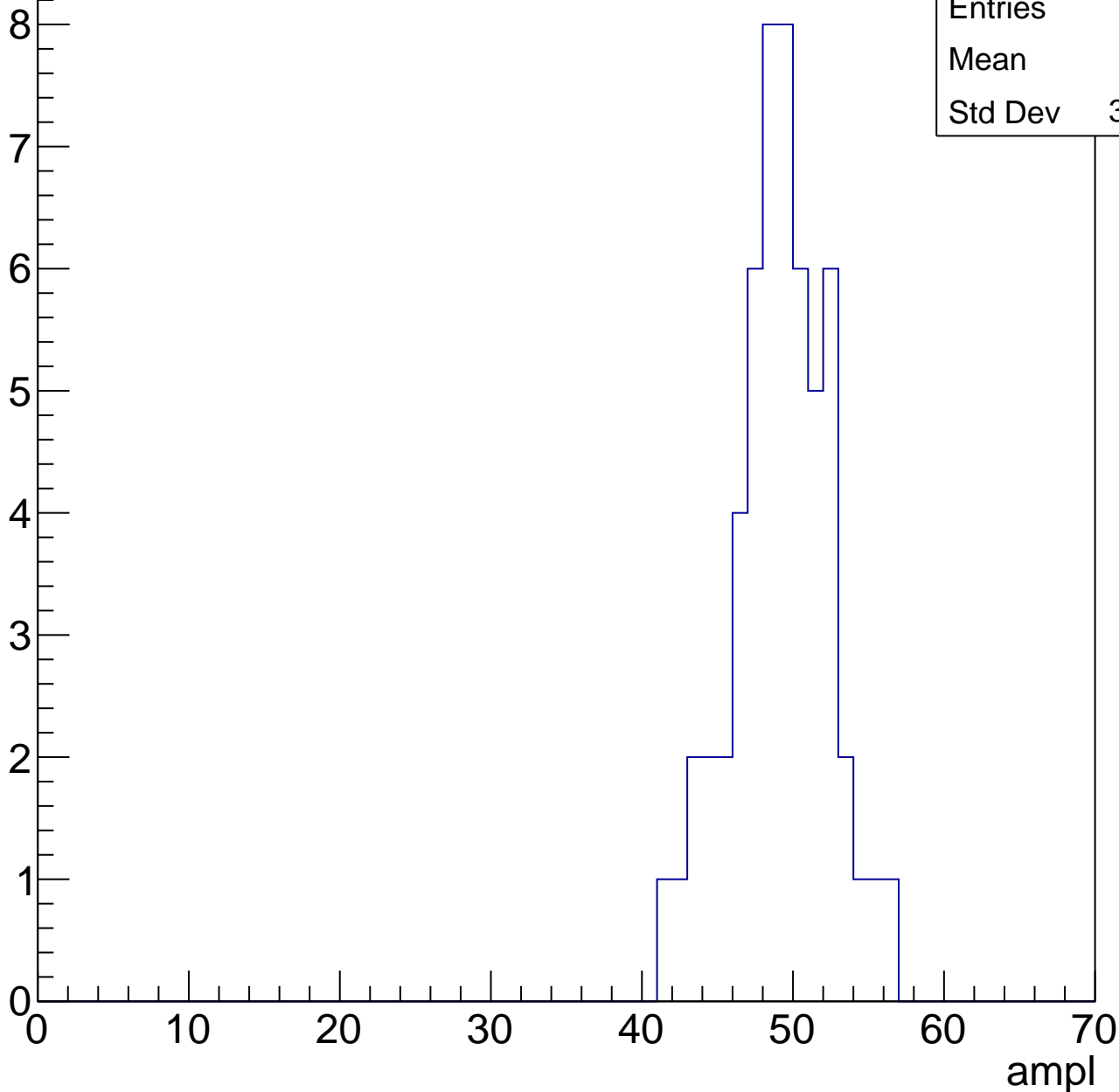


# B1L002S, U8-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

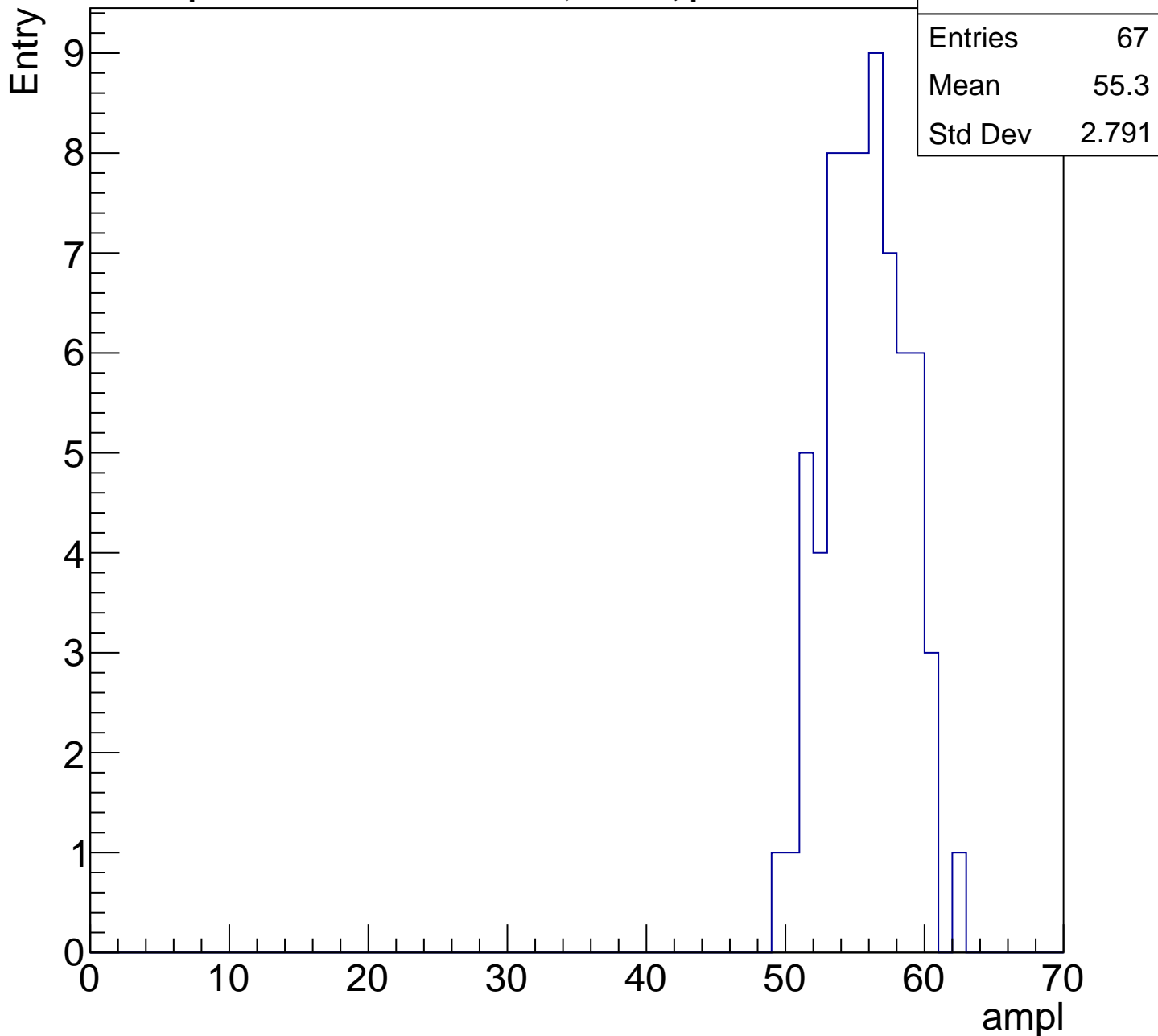
Entry

Entries	56
Mean	48.7
Std Dev	3.139



# B1L002S, U8-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

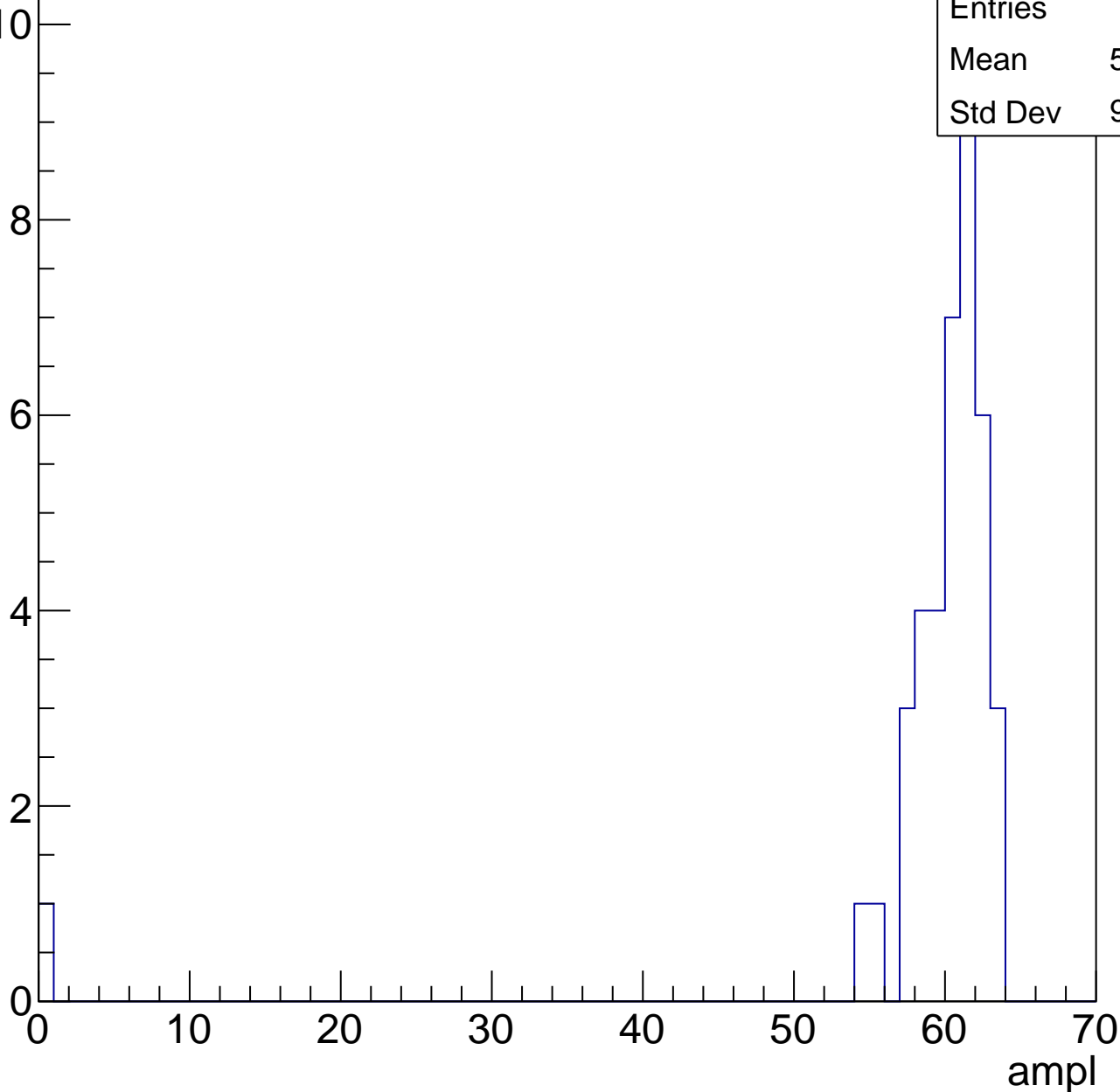


# B1L002S, U8-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	58.48
Std Dev	9.586

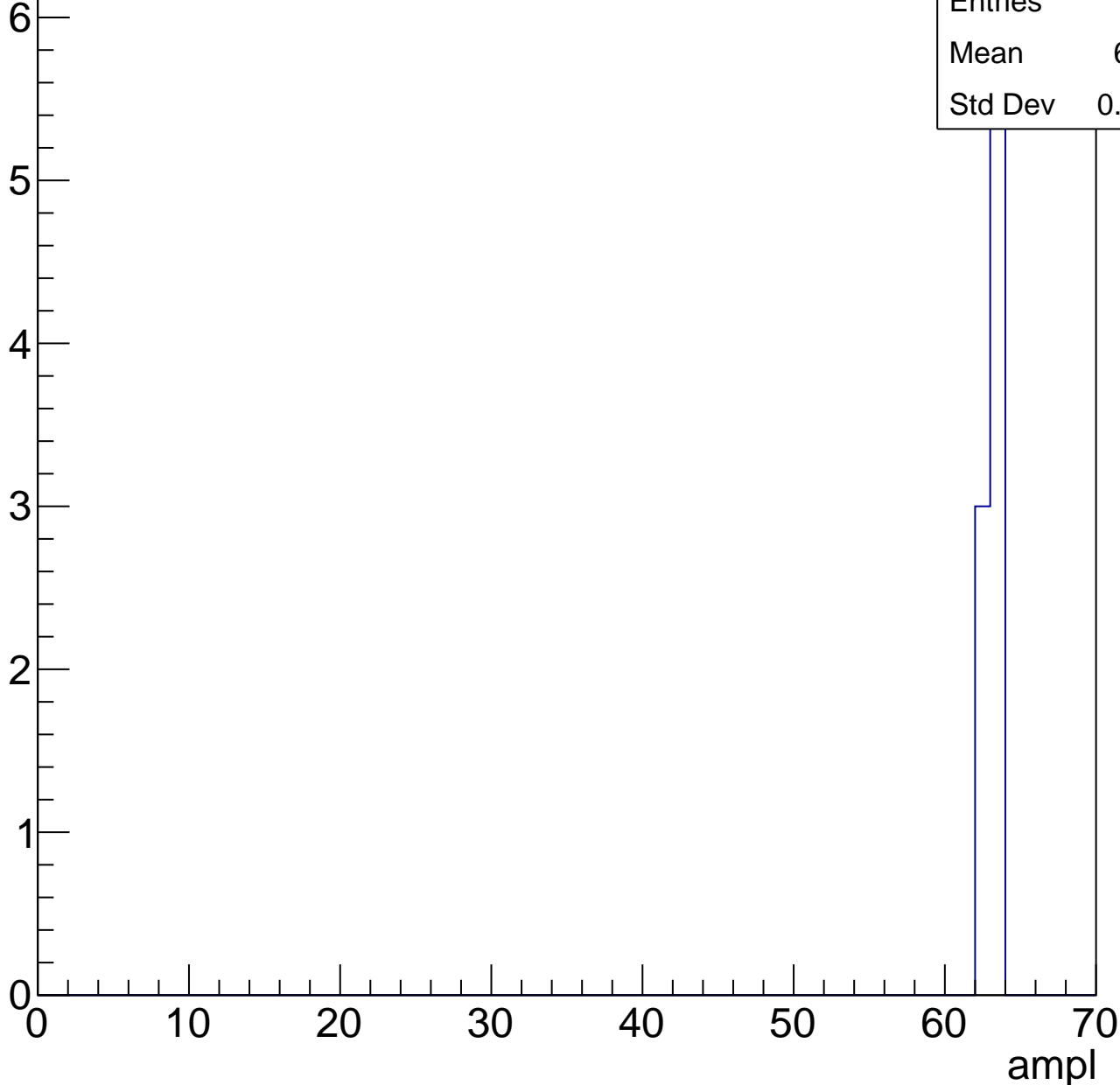


# B1L002S, U8-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	9
Mean	62.67
Std Dev	0.4714





# B1L002S, U8-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U8-ch36, adc0

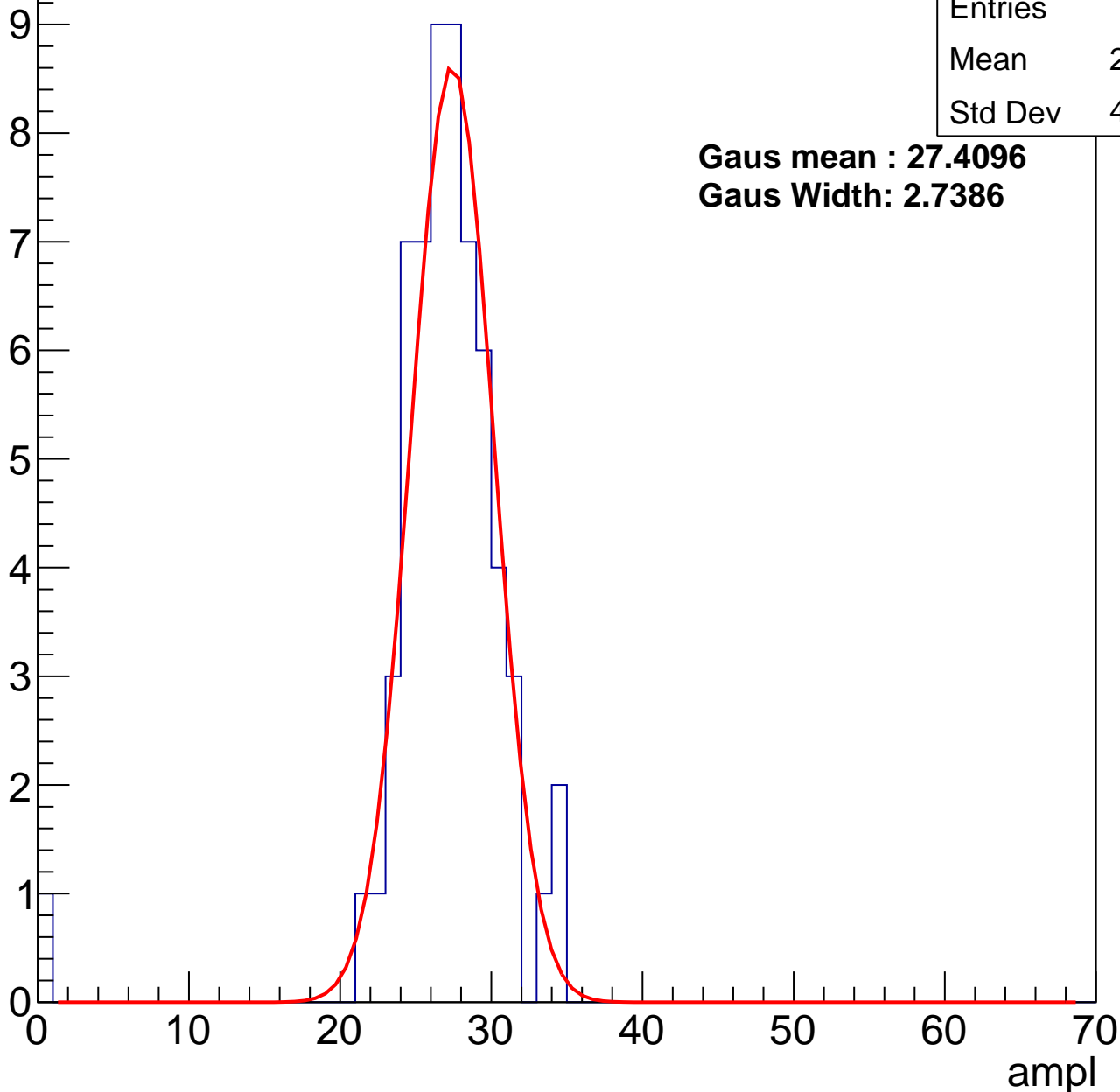
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	26.49
Std Dev	4.382

**Gaus mean : 27.4096**

**Gaus Width: 2.7386**



# B1L002S, U8-ch36, adc1

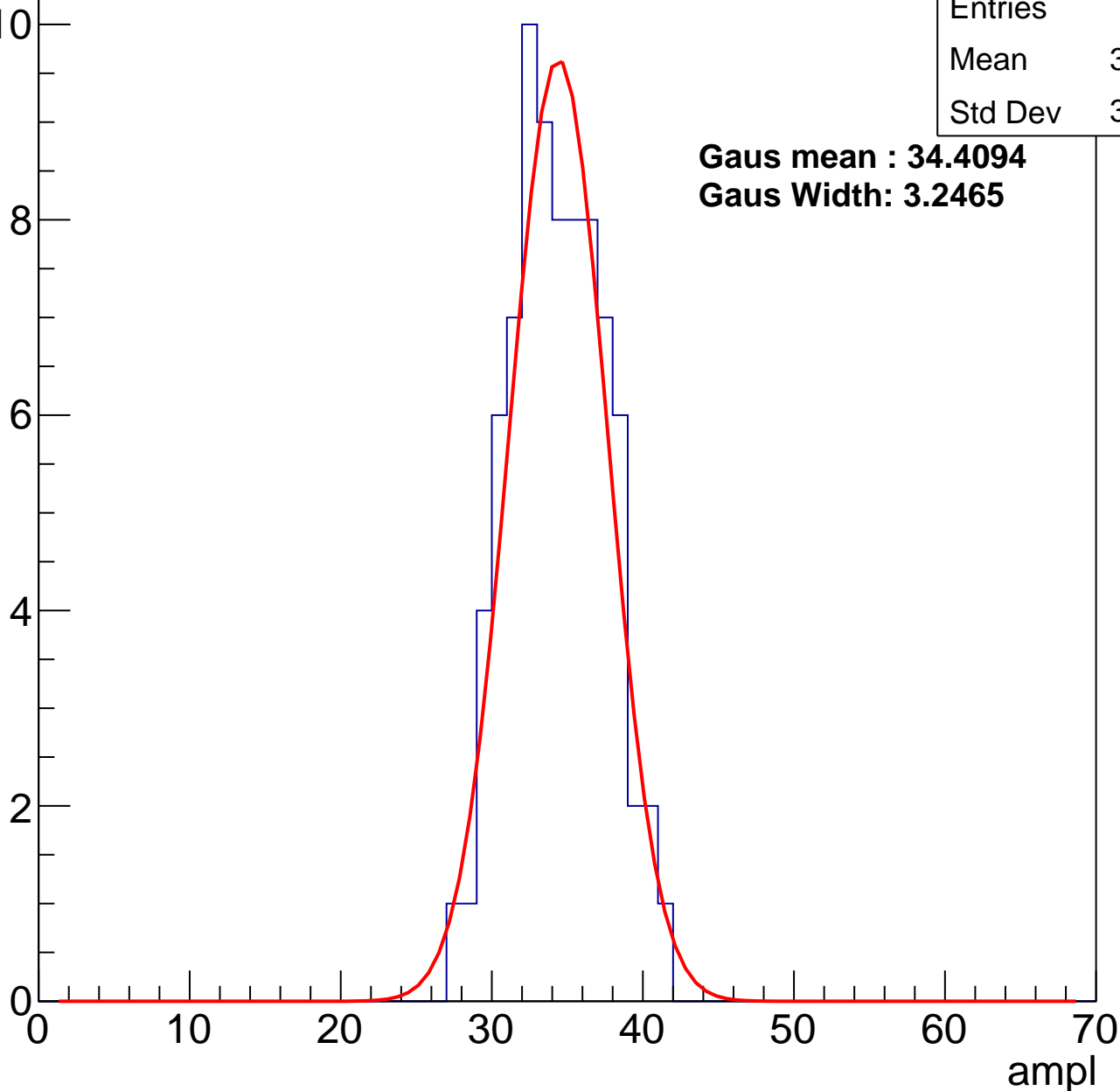
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	33.89
Std Dev	3.086

**Gaus mean : 34.4094**

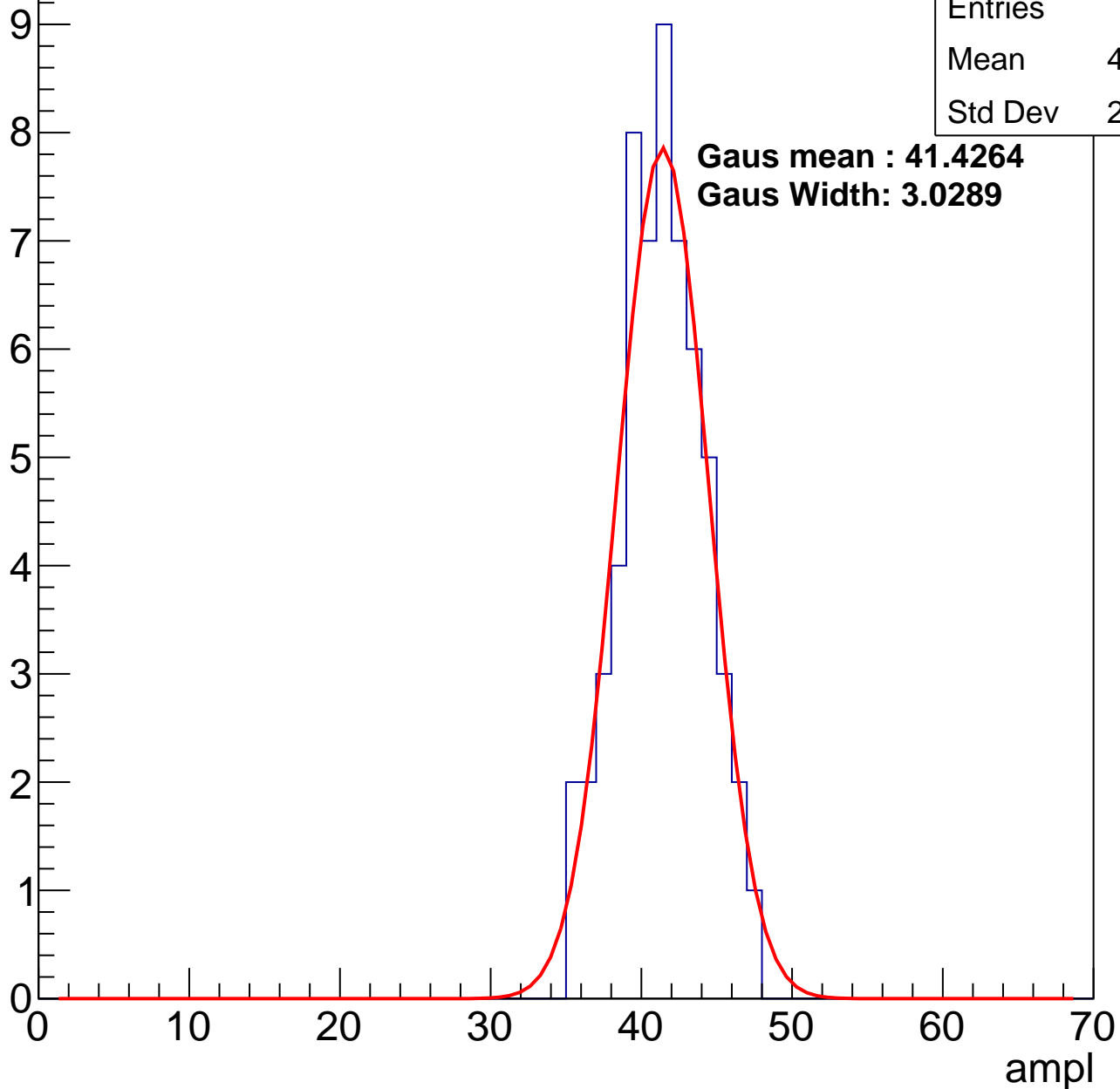
**Gaus Width: 3.2465**



# B1L002S, U8-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

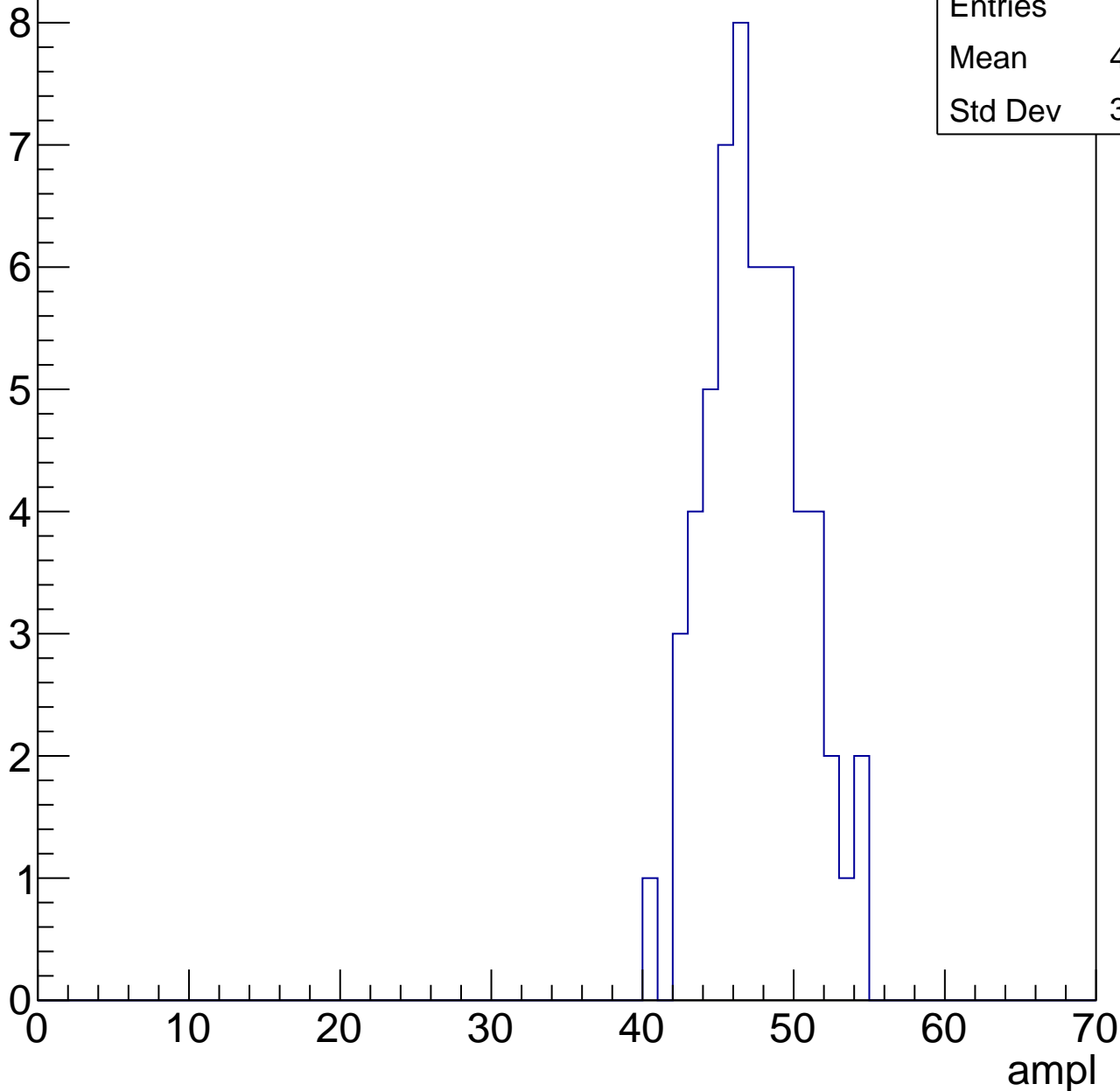


# B1L002S, U8-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.02
Std Dev	3.143

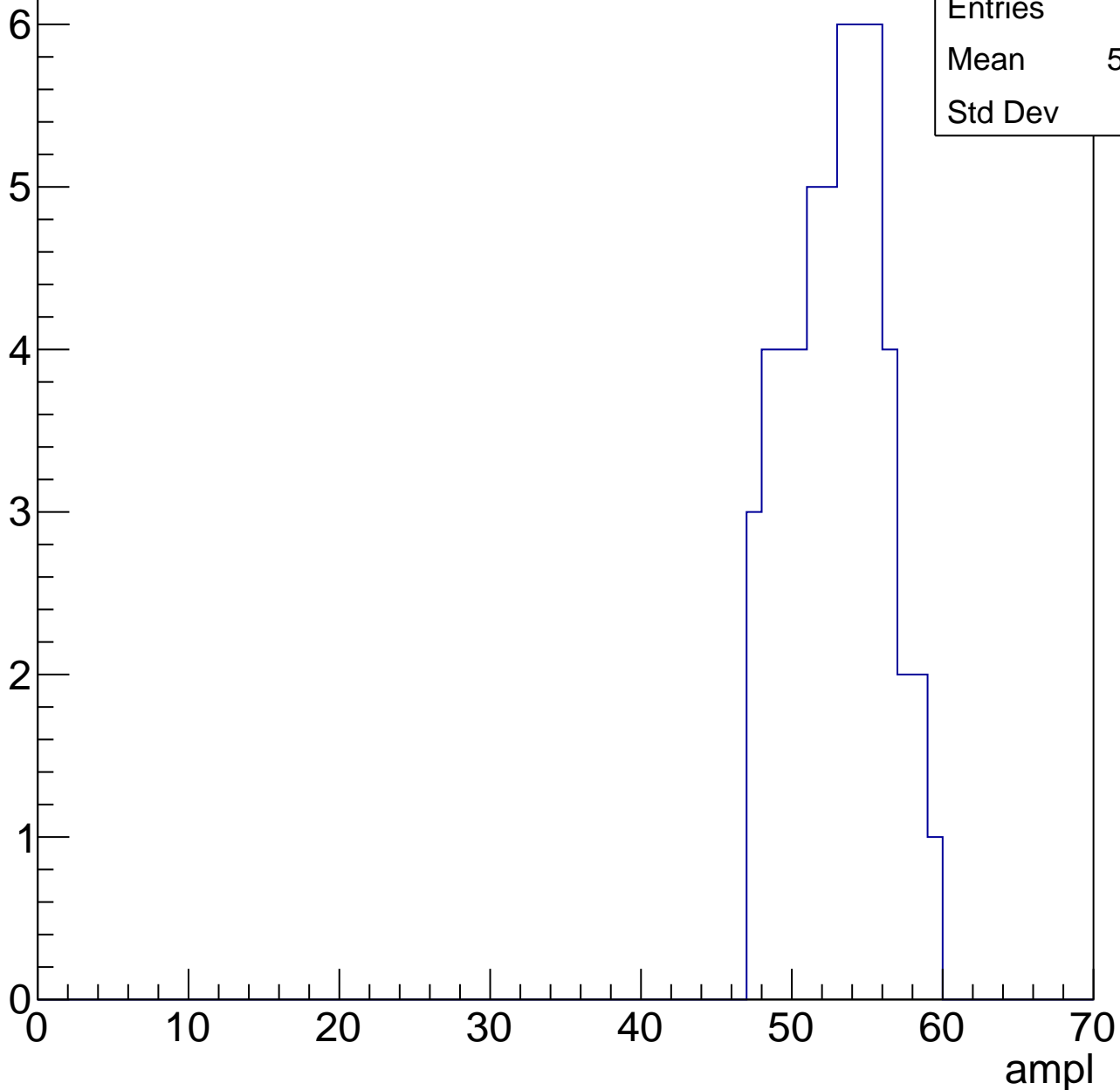


# B1L002S, U8-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

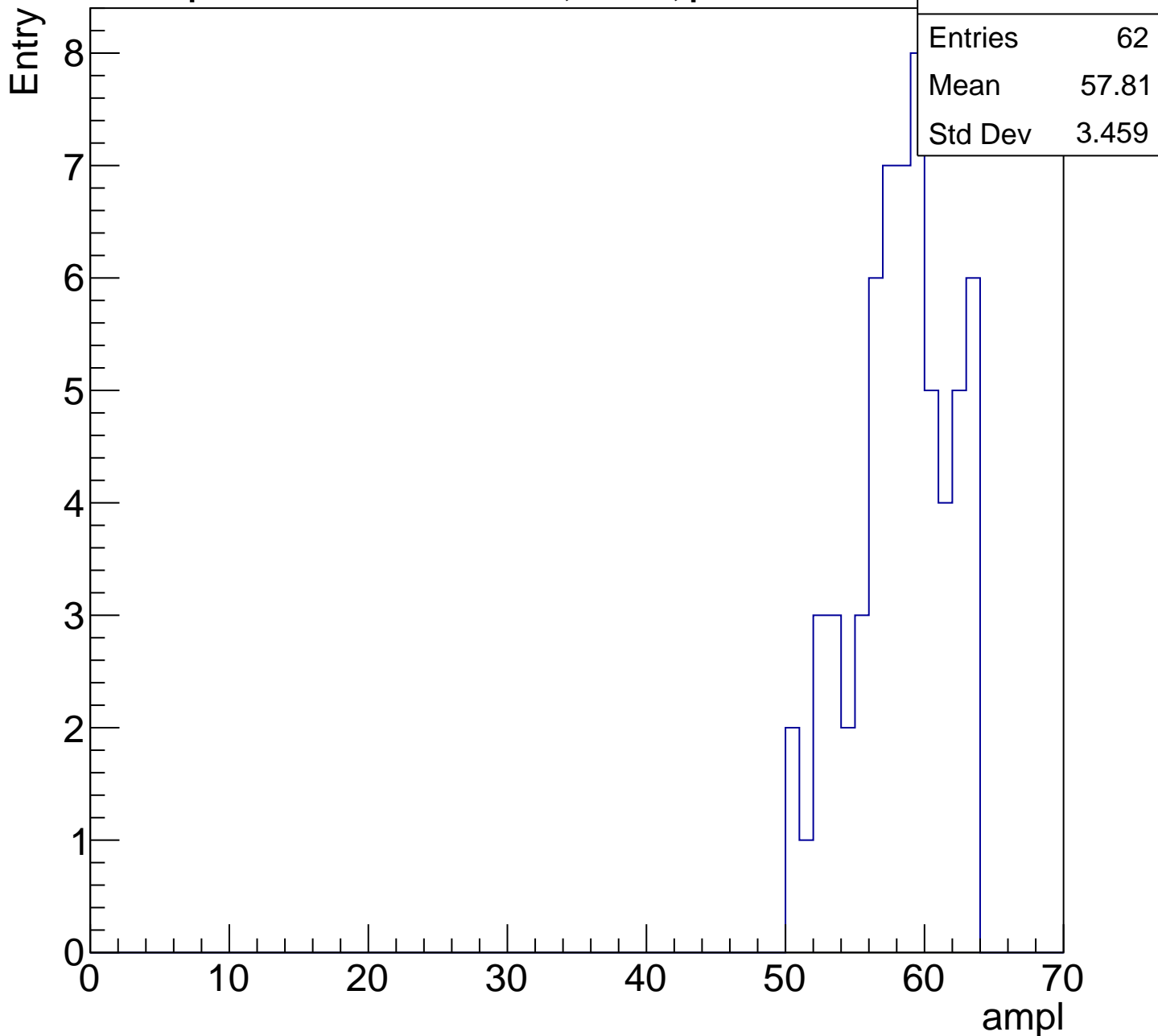
Entry

Entries	52
Mean	52.48
Std Dev	3.11



# B1L002S, U8-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

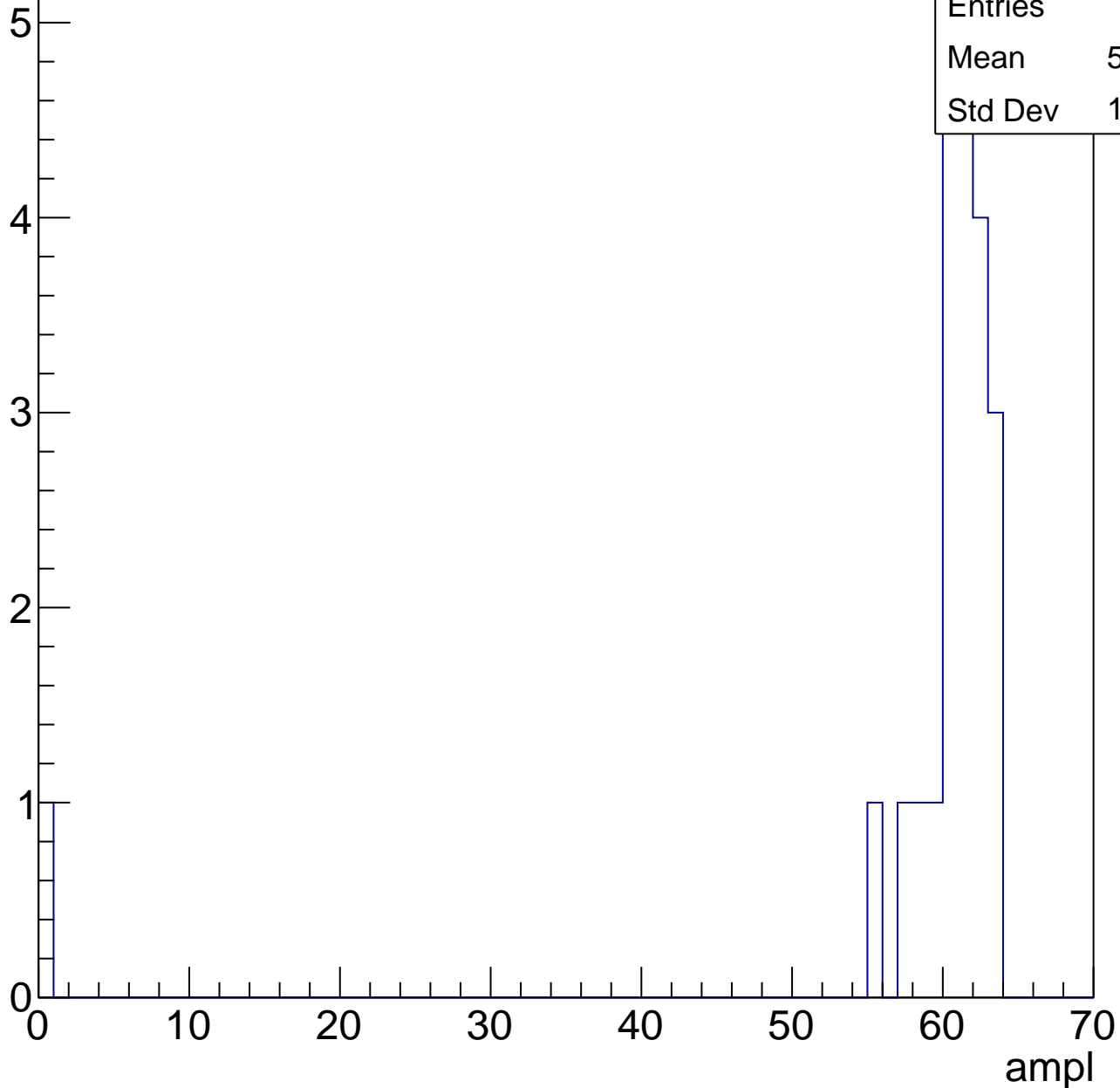


# B1L002S, U8-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	57.77
Std Dev	12.75

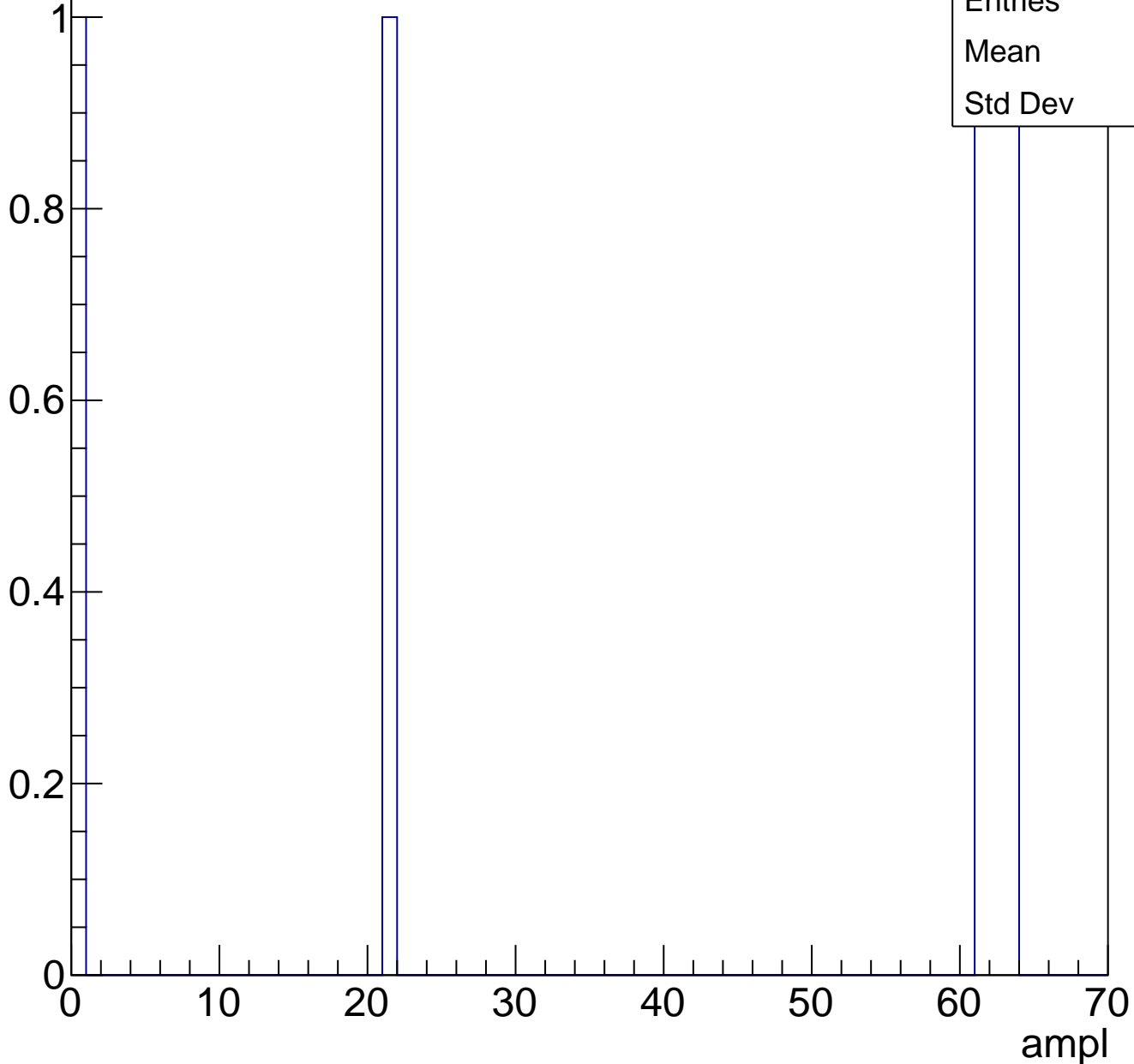




# B1L002S, U8-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch37, adc0

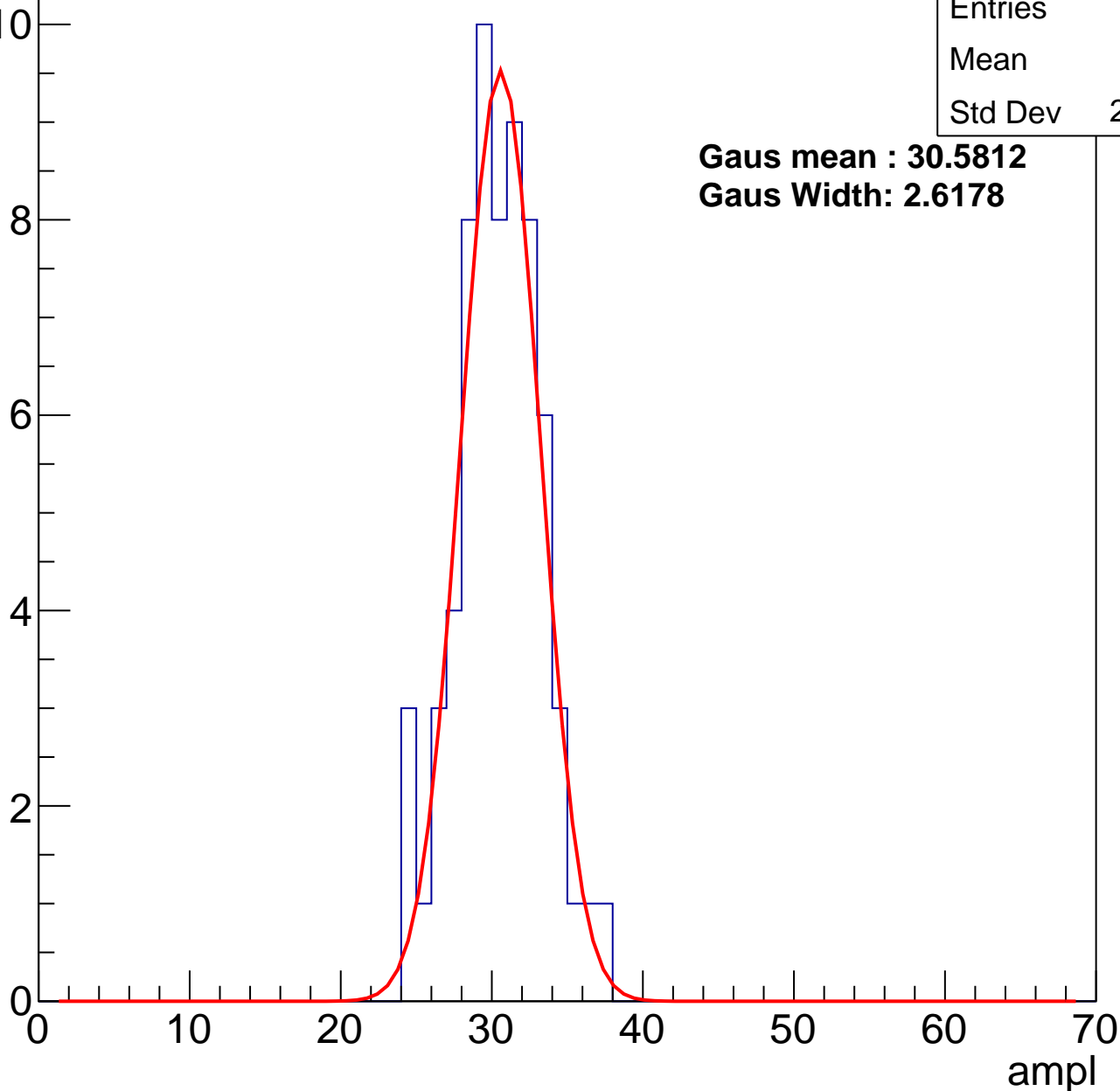
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	30
Std Dev	2.785

**Gaus mean : 30.5812**

**Gaus Width: 2.6178**



# B1L002S, U8-ch37, adc1

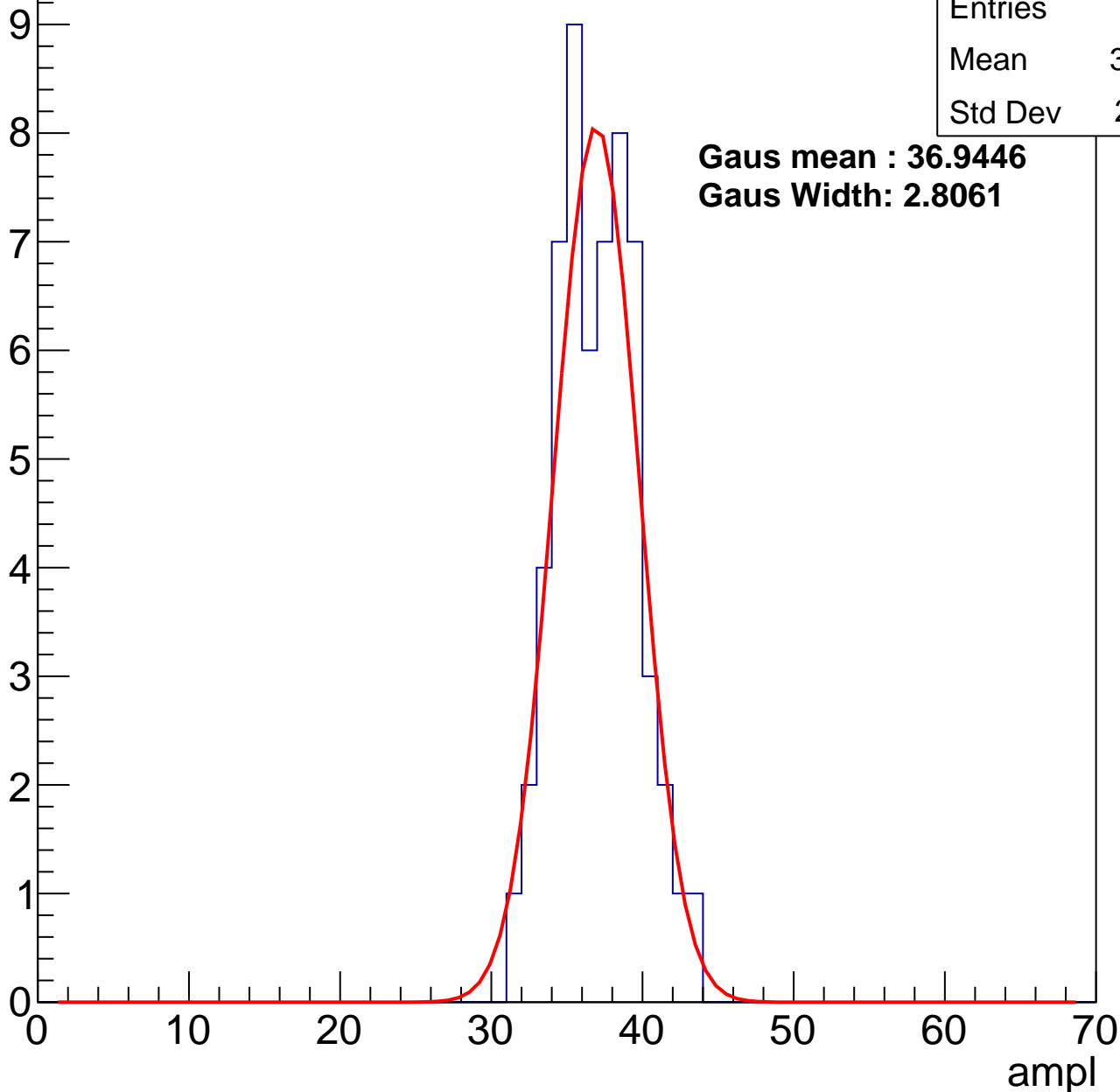
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	36.53
Std Dev	2.621

**Gaus mean : 36.9446**

**Gaus Width: 2.8061**



# B1L002S, U8-ch37, adc2

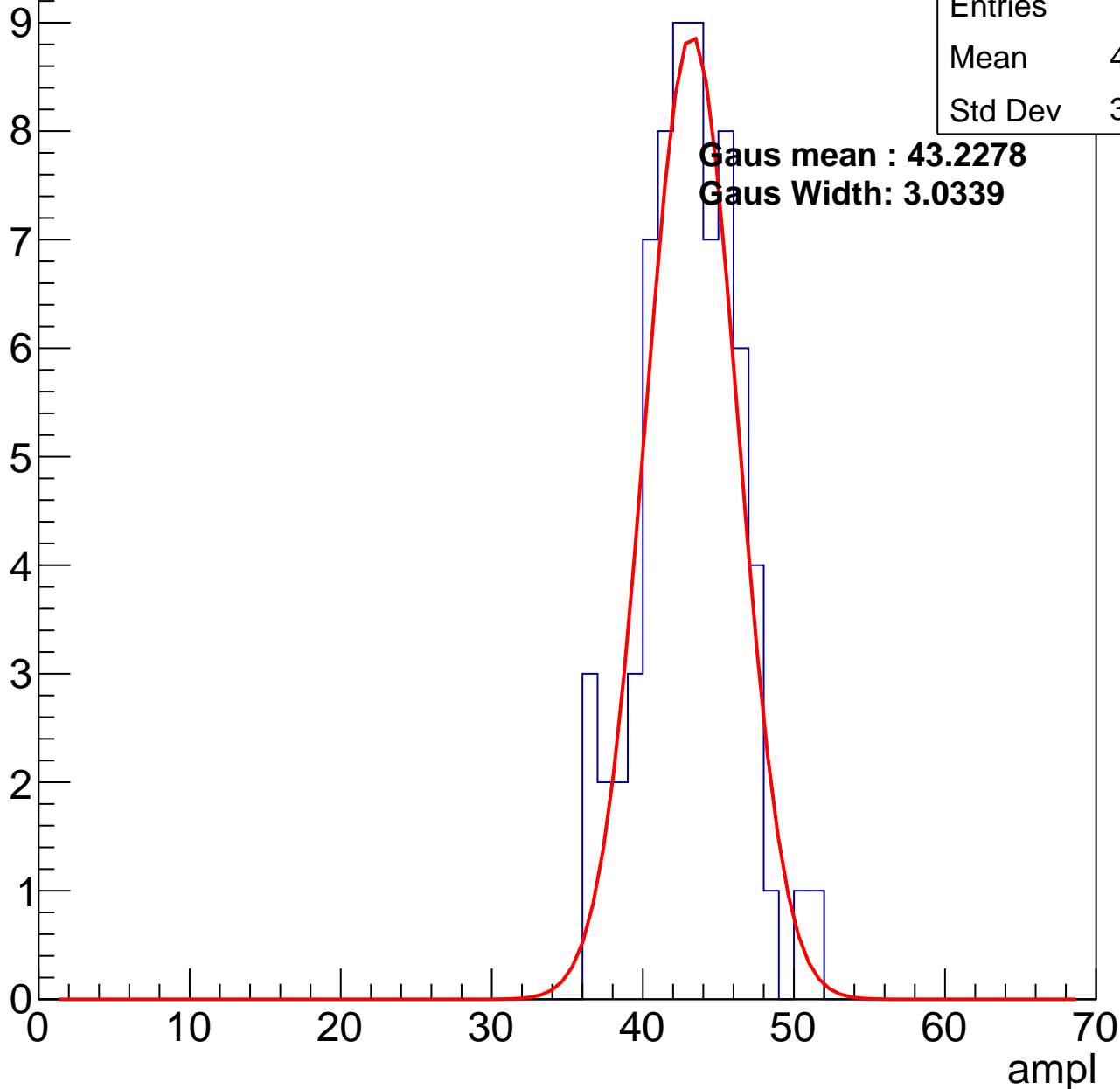
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	42.66
Std Dev	3.158

**Gaus mean : 43.2278**

**Gaus Width: 3.0339**

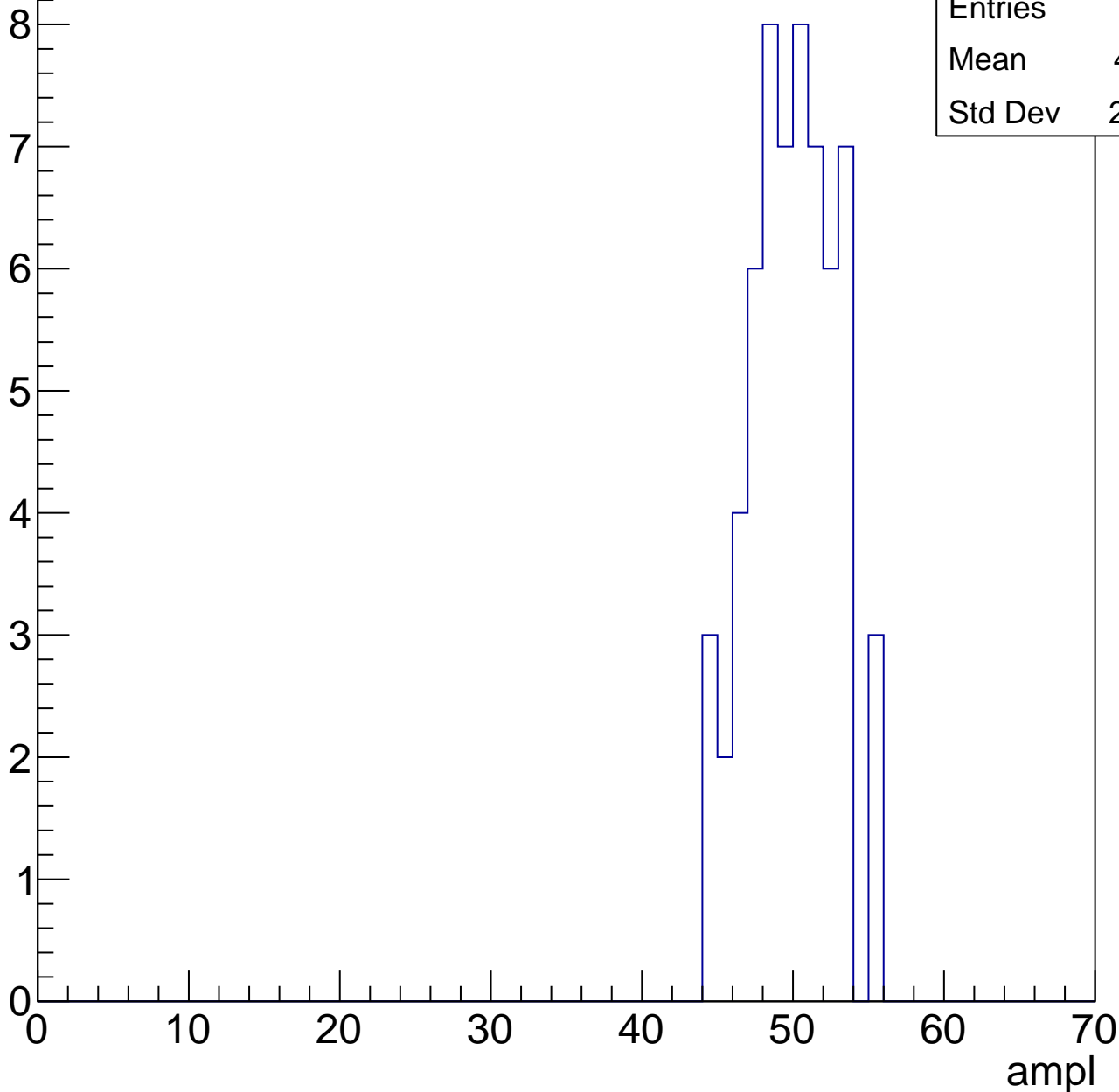


# B1L002S, U8-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

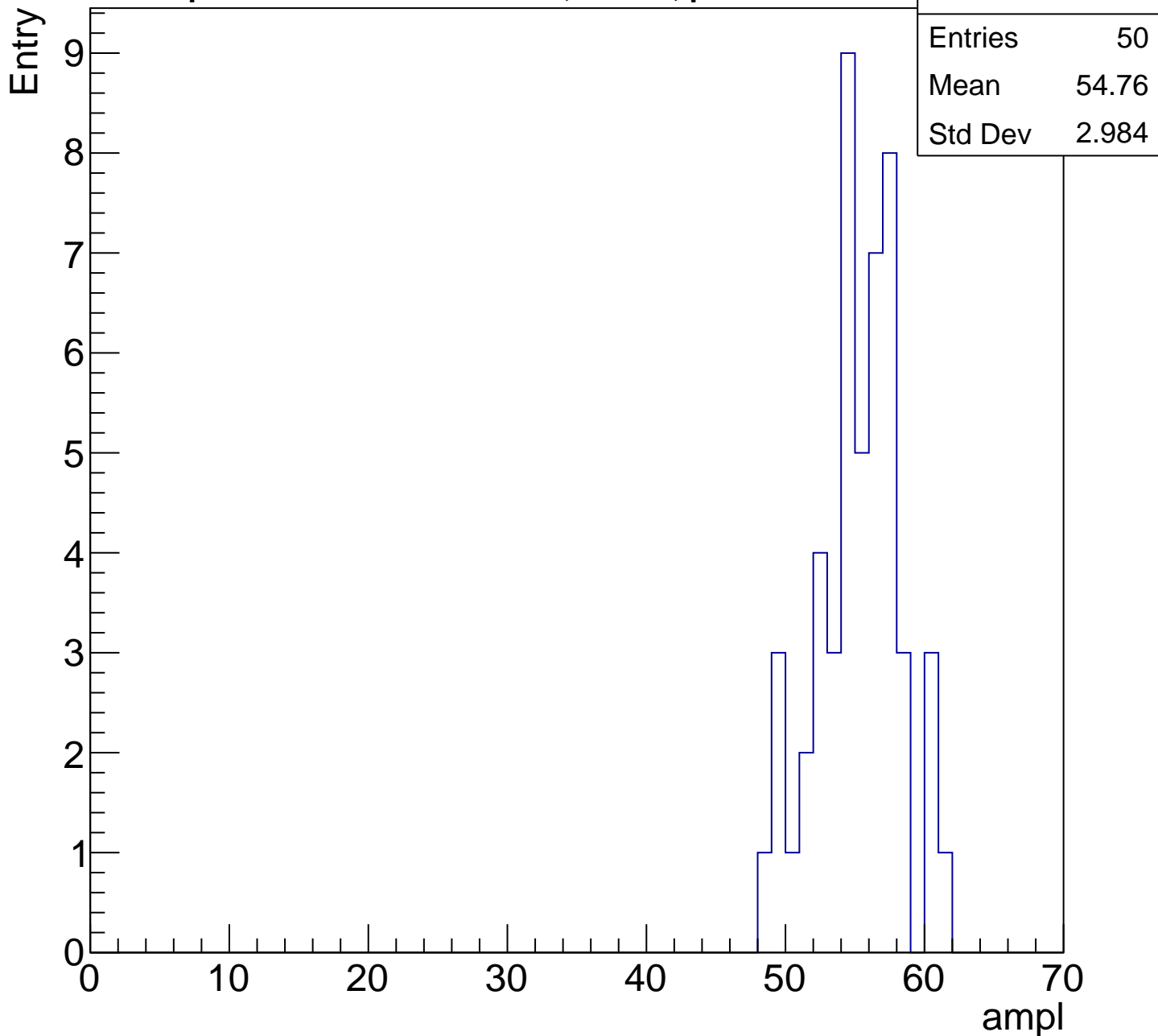
Entry

Entries	61
Mean	49.51
Std Dev	2.774



# B1L002S, U8-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

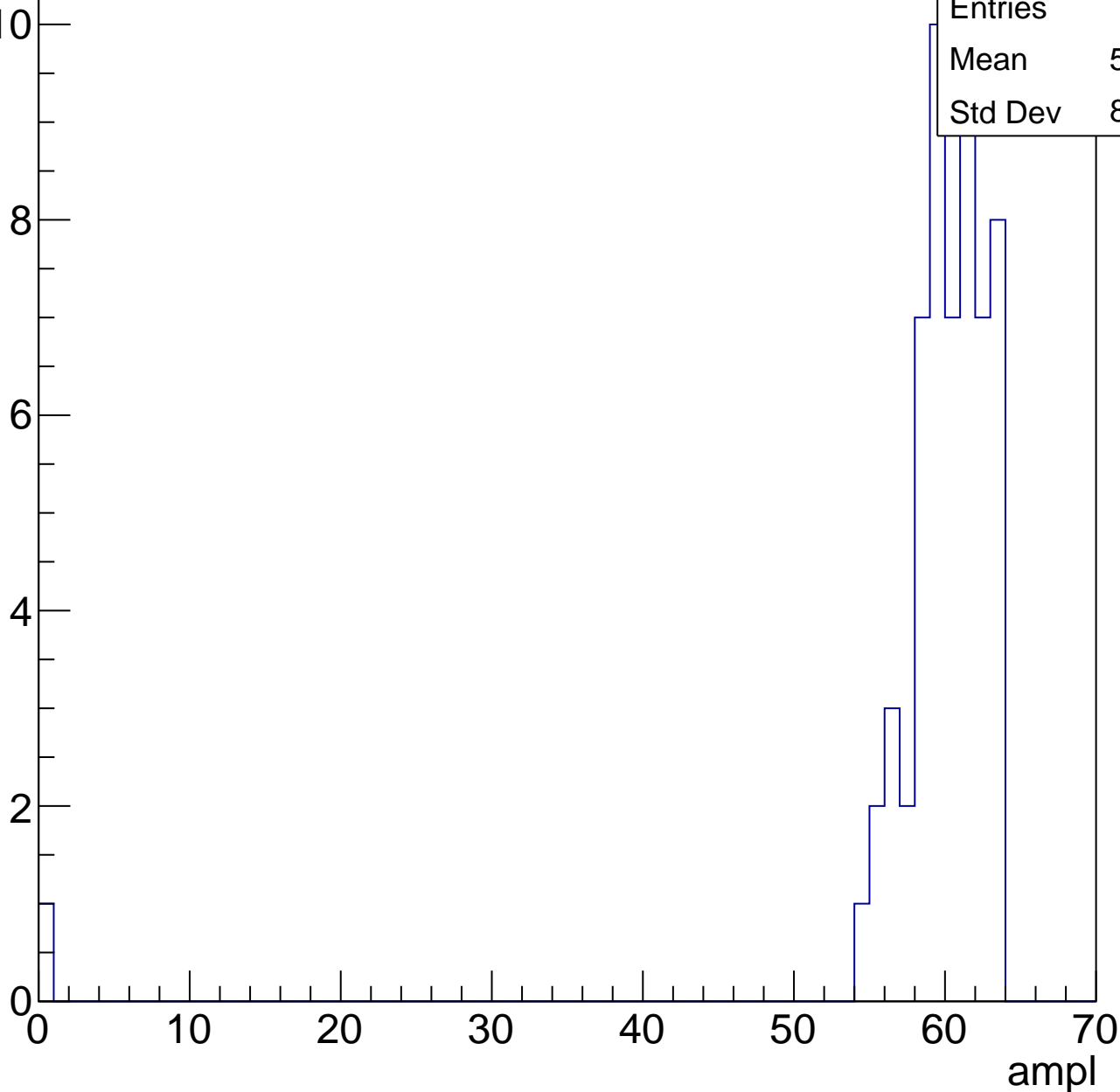


# B1L002S, U8-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

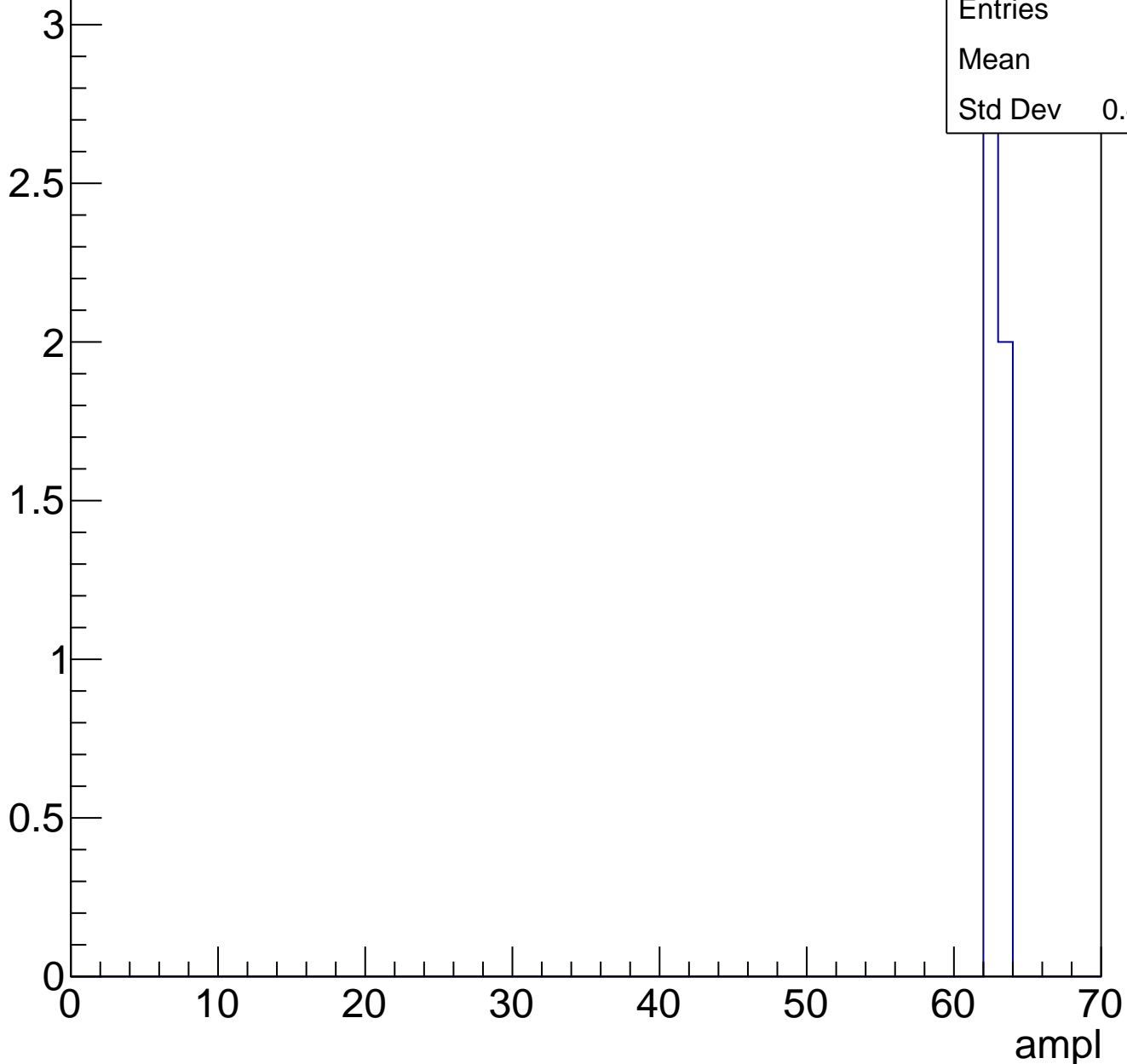
Entries	57
Mean	58.75
Std Dev	8.176



# B1L002S, U8-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch38, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	29.63
Std Dev	2.812

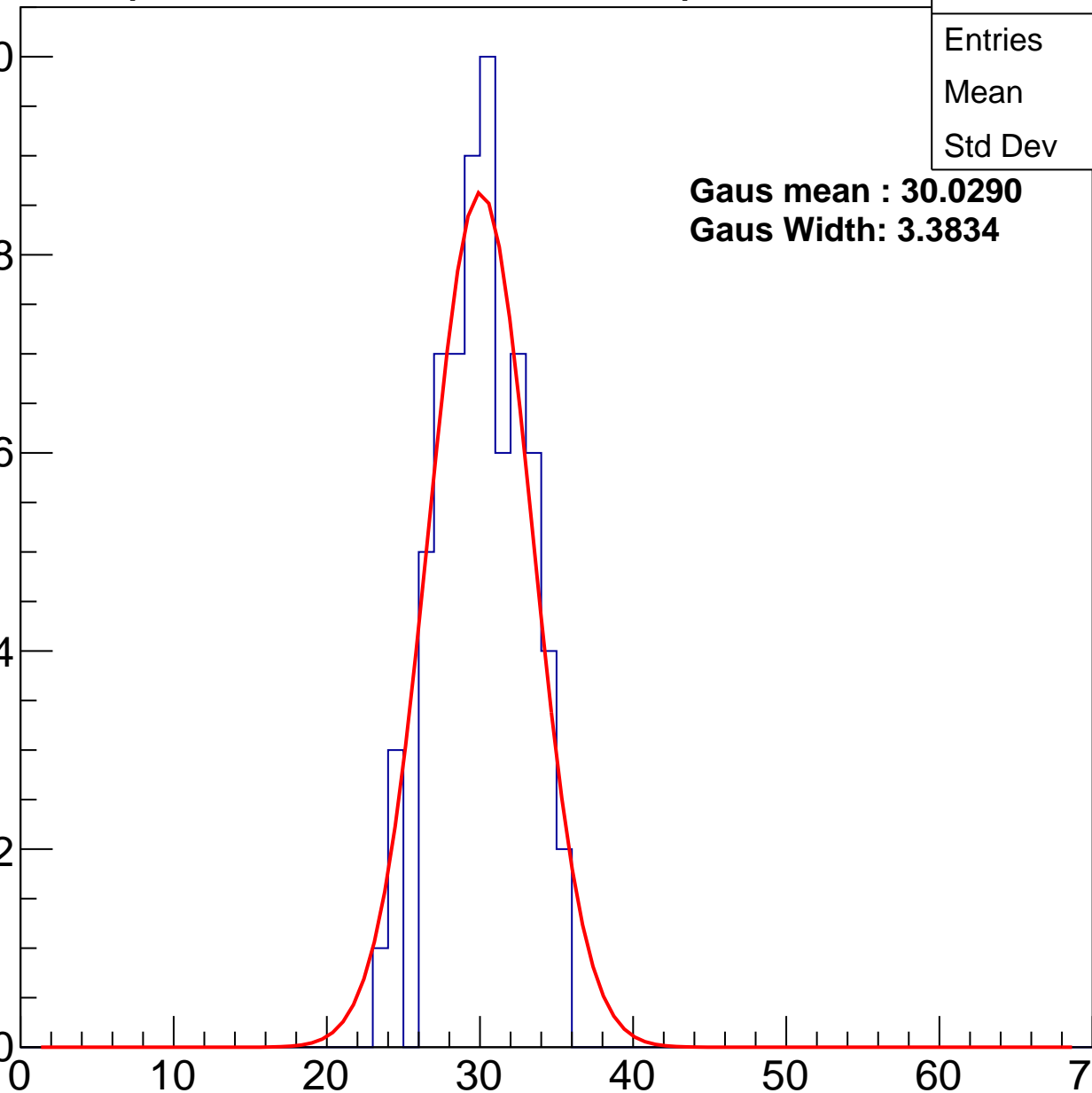
**Gaus mean : 30.0290**

**Gaus Width: 3.3834**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U8-ch38, adc1

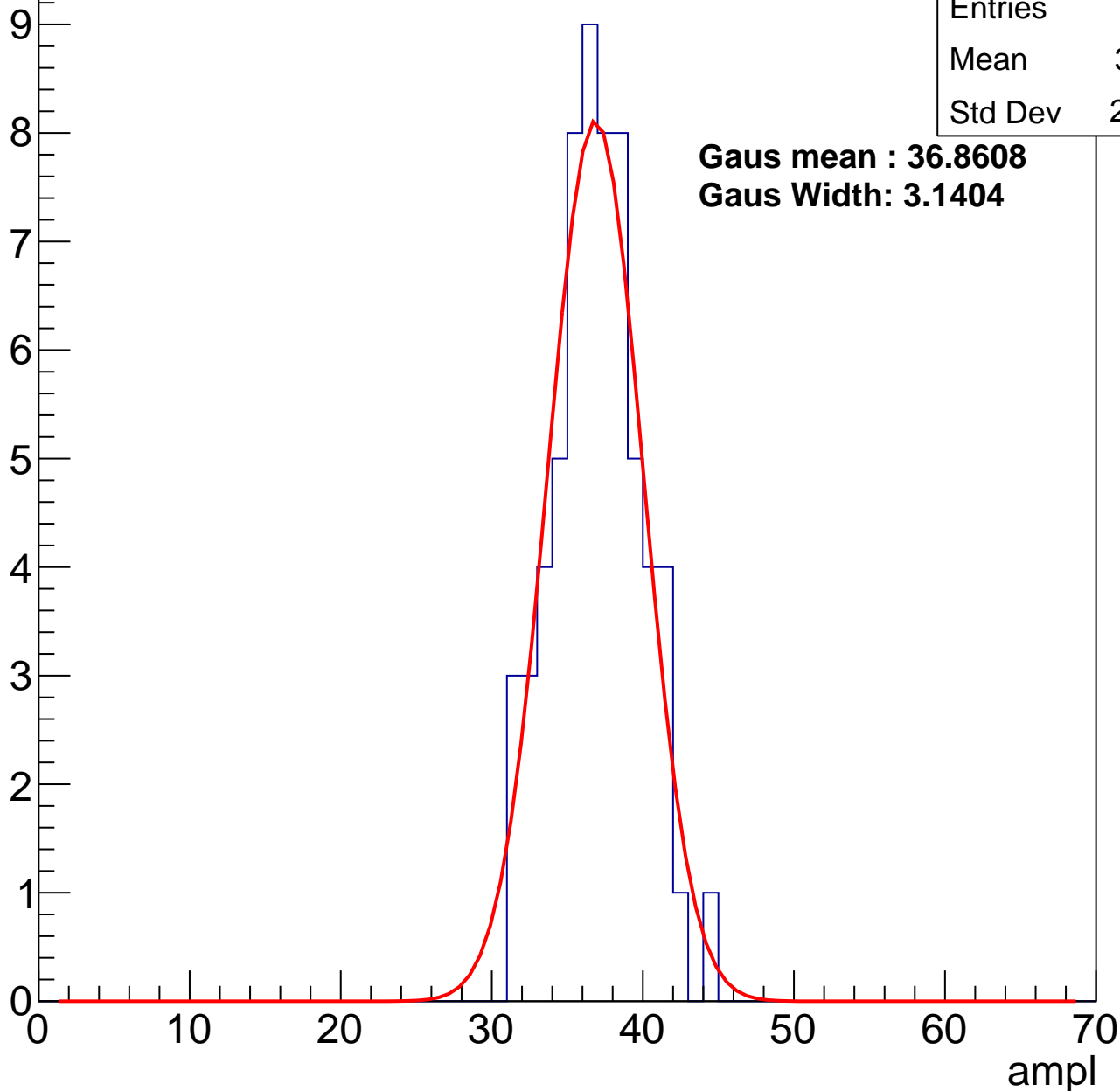
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.51
Std Dev	2.872

**Gaus mean : 36.8608**

**Gaus Width: 3.1404**



# B1L002S, U8-ch38, adc2

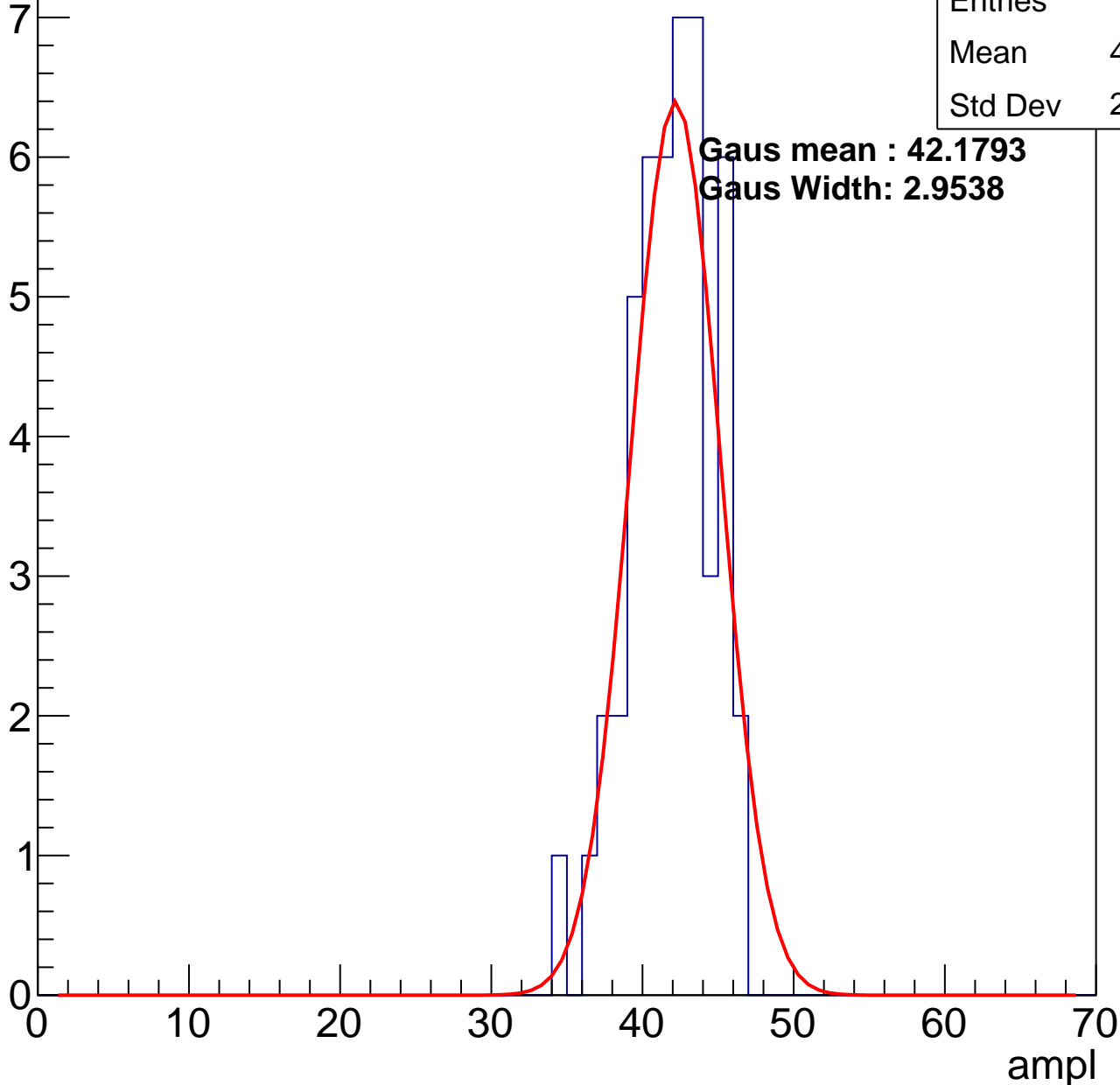
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	41.46
Std Dev	2.692

**Gaus mean : 42.1793**

**Gaus Width: 2.9538**

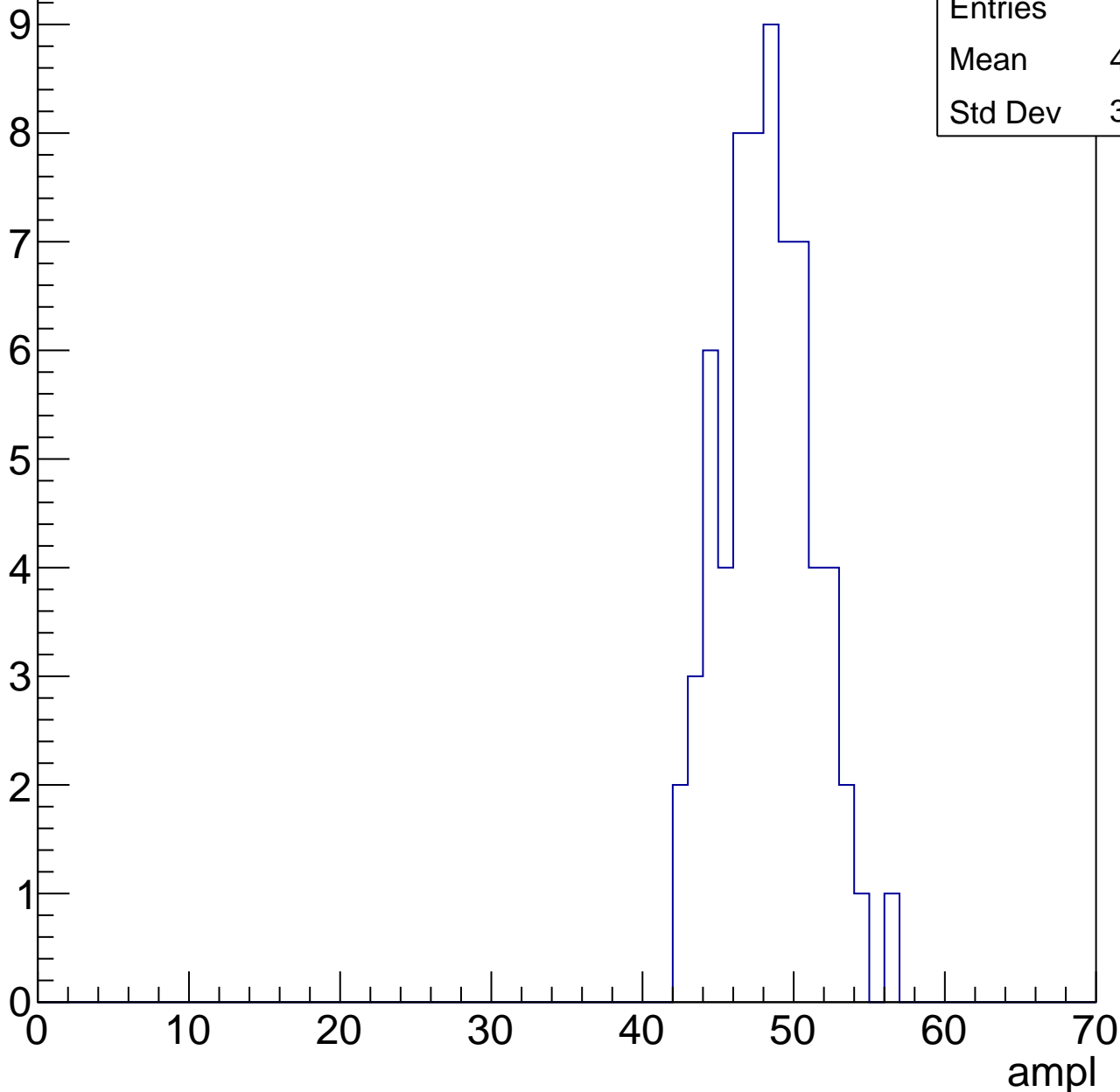


# B1L002S, U8-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

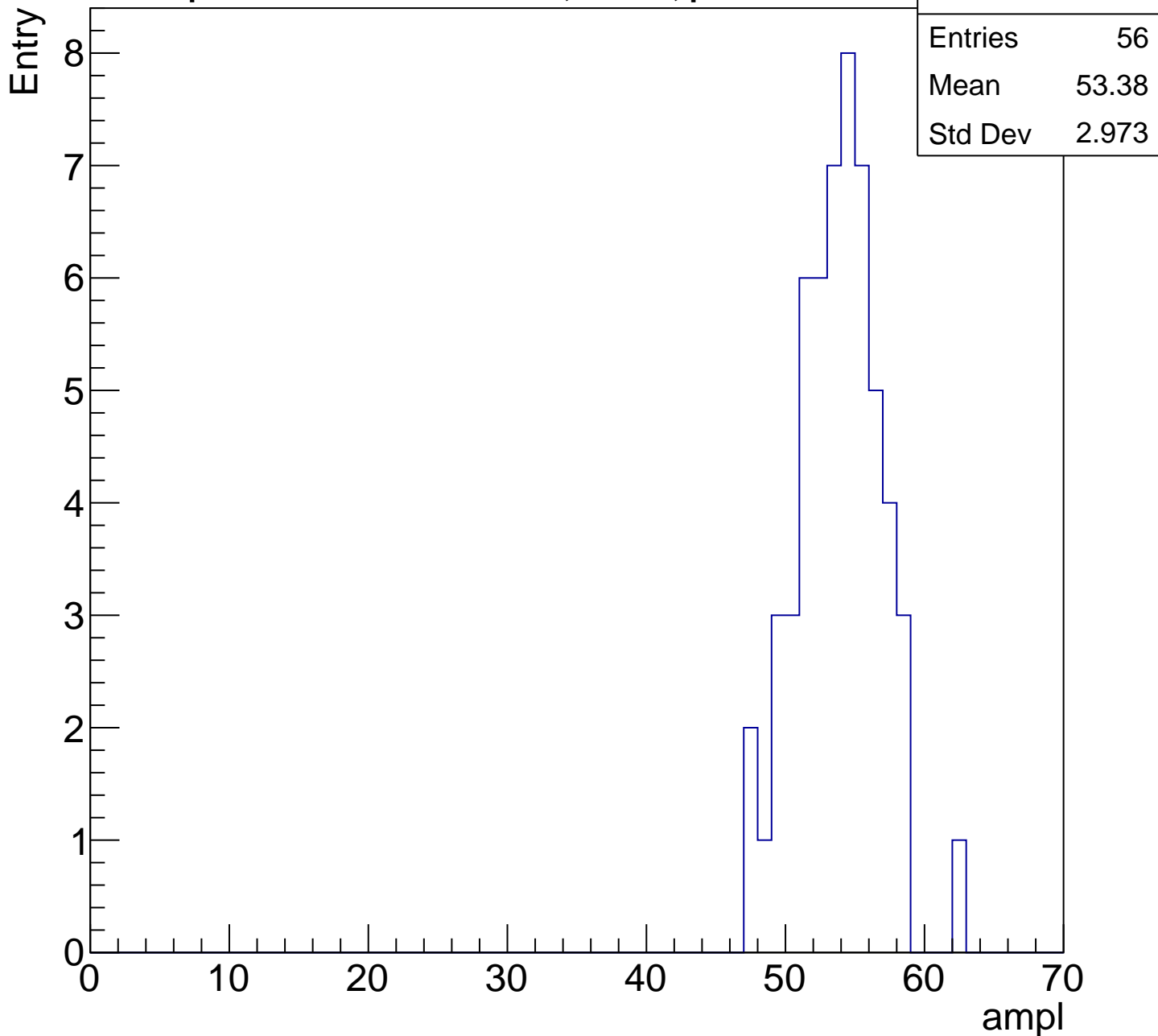
Entry

Entries	66
Mean	47.79
Std Dev	3.018



# B1L002S, U8-ch38, adc4

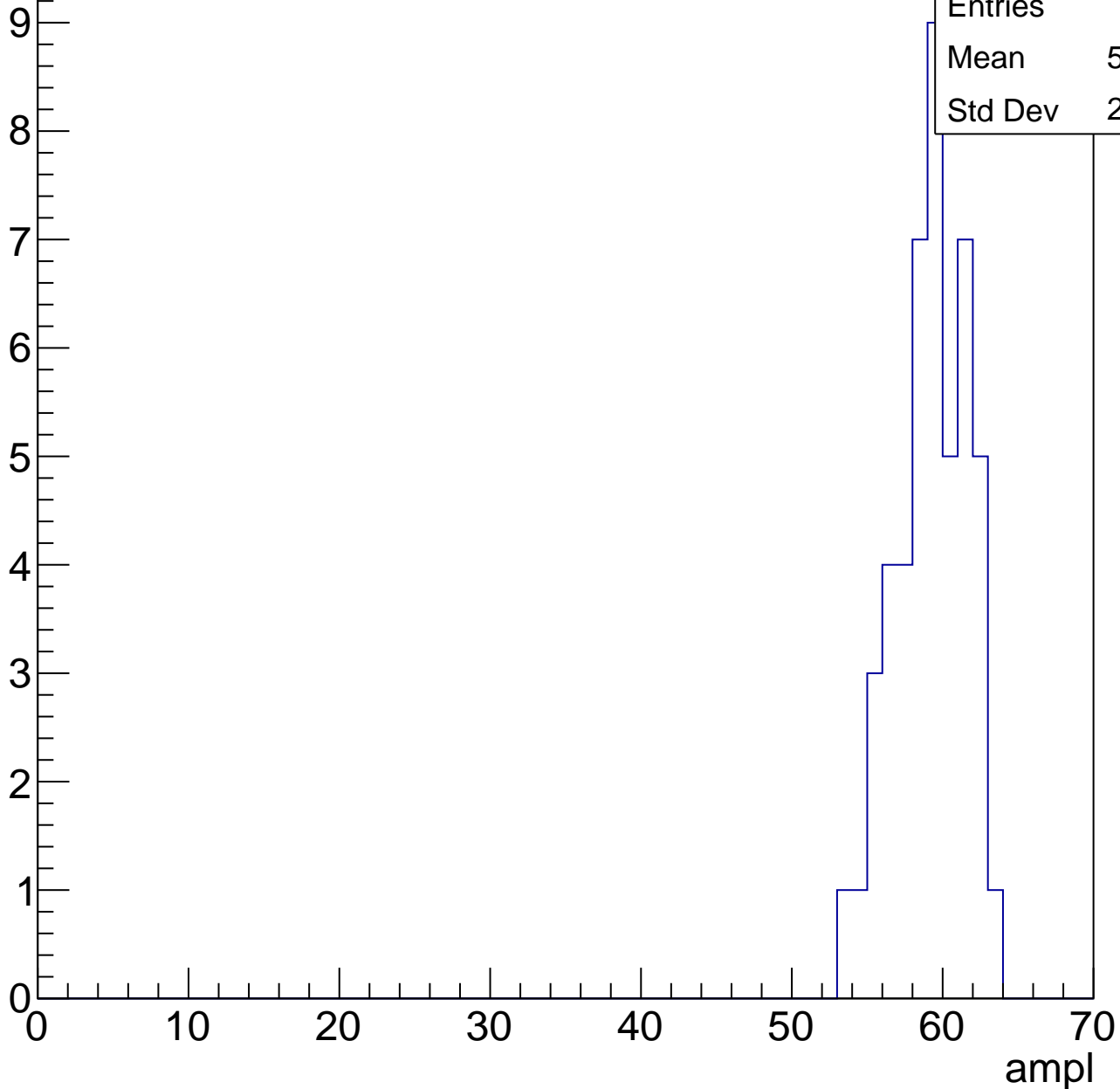
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



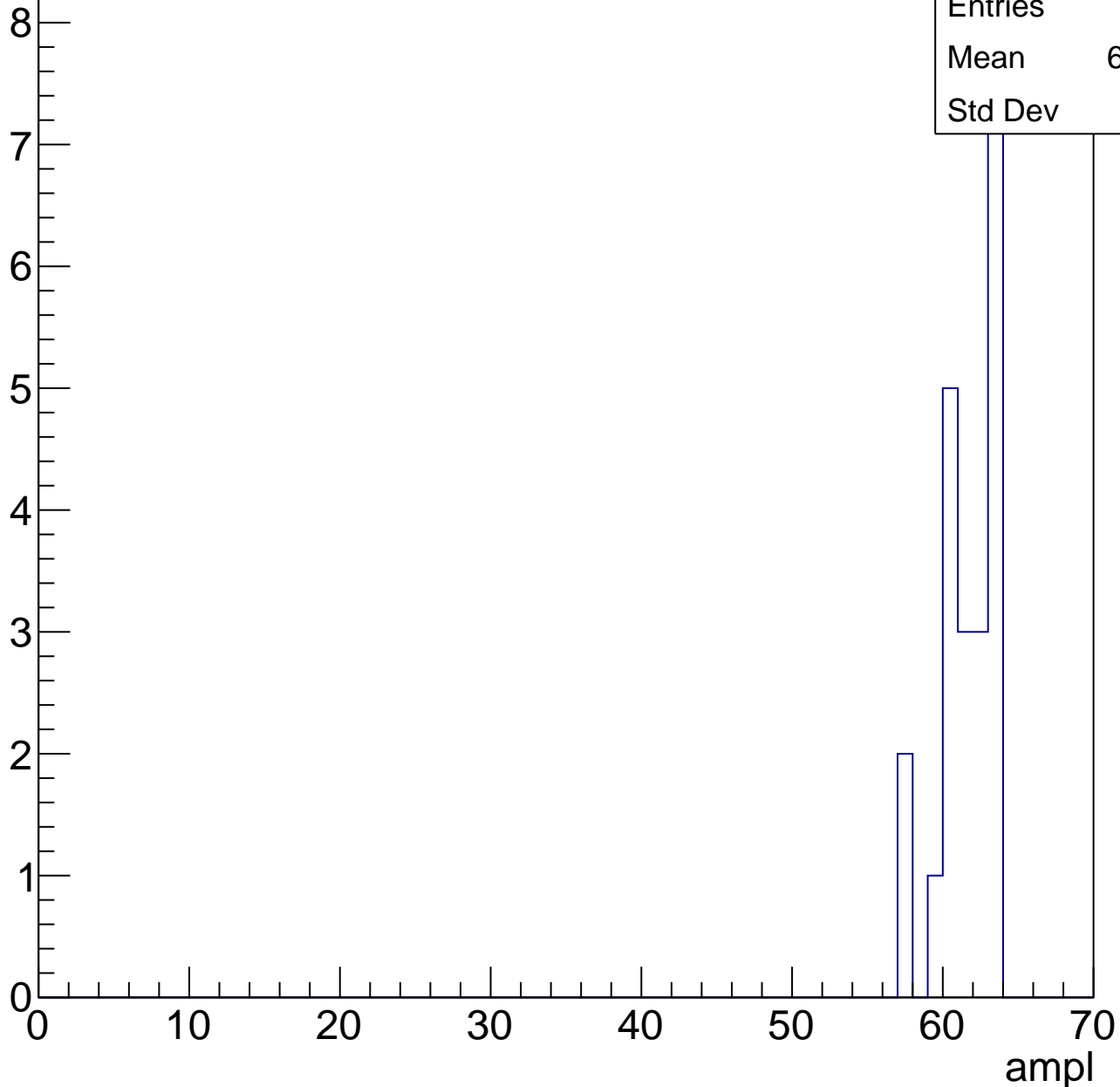
Entries	47
Mean	58.74
Std Dev	2.347

# B1L002S, U8-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	61.18
Std Dev	1.85

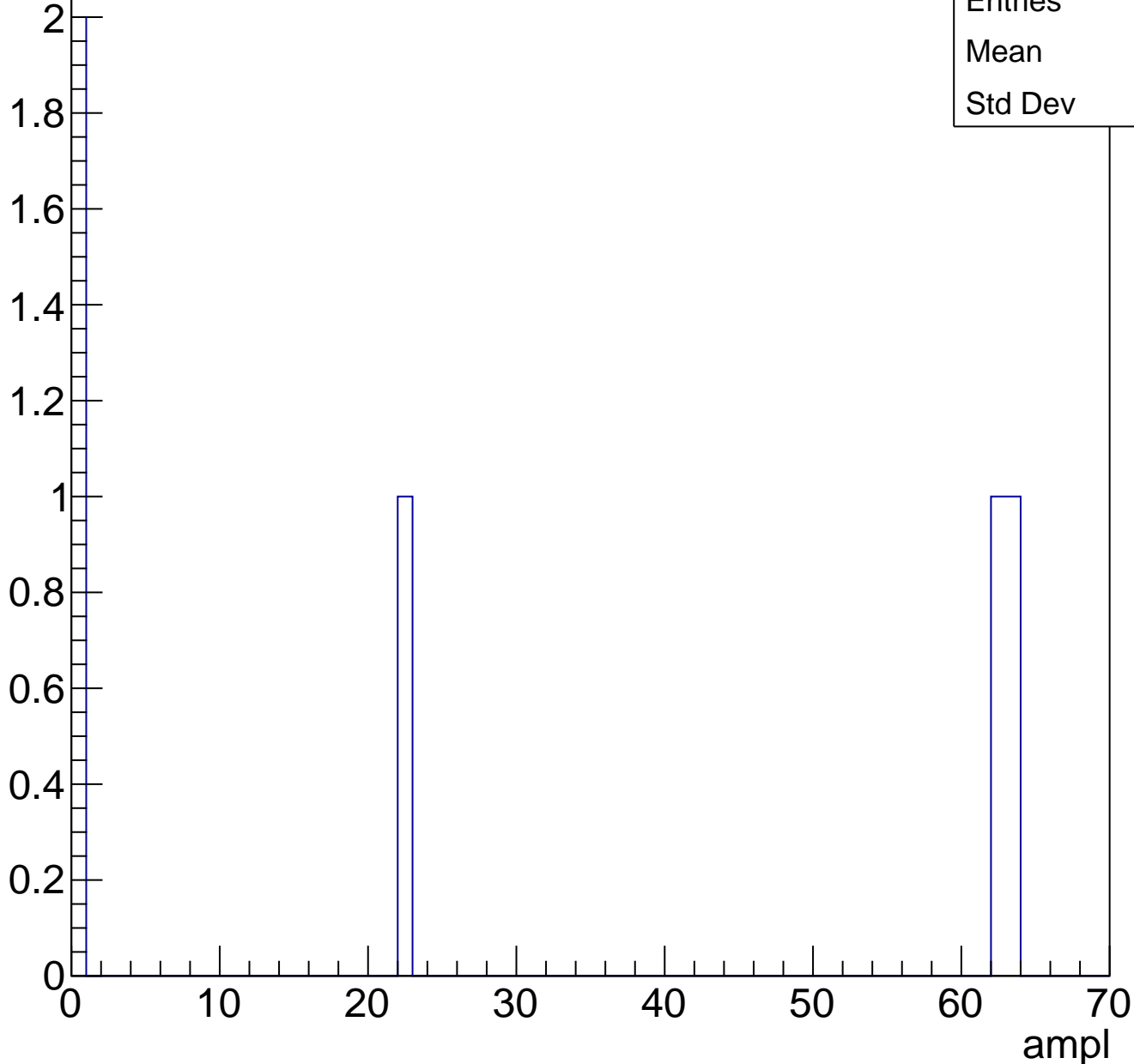




# B1L002S, U8-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	29.4
Std Dev	28.2

# B1L002S, U8-ch39, adc0

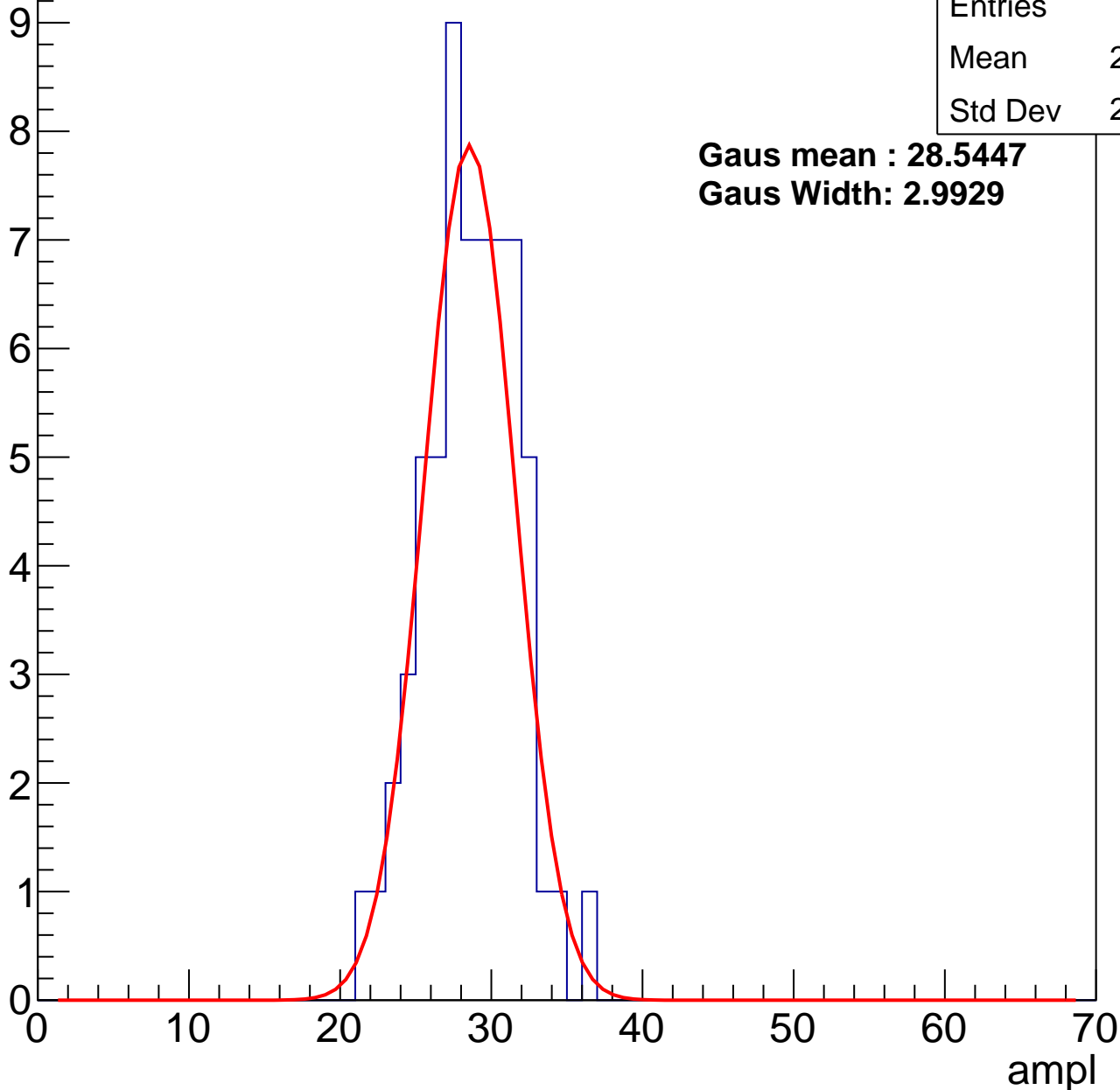
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.19
Std Dev	2.999

**Gaus mean : 28.5447**

**Gaus Width: 2.9929**



# B1L002S, U8-ch39, adc1

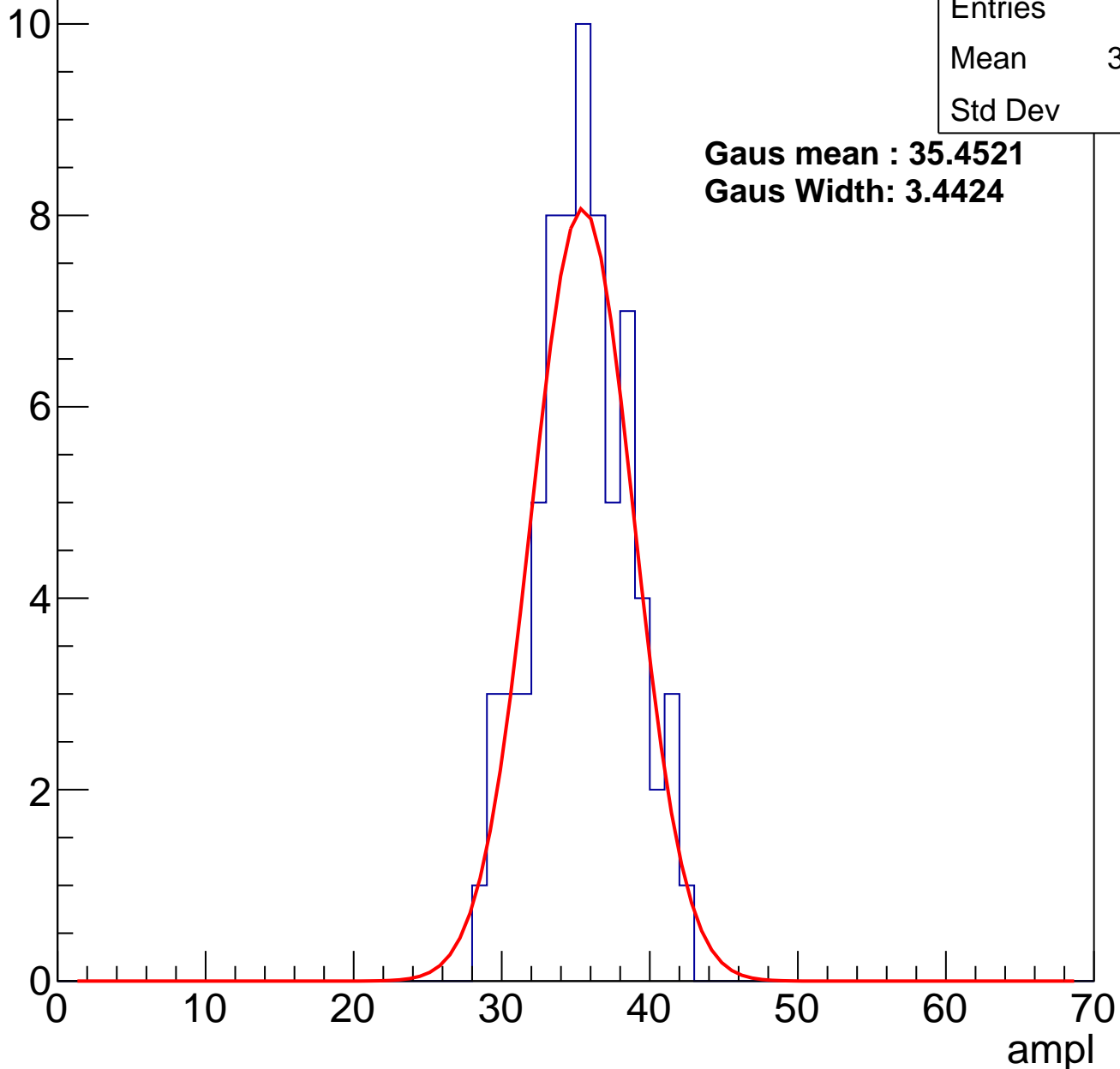
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	34.99
Std Dev	3.2

**Gaus mean : 35.4521**

**Gaus Width: 3.4424**

Entry



# B1L002S, U8-ch39, adc2

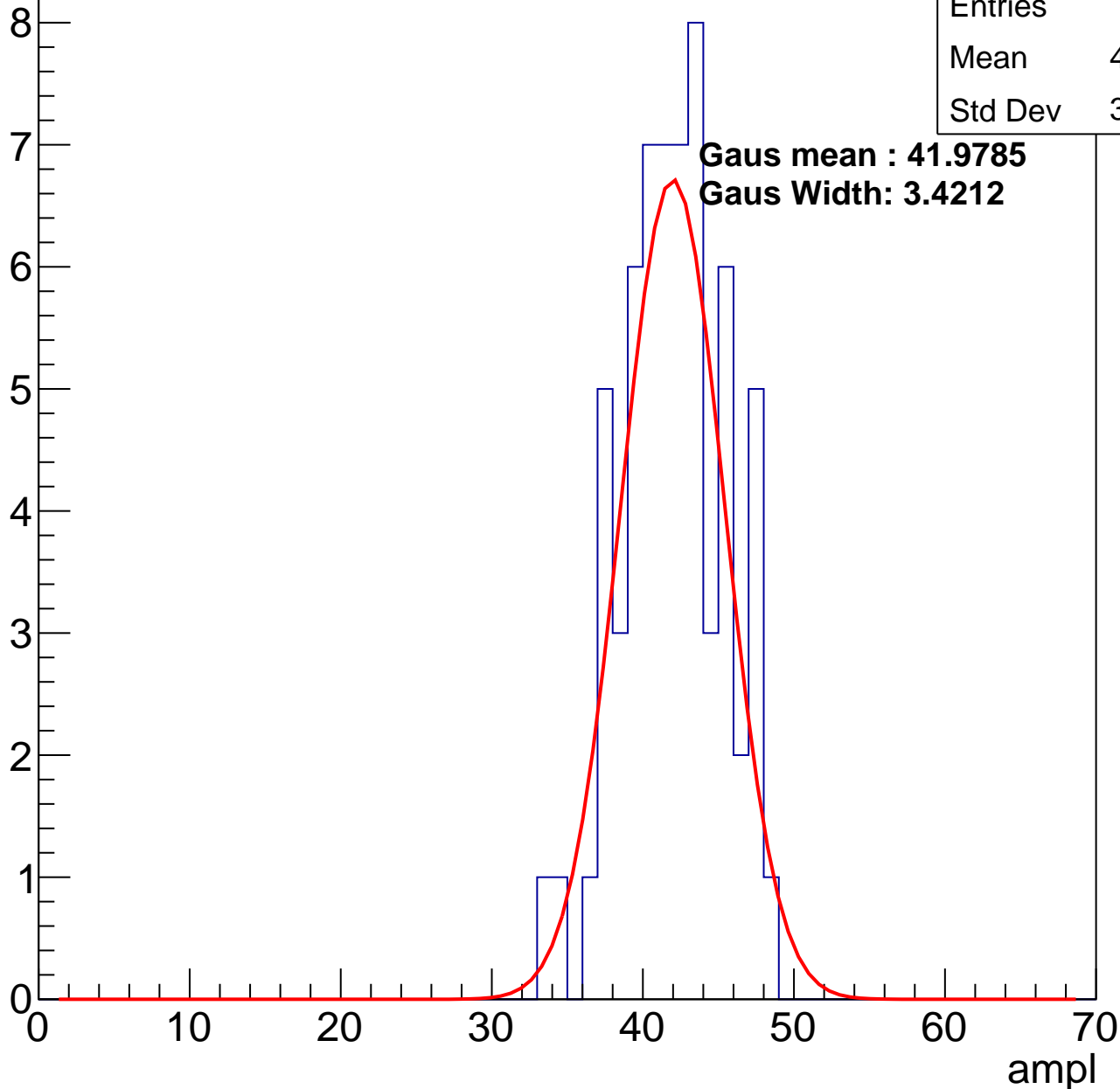
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.56
Std Dev	3.328

**Gaus mean : 41.9785**

**Gaus Width: 3.4212**

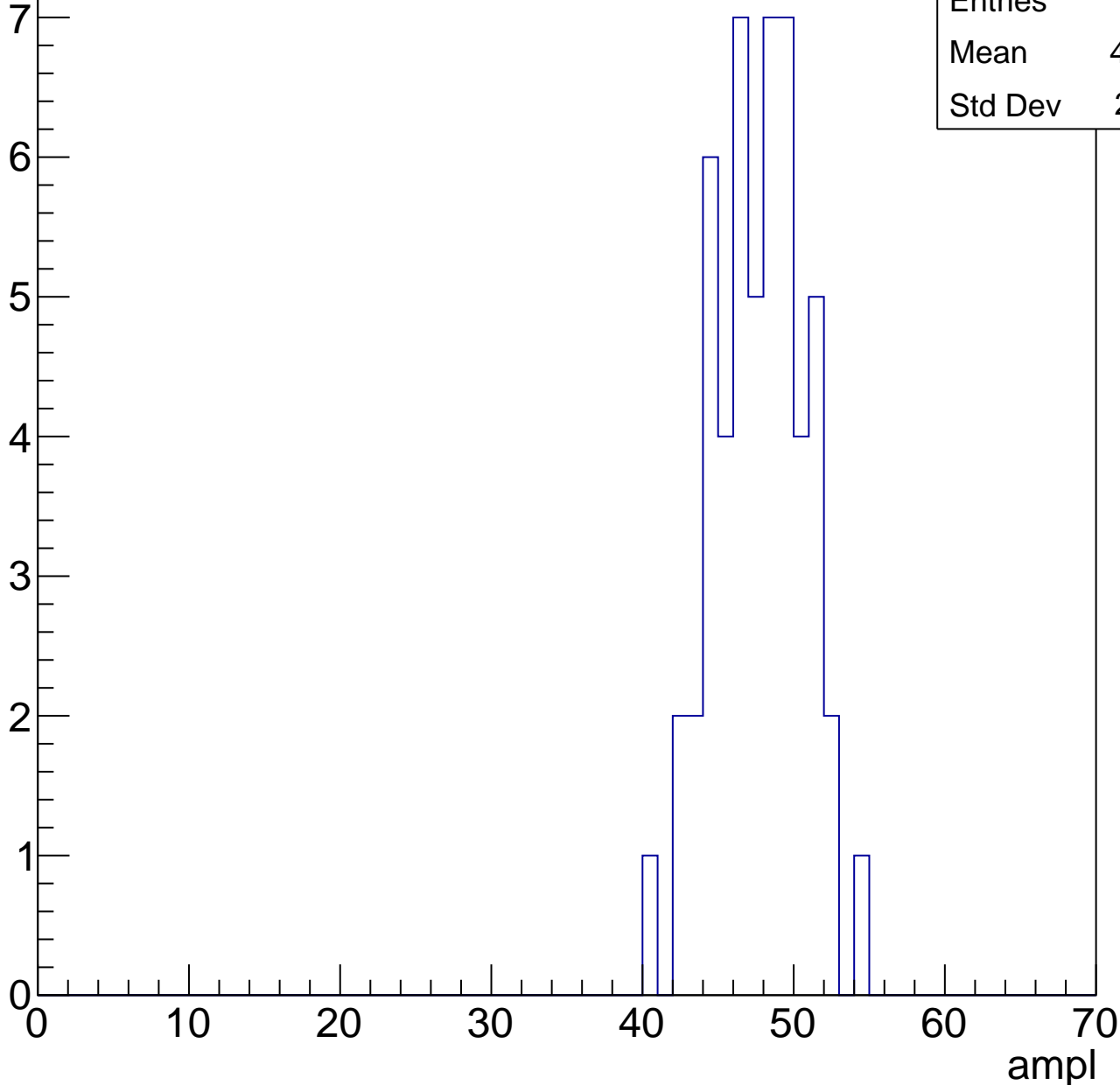


# B1L002S, U8-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	47.23
Std Dev	2.931

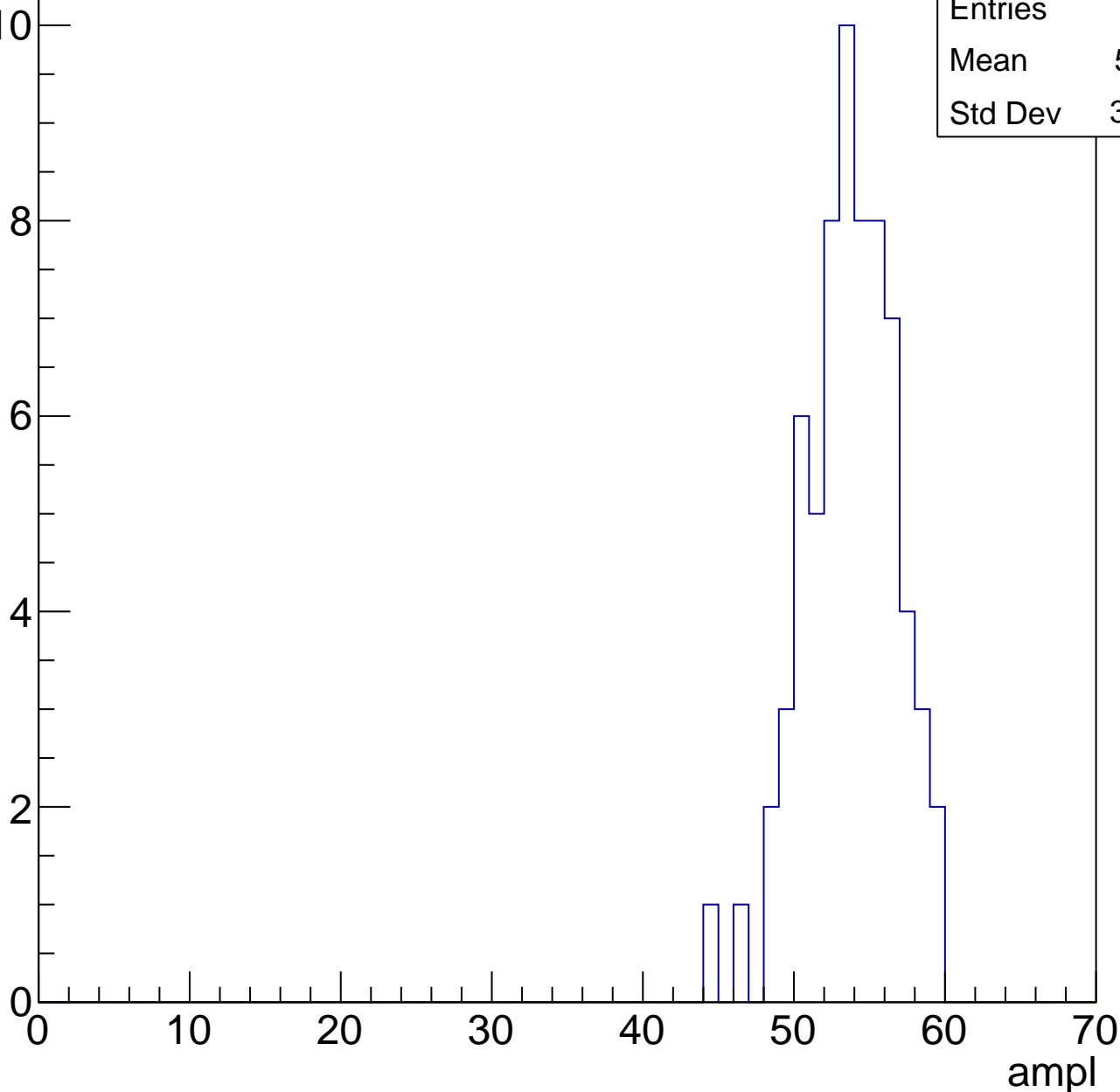


# B1L002S, U8-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

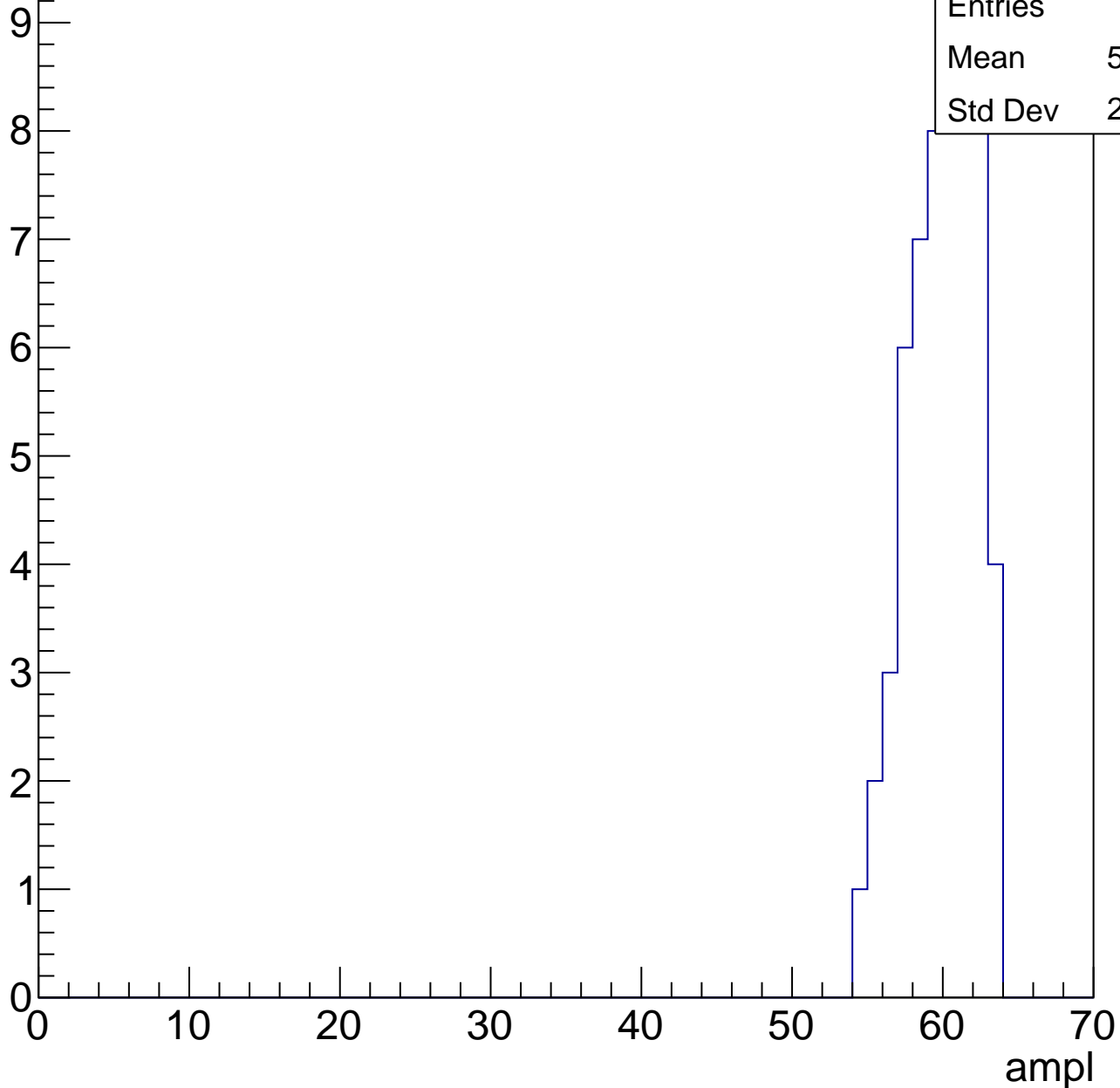
Entries	68
Mean	53.21
Std Dev	3.022



# B1L002S, U8-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

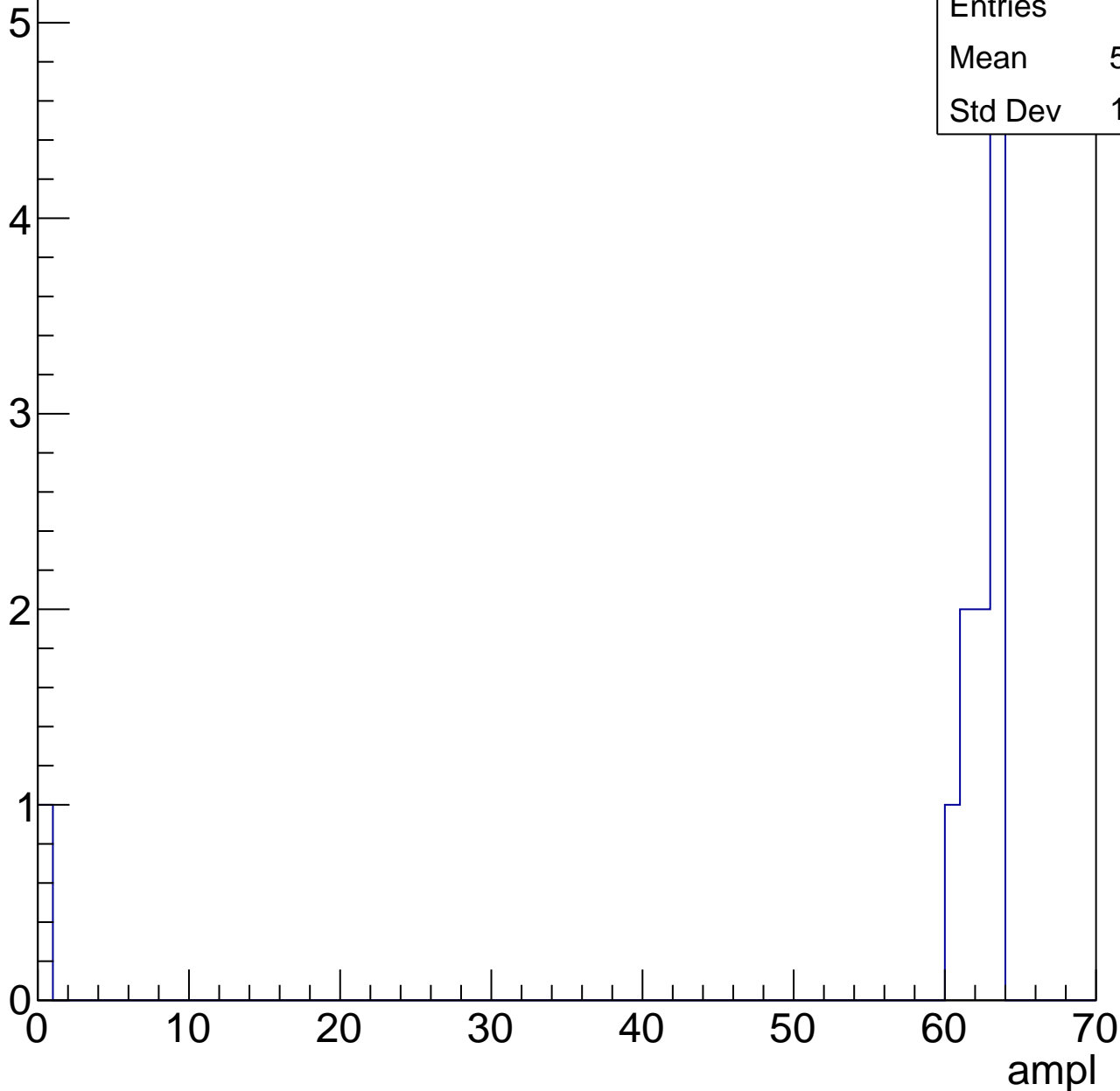


# B1L002S, U8-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	56.45
Std Dev	17.88





# B1L002S, U8-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch40, adc0

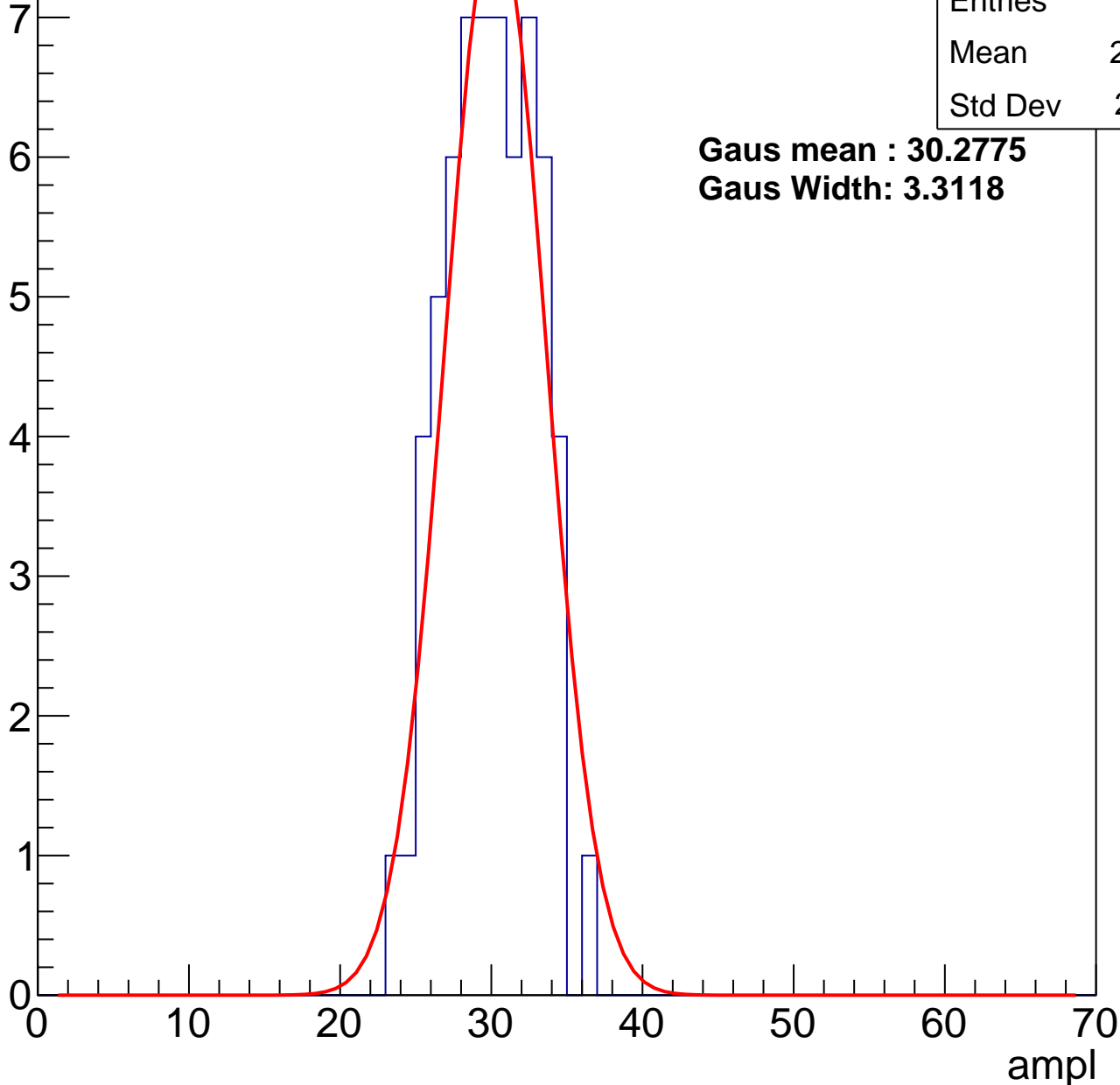
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.48
Std Dev	2.911

**Gaus mean : 30.2775**

**Gaus Width: 3.3118**



# B1L002S, U8-ch40, adc1

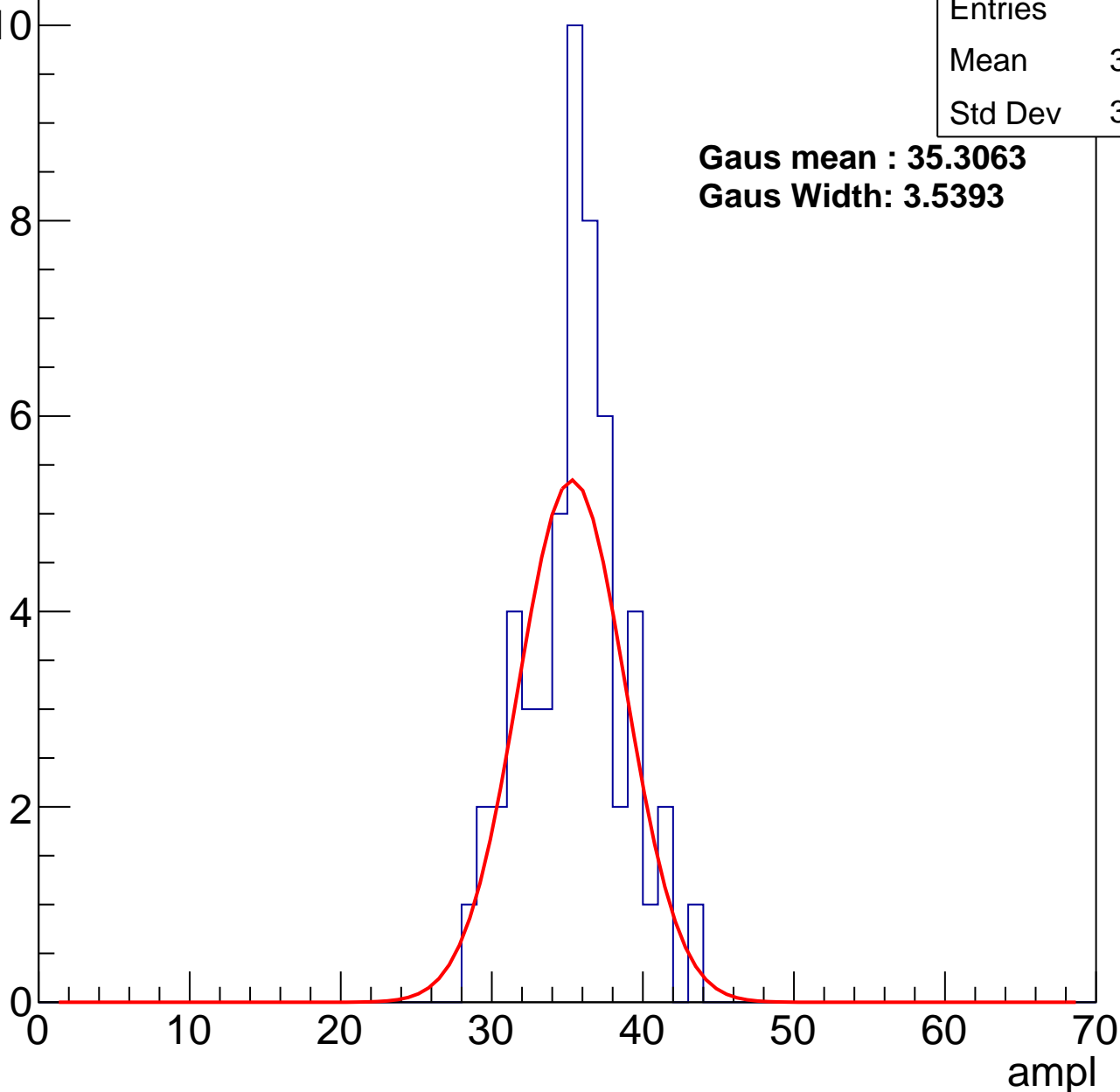
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	35.04
Std Dev	3.203

**Gaus mean : 35.3063**

**Gaus Width: 3.5393**



# B1L002S, U8-ch40, adc2

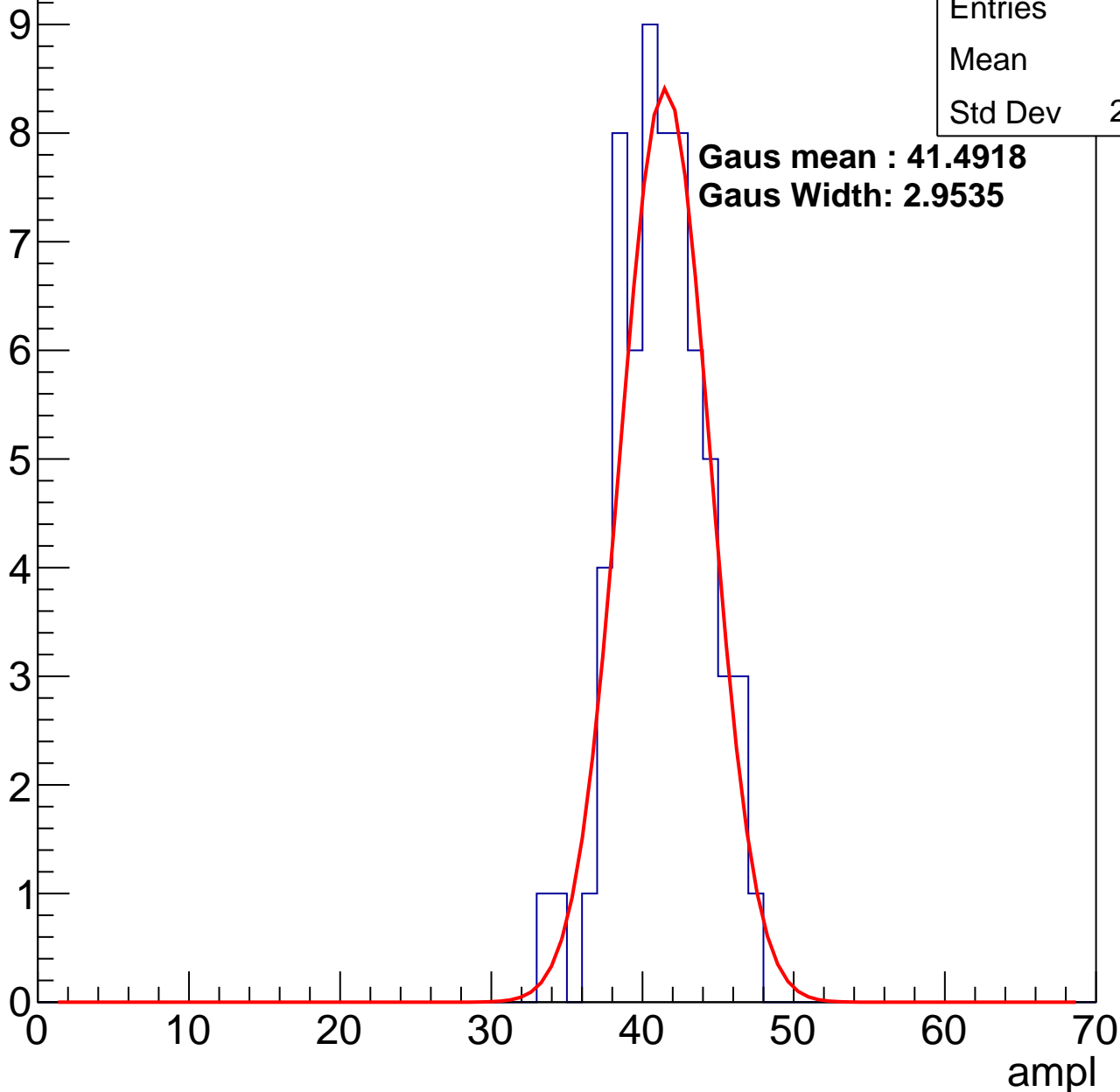
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.8
Std Dev	2.906

**Gaus mean : 41.4918**

**Gaus Width: 2.9535**

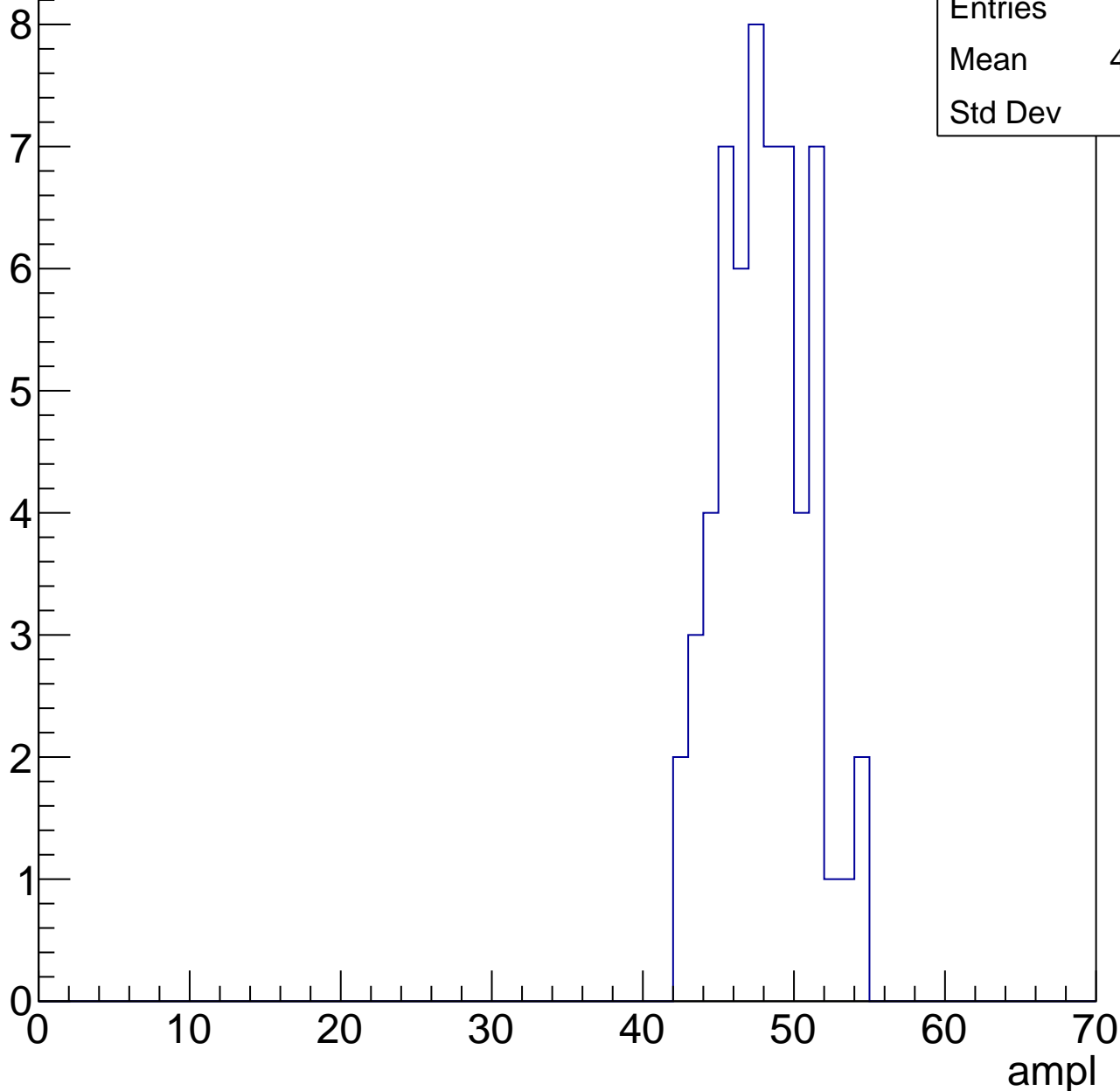


# B1L002S, U8-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

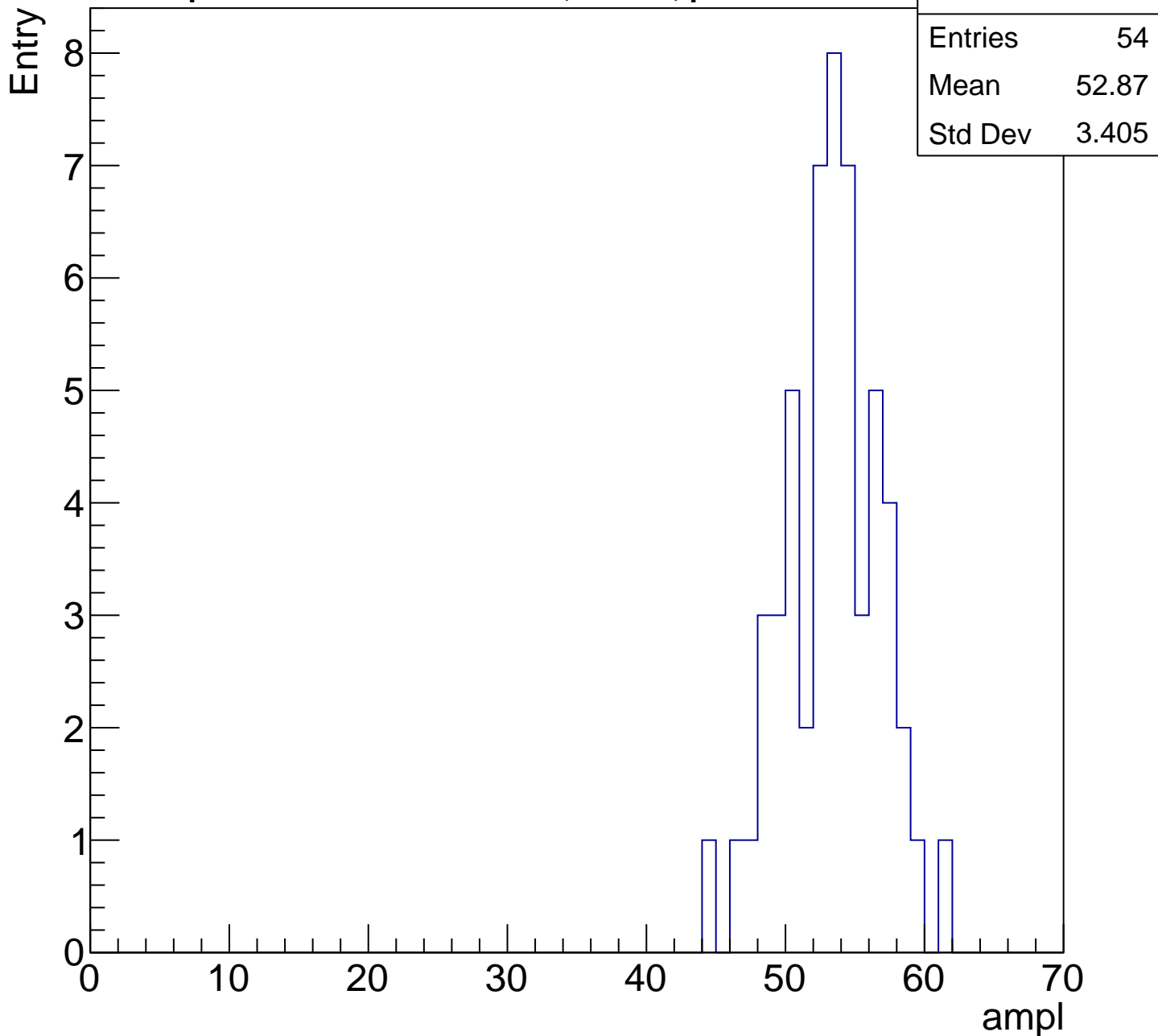
Entry

Entries	59
Mean	47.54
Std Dev	2.89



# B1L002S, U8-ch40, adc4

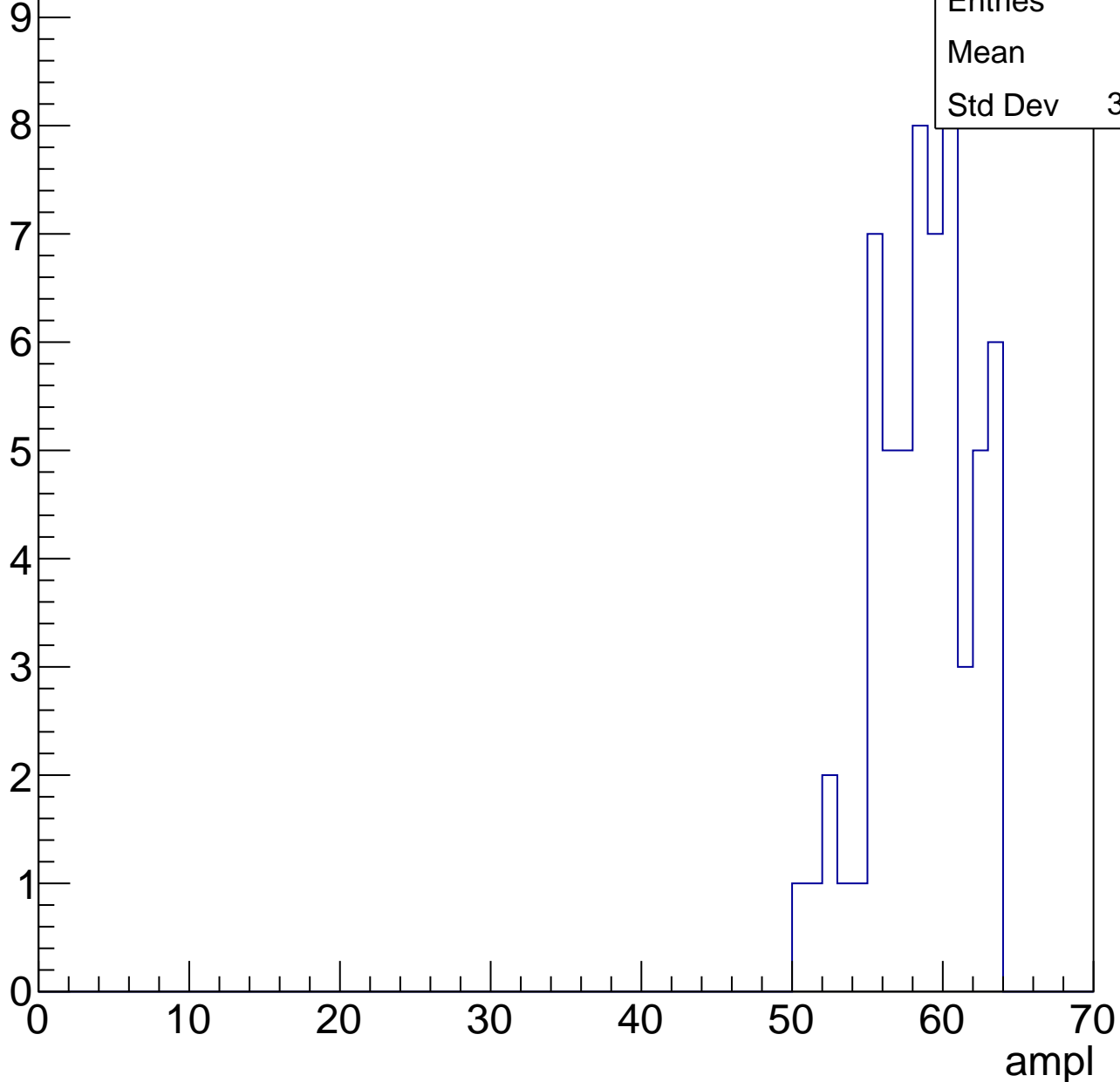
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

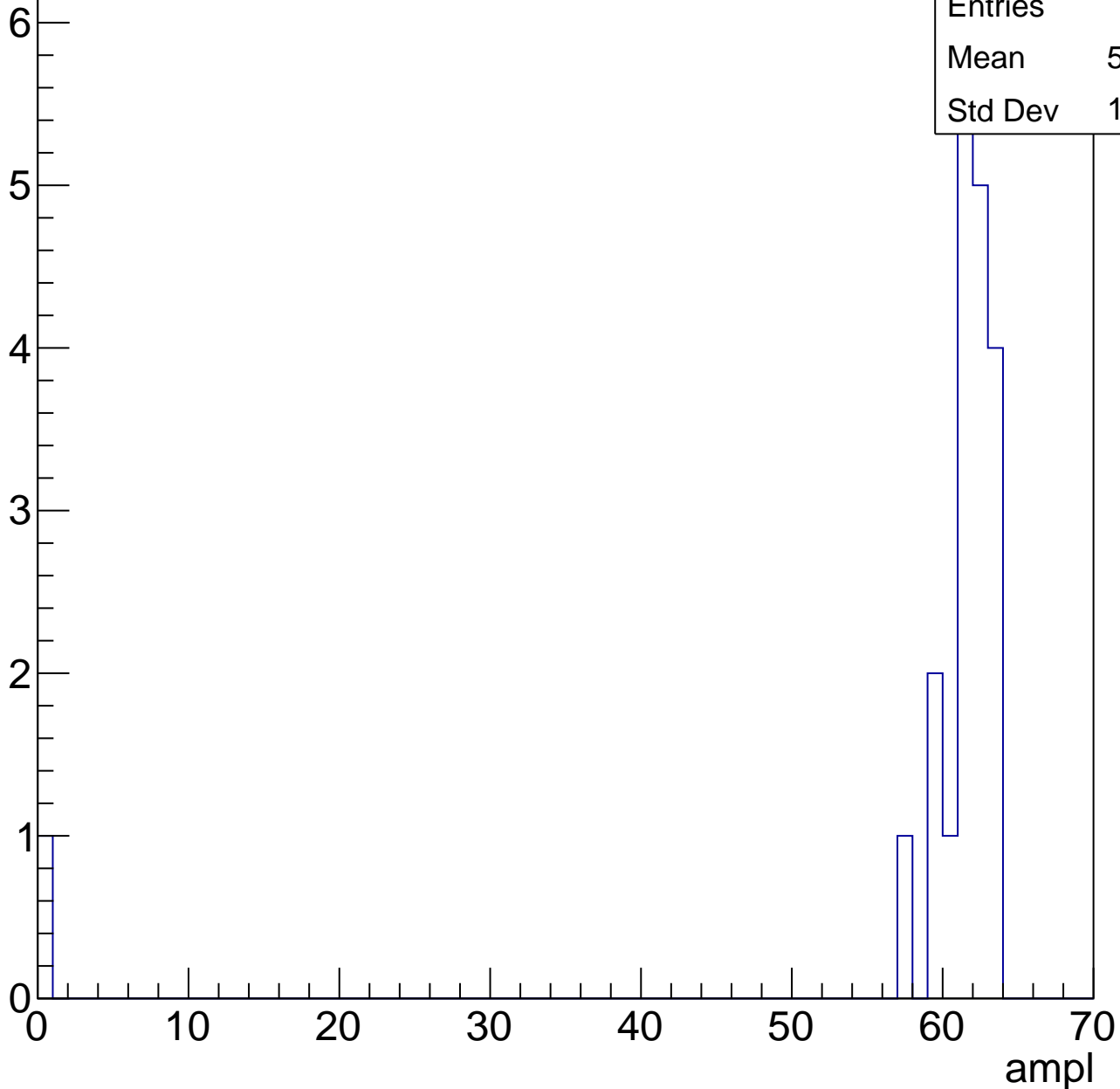


# B1L002S, U8-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.15
Std Dev	13.42





# B1L002S, U8-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch41, adc0

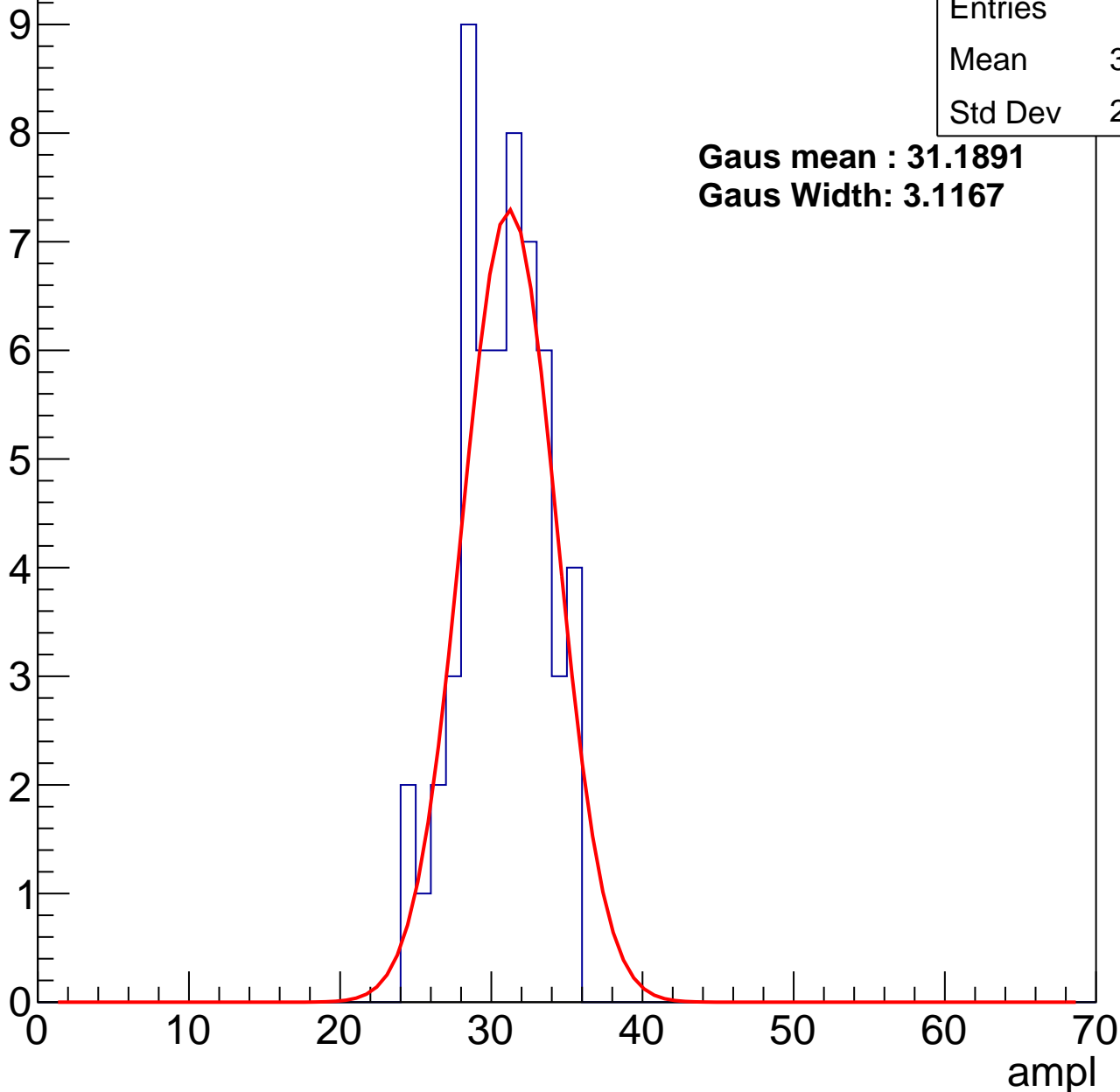
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	30.25
Std Dev	2.755

**Gaus mean : 31.1891**

**Gaus Width: 3.1167**



# B1L002S, U8-ch41, adc1

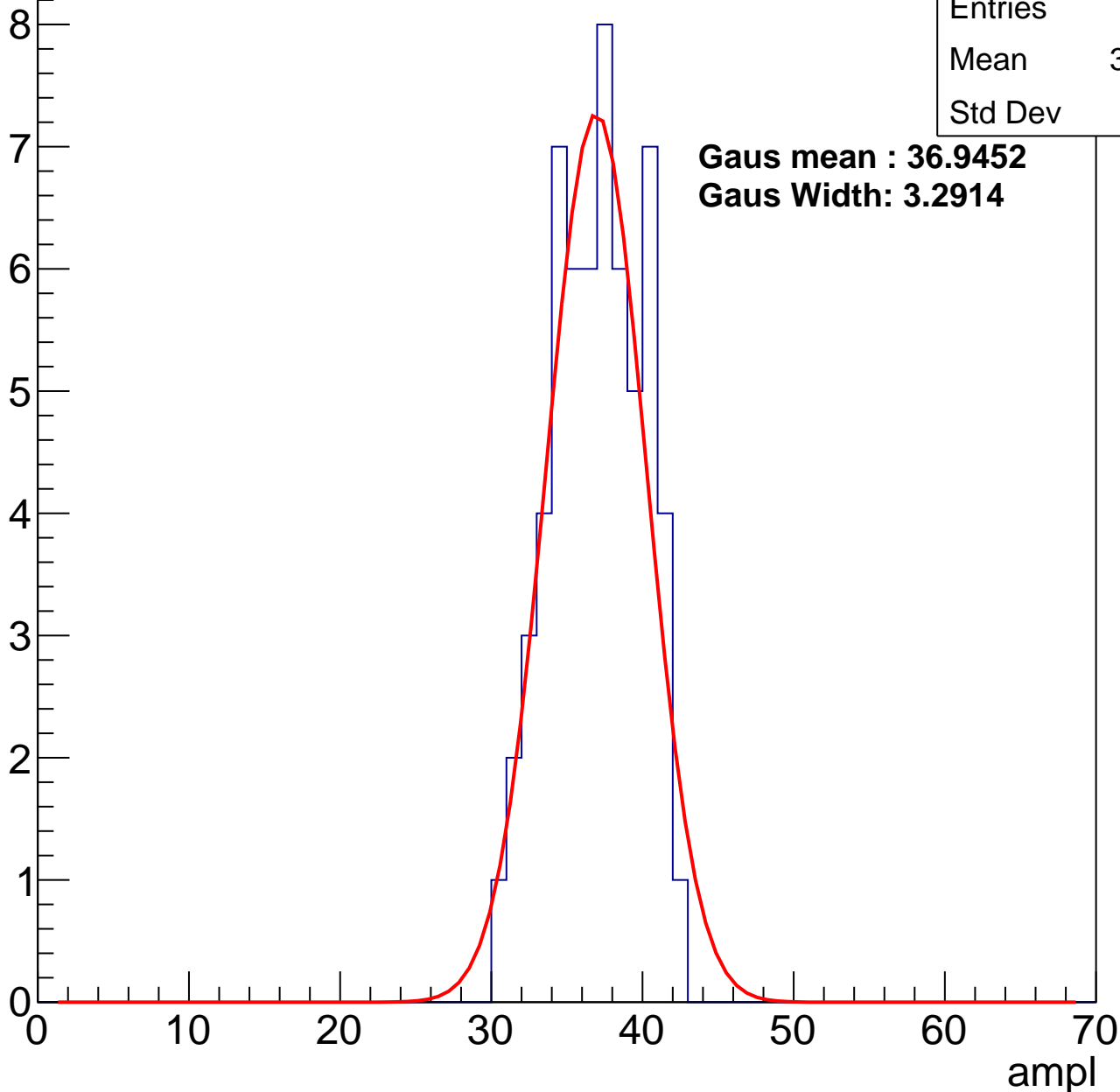
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	36.48
Std Dev	2.93

**Gaus mean : 36.9452**

**Gaus Width: 3.2914**



# B1L002S, U8-ch41, adc2

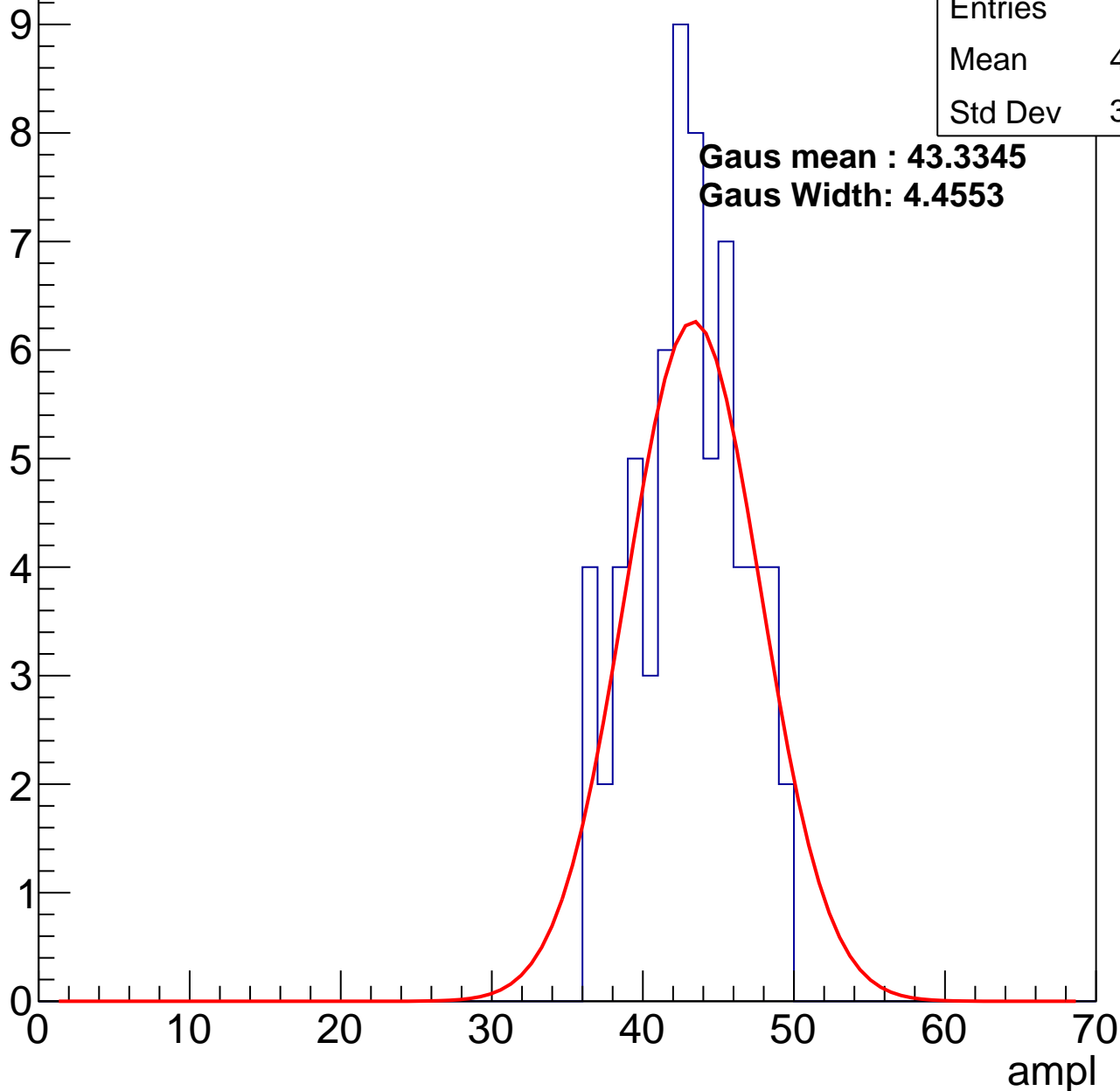
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	42.54
Std Dev	3.453

**Gaus mean : 43.3345**

**Gaus Width: 4.4553**

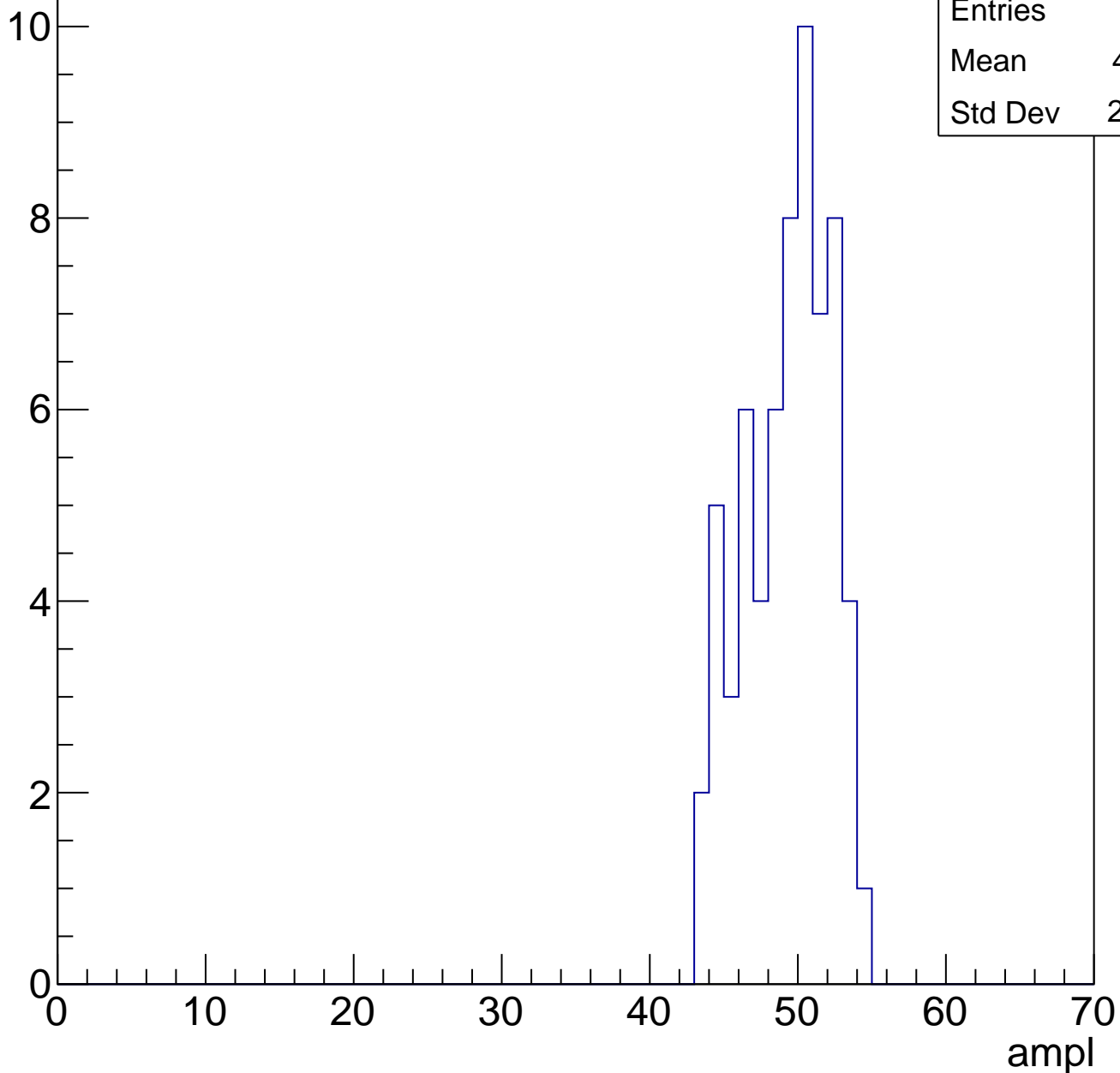


# B1L002S, U8-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

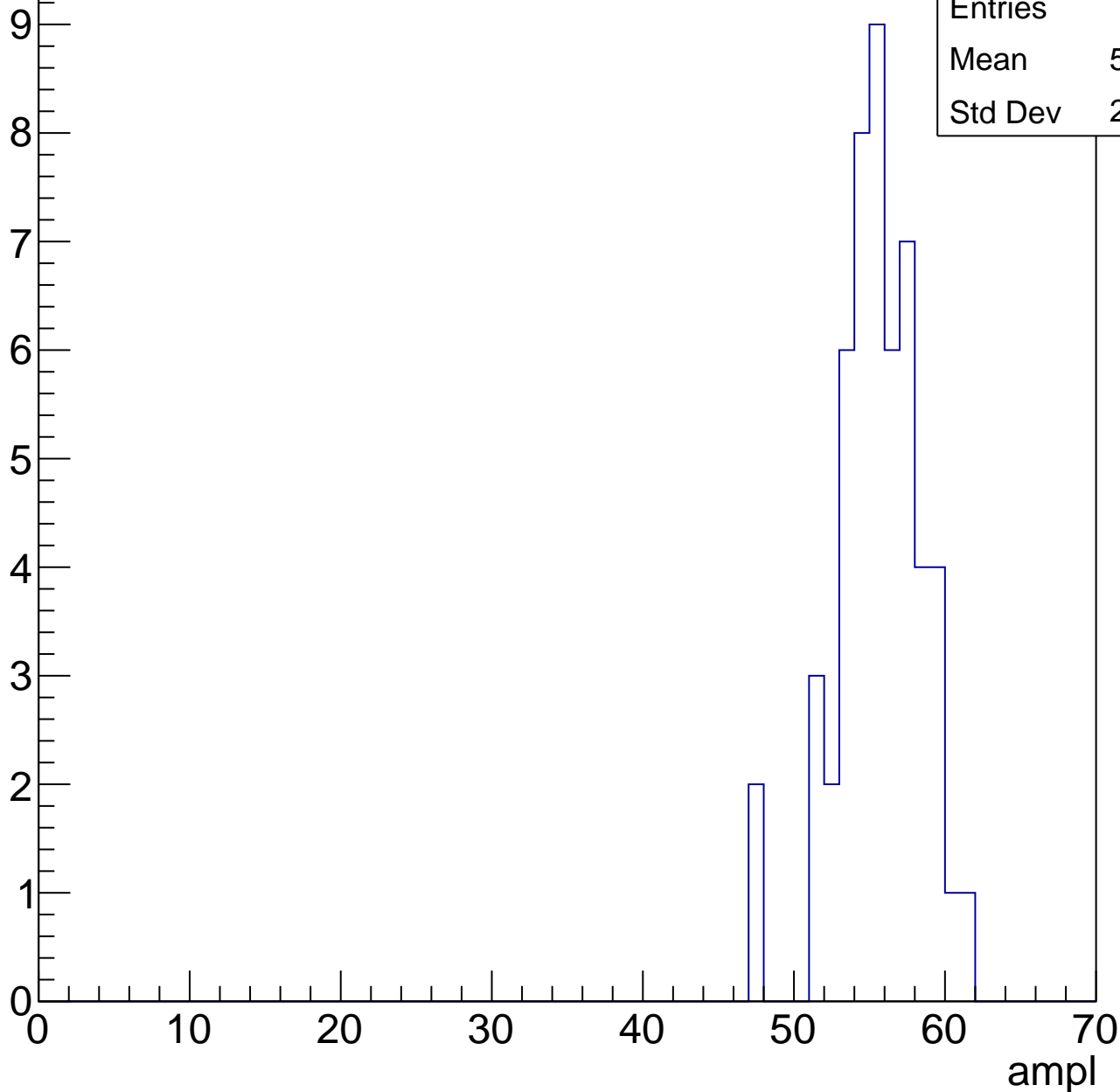
Entries	64
Mean	48.81
Std Dev	2.844



# B1L002S, U8-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	53
Mean	55.09
Std Dev	2.817

# B1L002S, U8-ch41, adc5

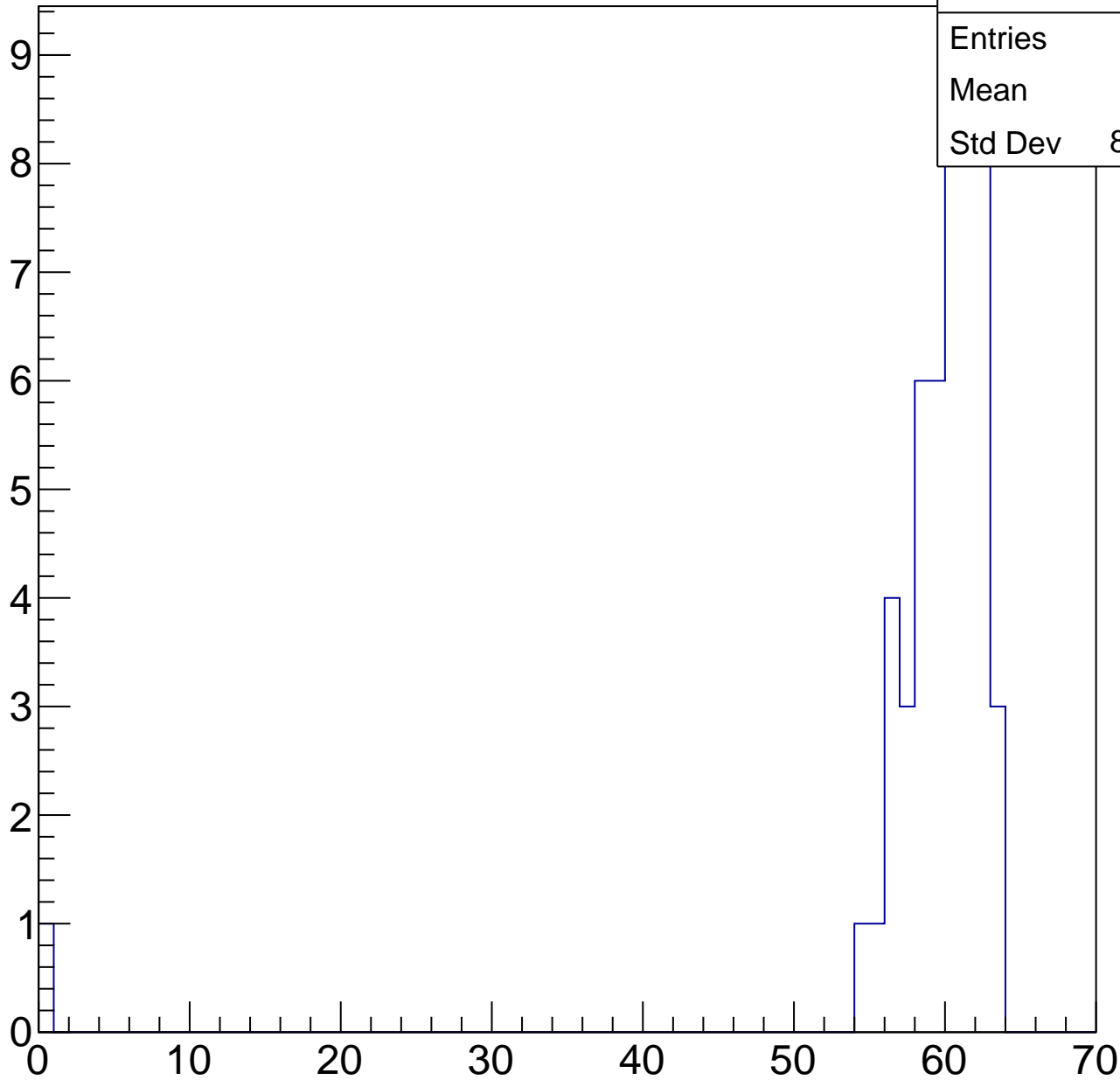
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.5
Std Dev	8.477

ampl

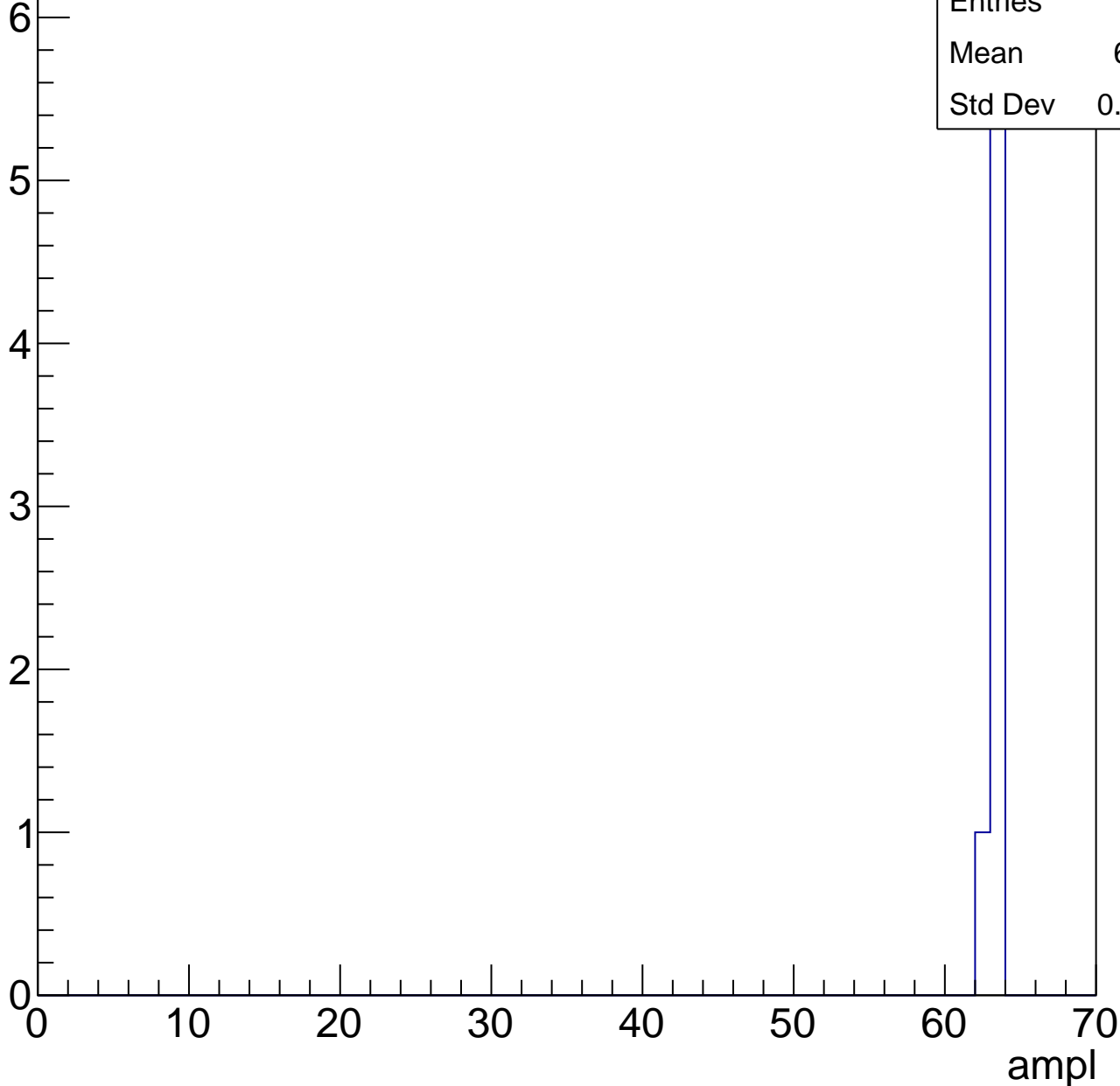


# B1L002S, U8-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	7
Mean	62.86
Std Dev	0.3499





# B1L002S, U8-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch42, adc0

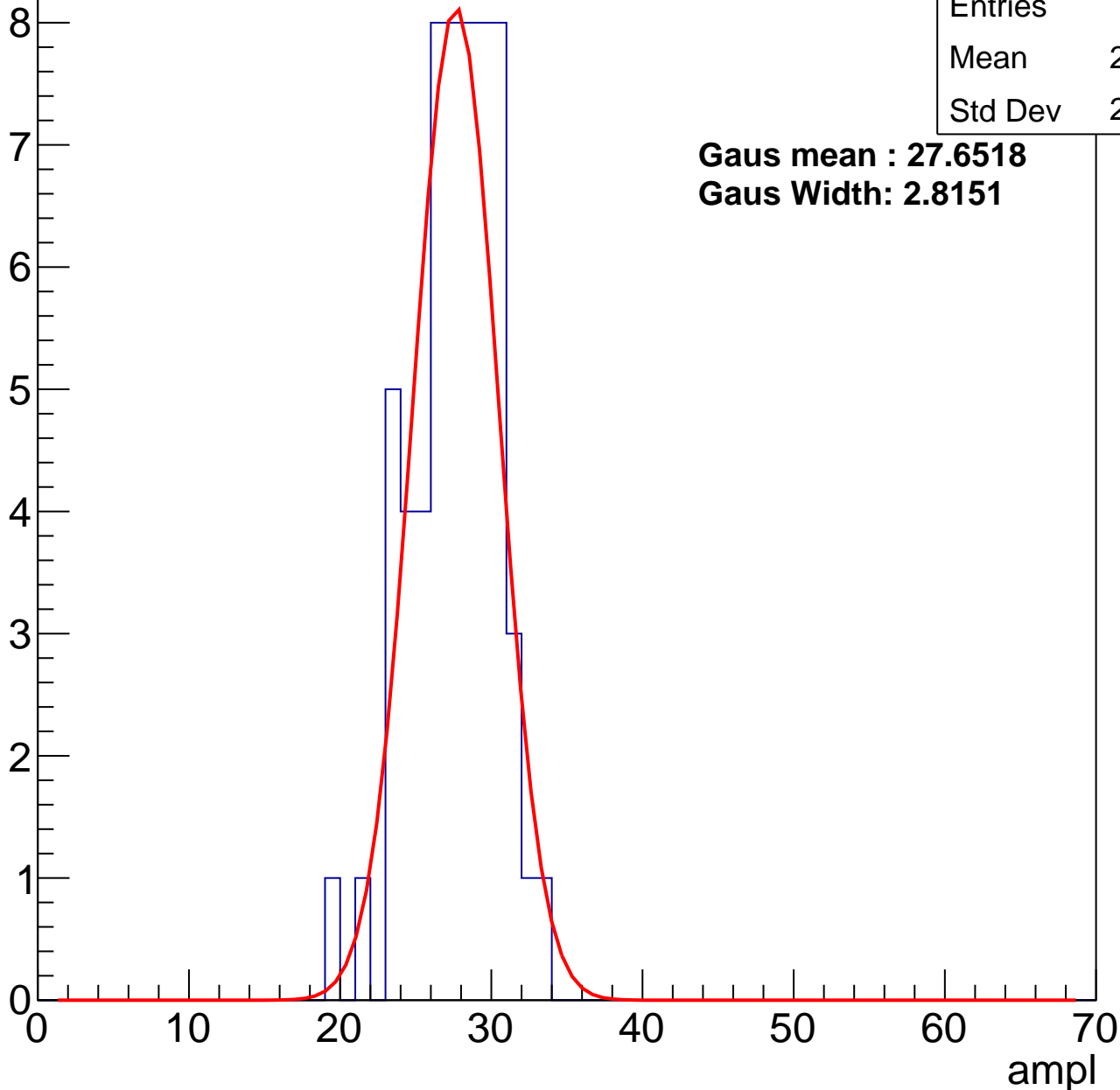
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.15
Std Dev	2.768

**Gaus mean : 27.6518**

**Gaus Width: 2.8151**



# B1L002S, U8-ch42, adc1

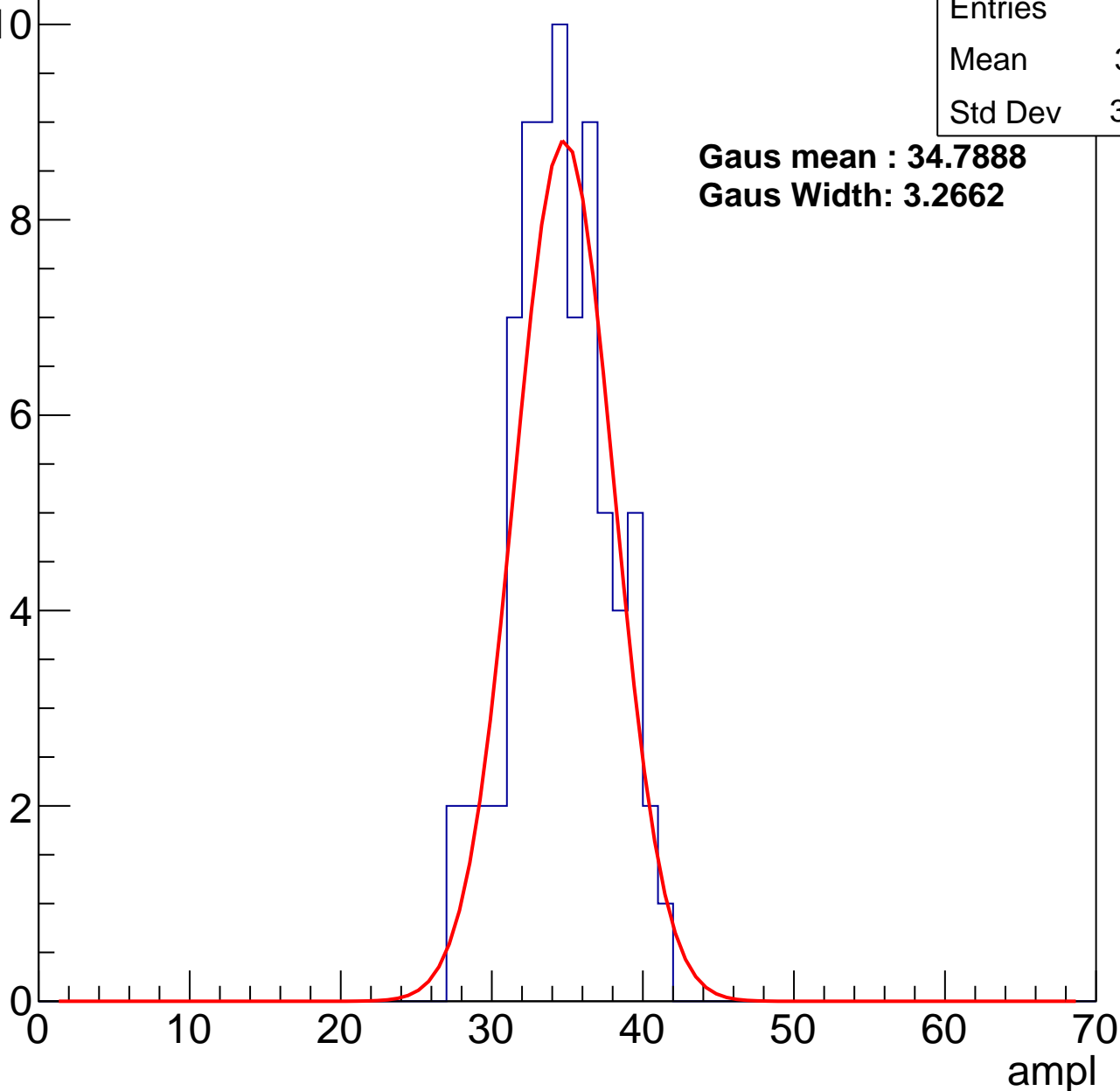
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	34.11
Std Dev	3.156

**Gaus mean : 34.7888**

**Gaus Width: 3.2662**



# B1L002S, U8-ch42, adc2

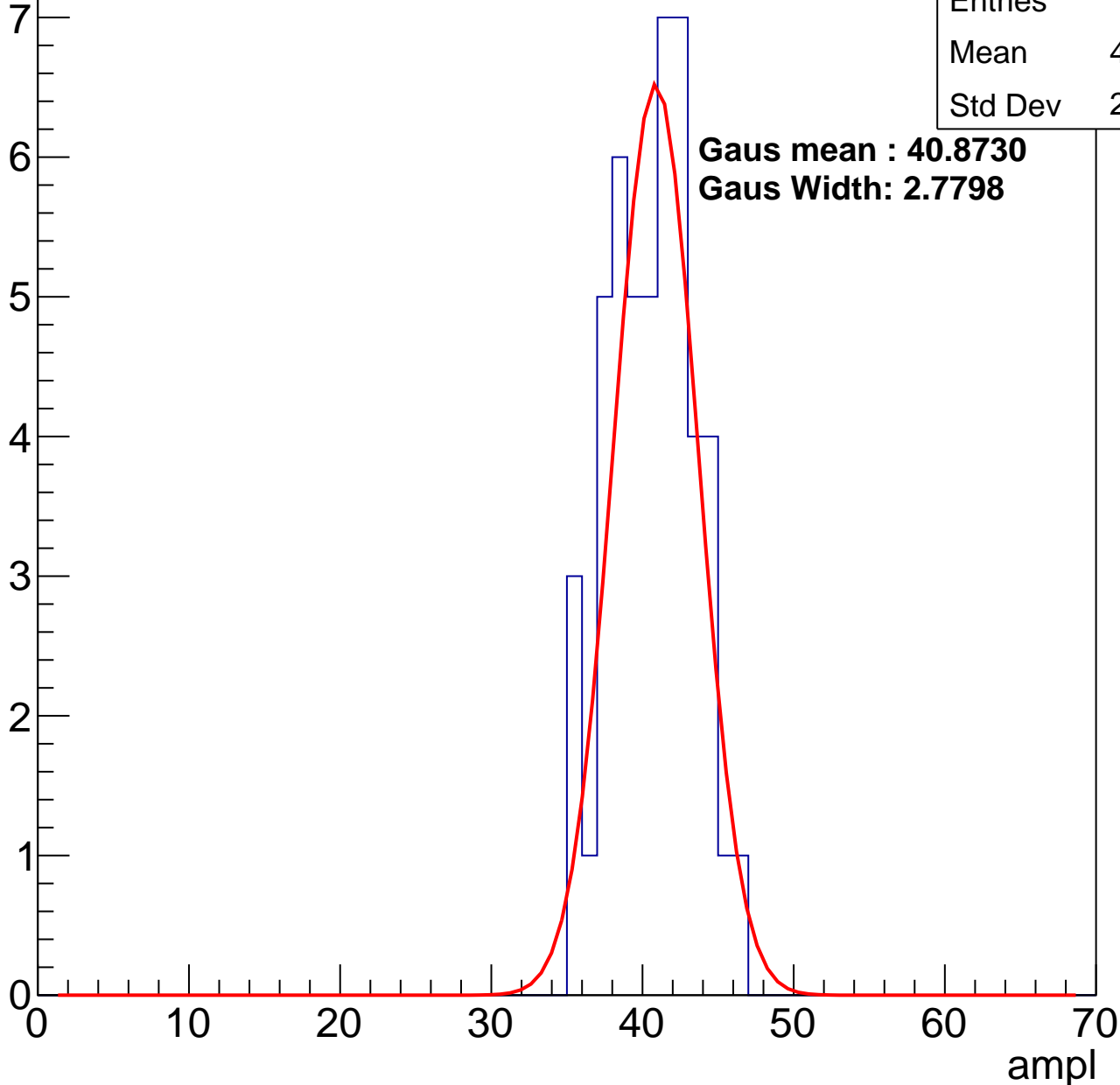
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.18
Std Dev	2.708

**Gaus mean : 40.8730**

**Gaus Width: 2.7798**

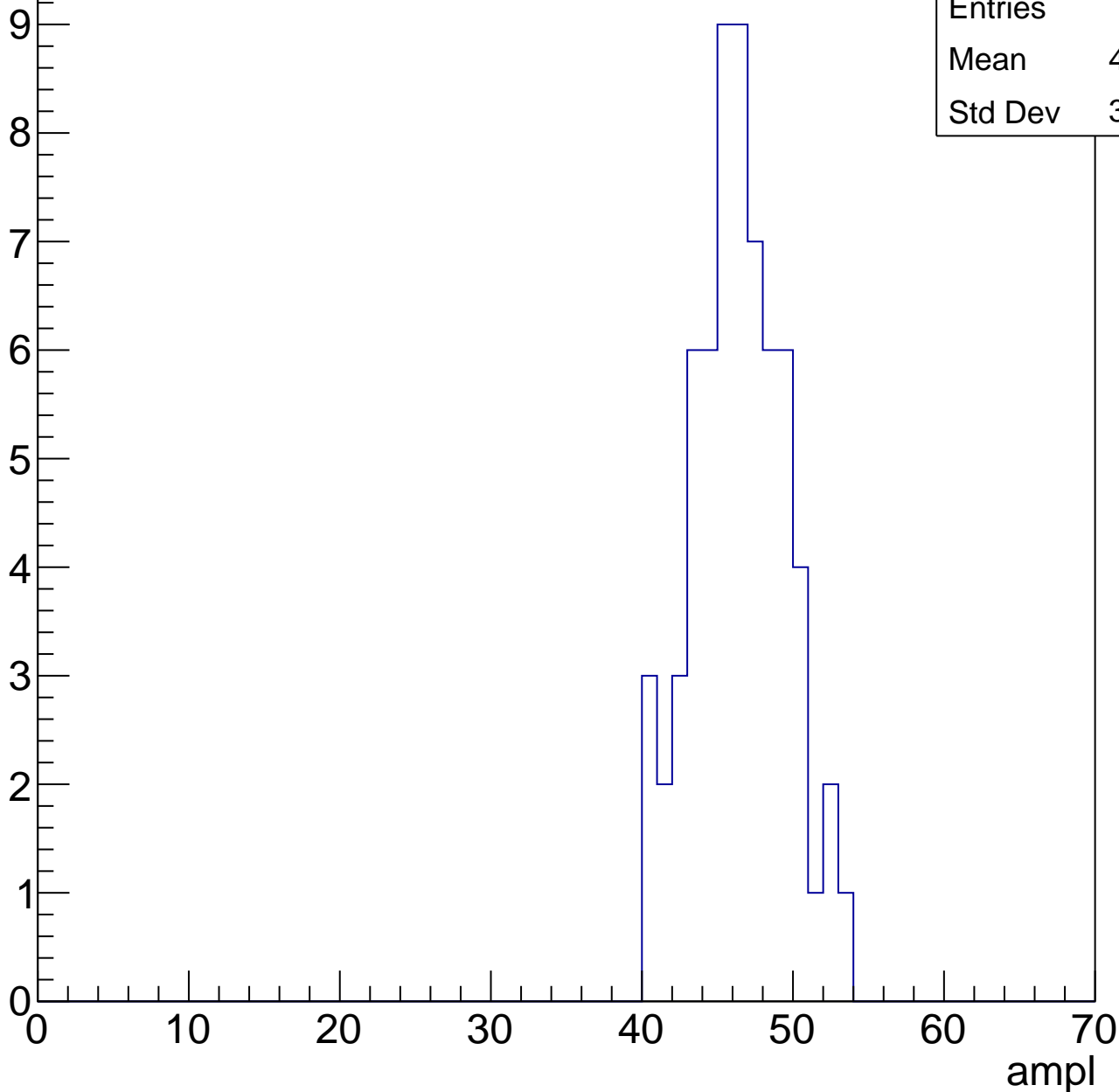


# B1L002S, U8-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	45.97
Std Dev	3.008

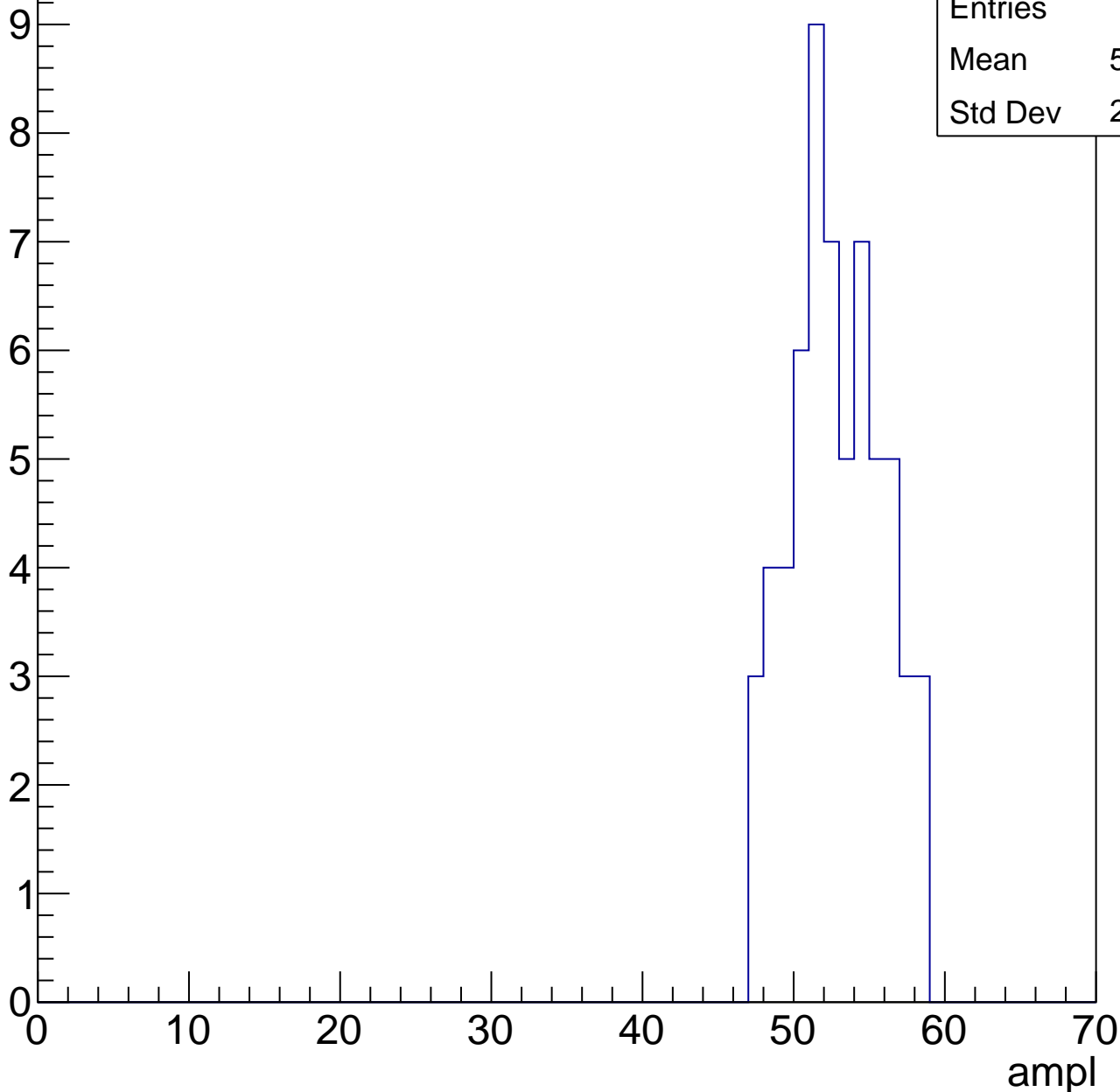


# B1L002S, U8-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	52.38
Std Dev	2.976

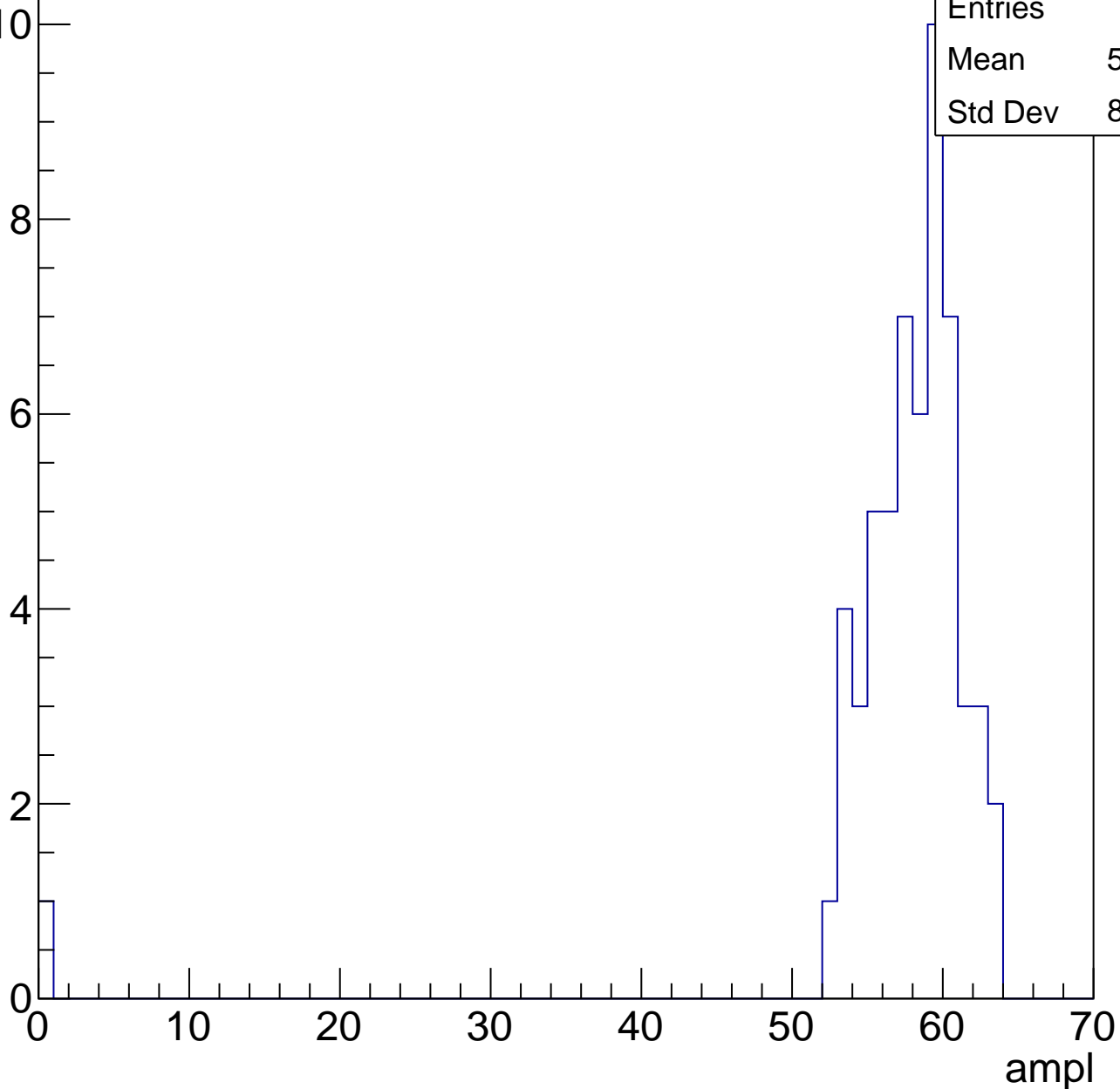


# B1L002S, U8-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	56.72
Std Dev	8.045

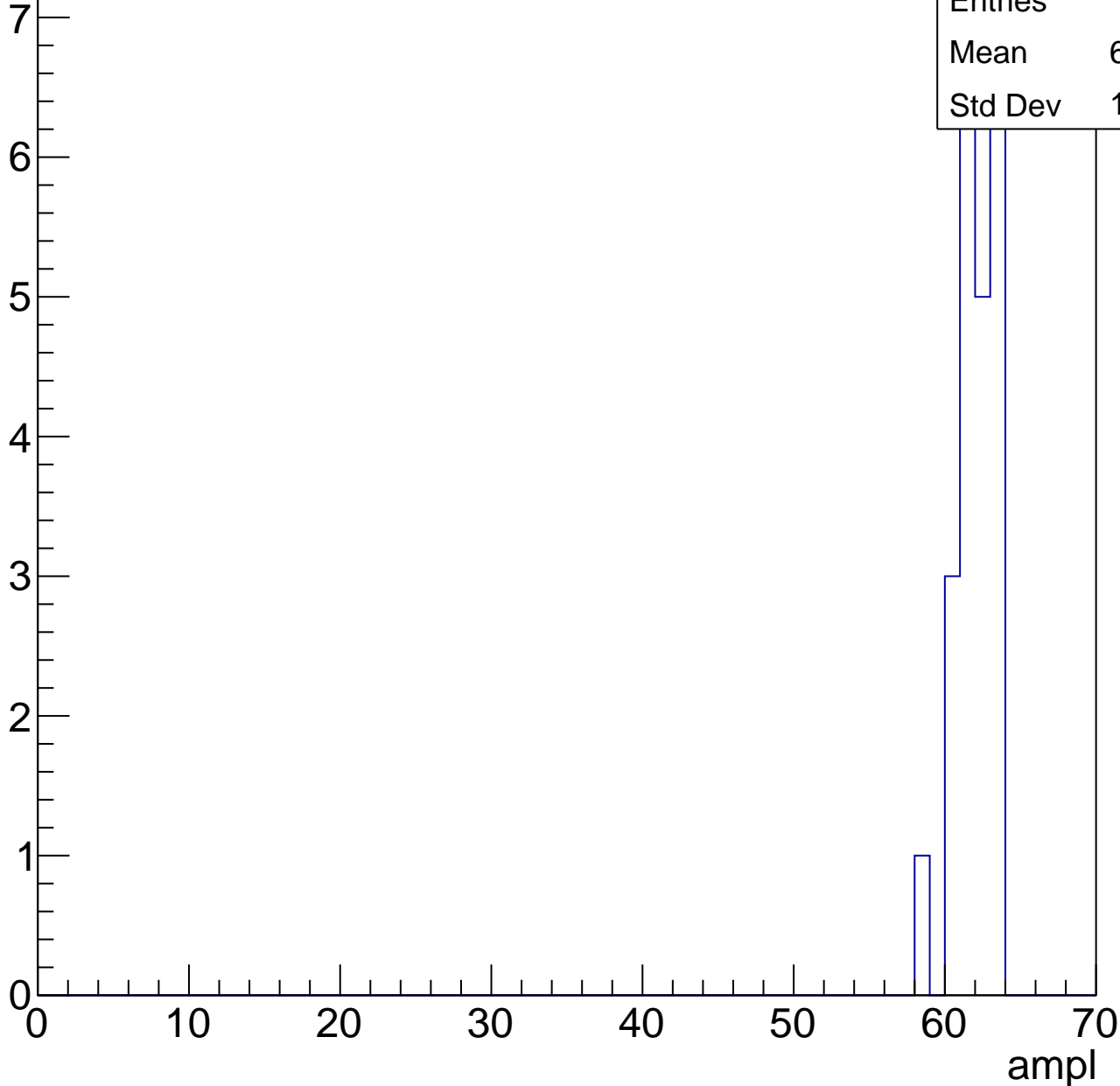


# B1L002S, U8-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.57
Std Dev	1.279

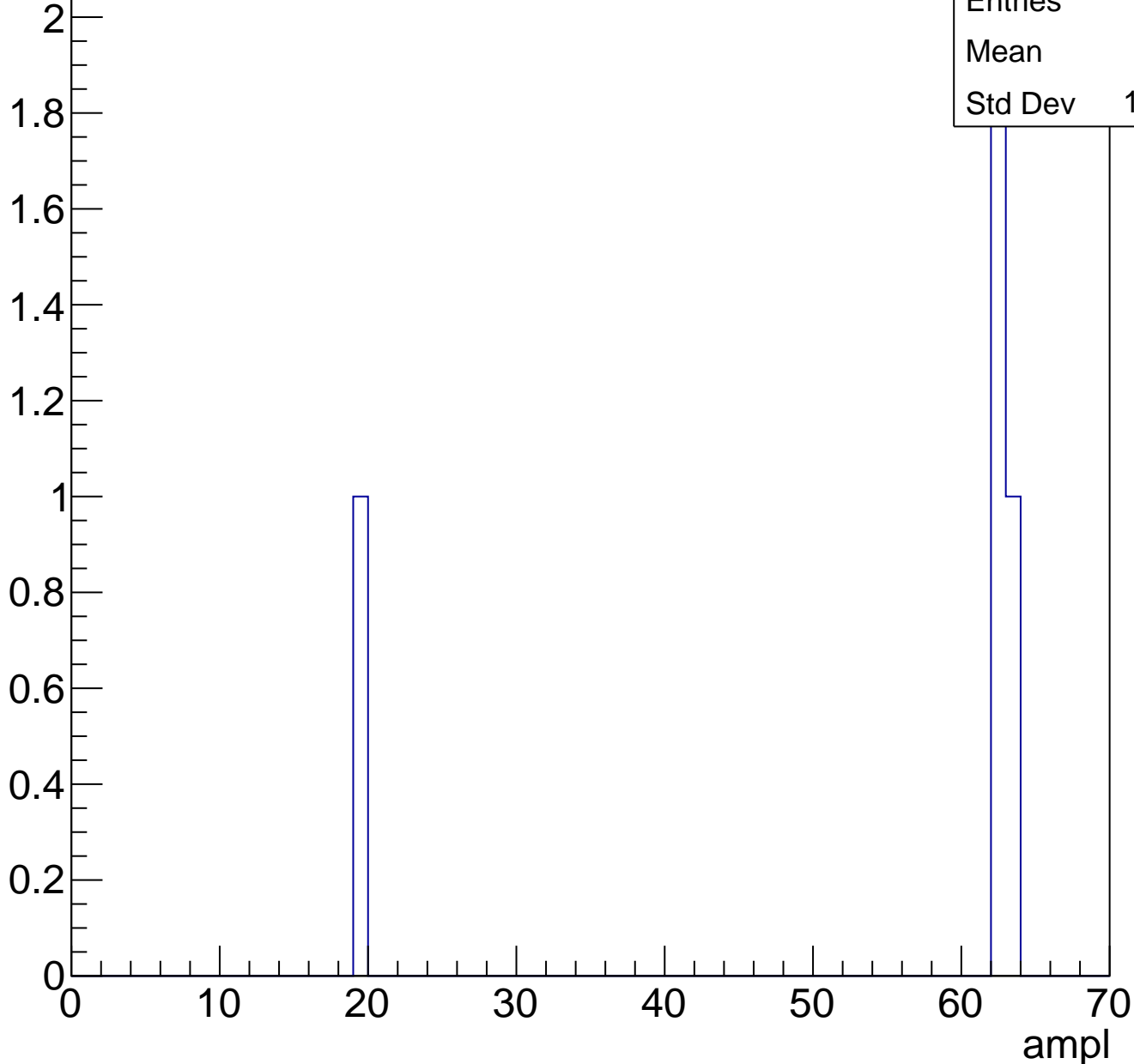




# B1L002S, U8-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch43, adc0

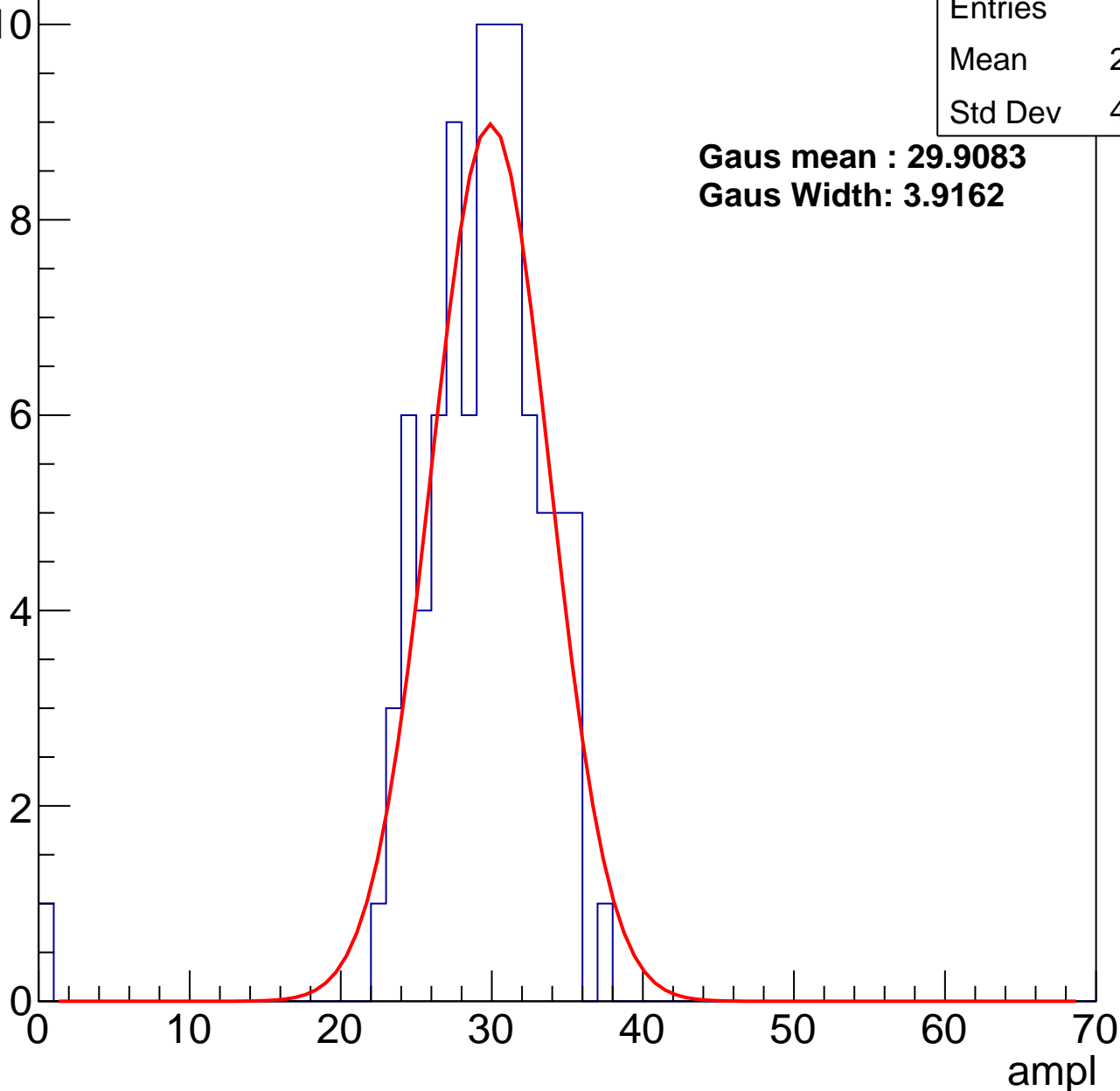
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	88
Mean	28.88
Std Dev	4.597

**Gaus mean : 29.9083**

**Gaus Width: 3.9162**



# B1L002S, U8-ch43, adc1

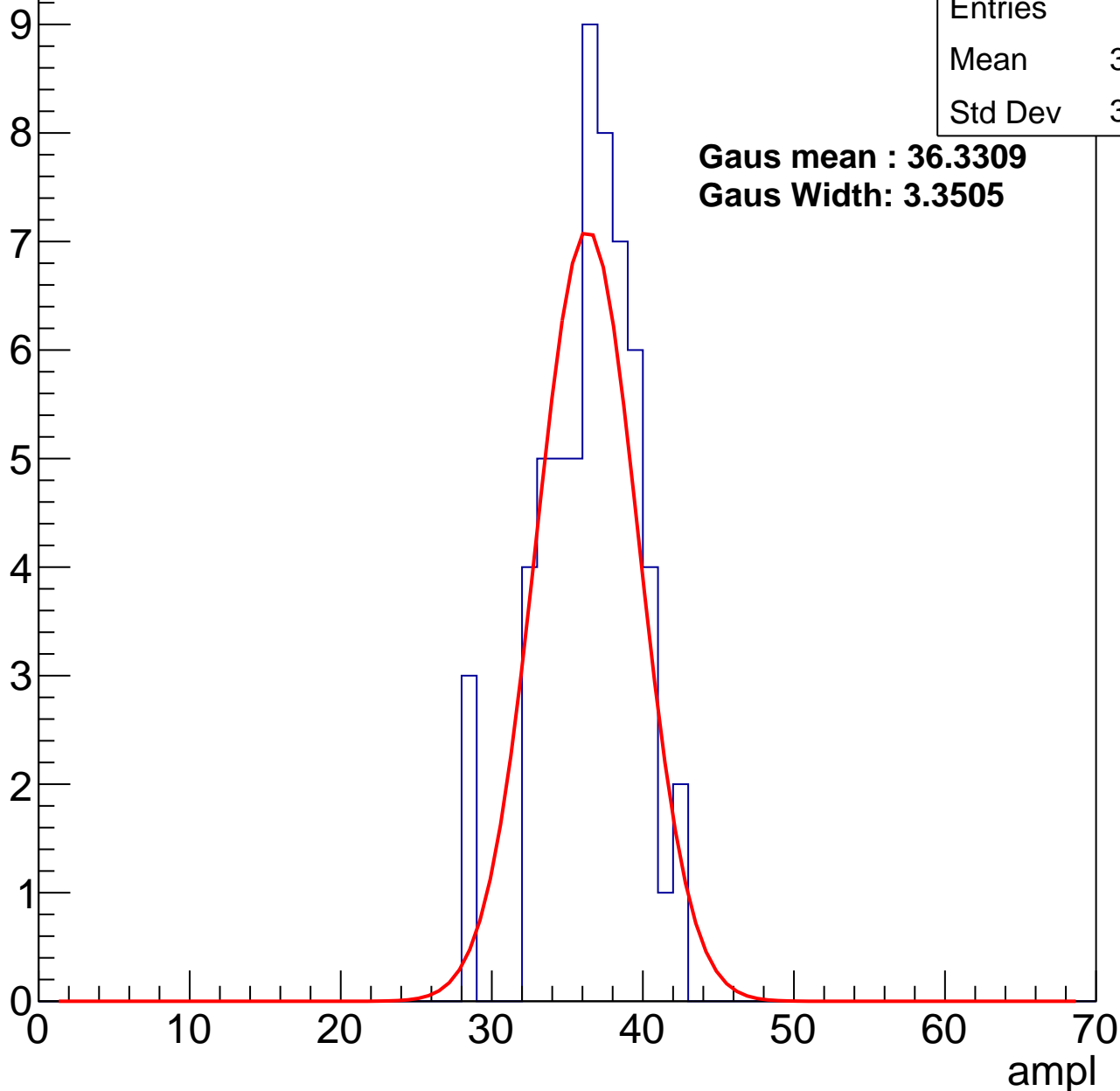
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	36.05
Std Dev	3.127

**Gaus mean : 36.3309**

**Gaus Width: 3.3505**



# B1L002S, U8-ch43, adc2

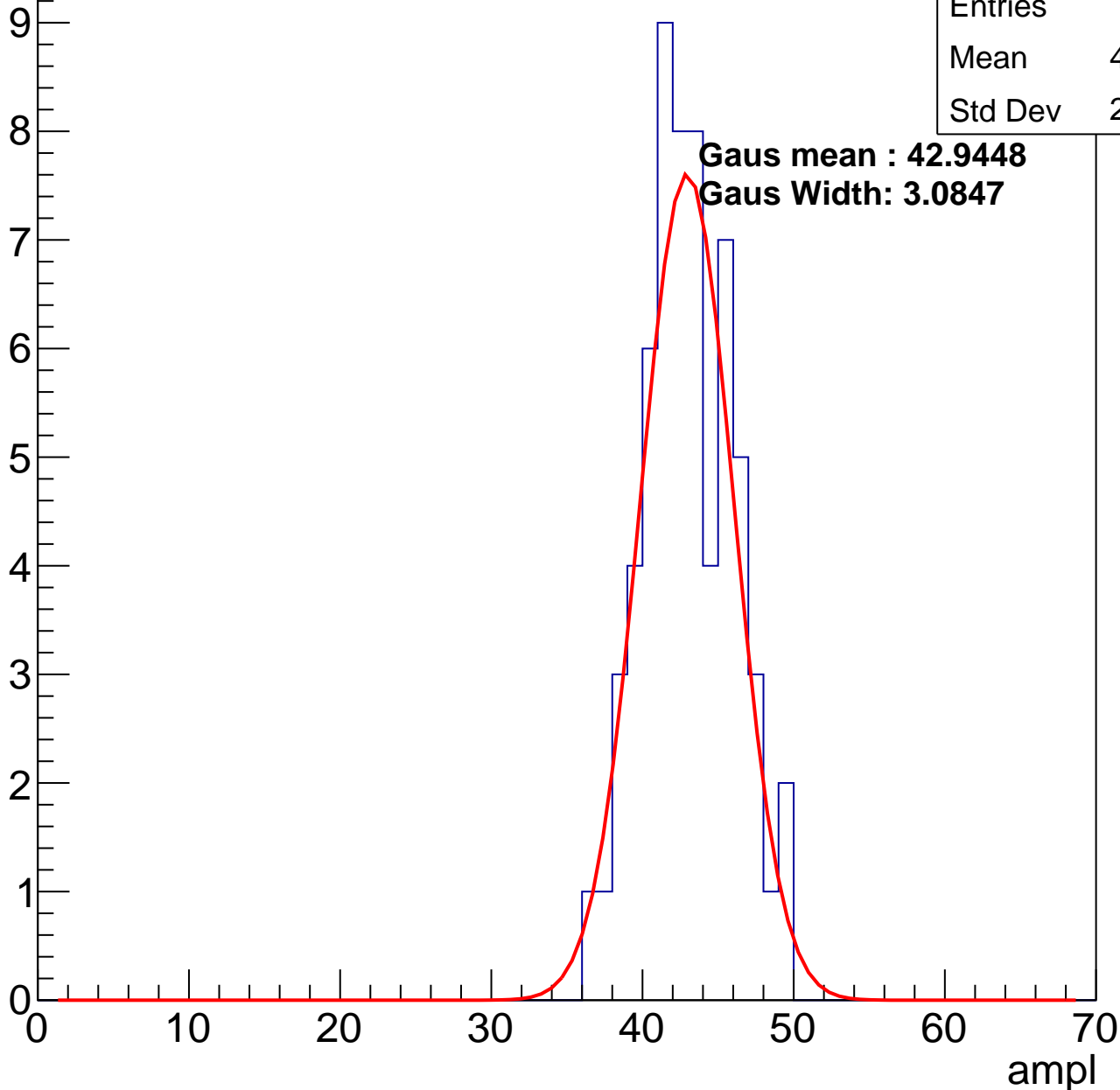
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	42.58
Std Dev	2.932

**Gaus mean : 42.9448**

**Gaus Width: 3.0847**

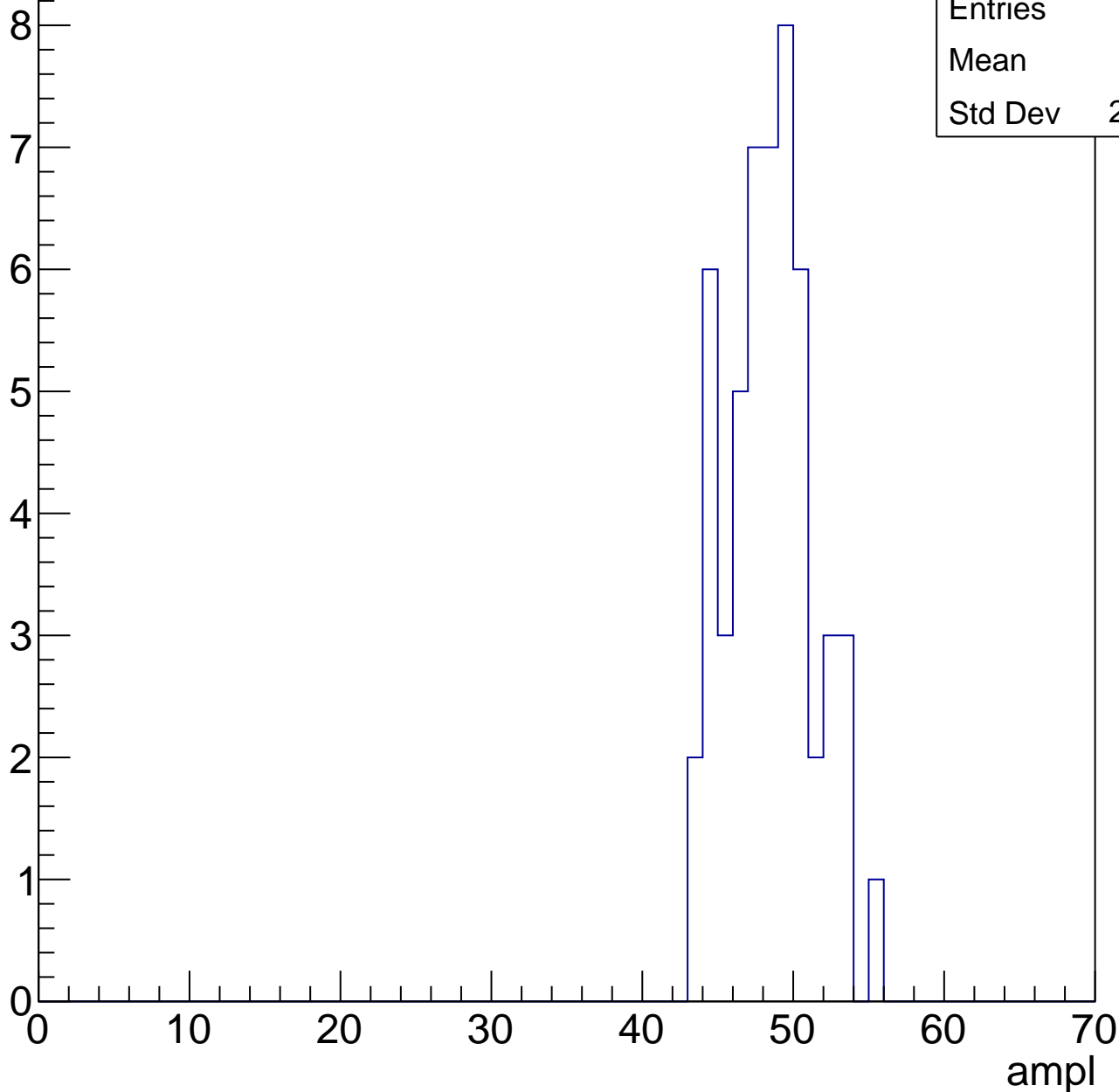


# B1L002S, U8-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	48
Std Dev	2.822



# B1L002S, U8-ch43, adc4

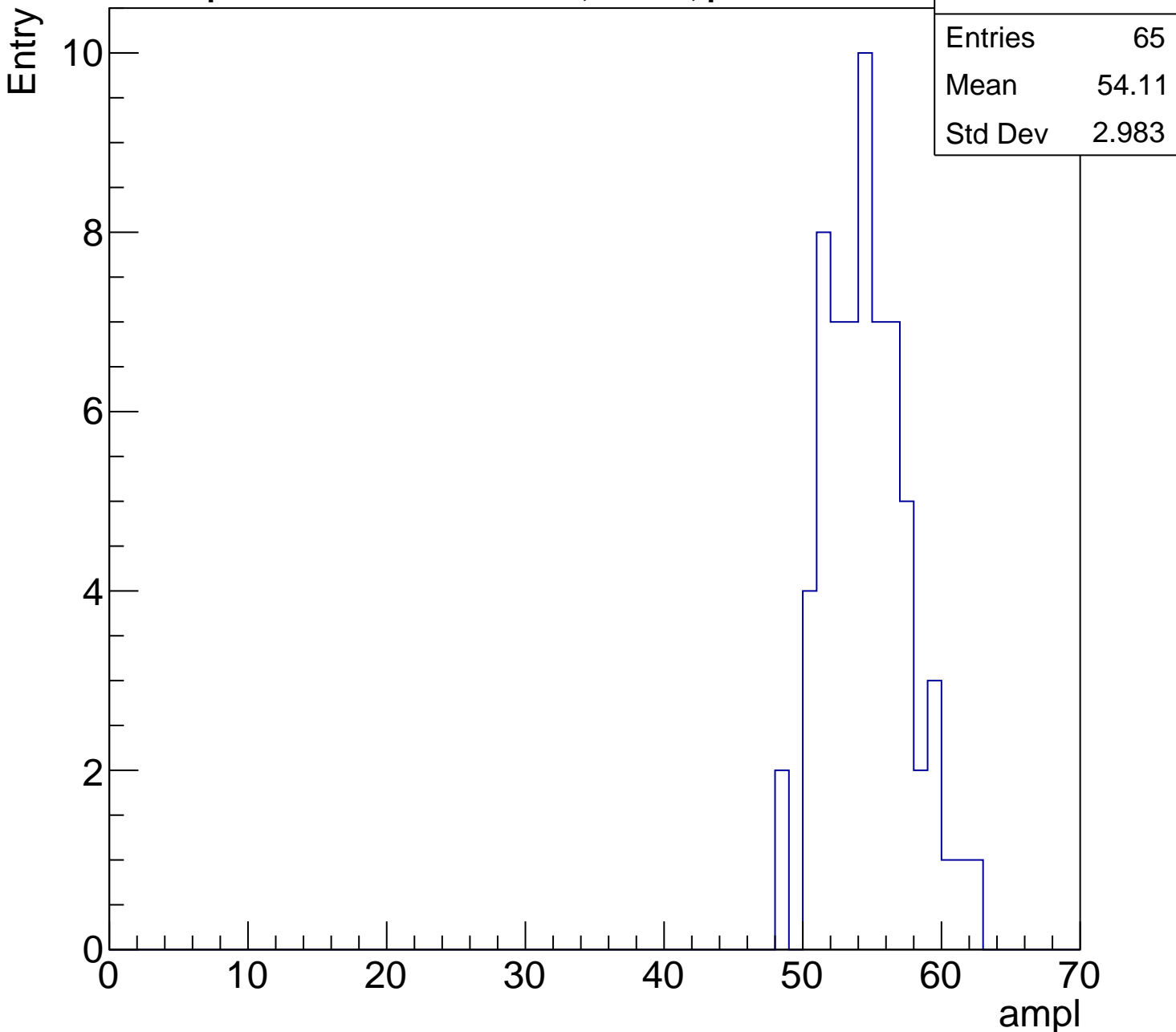
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	54.11
Std Dev	2.983

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl

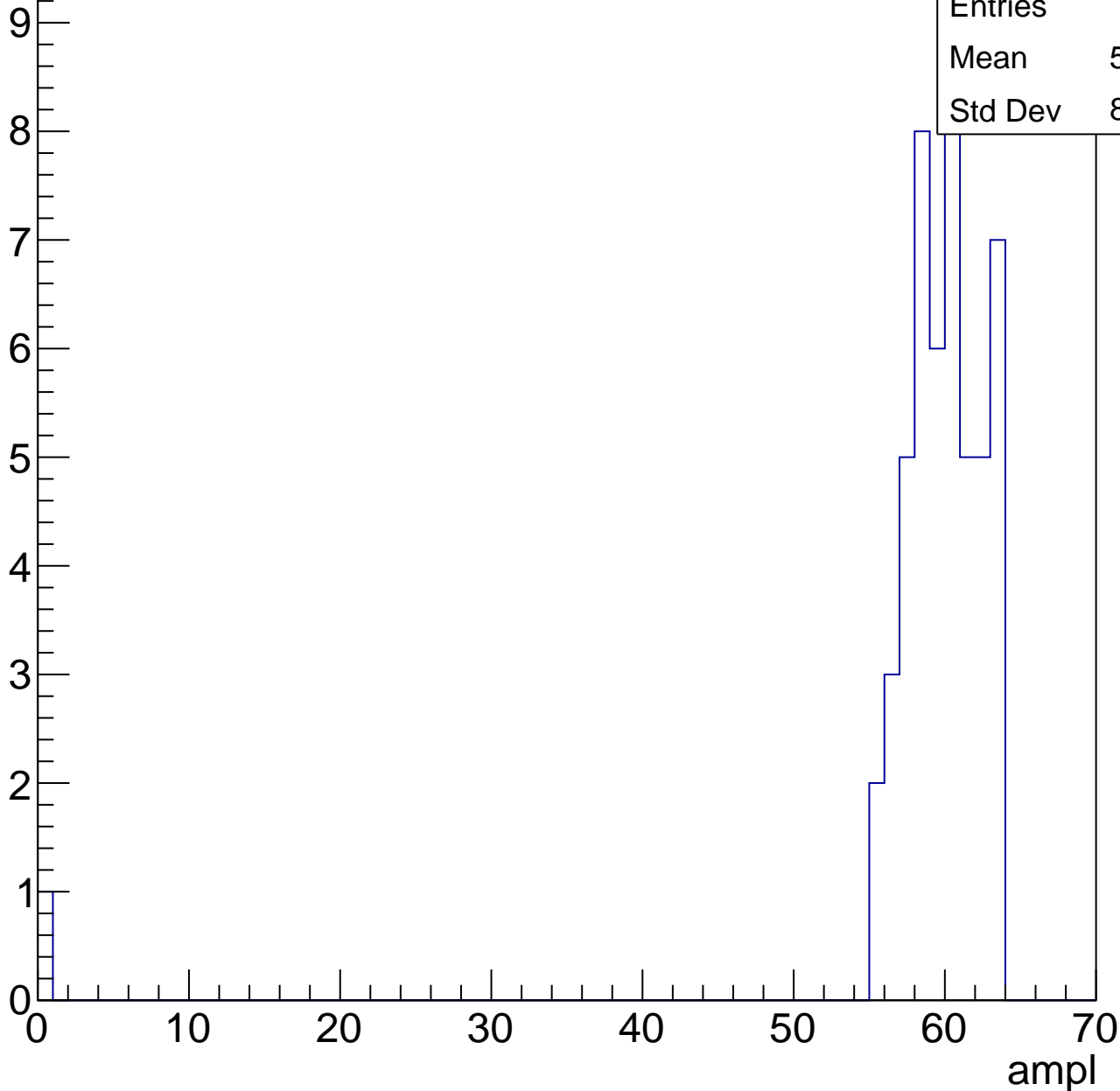


# B1L002S, U8-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

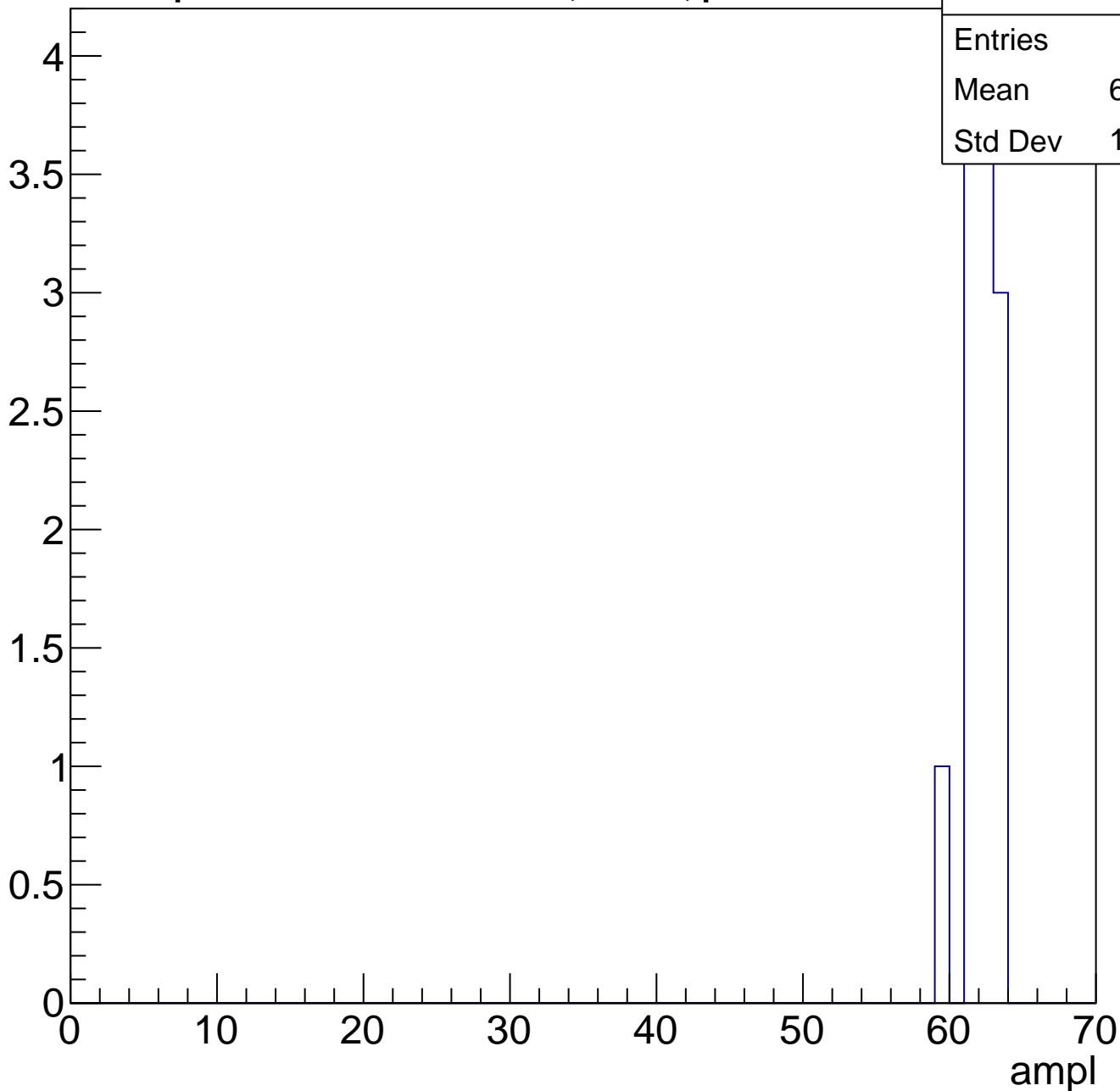
Entries	51
Mean	58.37
Std Dev	8.557



# B1L002S, U8-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch44, adc0

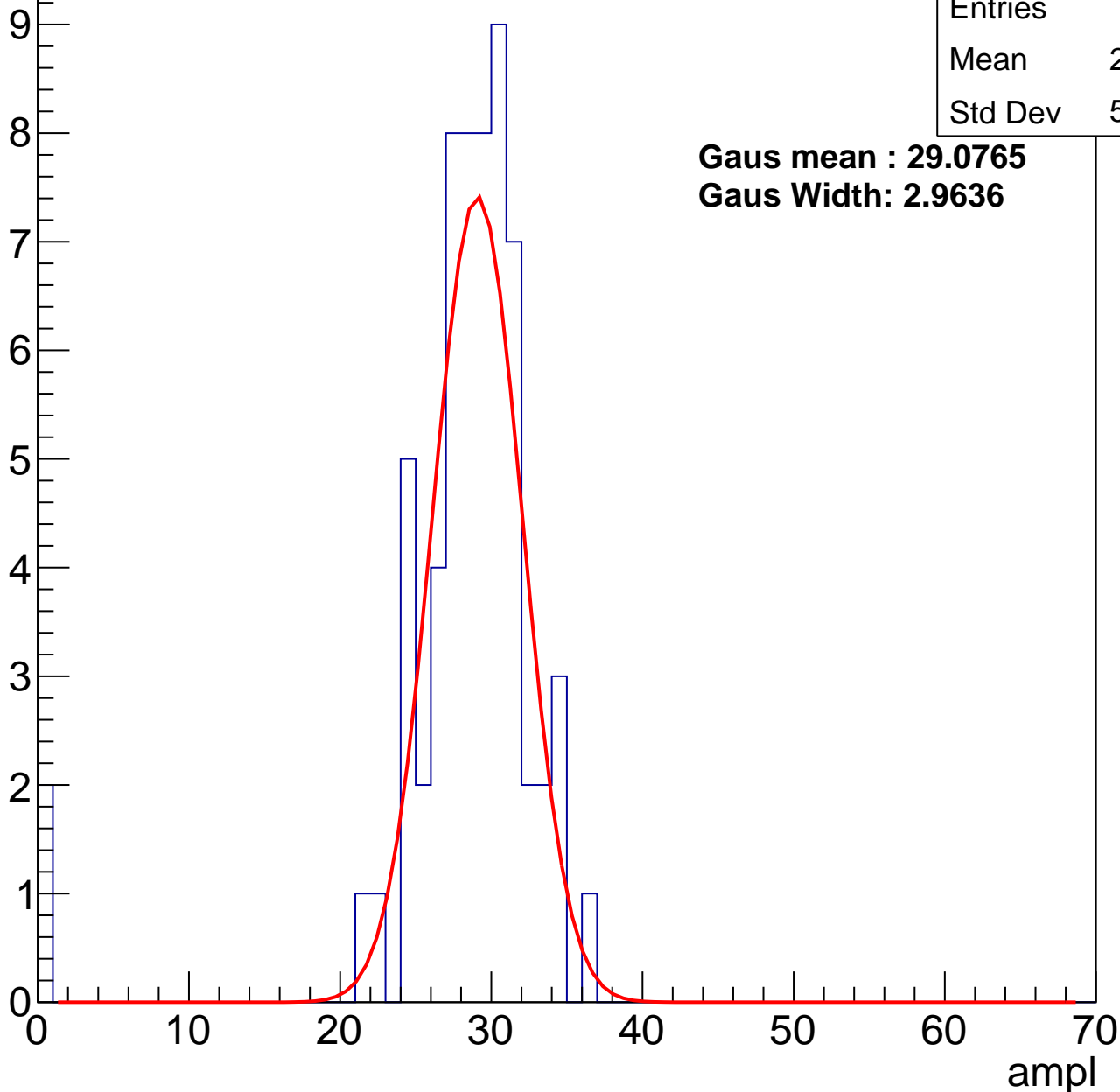
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.68
Std Dev	5.814

**Gaus mean : 29.0765**

**Gaus Width: 2.9636**



# B1L002S, U8-ch44, adc1

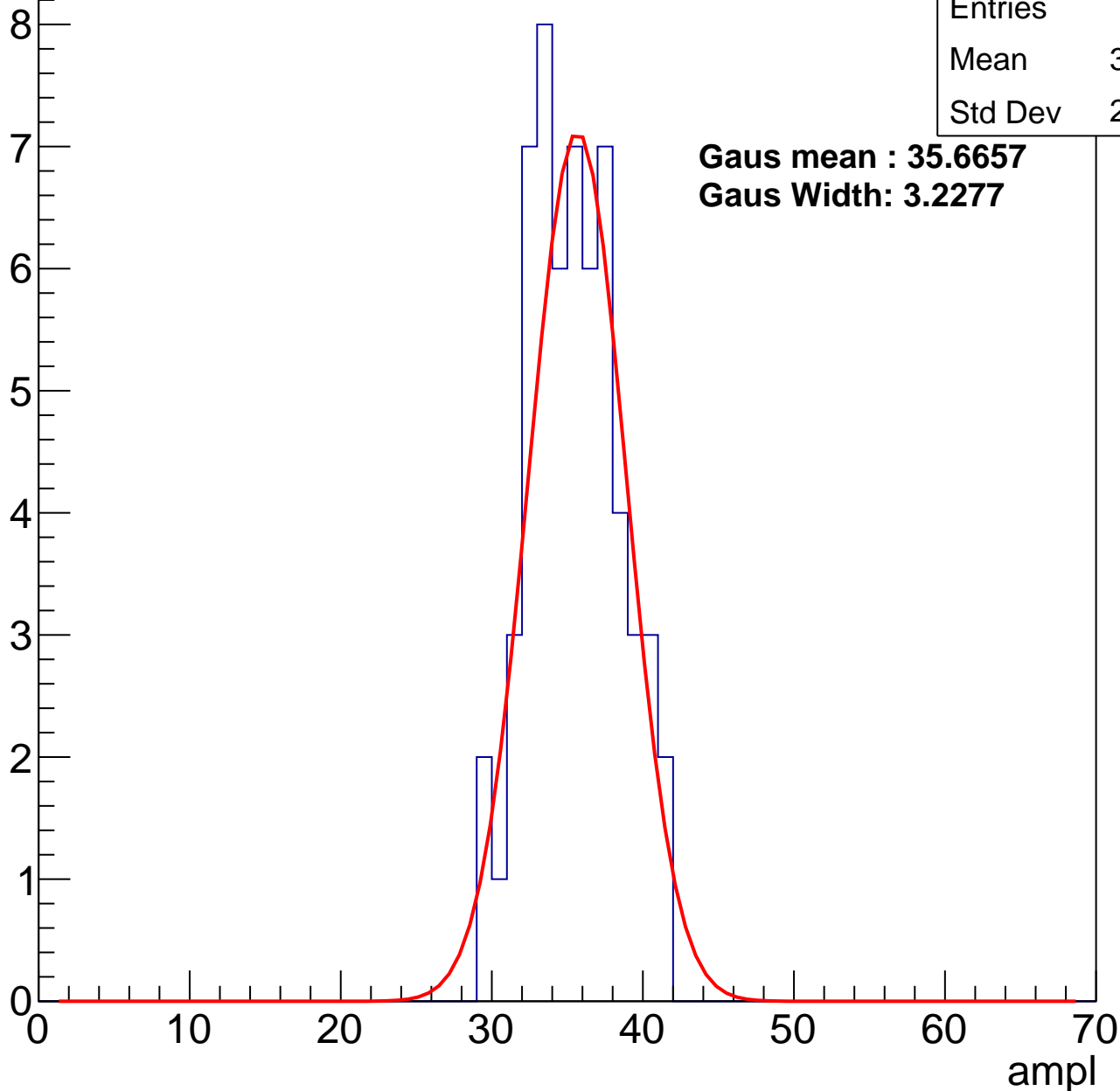
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.98
Std Dev	2.943

**Gaus mean : 35.6657**

**Gaus Width: 3.2277**



# B1L002S, U8-ch44, adc2

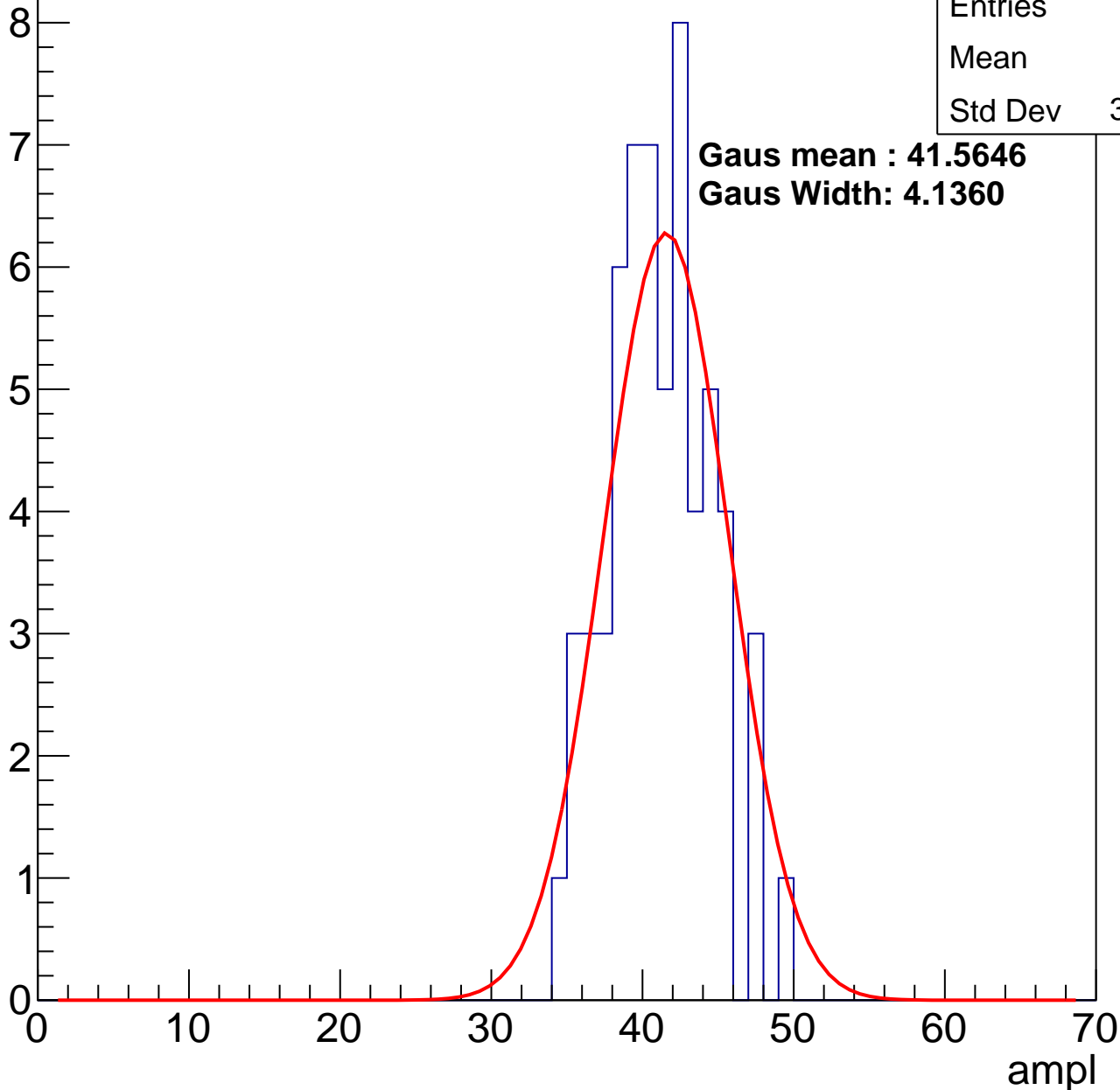
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	40.7
Std Dev	3.338

**Gaus mean : 41.5646**

**Gaus Width: 4.1360**



# B1L002S, U8-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

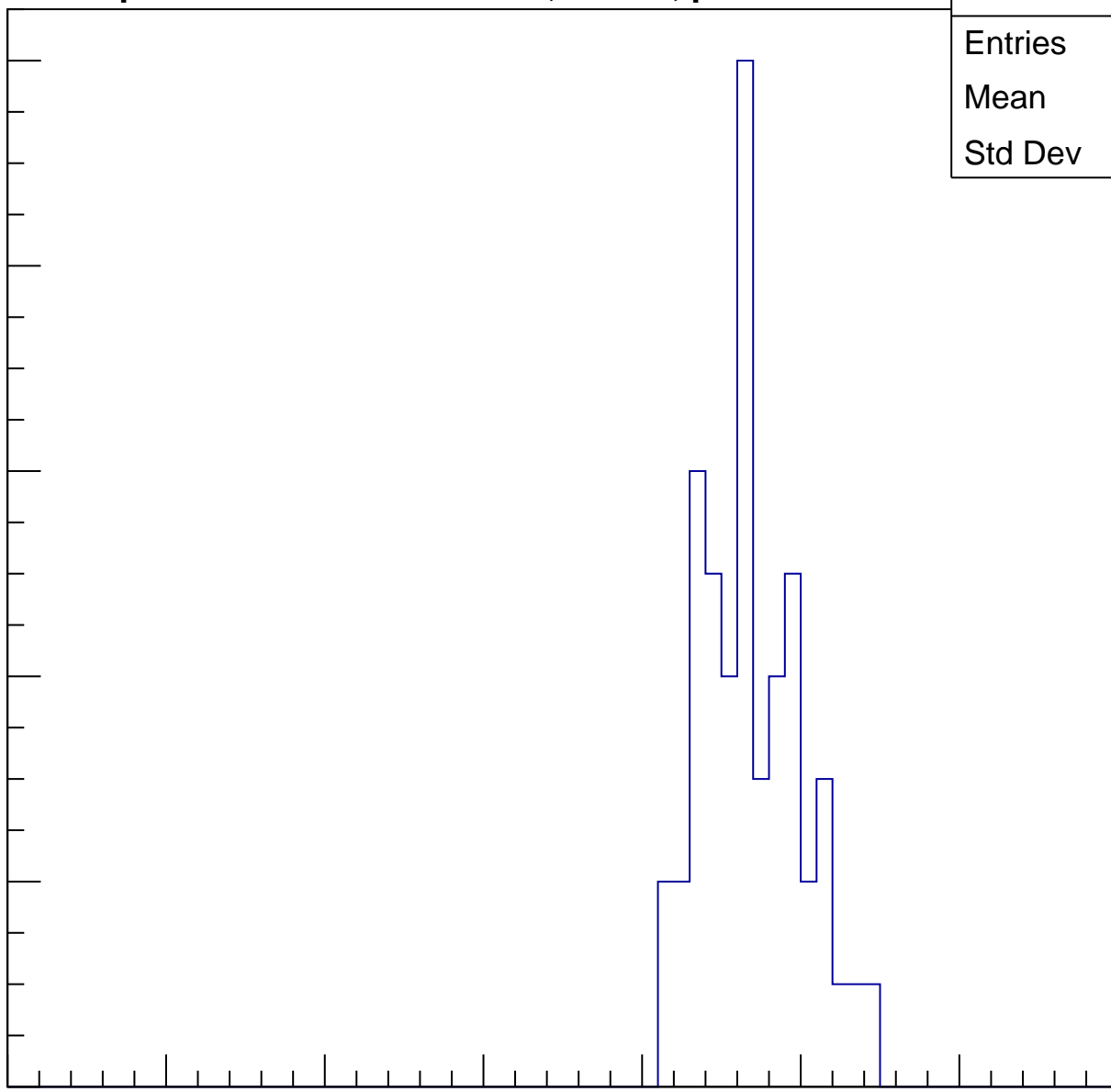
Entries	49
Mean	46.41
Std Dev	3.103

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

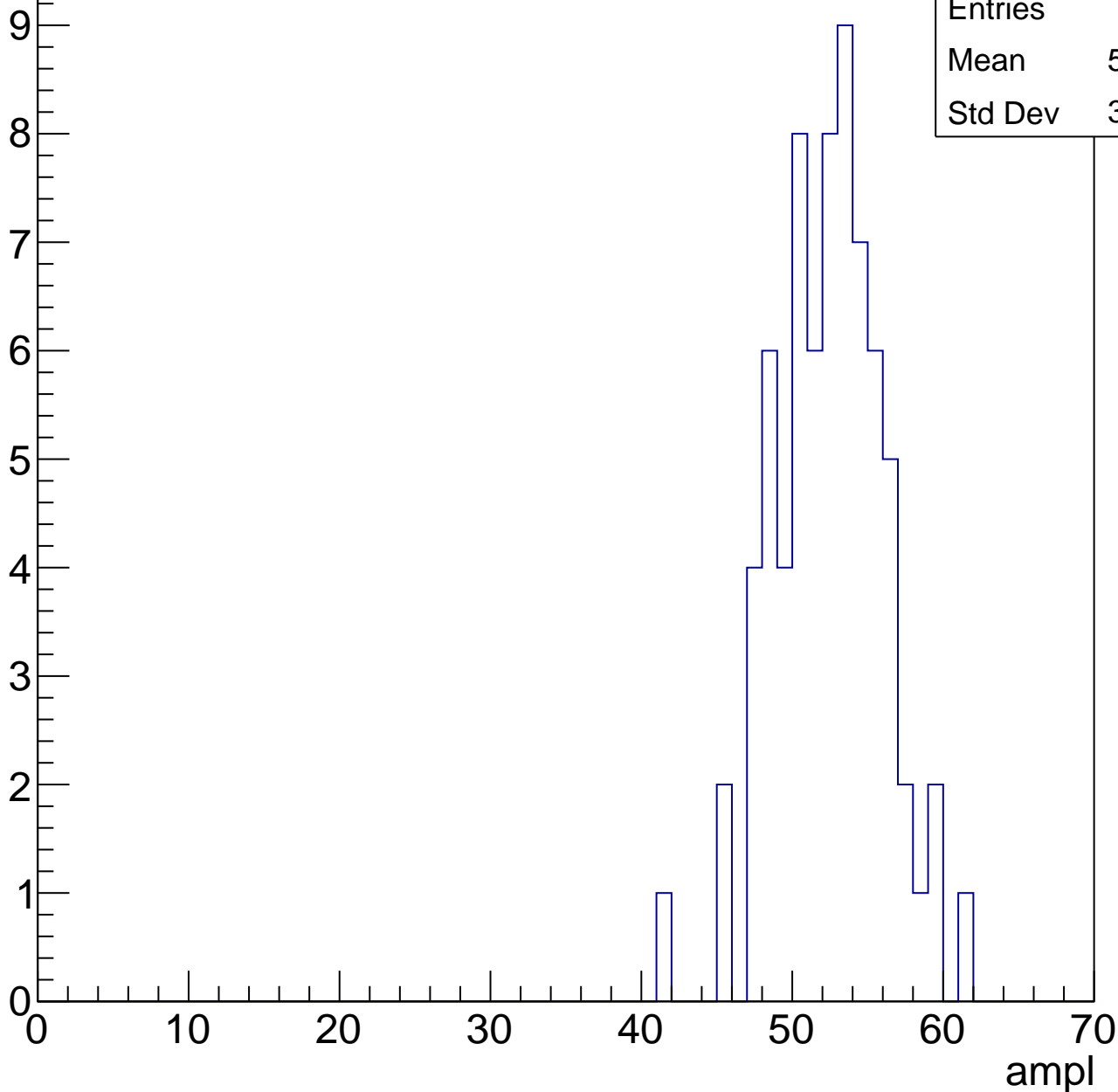


# B1L002S, U8-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	51.96
Std Dev	3.584

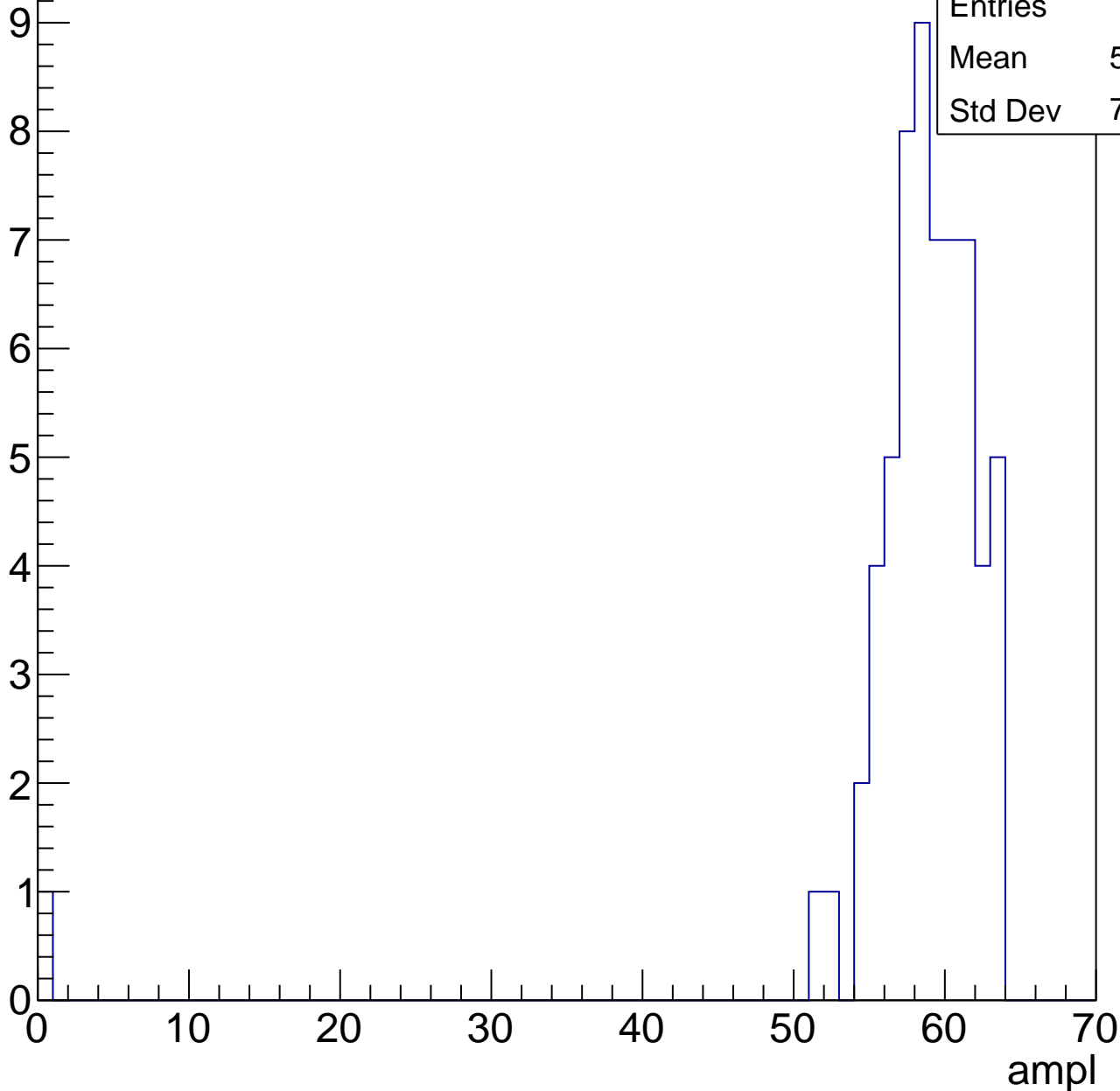


# B1L002S, U8-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.57
Std Dev	7.914

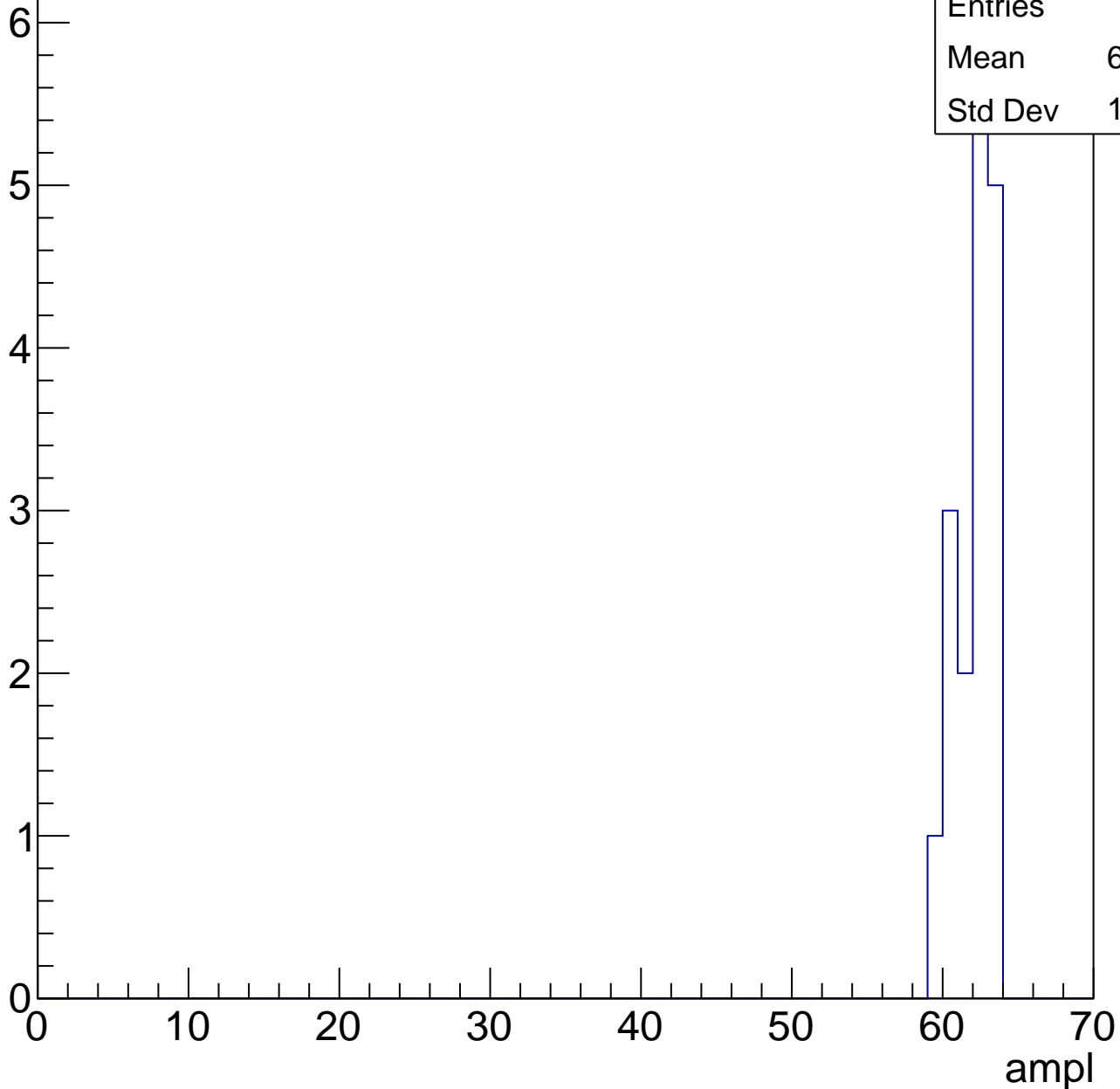


# B1L002S, U8-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.65
Std Dev	1.234





# B1L002S, U8-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch45, adc0

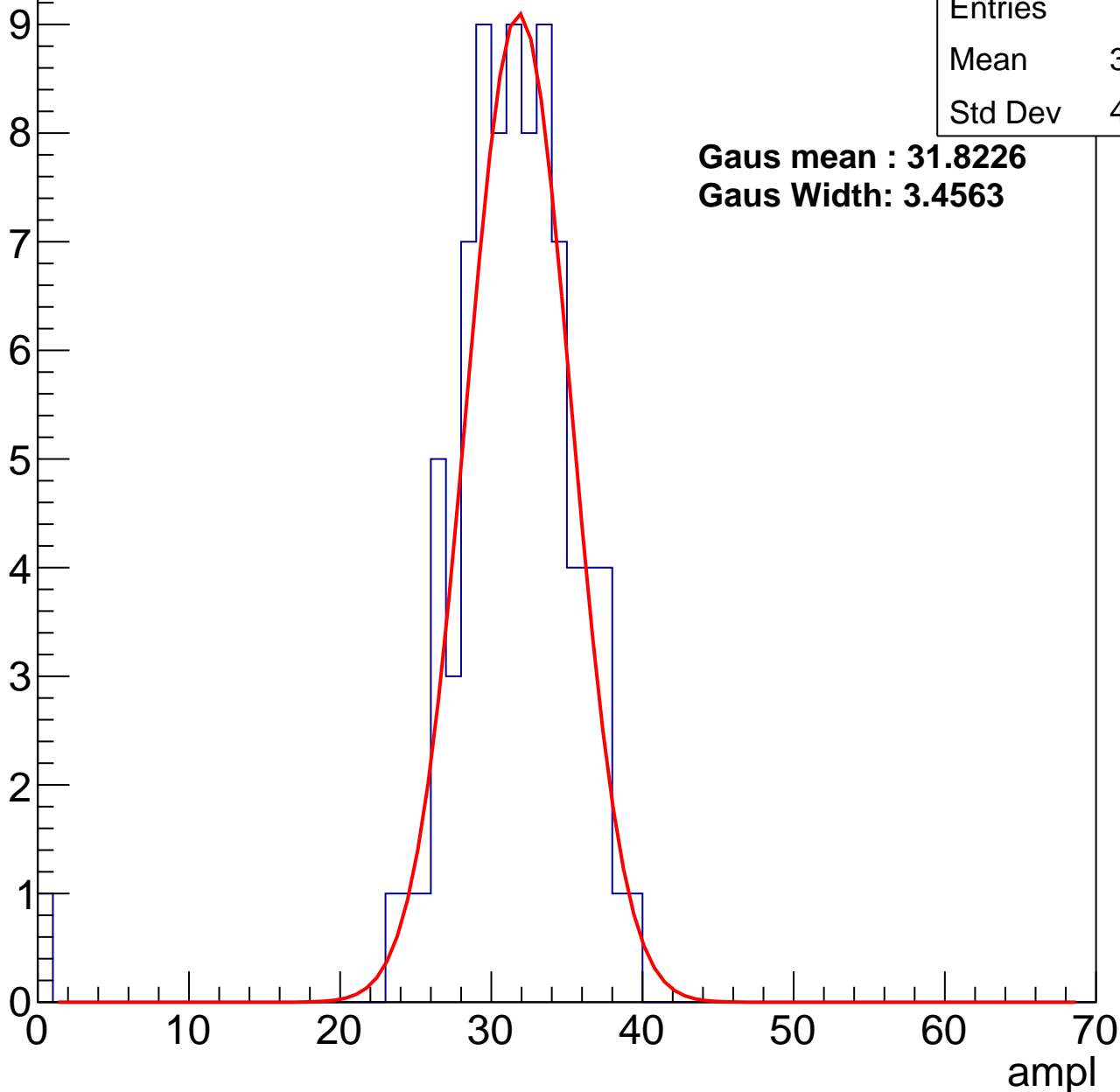
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	30.83
Std Dev	4.804

**Gaus mean : 31.8226**

**Gaus Width: 3.4563**



# B1L002S, U8-ch45, adc1

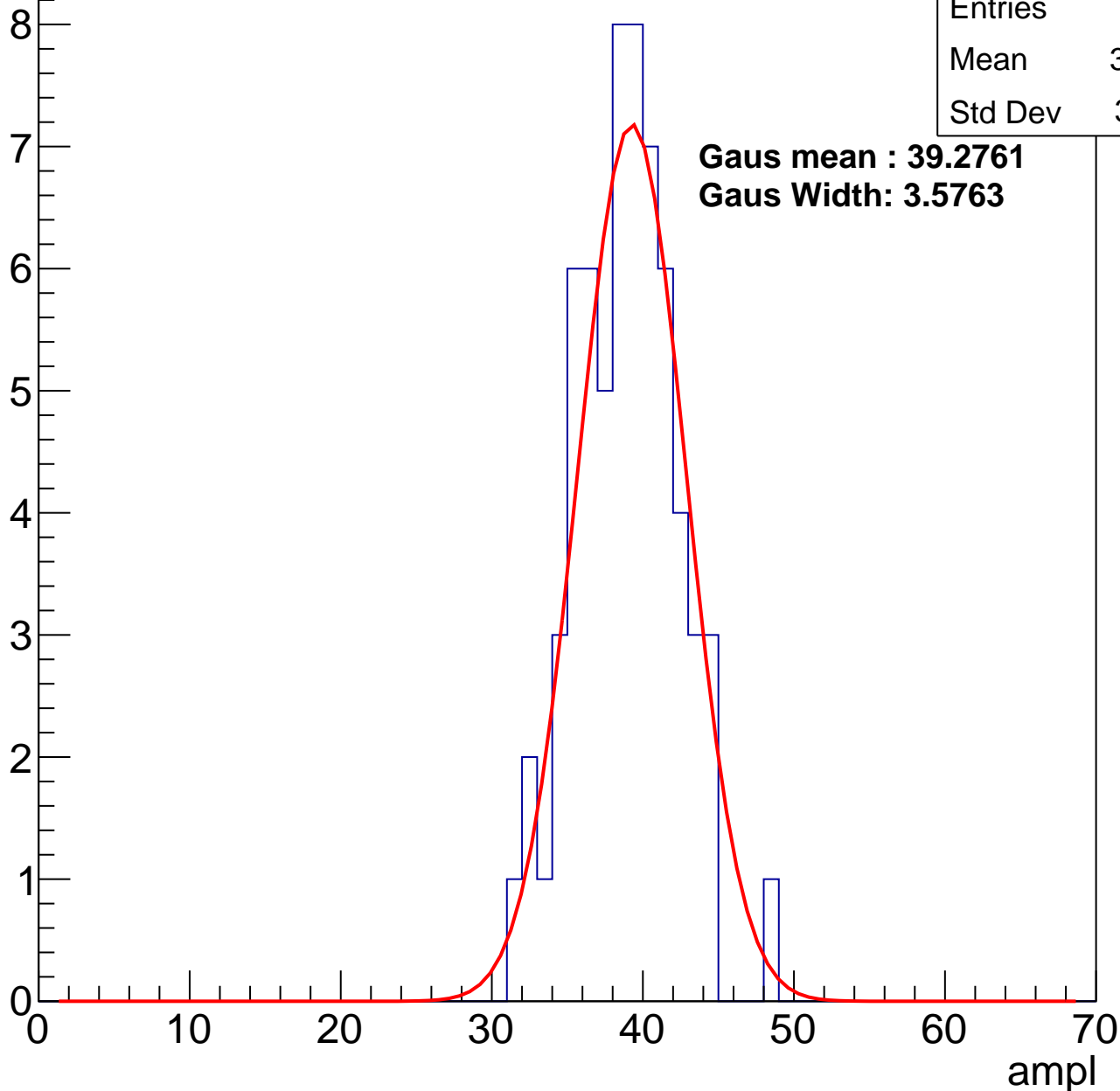
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	38.44
Std Dev	3.311

**Gaus mean : 39.2761**

**Gaus Width: 3.5763**



# B1L002S, U8-ch45, adc2

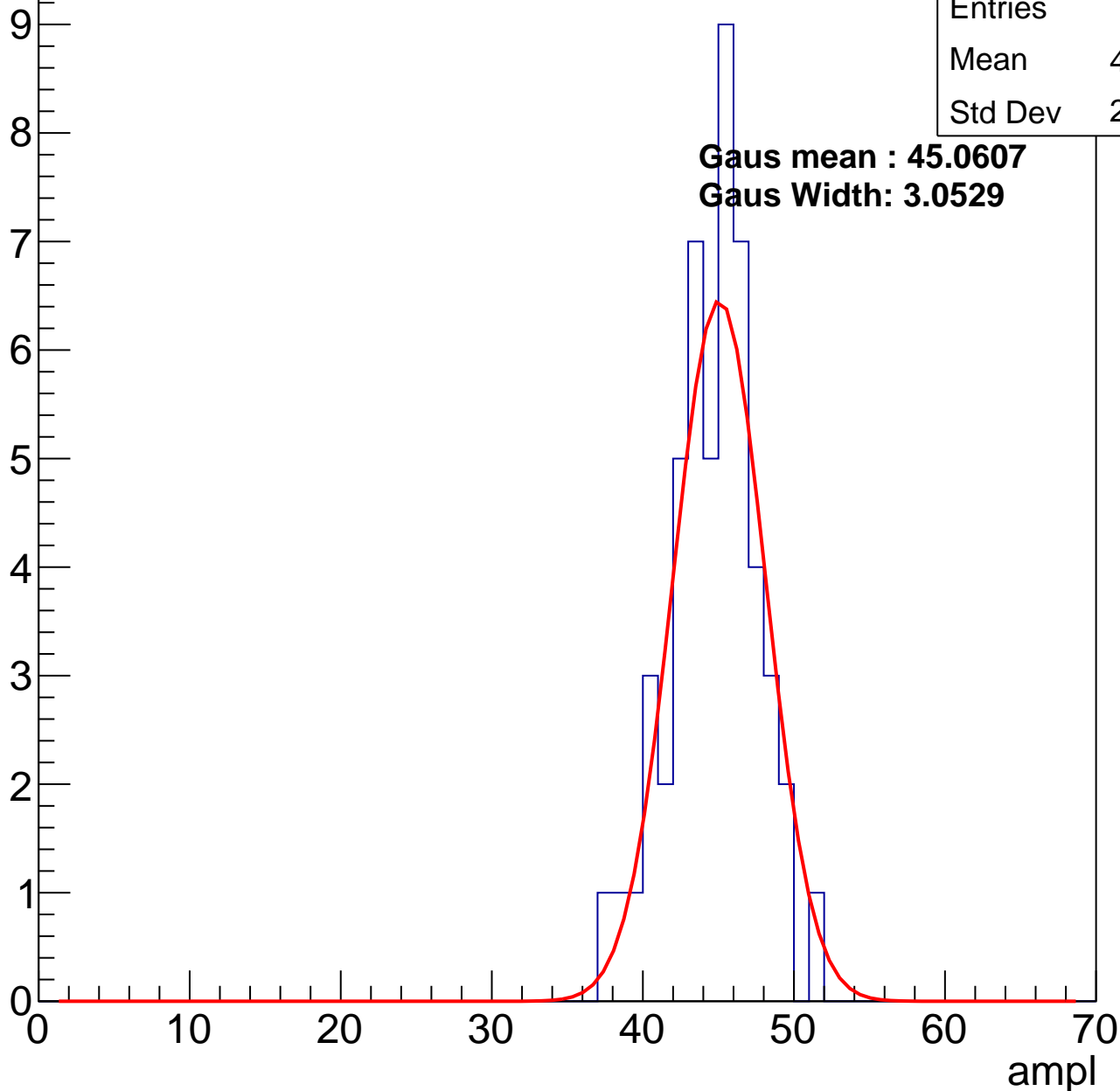
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	44.22
Std Dev	2.872

**Gaus mean : 45.0607**

**Gaus Width: 3.0529**

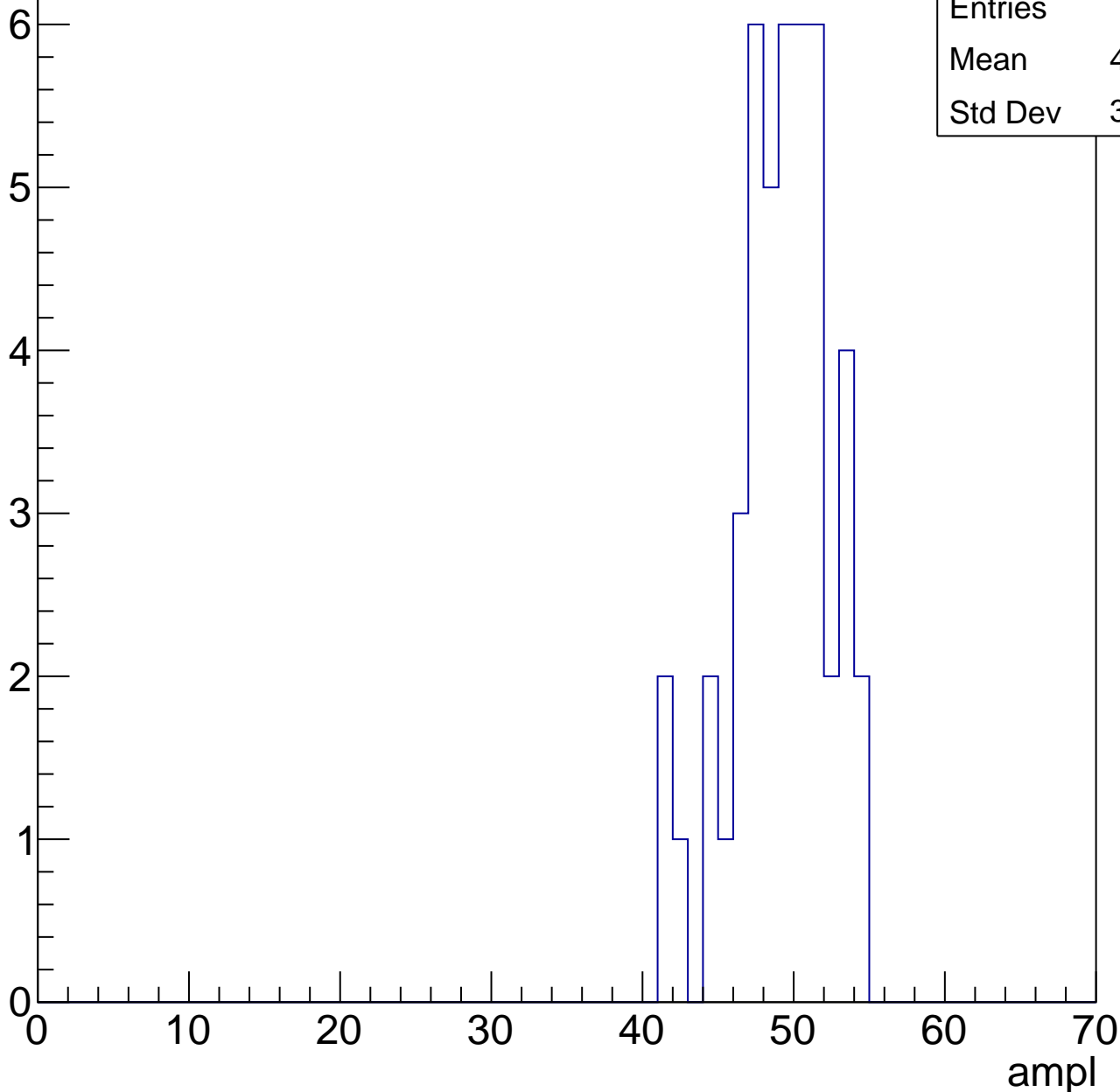


# B1L002S, U8-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

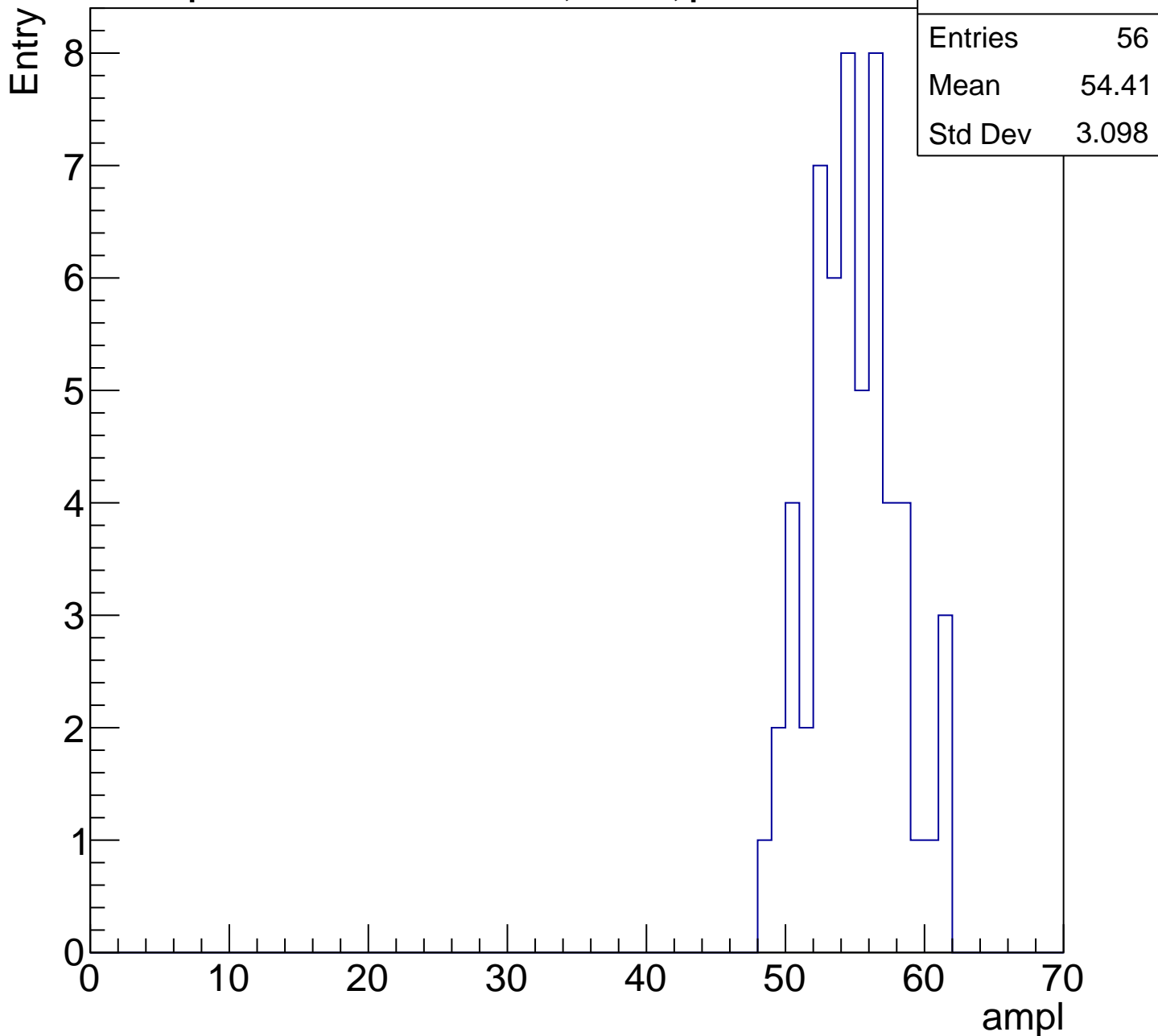
Entry

Entries	46
Mean	48.72
Std Dev	3.153



# B1L002S, U8-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

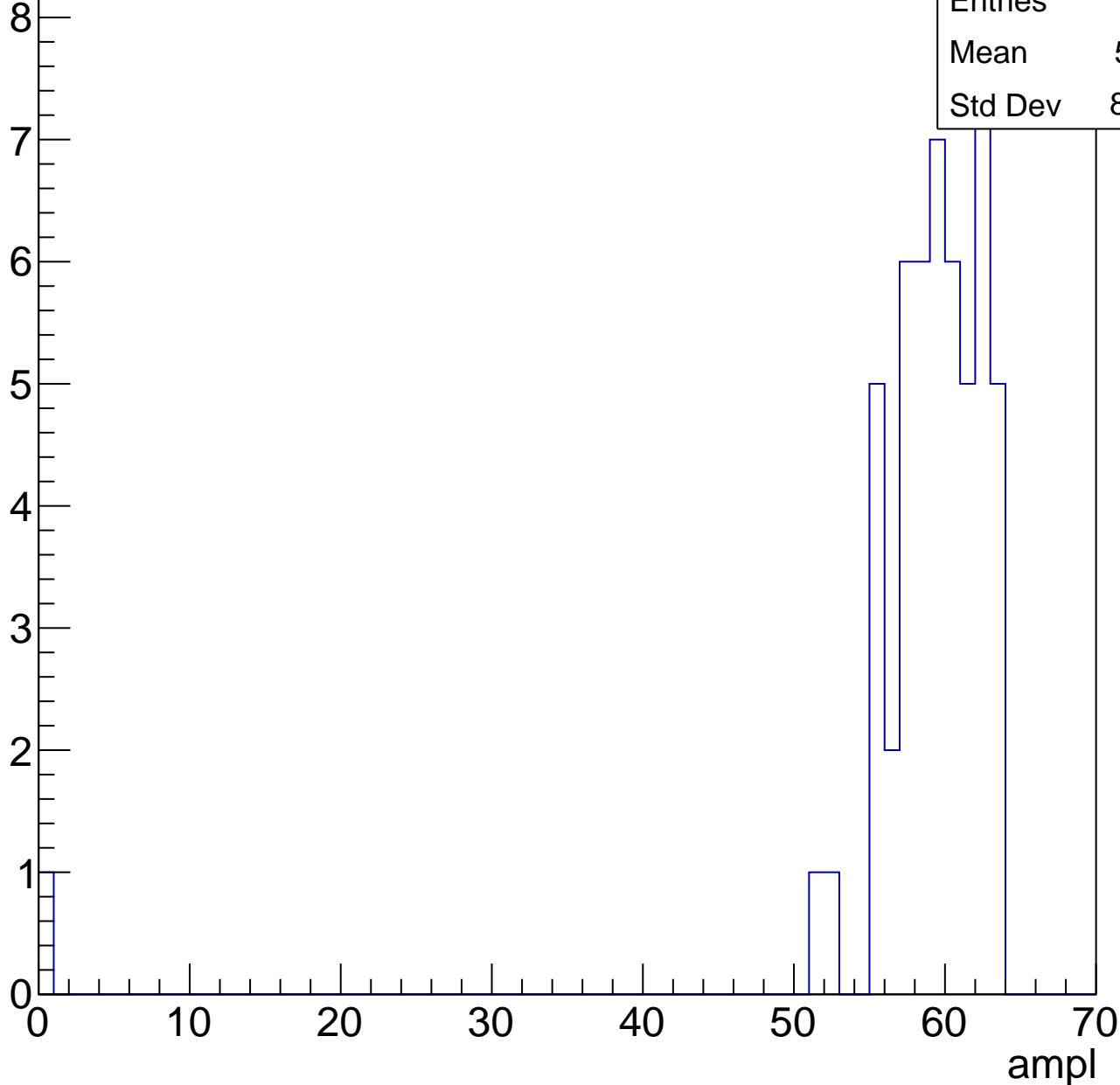


# B1L002S, U8-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.91
Std Dev	8.508



# B1L002S, U8-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

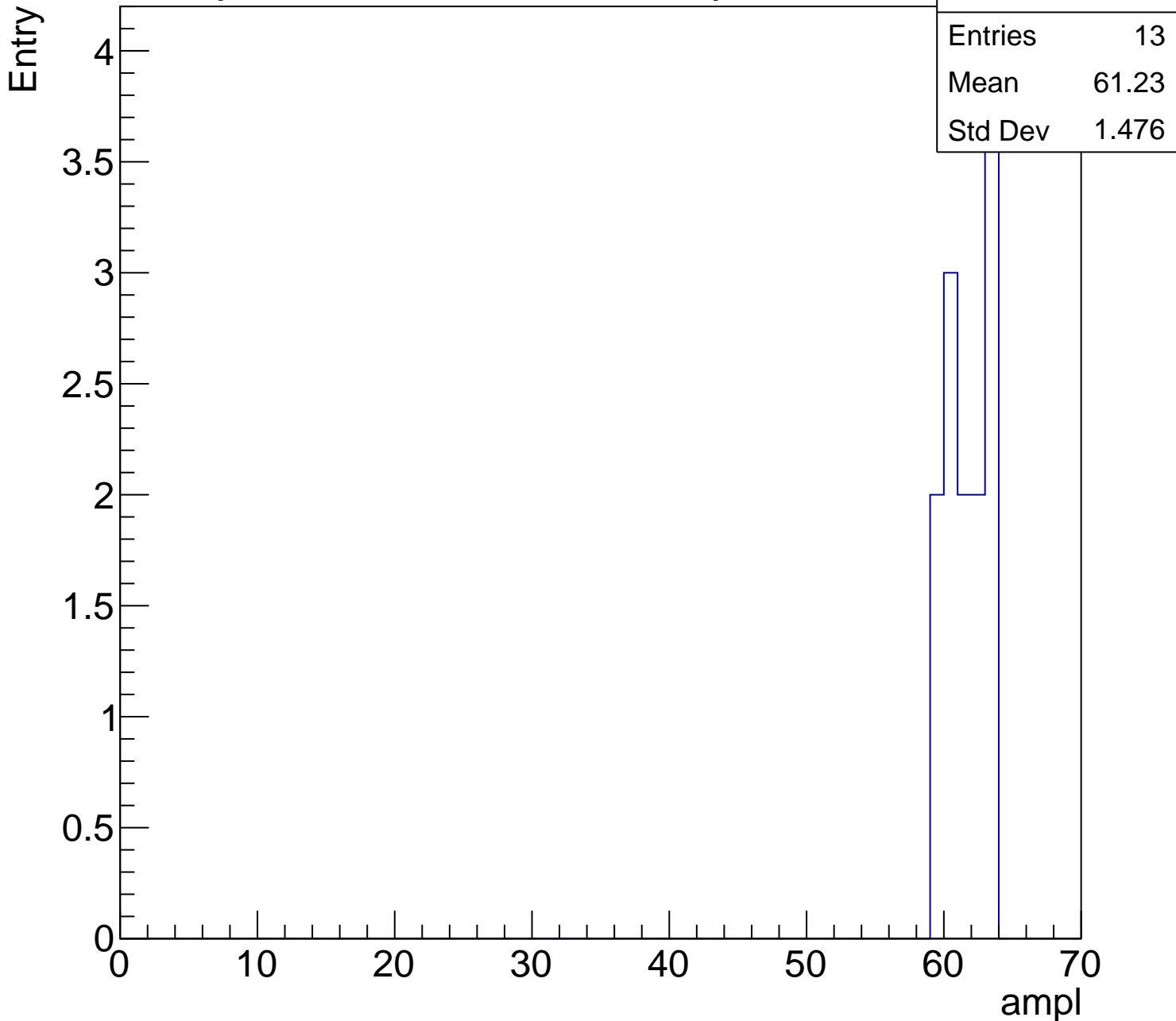
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	13
Mean	61.23
Std Dev	1.476

ampl

0 10 20 30 40 50 60 70





# B1L002S, U8-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch46, adc0

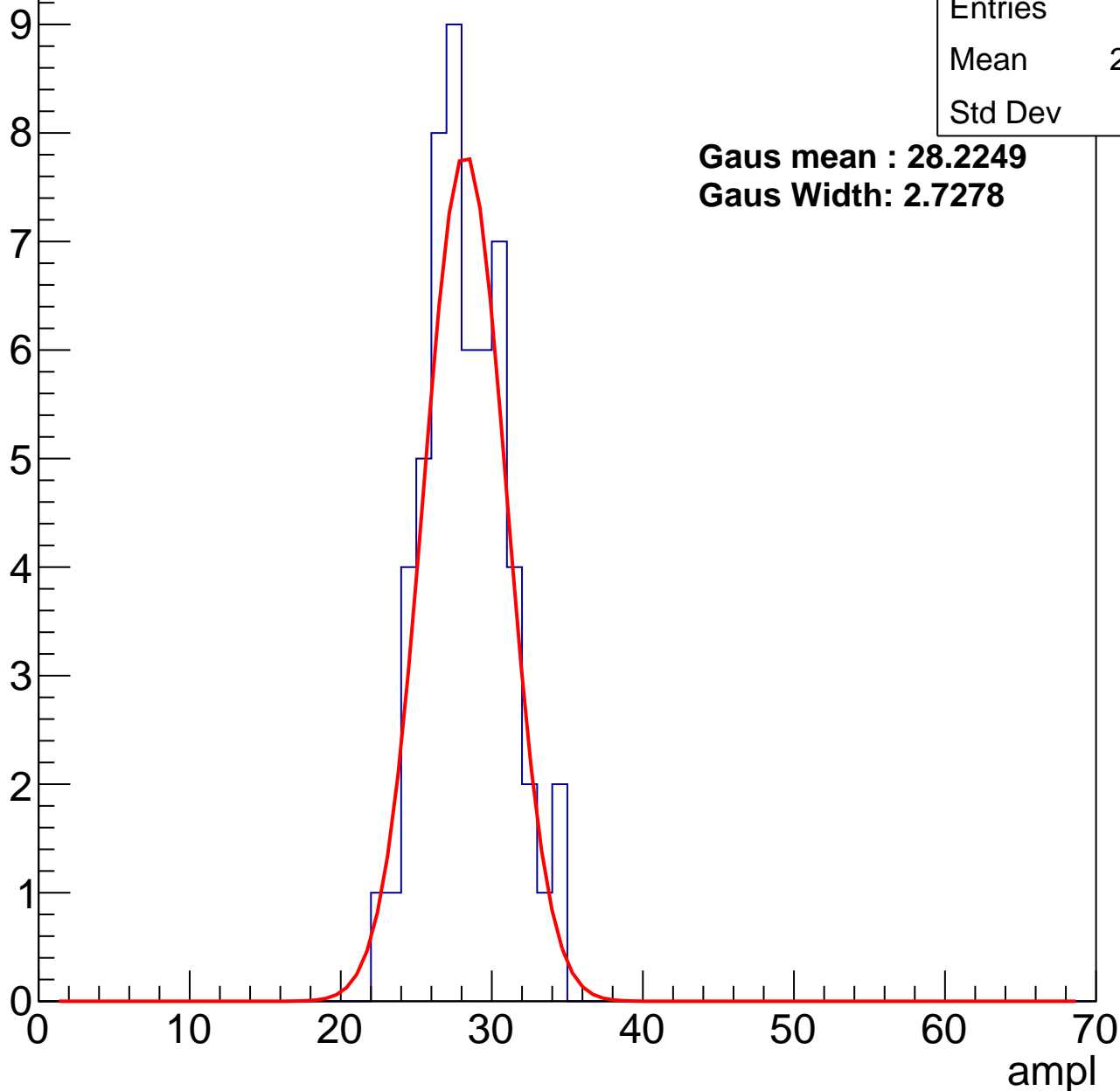
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	27.82
Std Dev	2.7

**Gaus mean : 28.2249**

**Gaus Width: 2.7278**



# B1L002S, U8-ch46, adc1

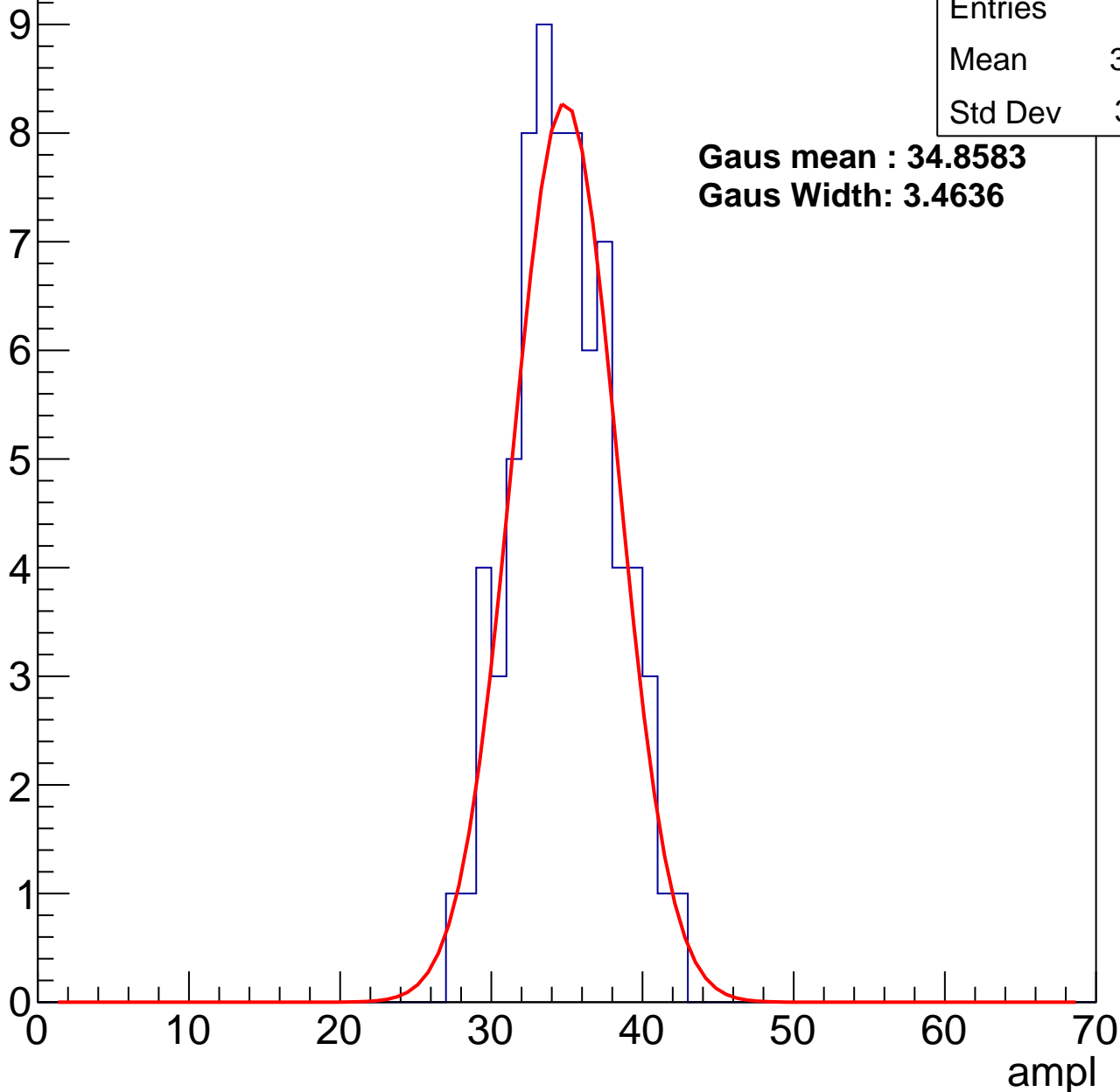
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34.34
Std Dev	3.291

**Gaus mean : 34.8583**

**Gaus Width: 3.4636**

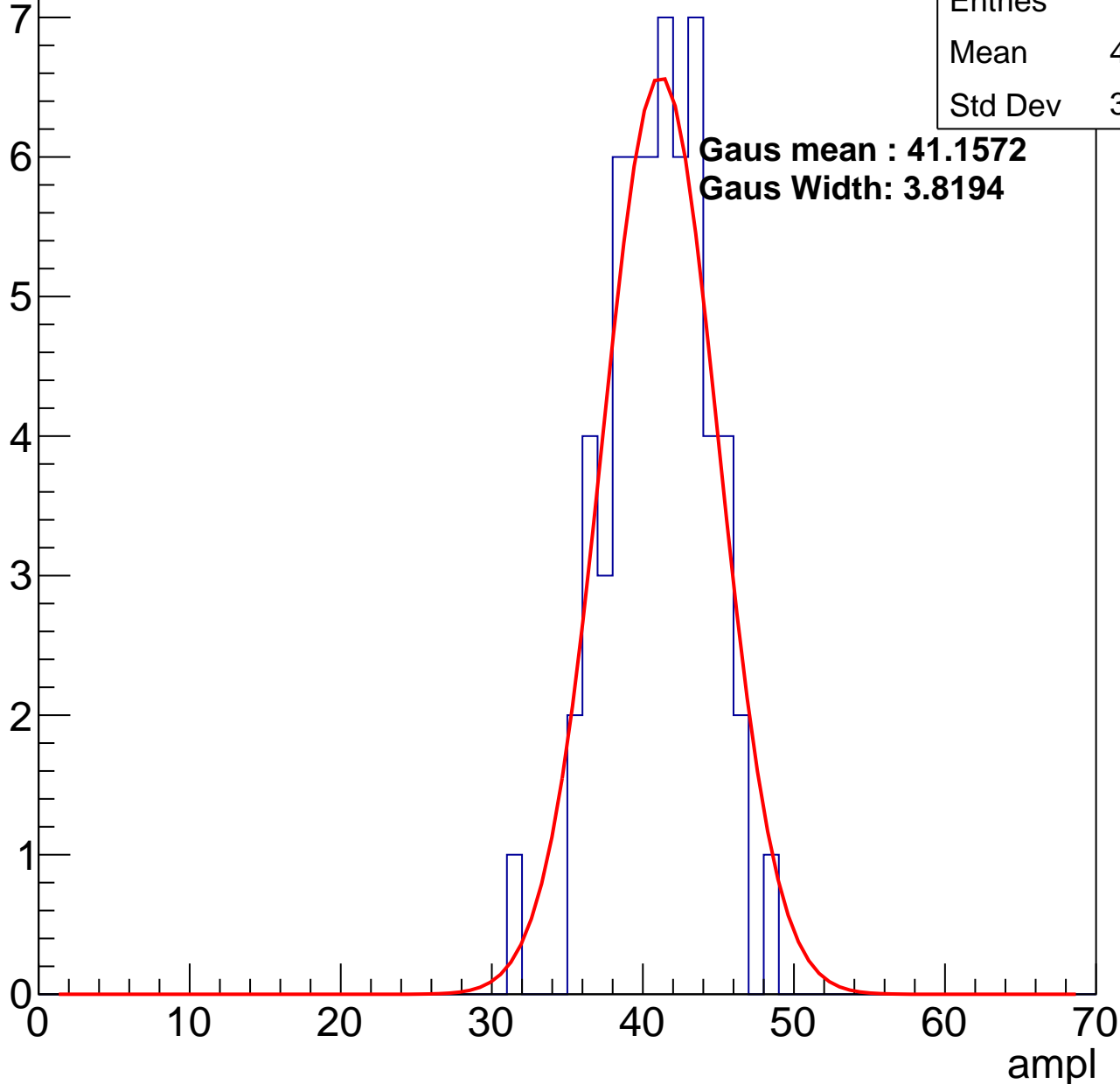


# B1L002S, U8-ch46, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

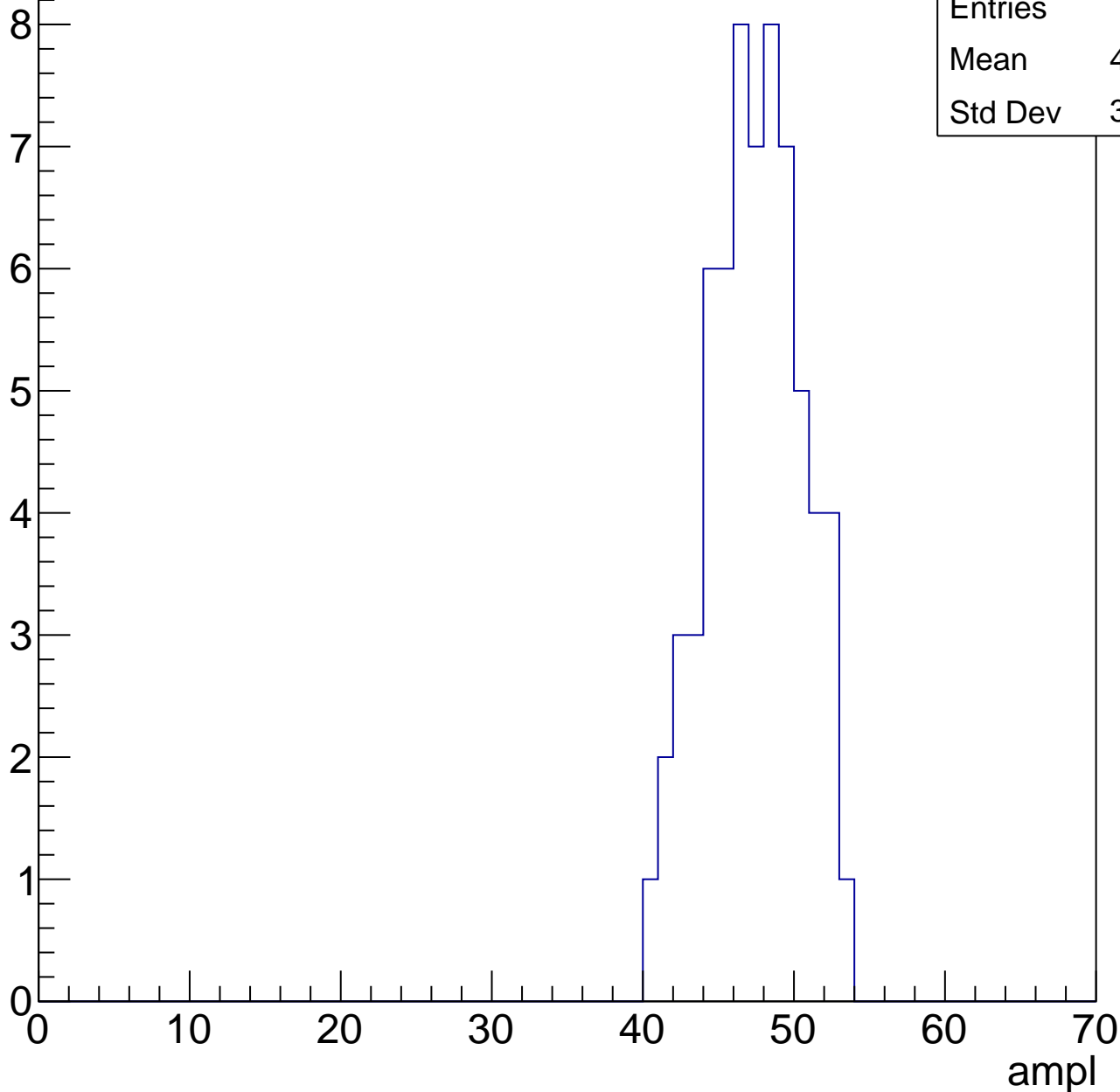
Entries	59
Mean	40.58
Std Dev	3.258



# B1L002S, U8-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

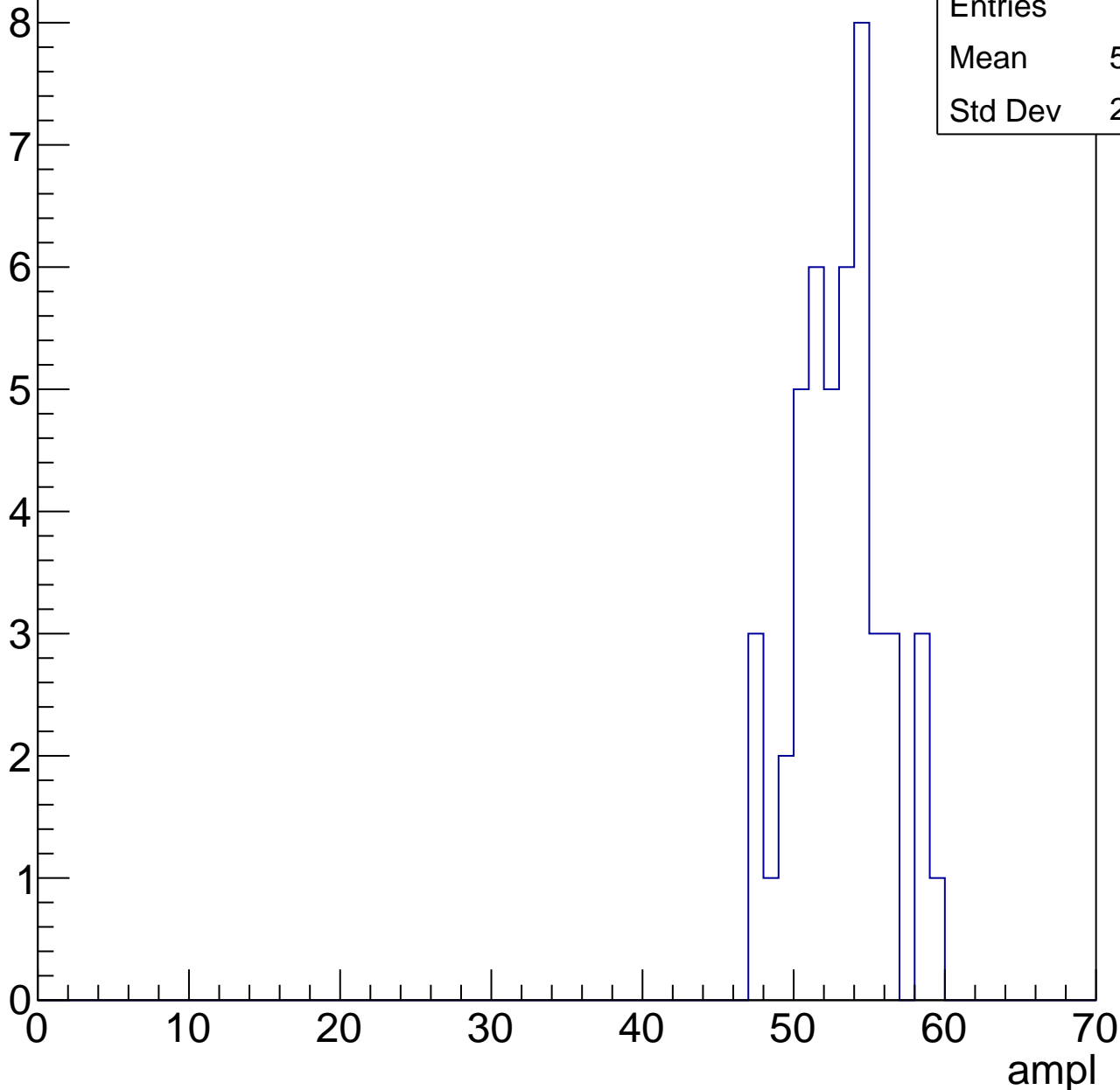


# B1L002S, U8-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

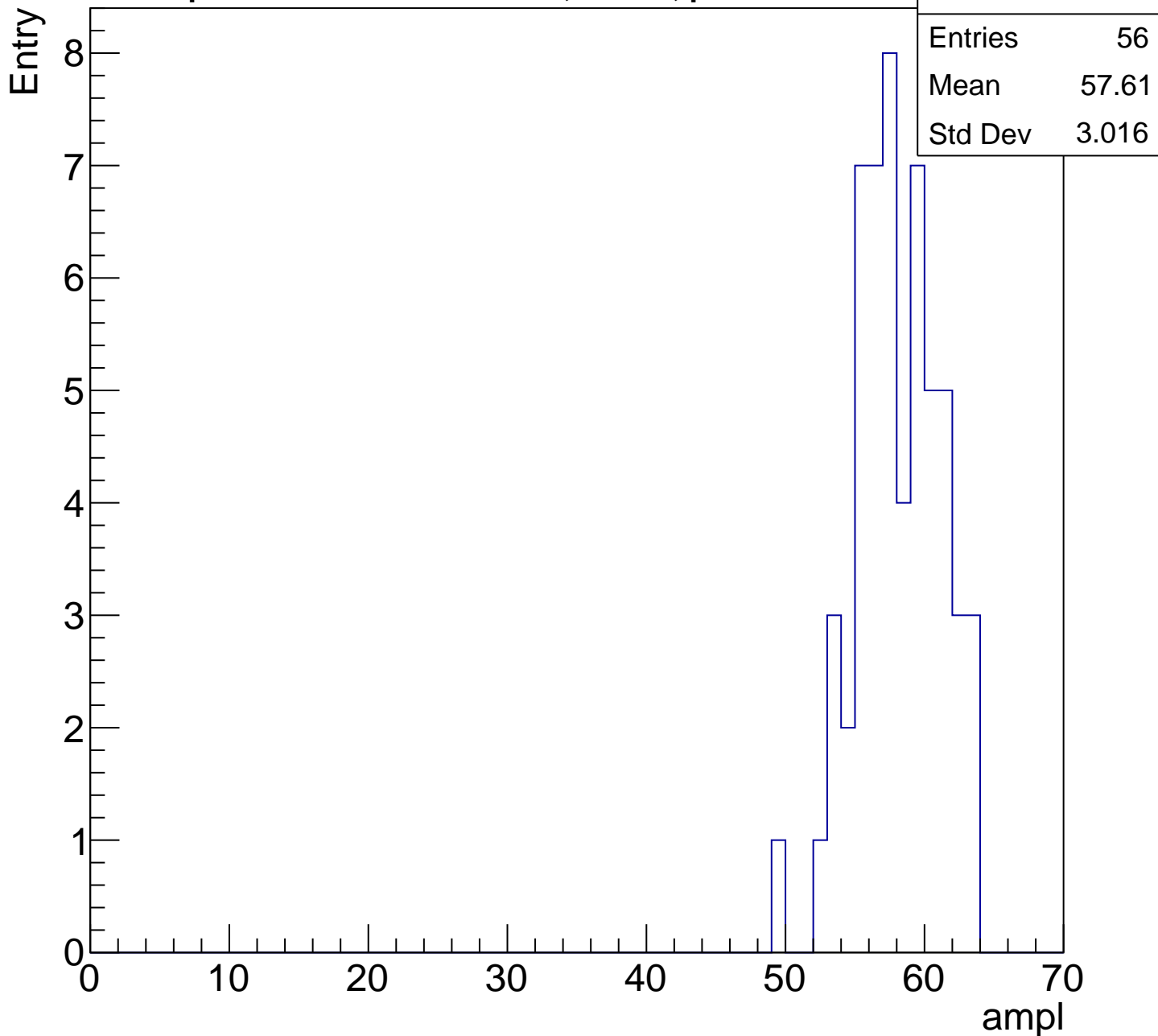
Entry

Entries	46
Mean	52.59
Std Dev	2.909



# B1L002S, U8-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

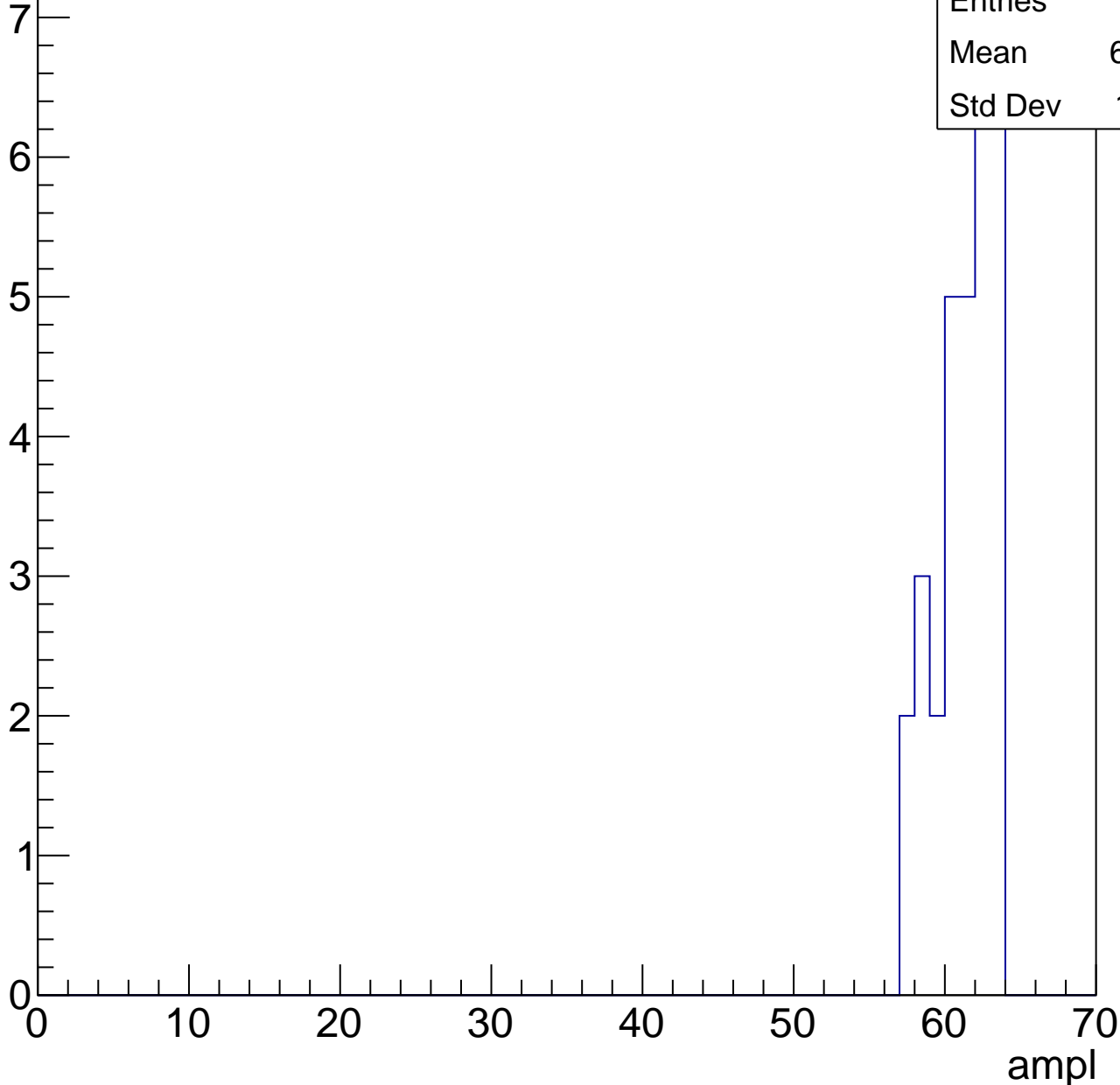


# B1L002S, U8-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	60.84
Std Dev	1.851





# B1L002S, U8-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch47, adc0

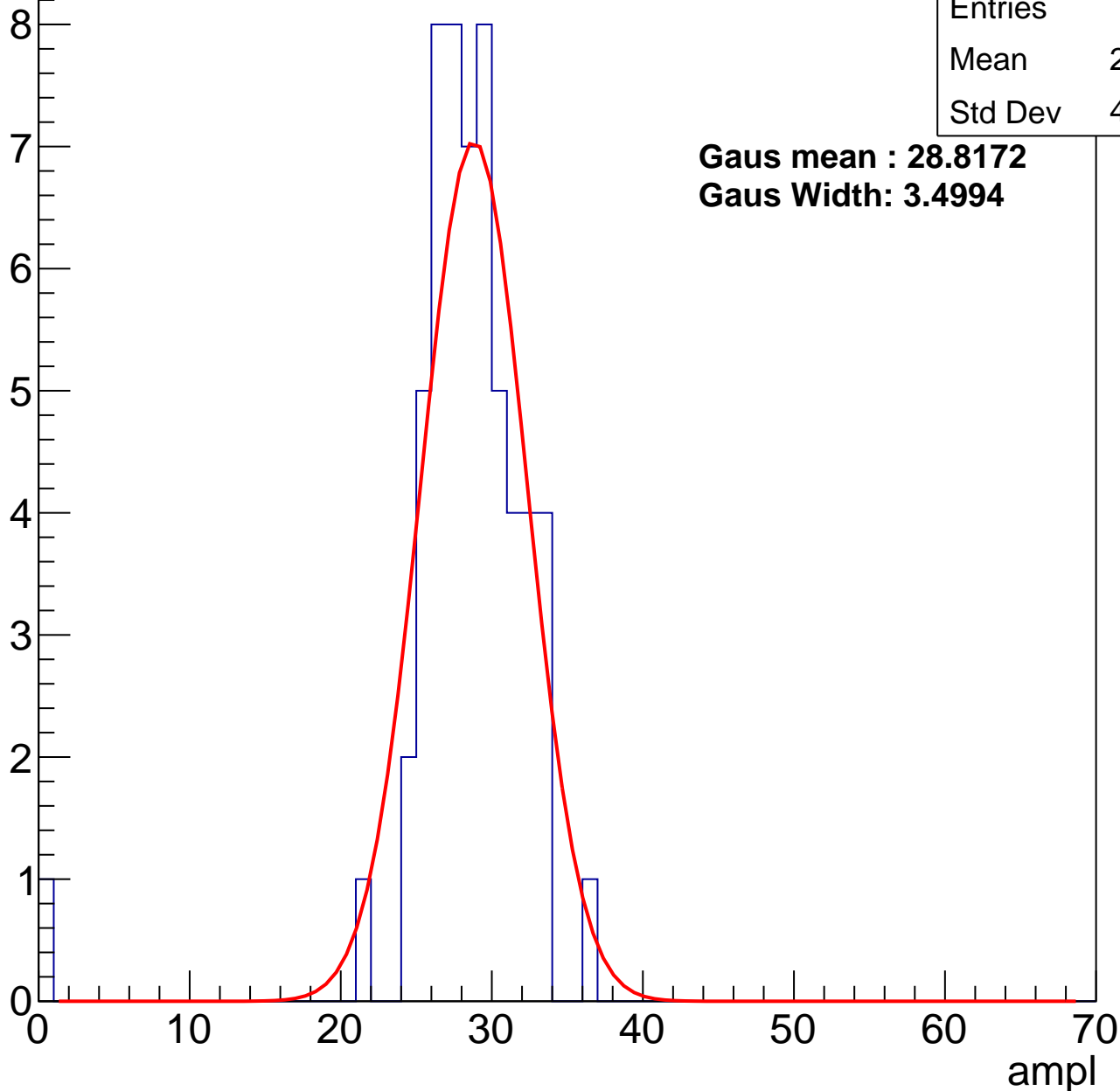
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.86
Std Dev	4.625

**Gaus mean : 28.8172**

**Gaus Width: 3.4994**



# B1L002S, U8-ch47, adc1

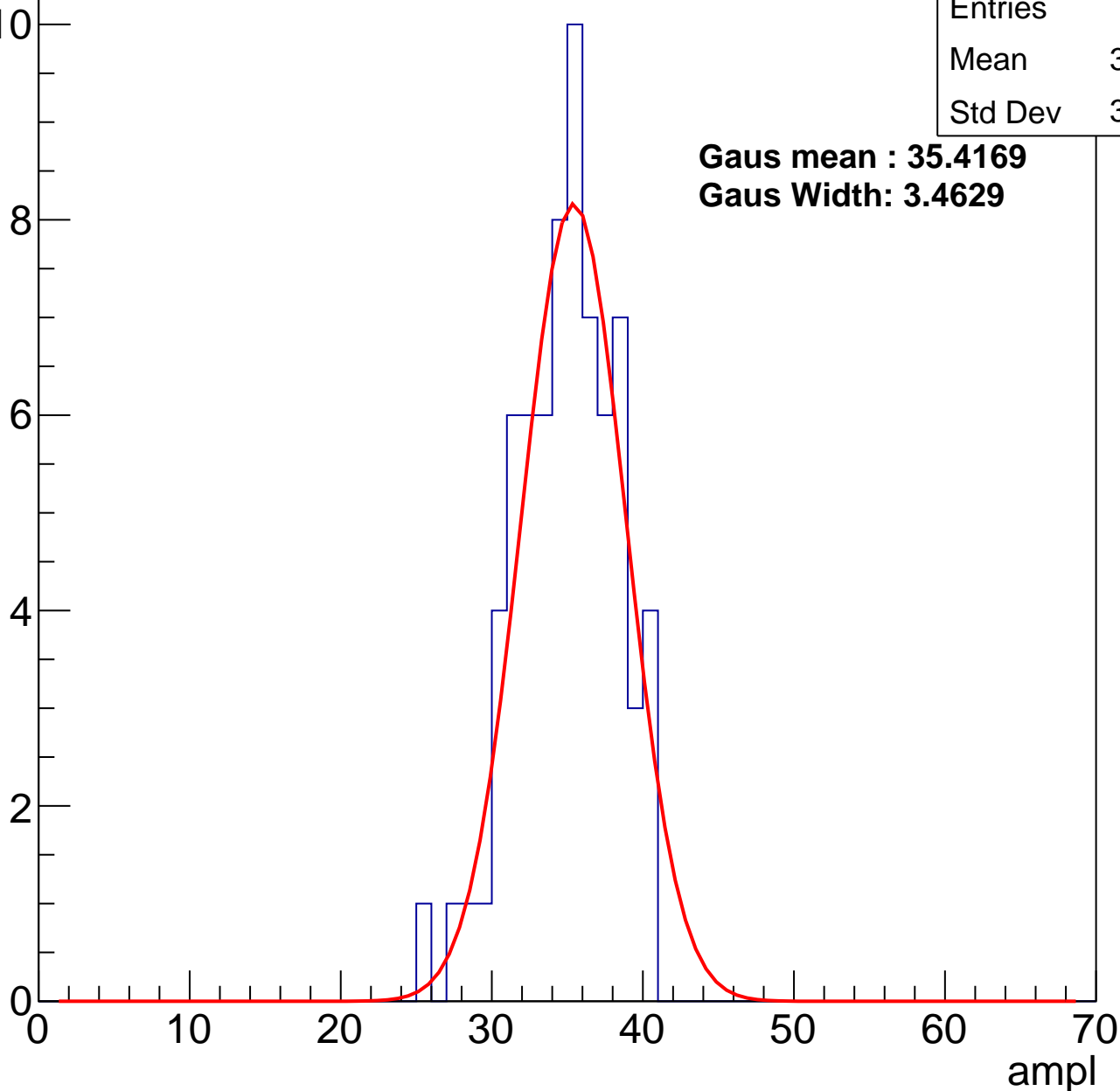
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.42
Std Dev	3.249

**Gaus mean : 35.4169**

**Gaus Width: 3.4629**

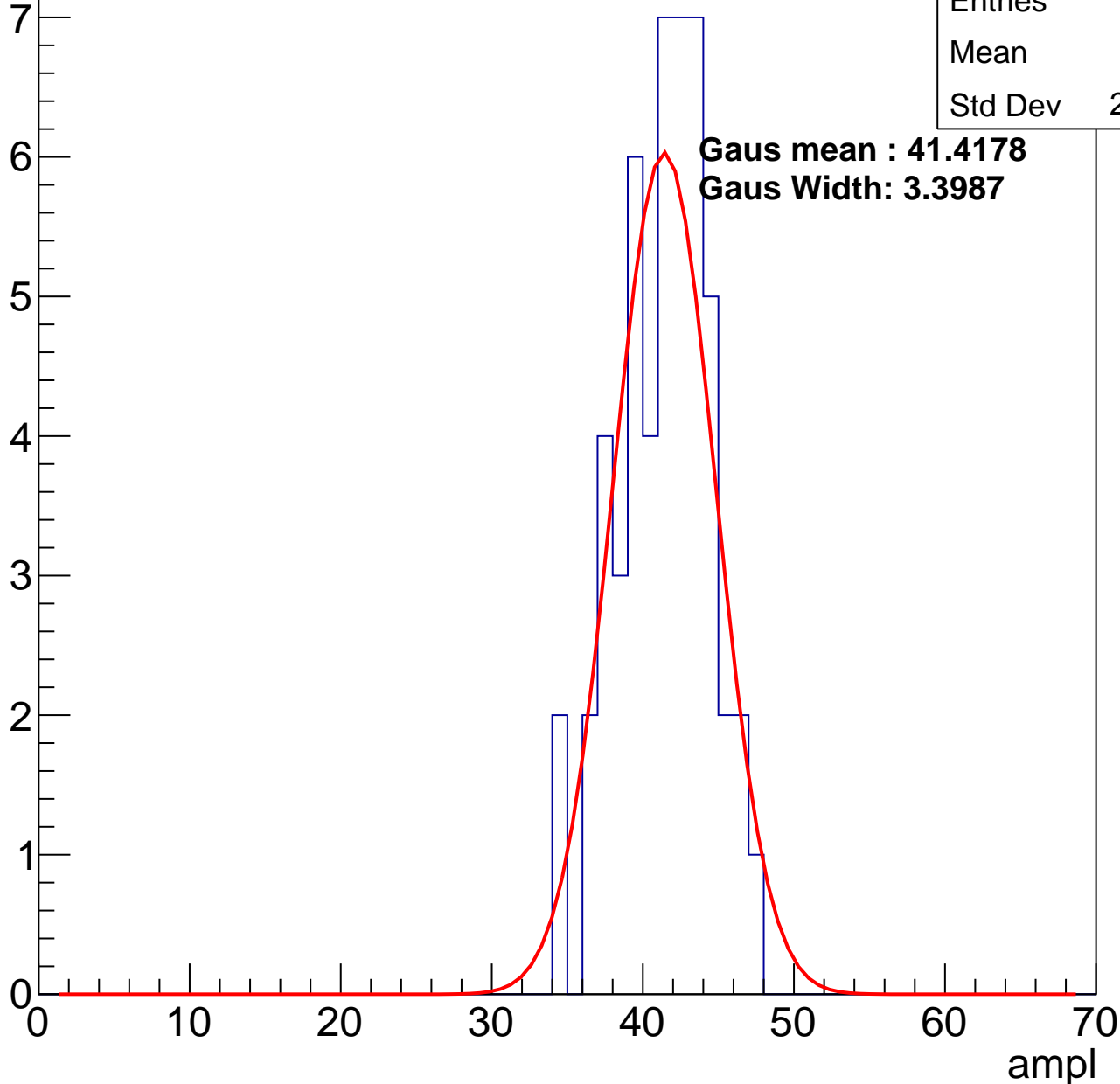


# B1L002S, U8-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

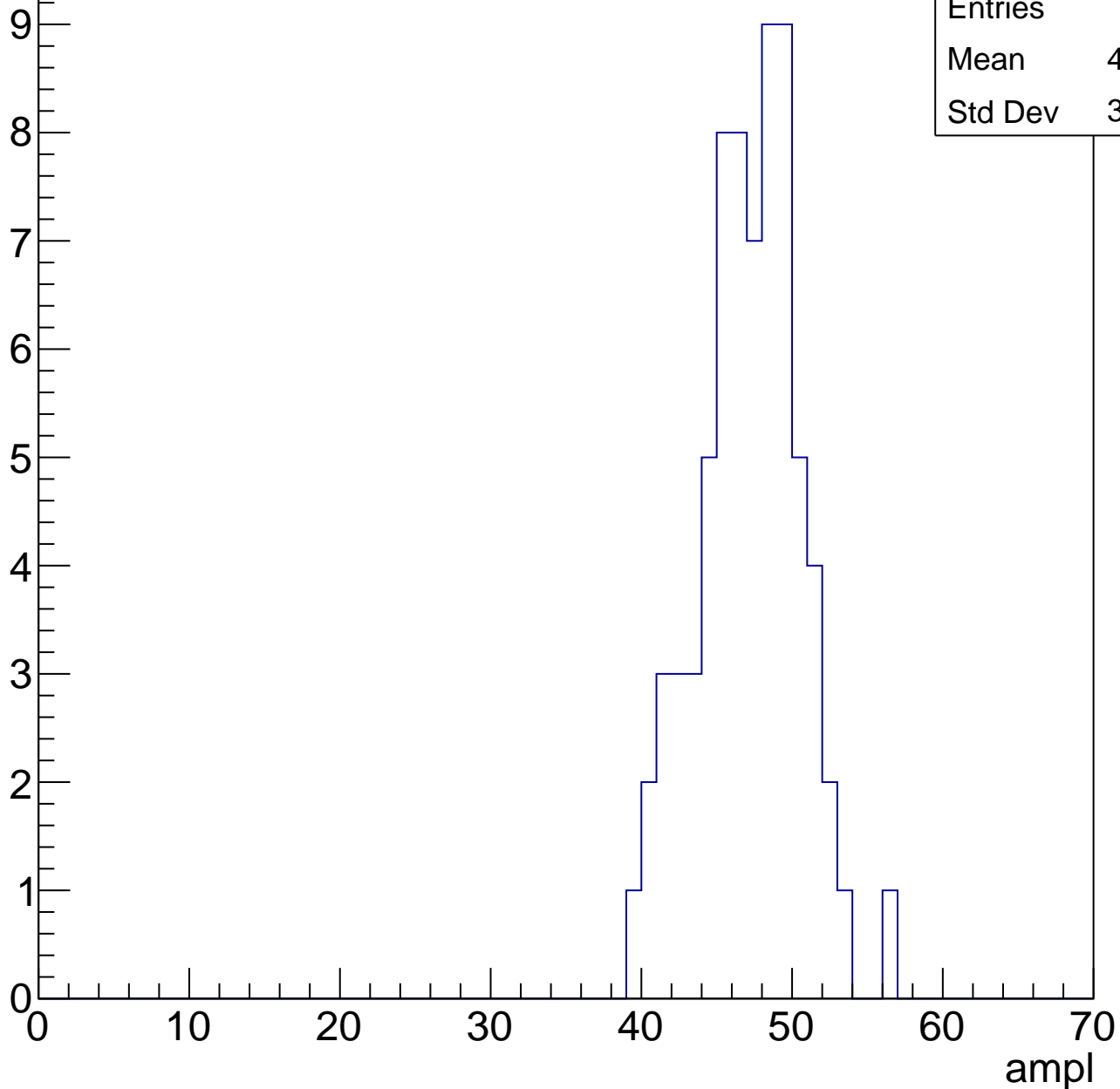
Entries	52
Mean	40.9
Std Dev	2.989



# B1L002S, U8-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



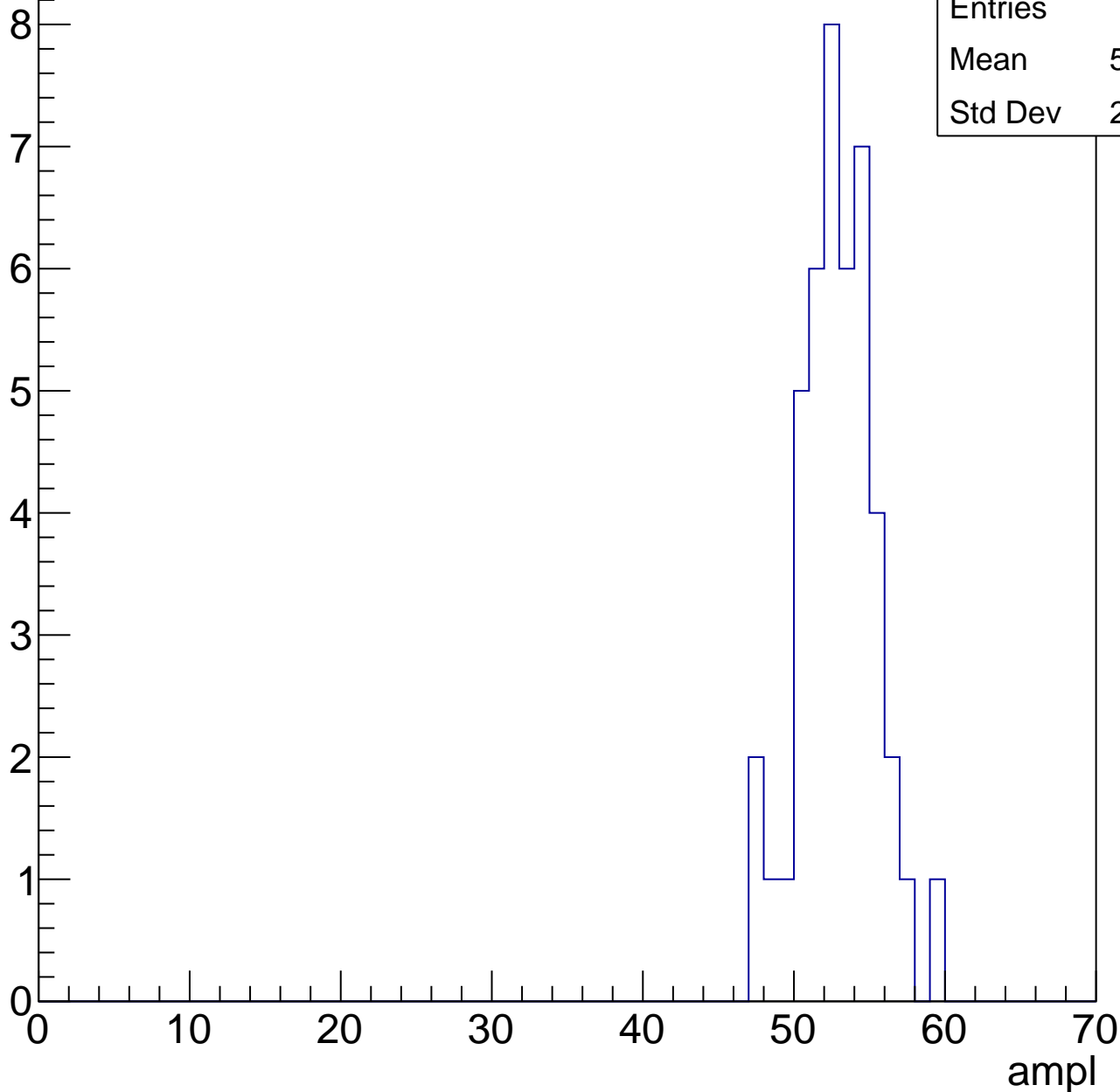
Entries	71
Mean	46.68
Std Dev	3.339

# B1L002S, U8-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	52.43
Std Dev	2.472



# B1L002S, U8-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	58.09
Std Dev	2.495

Entry

10

8

6

4

2

0

0

10

20

30

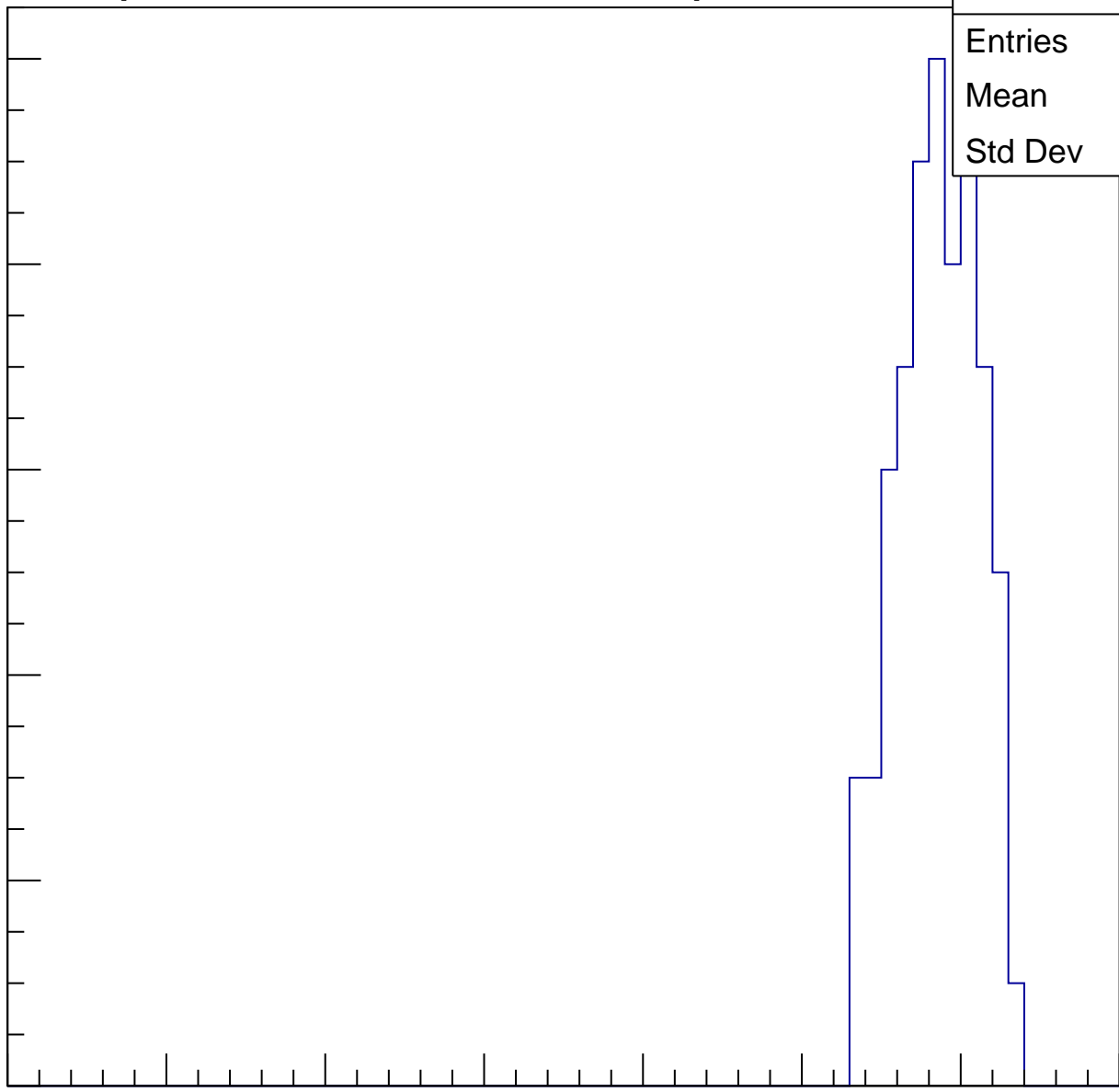
40

50

60

70

ampl

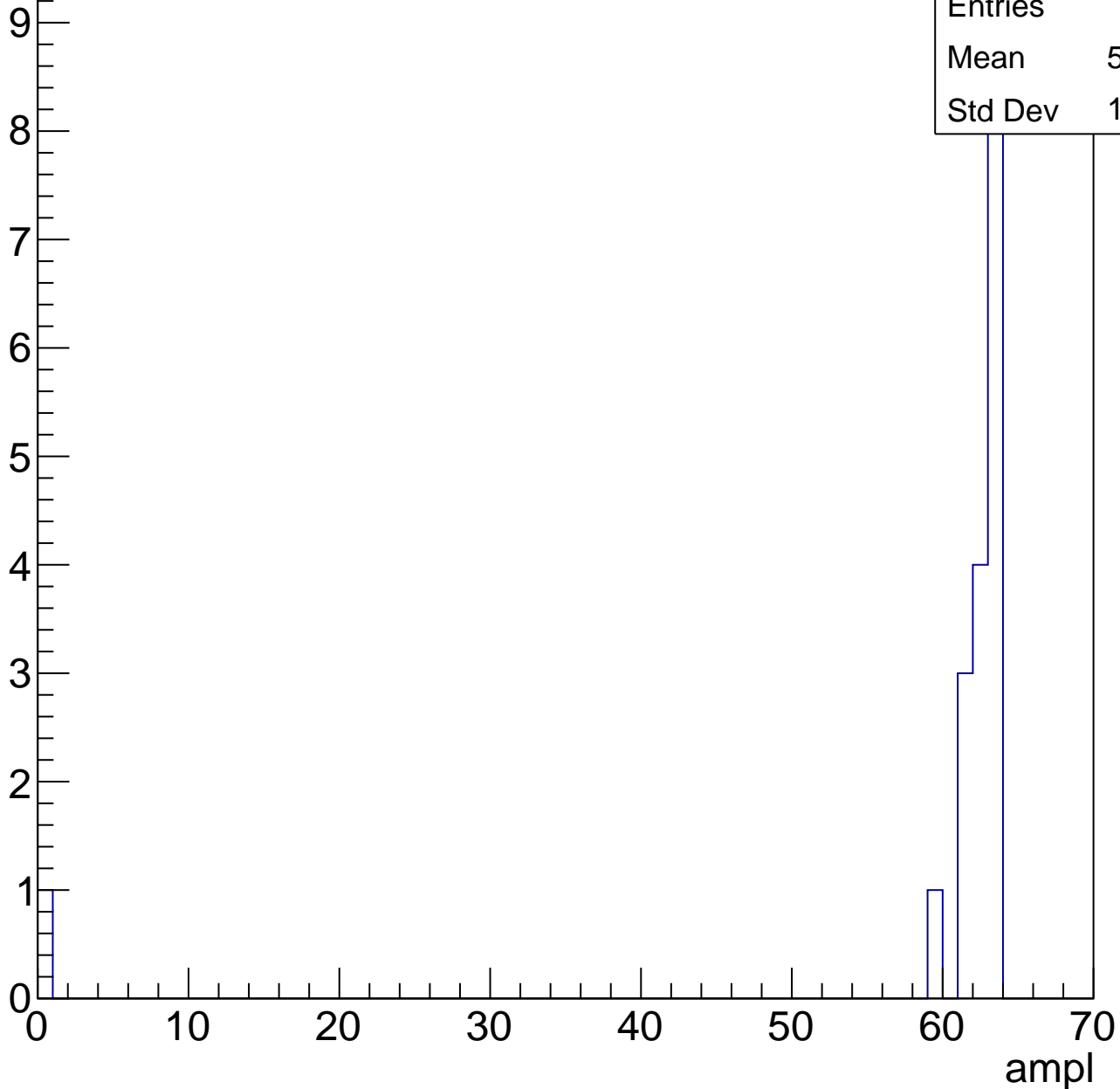


# B1L002S, U8-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.72
Std Dev	14.28

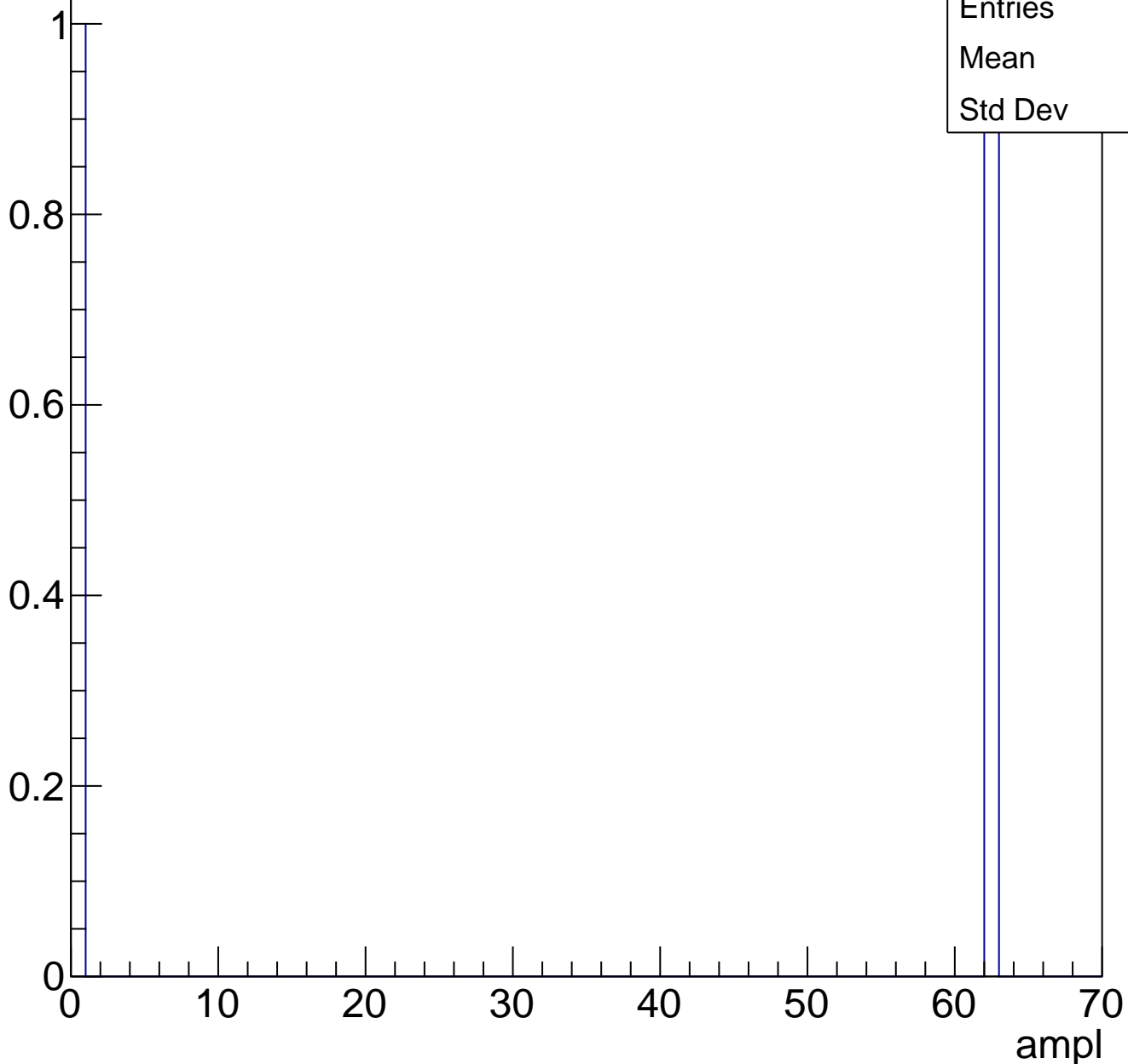




# B1L002S, U8-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	31
Std Dev	31

# B1L002S, U8-ch48, adc0

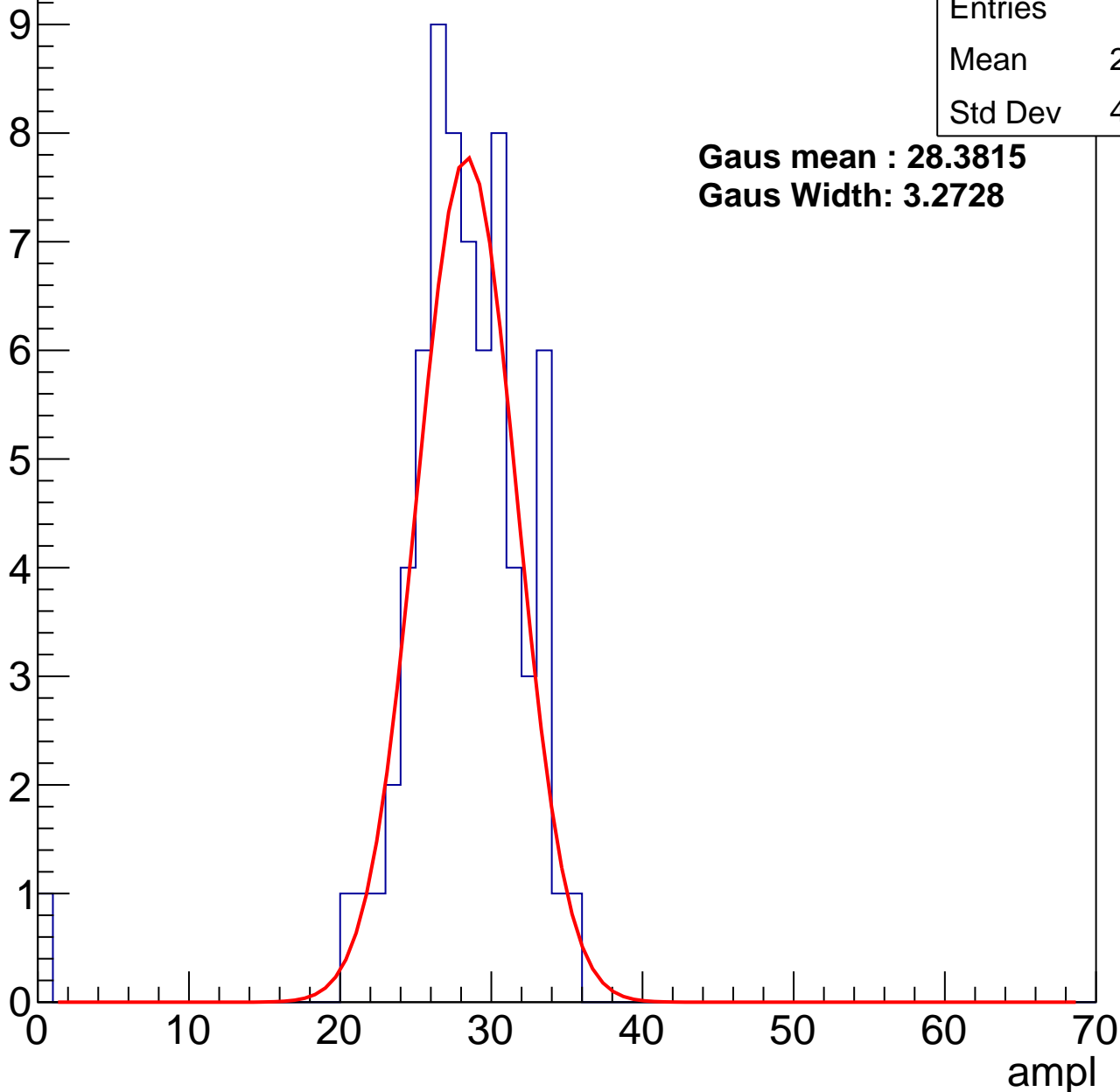
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.57
Std Dev	4.645

**Gaus mean : 28.3815**

**Gaus Width: 3.2728**



# B1L002S, U8-ch48, adc1

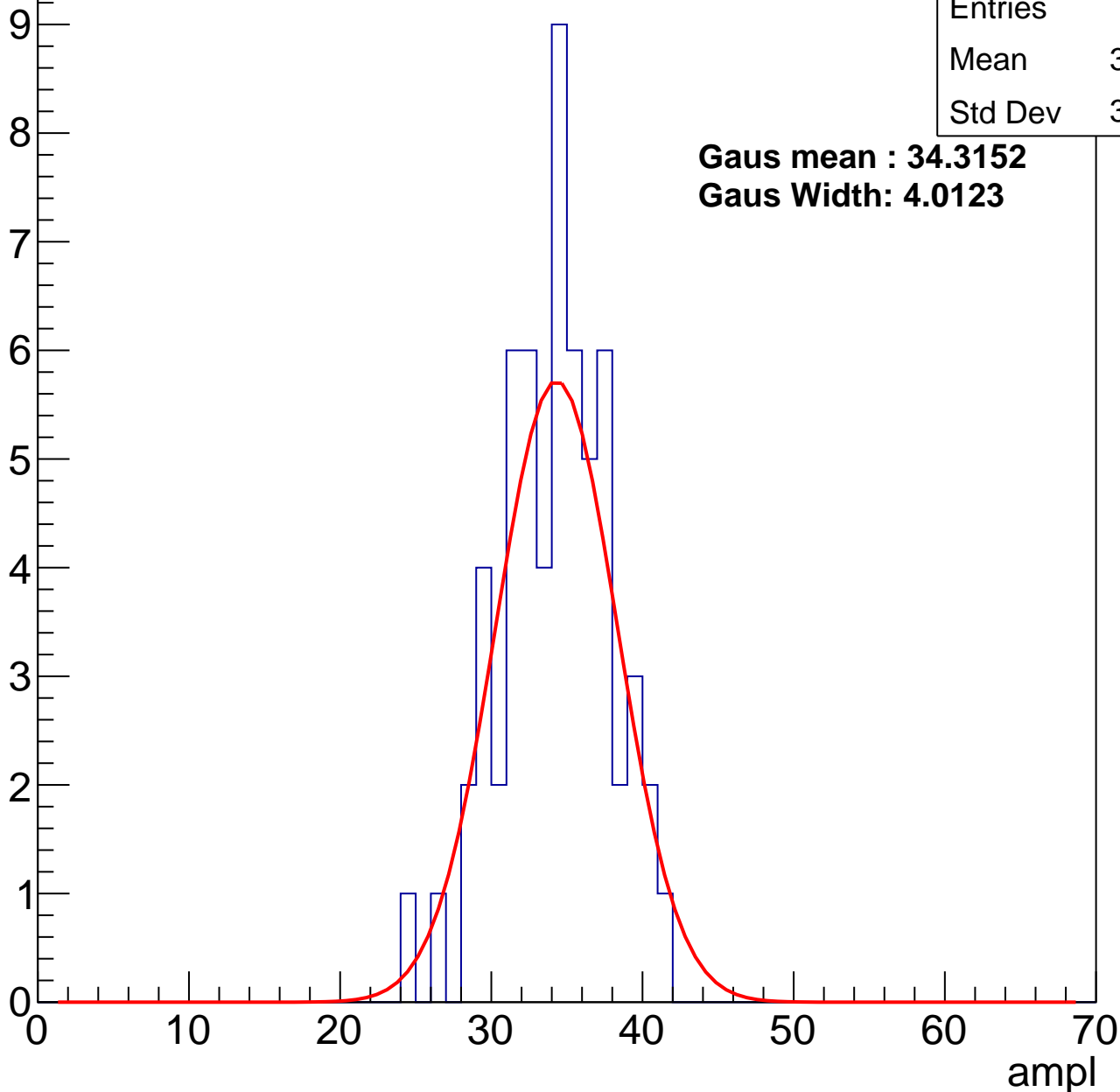
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.73
Std Dev	3.544

**Gaus mean : 34.3152**

**Gaus Width: 4.0123**



# B1L002S, U8-ch48, adc2

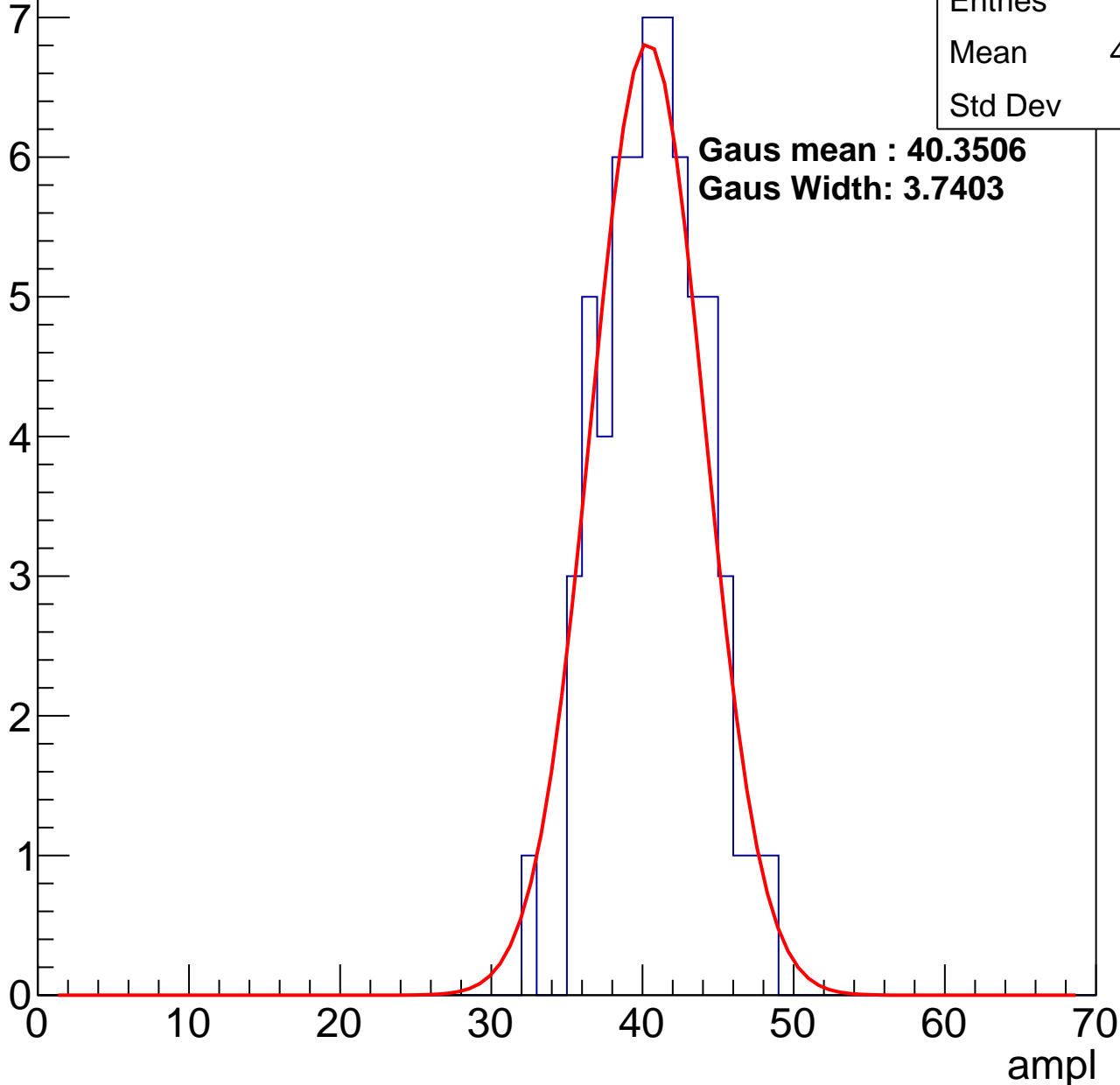
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.28
Std Dev	3.29

**Gaus mean : 40.3506**

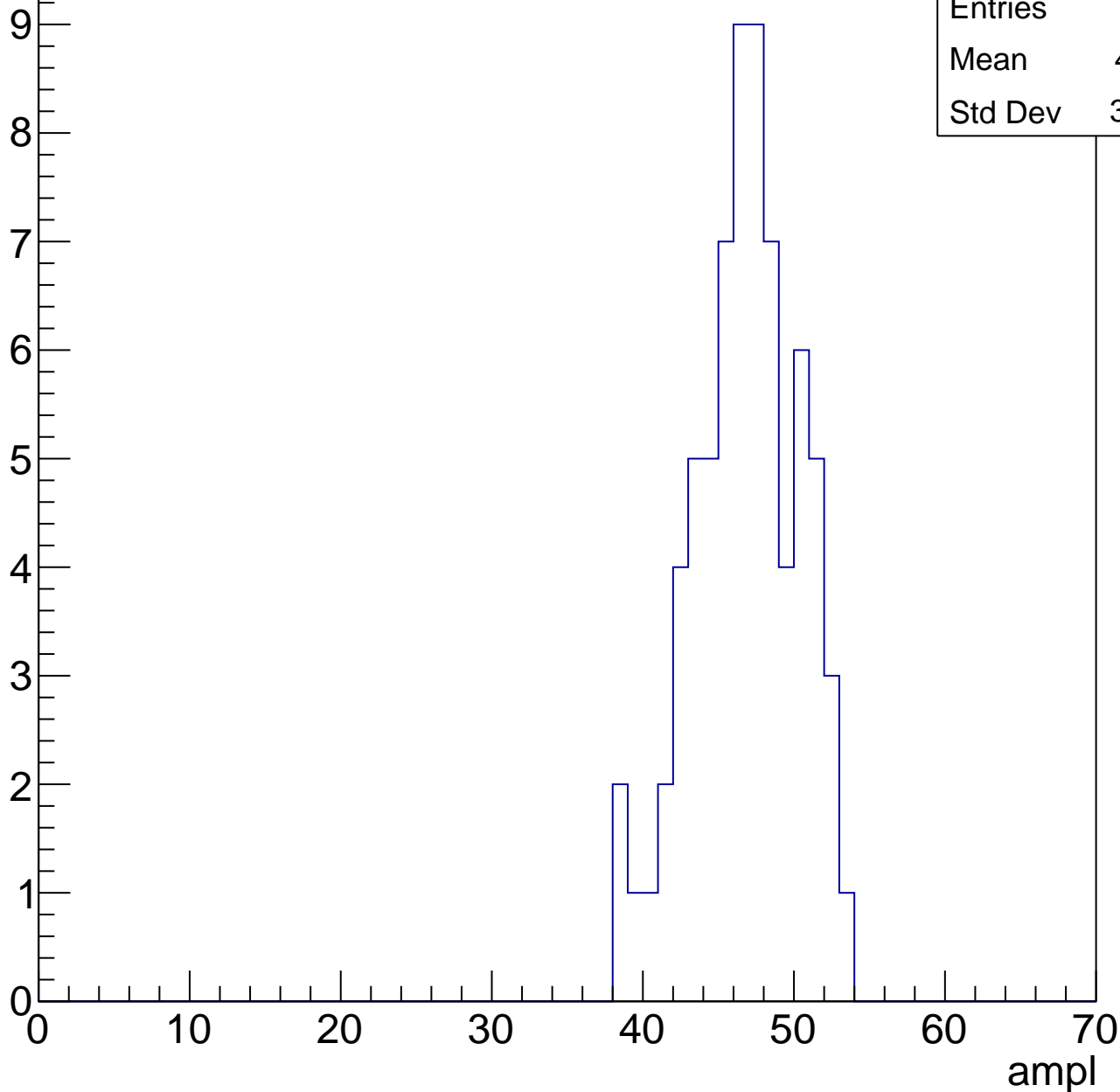
**Gaus Width: 3.7403**



# B1L002S, U8-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

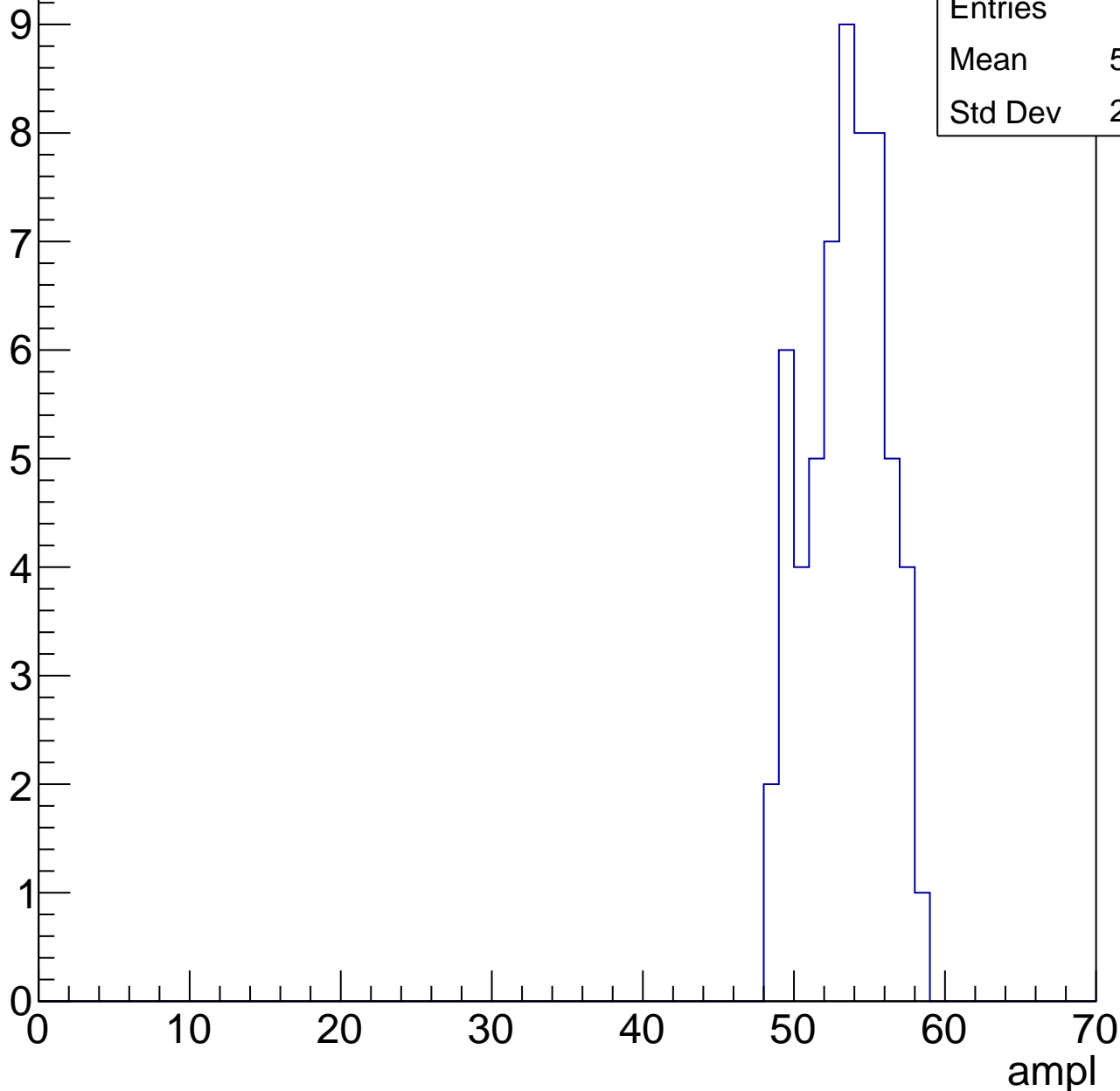


Entries	71
Mean	46.31
Std Dev	3.446

# B1L002S, U8-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

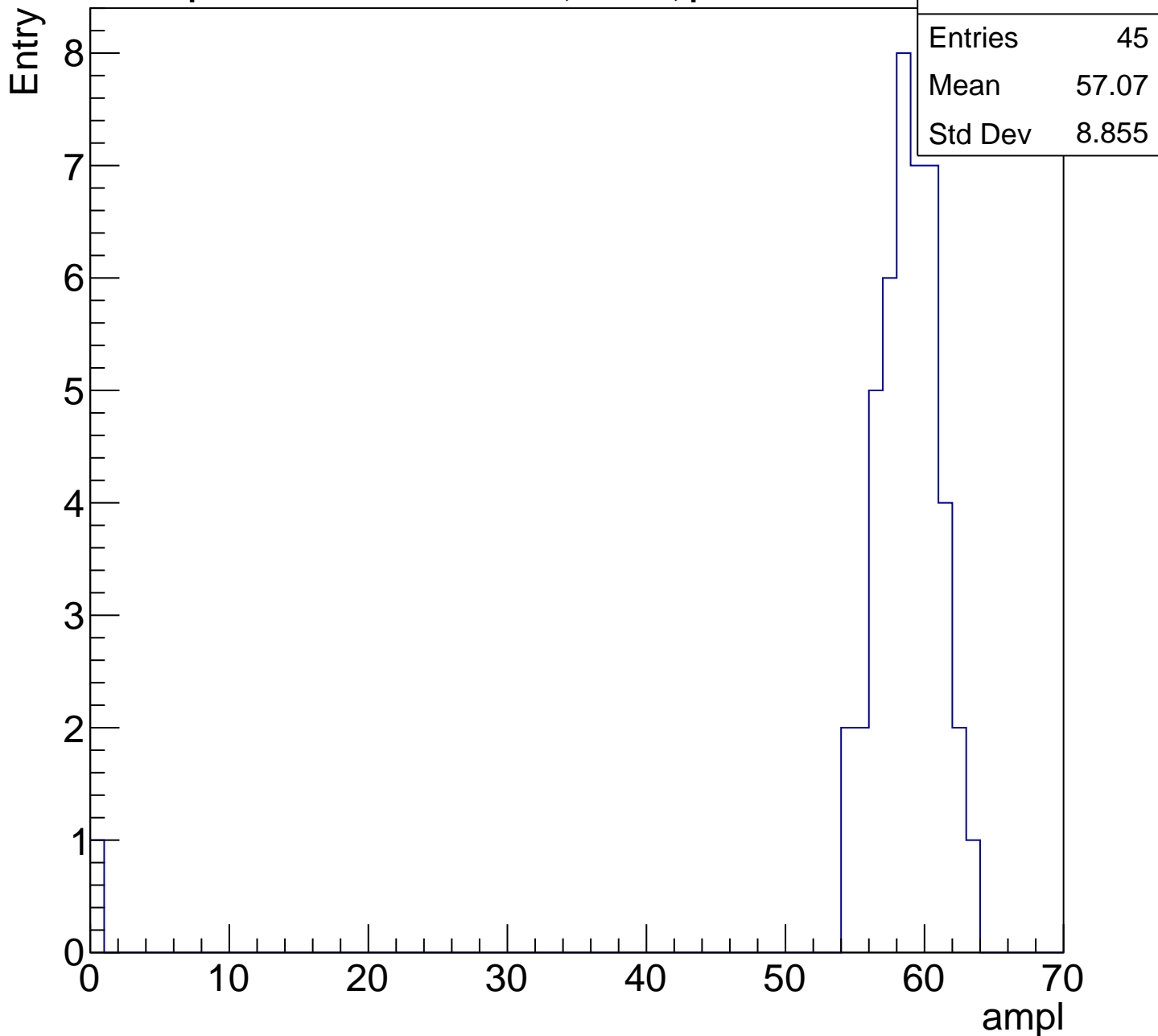
Entry



Entries	59
Mean	52.95
Std Dev	2.547

# B1L002S, U8-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

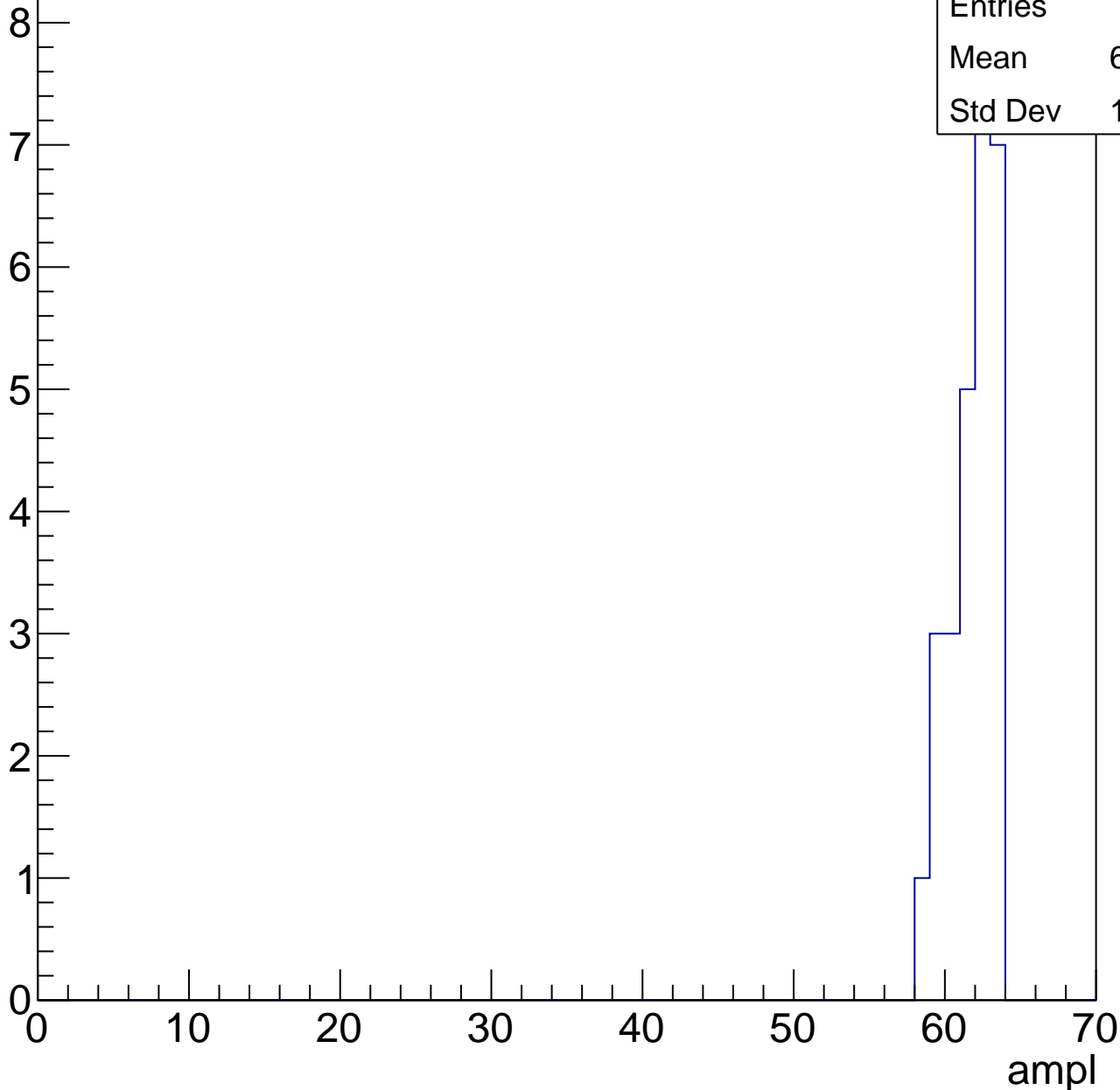


# B1L002S, U8-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	61.37
Std Dev	1.444





# B1L002S, U8-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch49, adc0

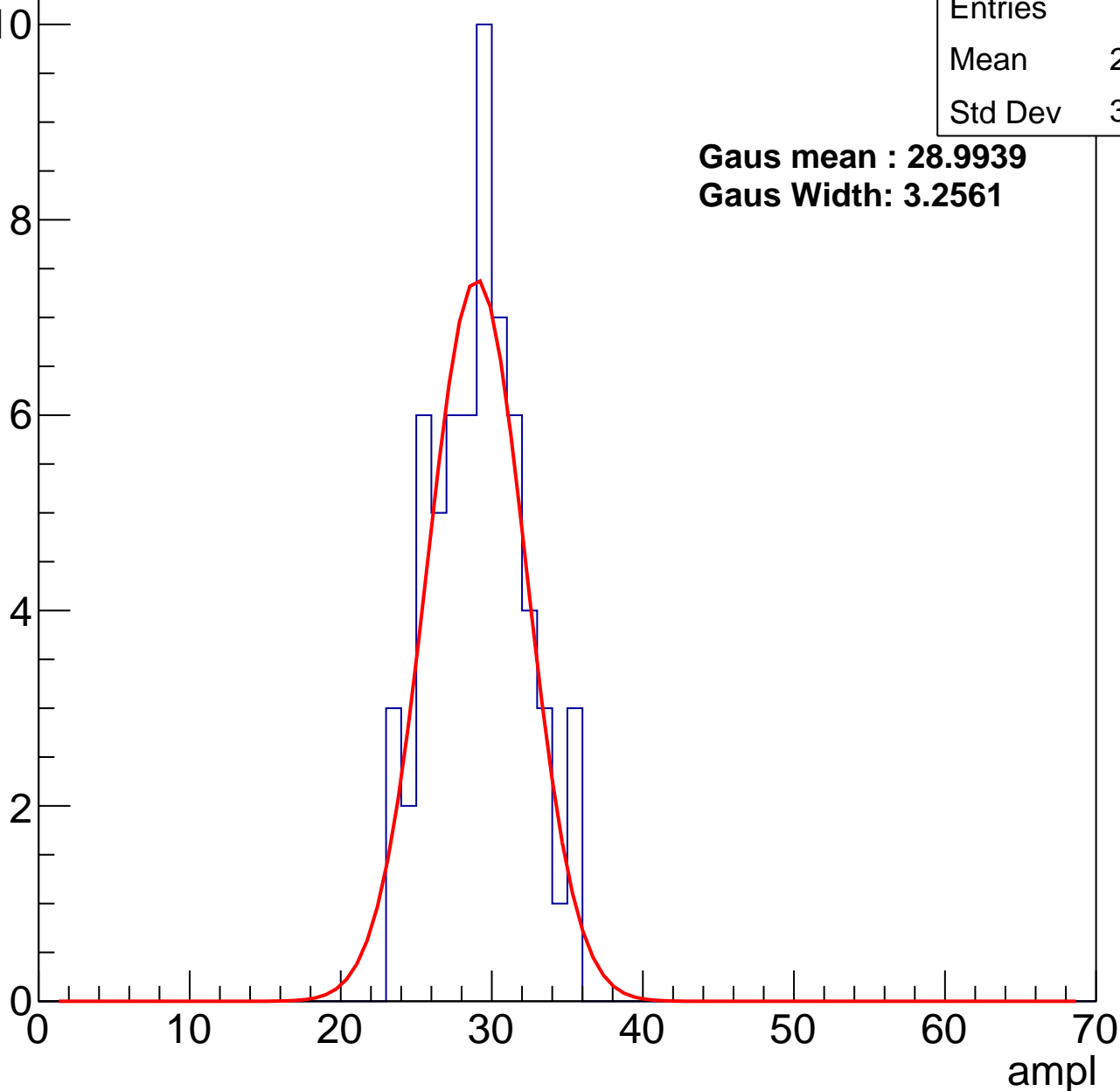
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.69
Std Dev	3.035

**Gaus mean : 28.9939**

**Gaus Width: 3.2561**



# B1L002S, U8-ch49, adc1

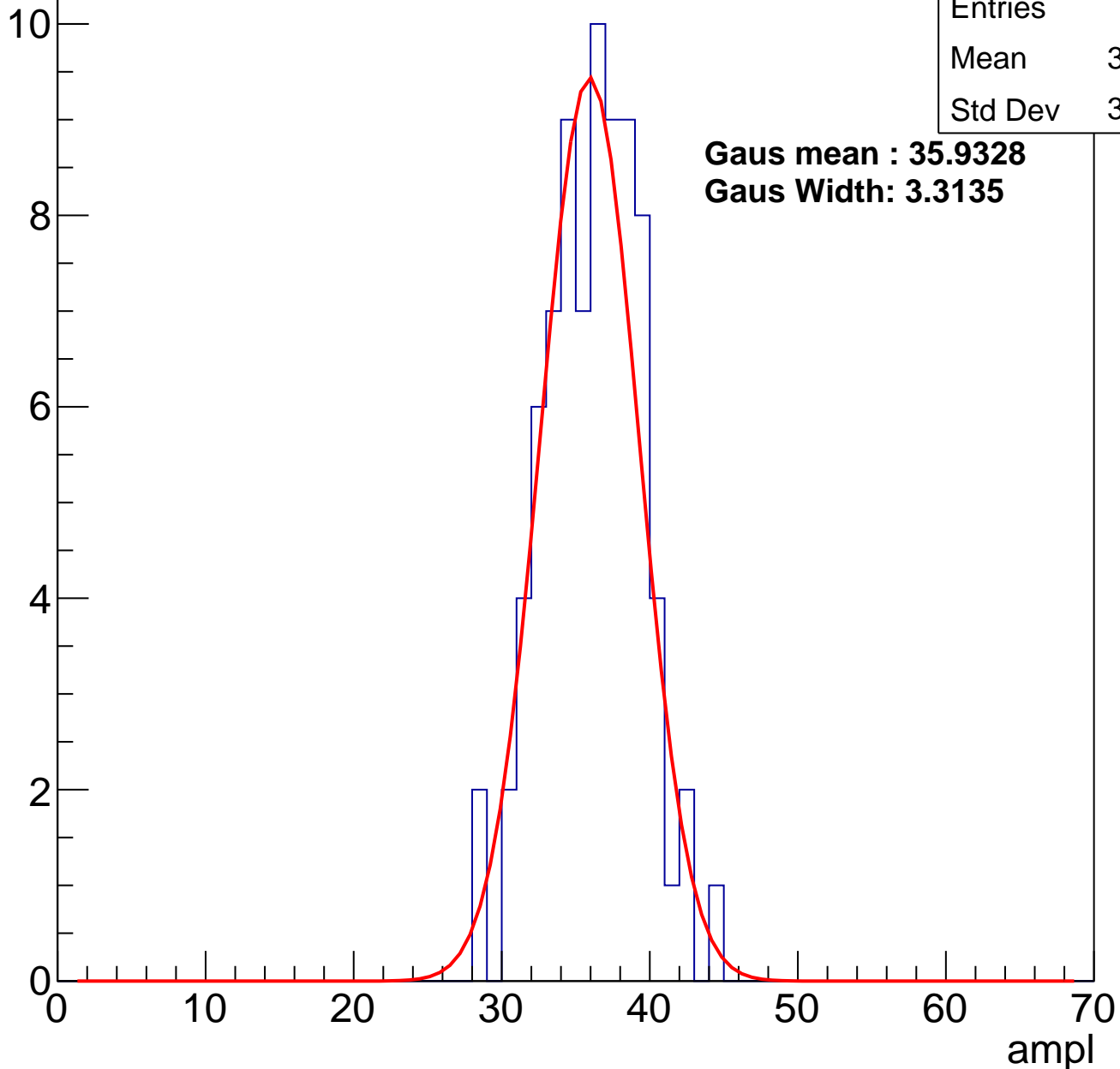
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	35.68
Std Dev	3.212

**Gaus mean : 35.9328**

**Gaus Width: 3.3135**

Entry



# B1L002S, U8-ch49, adc2

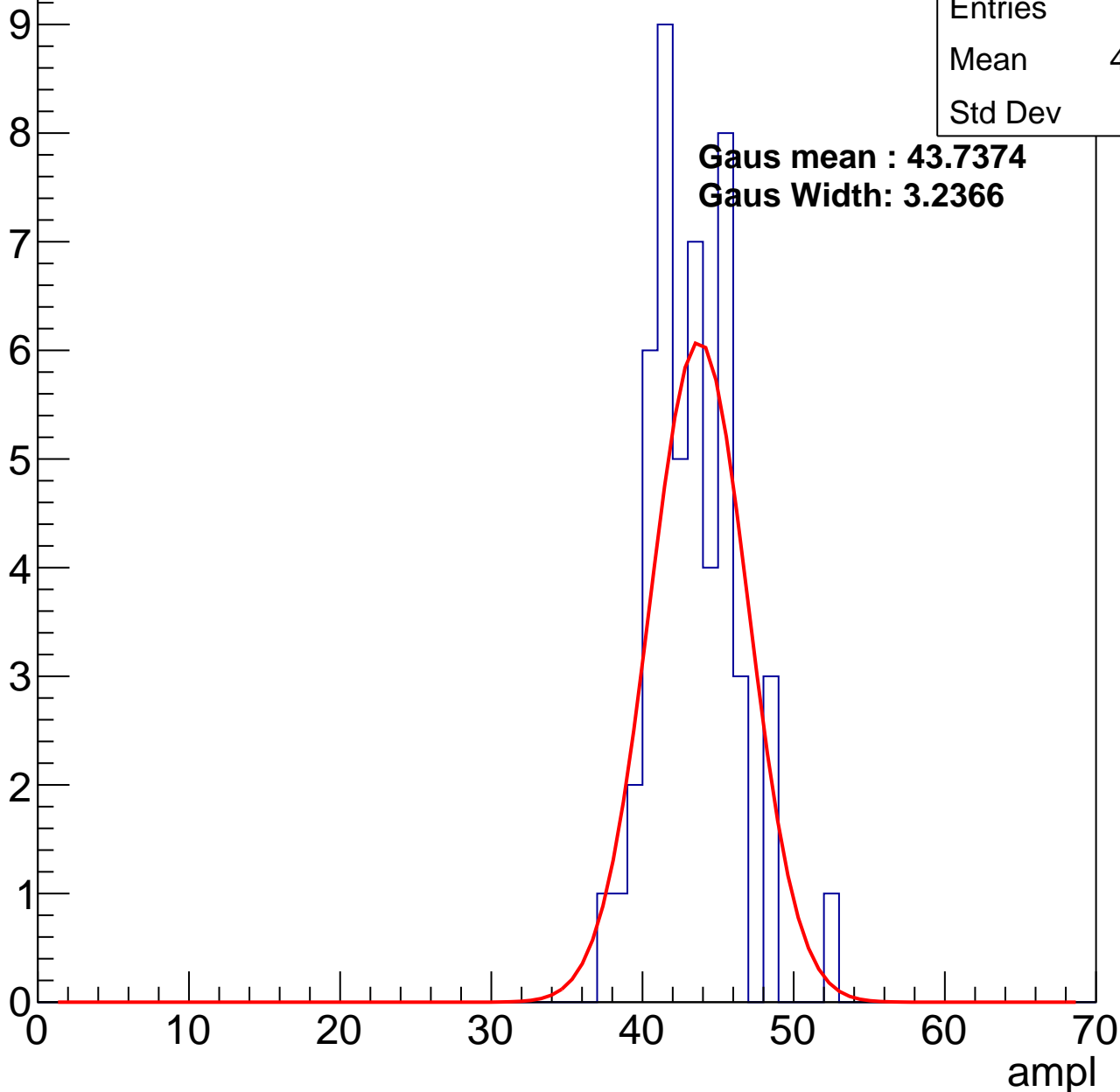
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	42.86
Std Dev	2.85

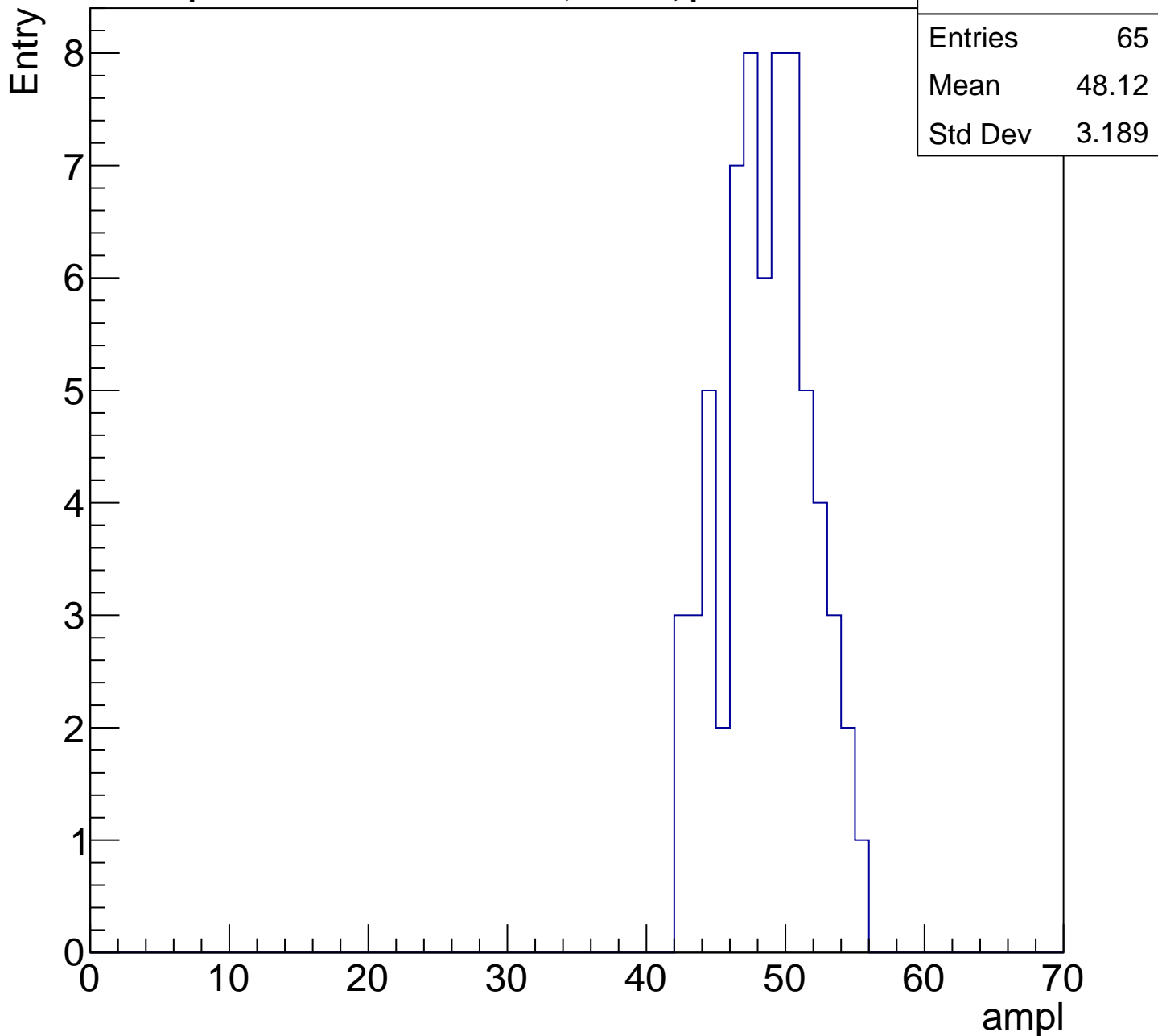
**Gaus mean : 43.7374**

**Gaus Width: 3.2366**



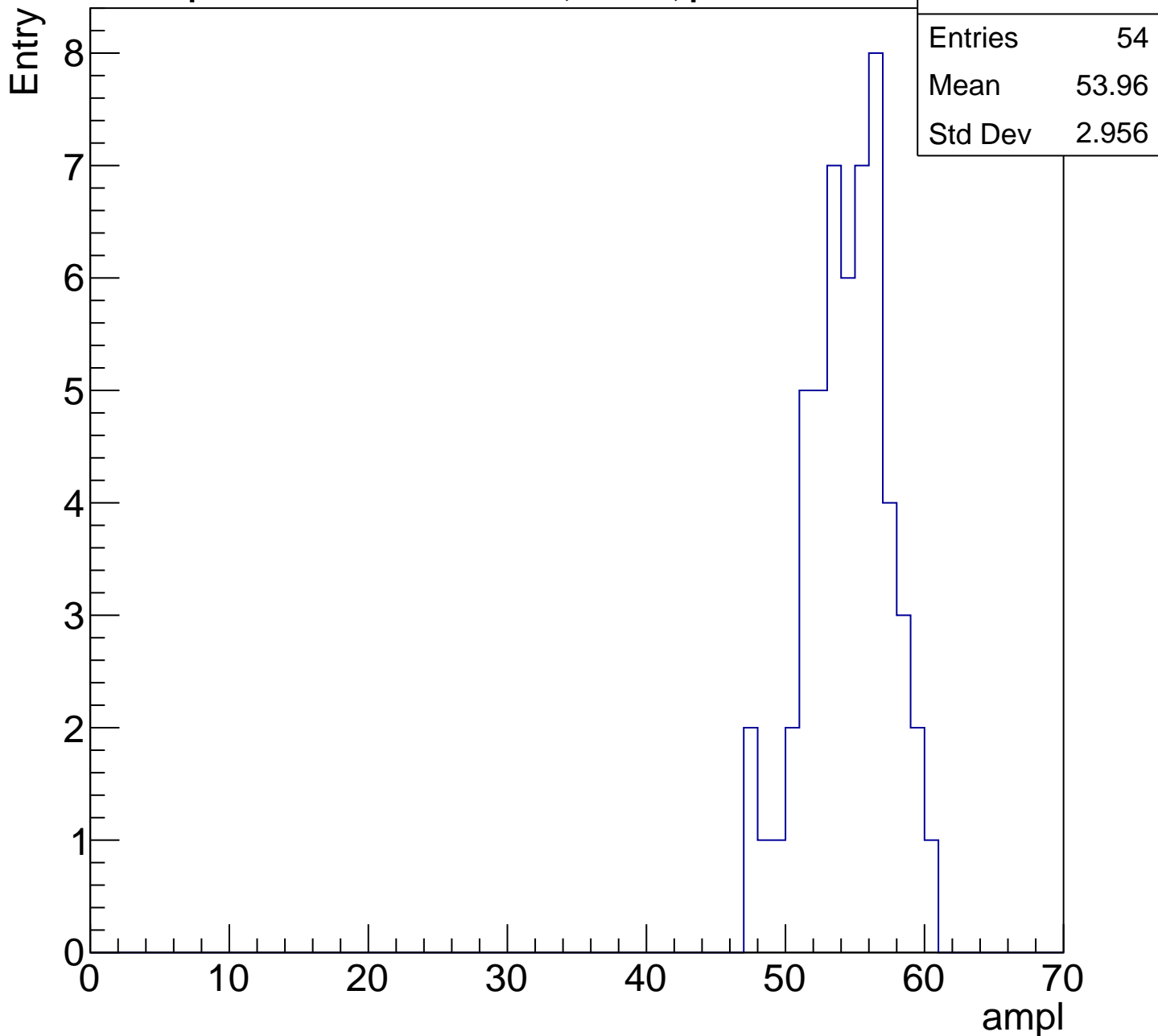
# B1L002S, U8-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch49, adc5

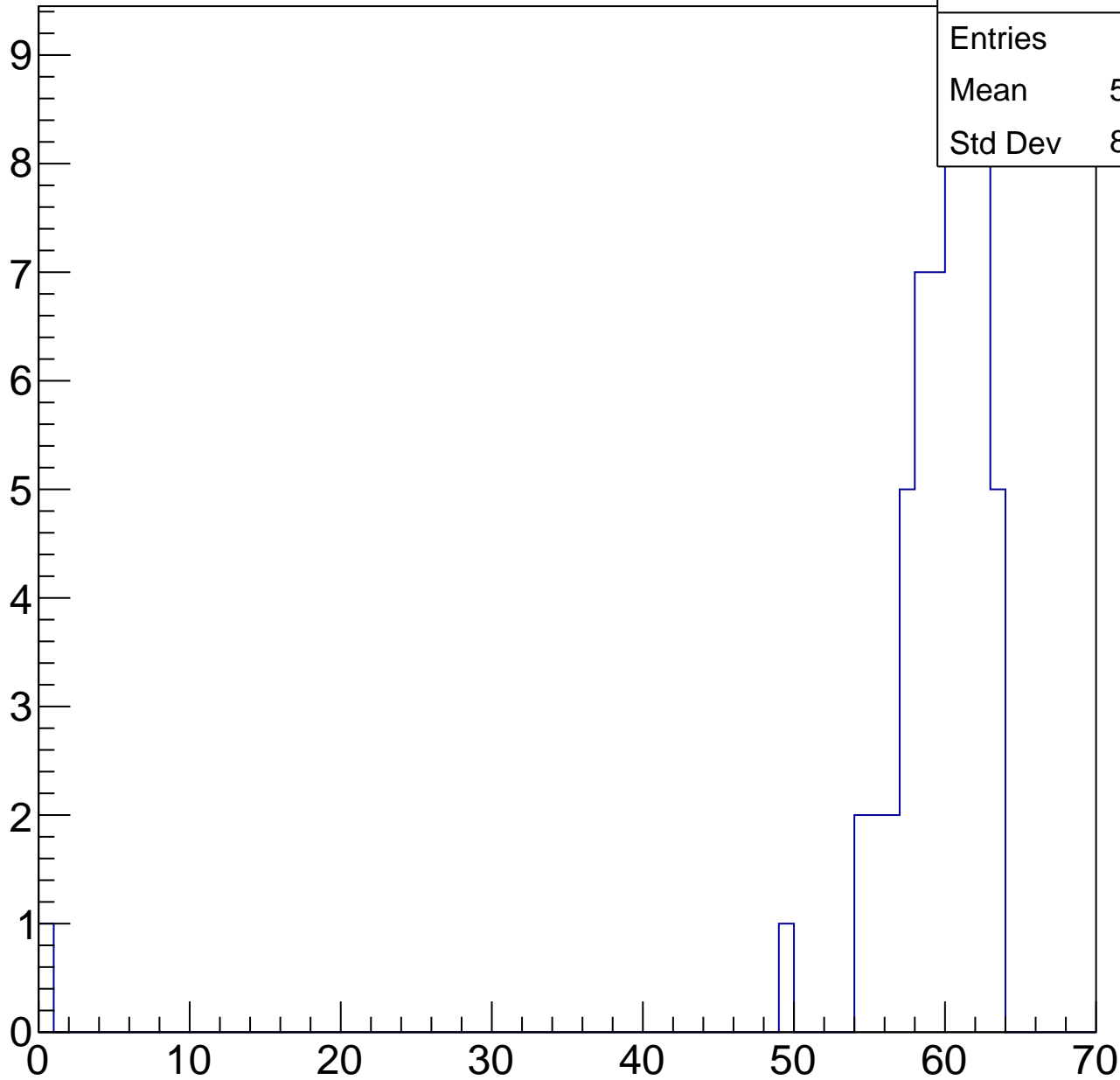
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.36
Std Dev	8.189

ampl

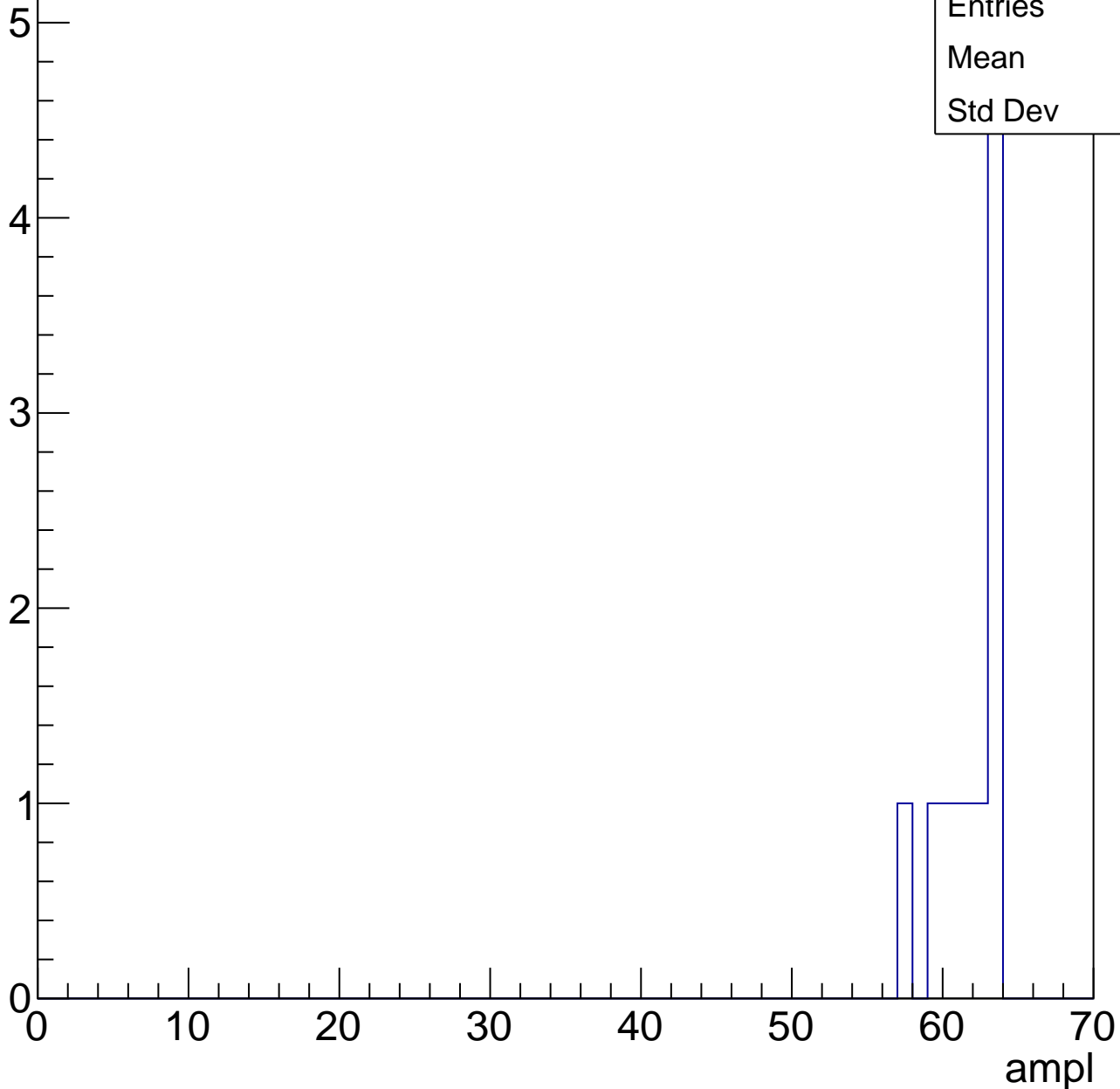


# B1L002S, U8-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	10
Mean	61.4
Std Dev	2.01





# B1L002S, U8-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch50, adc0

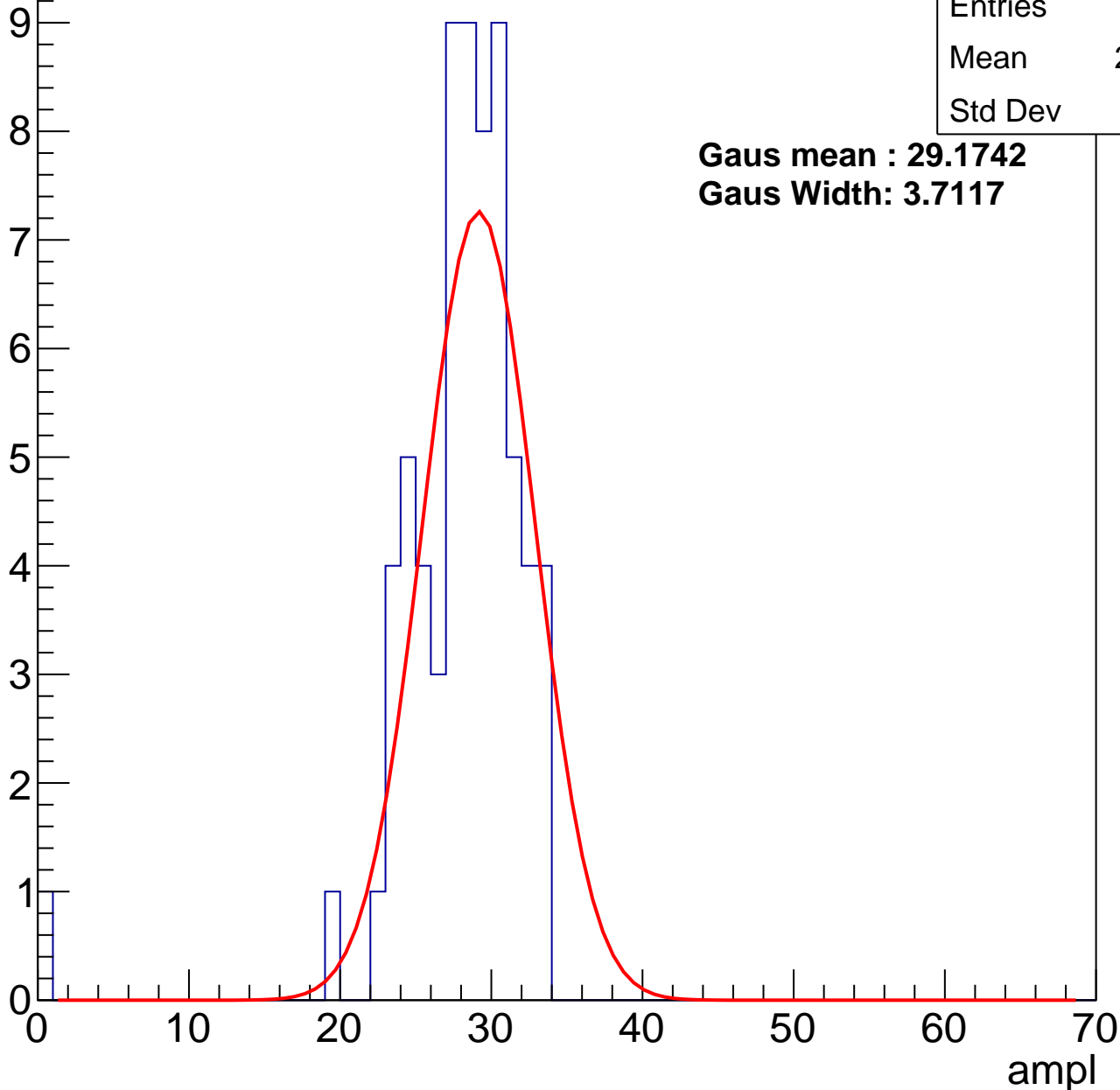
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.51
Std Dev	4.53

**Gaus mean : 29.1742**

**Gaus Width: 3.7117**



# B1L002S, U8-ch50, adc1

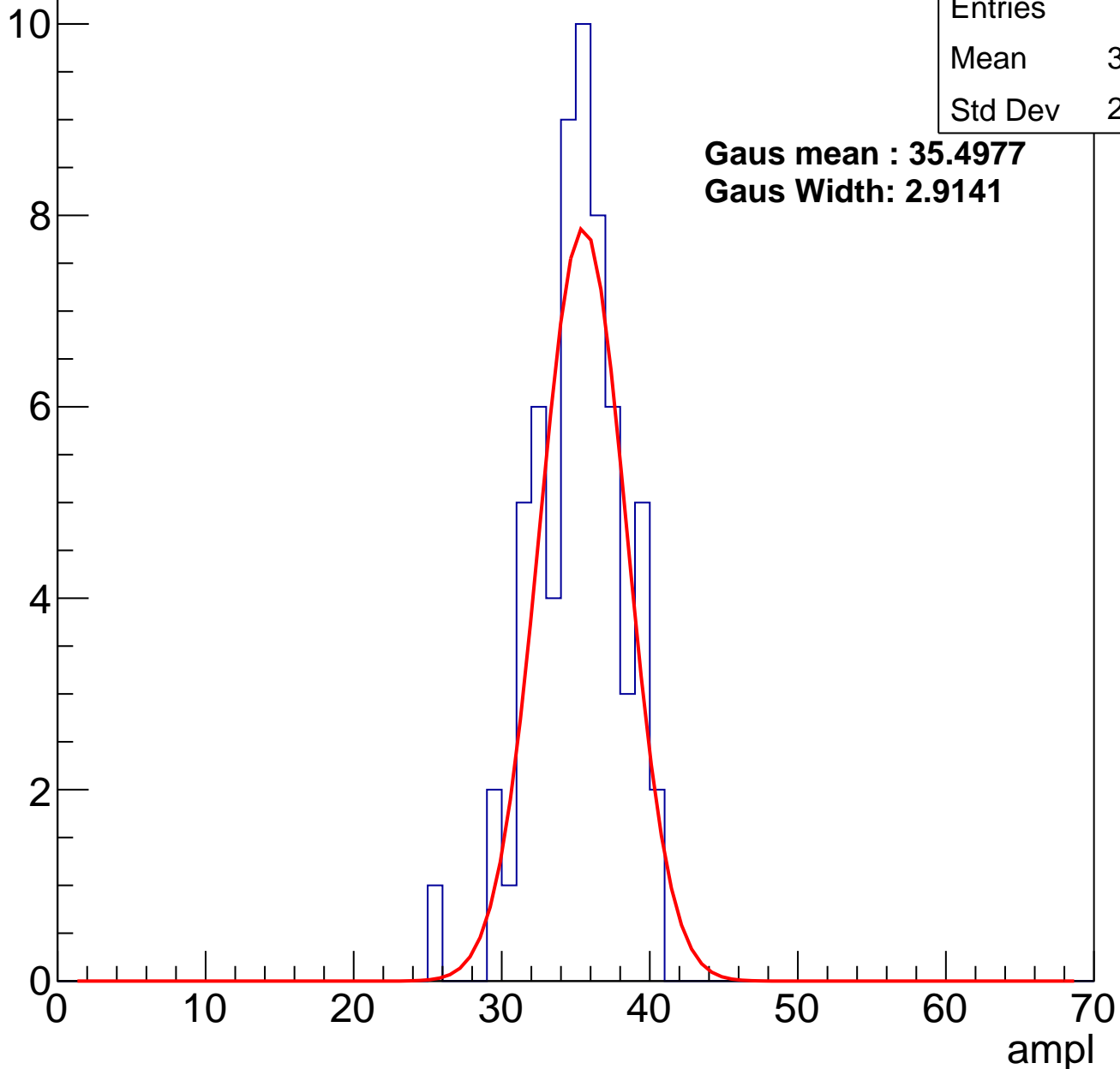
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	34.63
Std Dev	2.942

**Gaus mean : 35.4977**

**Gaus Width: 2.9141**

Entry



# B1L002S, U8-ch50, adc2

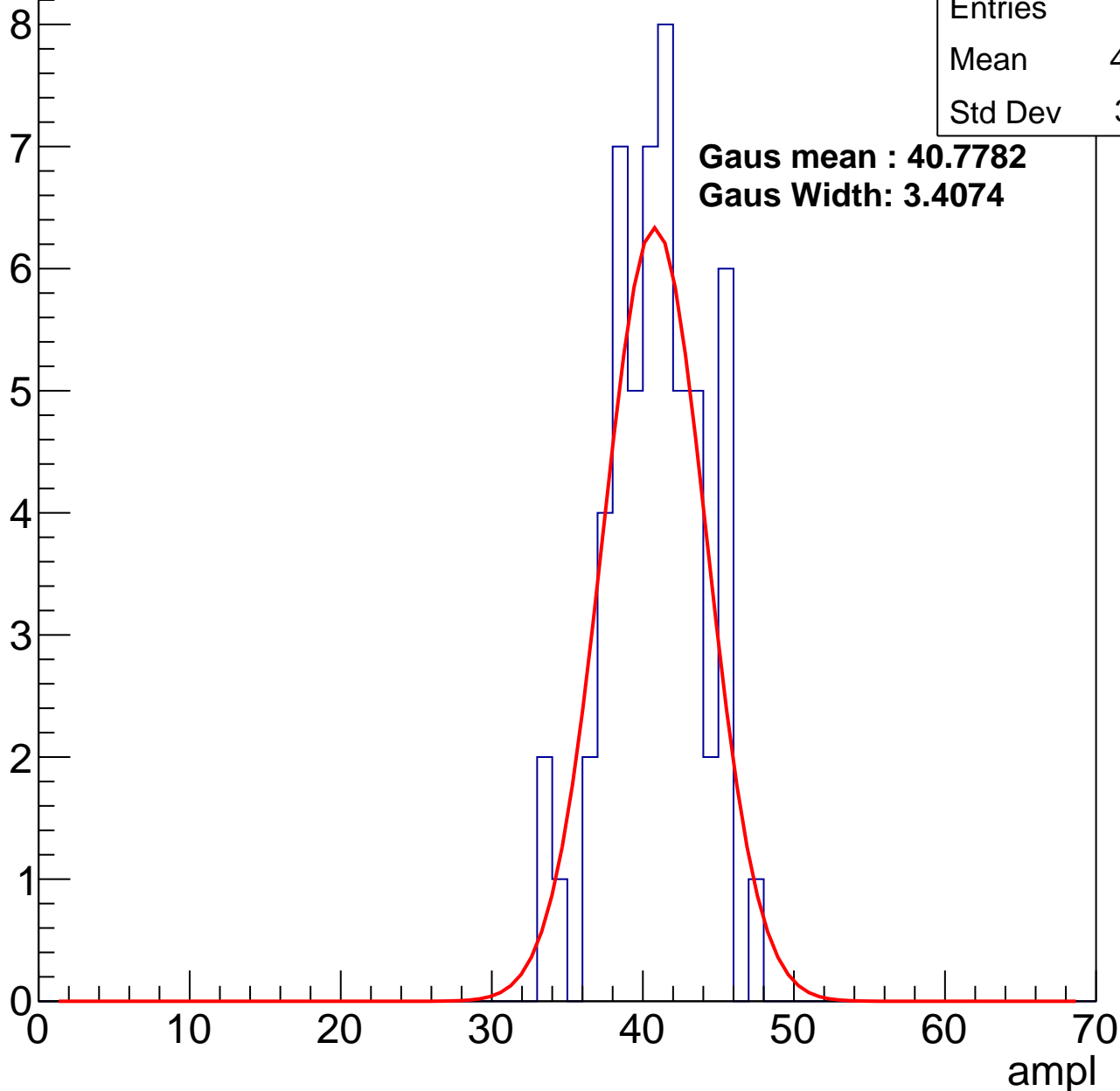
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.35
Std Dev	3.111

**Gaus mean : 40.7782**

**Gaus Width: 3.4074**

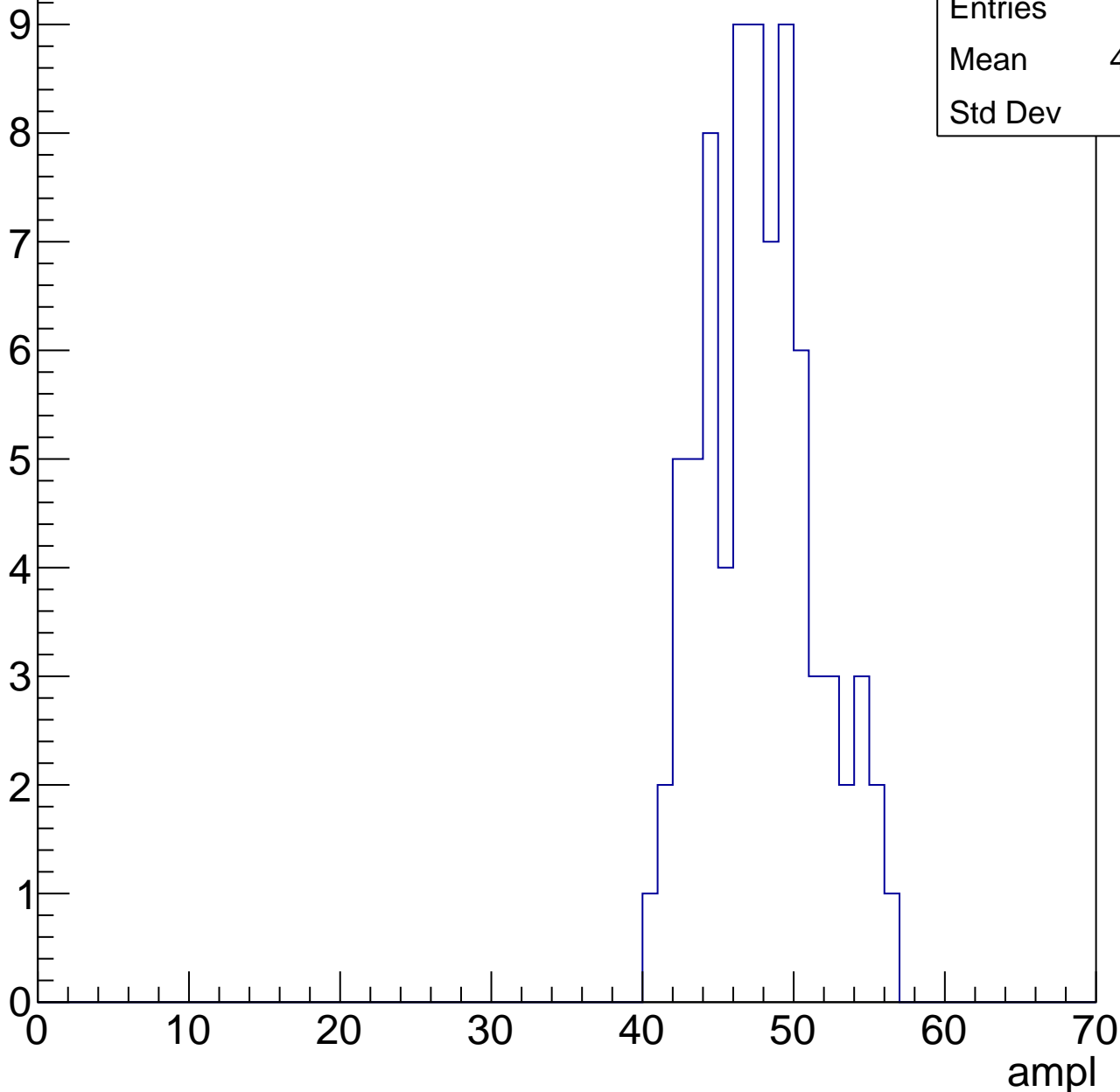


# B1L002S, U8-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	47.29
Std Dev	3.67

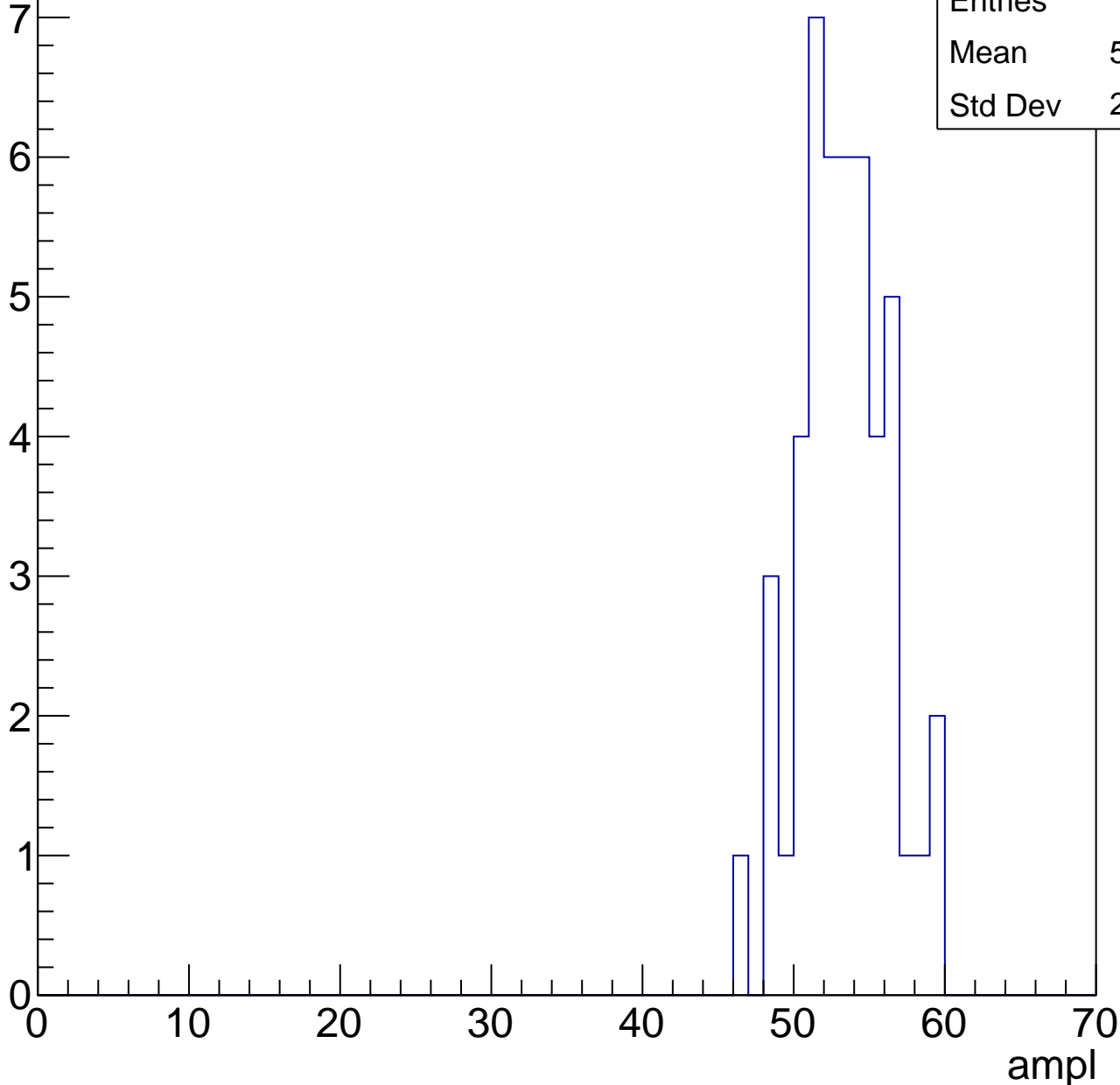


# B1L002S, U8-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	52.83
Std Dev	2.876

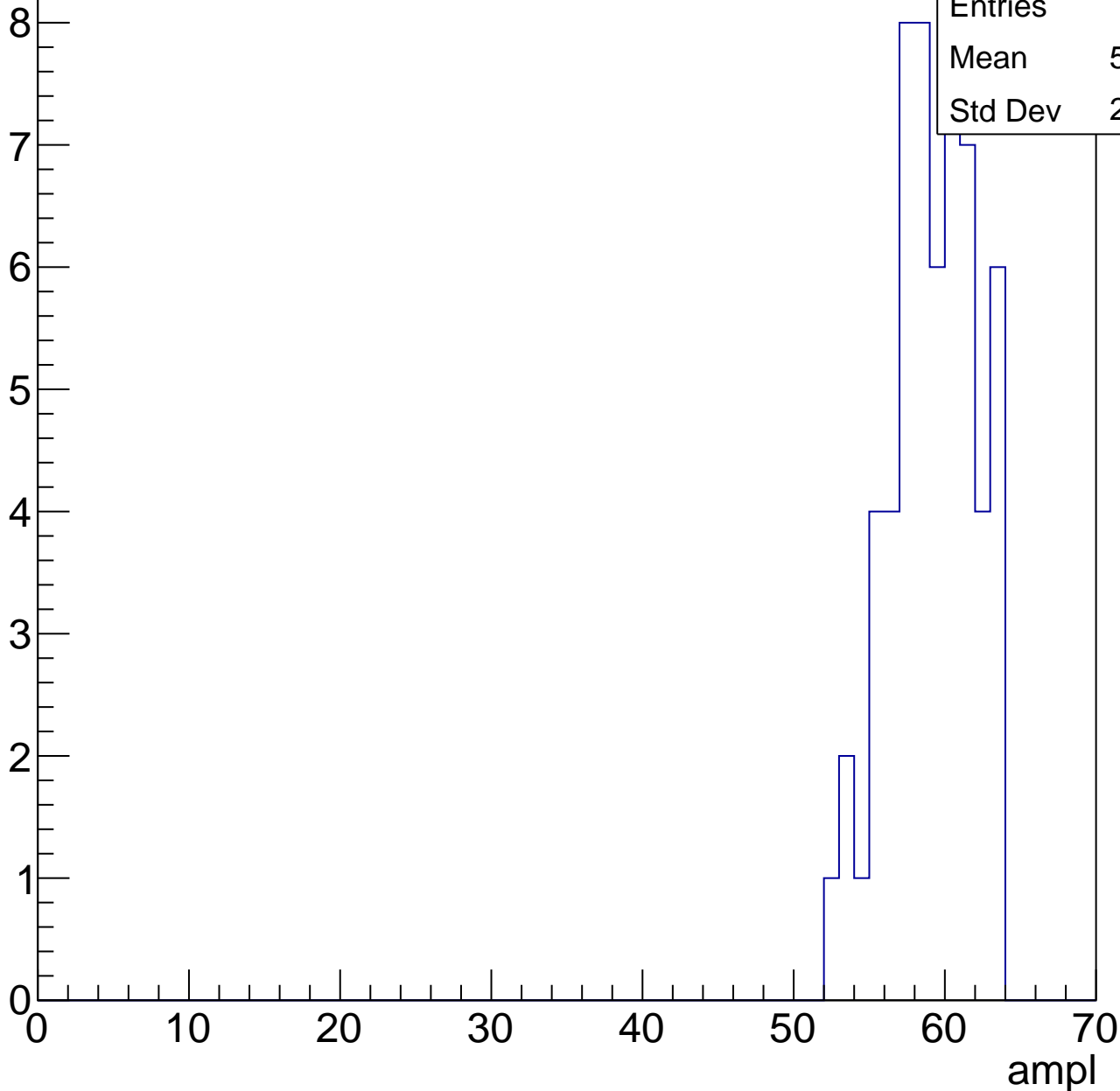


# B1L002S, U8-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	58.69
Std Dev	2.757

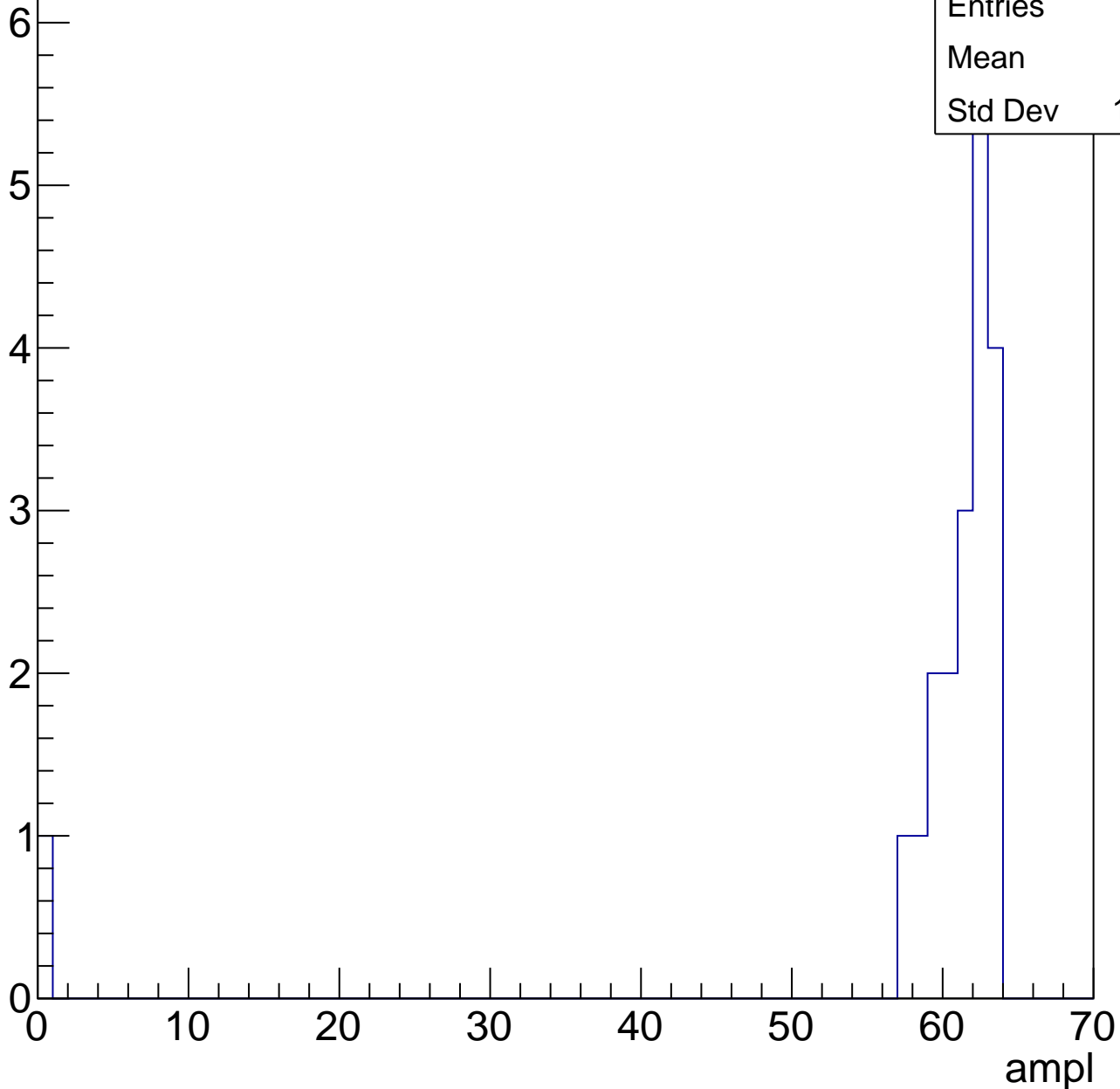


# B1L002S, U8-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58
Std Dev	13.41





# B1L002S, U8-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch51, adc0

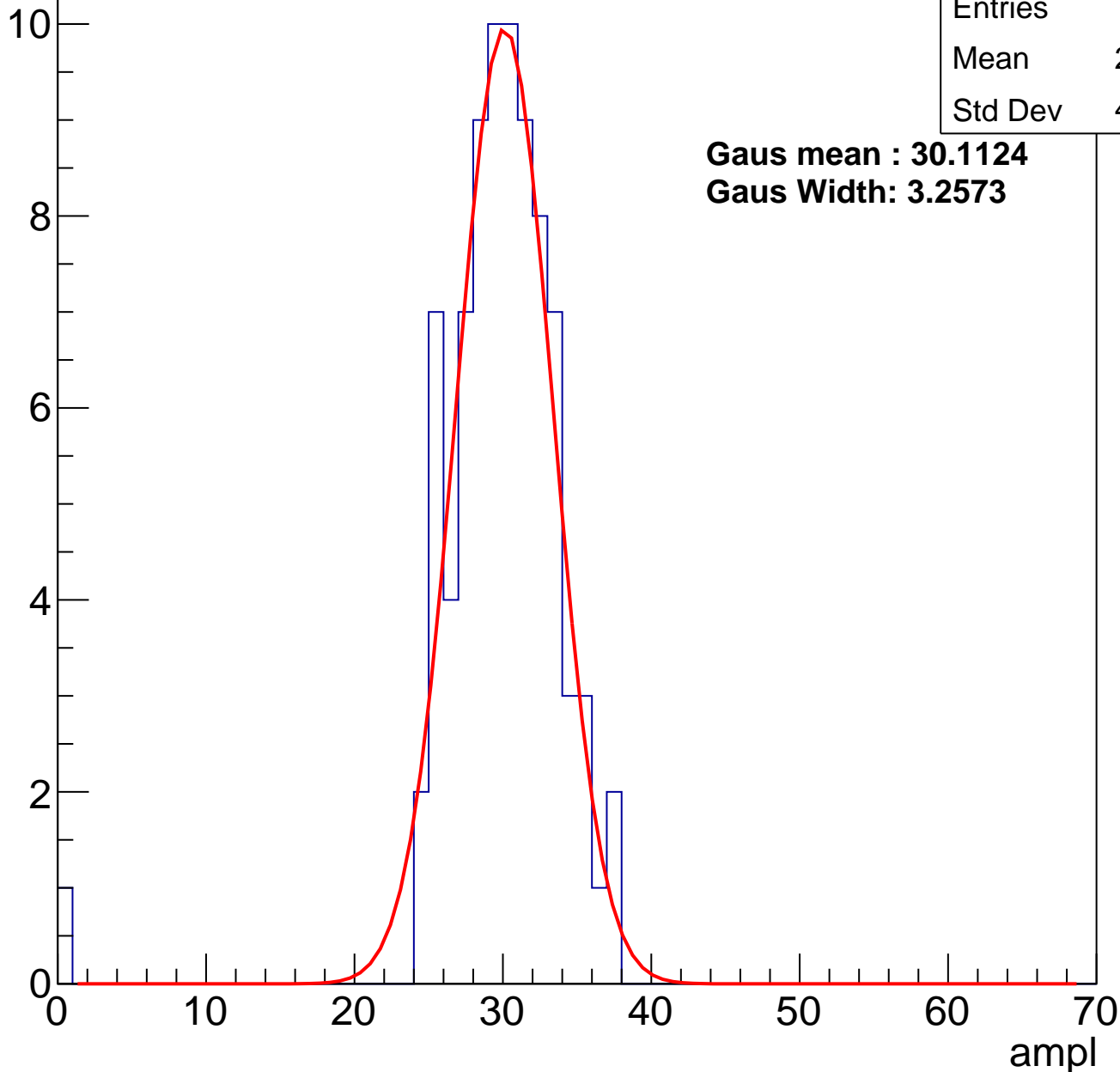
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	29.41
Std Dev	4.461

**Gaus mean : 30.1124**

**Gaus Width: 3.2573**

Entry



# B1L002S, U8-ch51, adc1

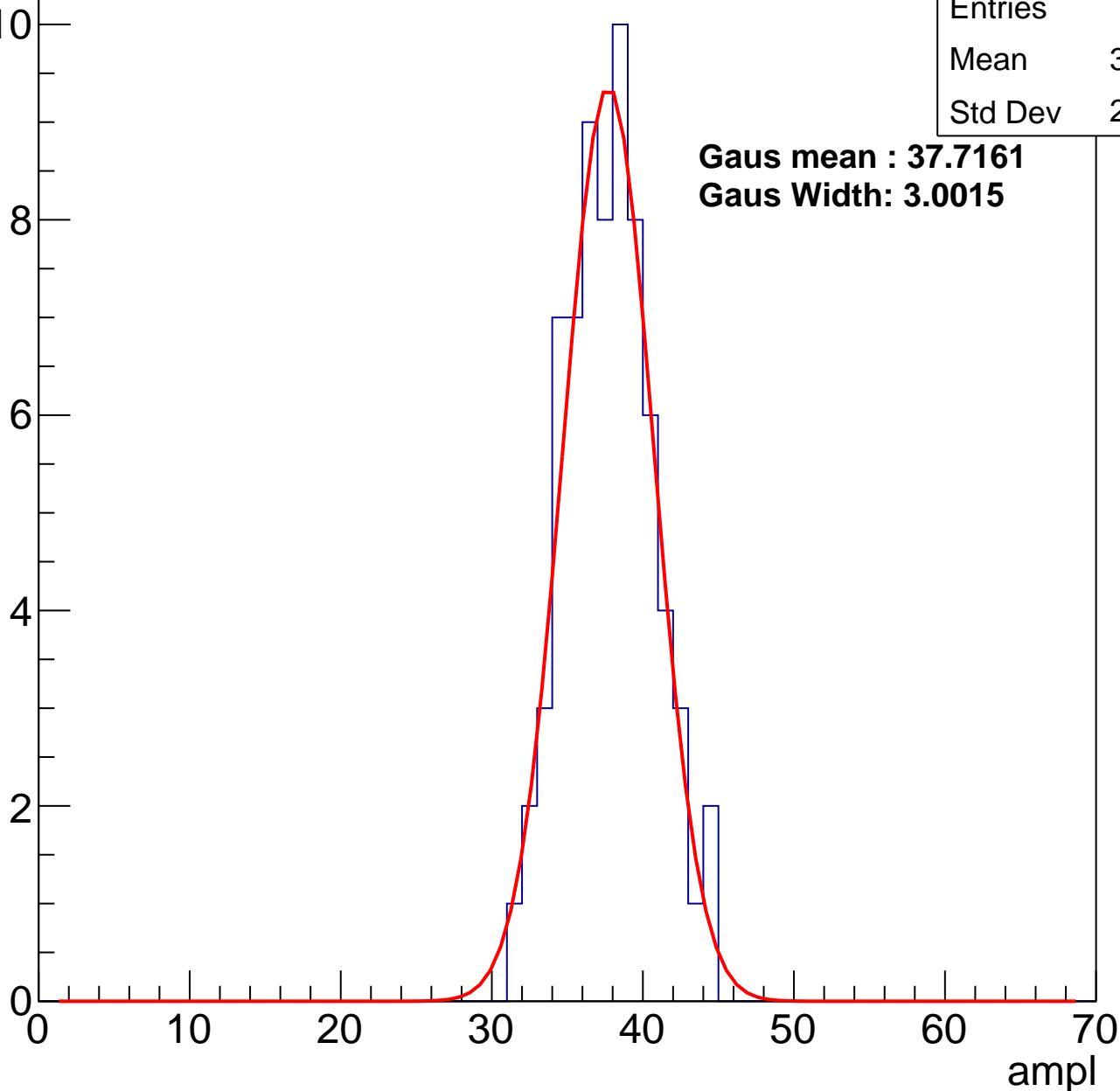
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	37.32
Std Dev	2.896

**Gaus mean : 37.7161**

**Gaus Width: 3.0015**



# B1L002S, U8-ch51, adc2

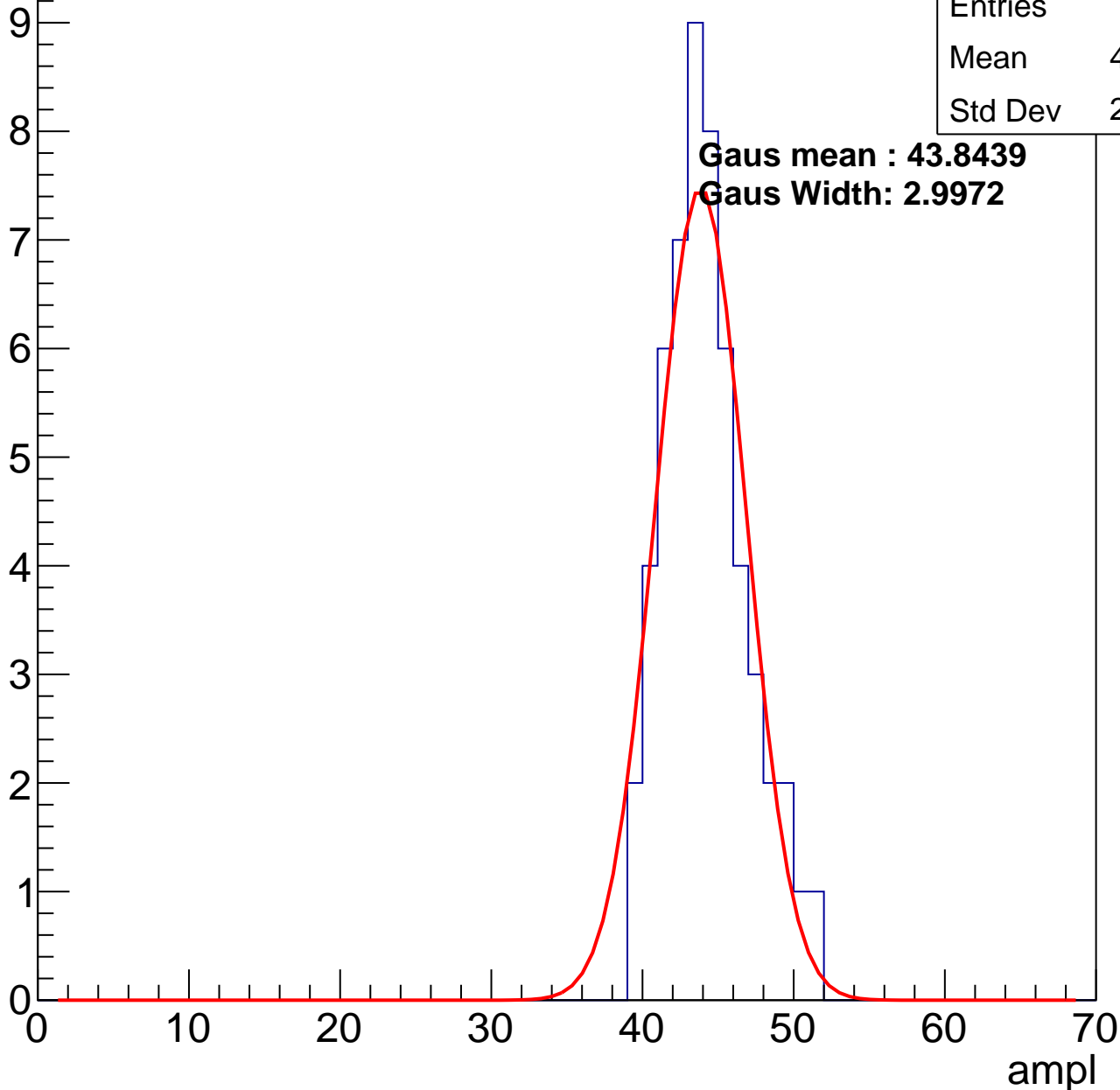
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	43.76
Std Dev	2.757

**Gaus mean : 43.8439**

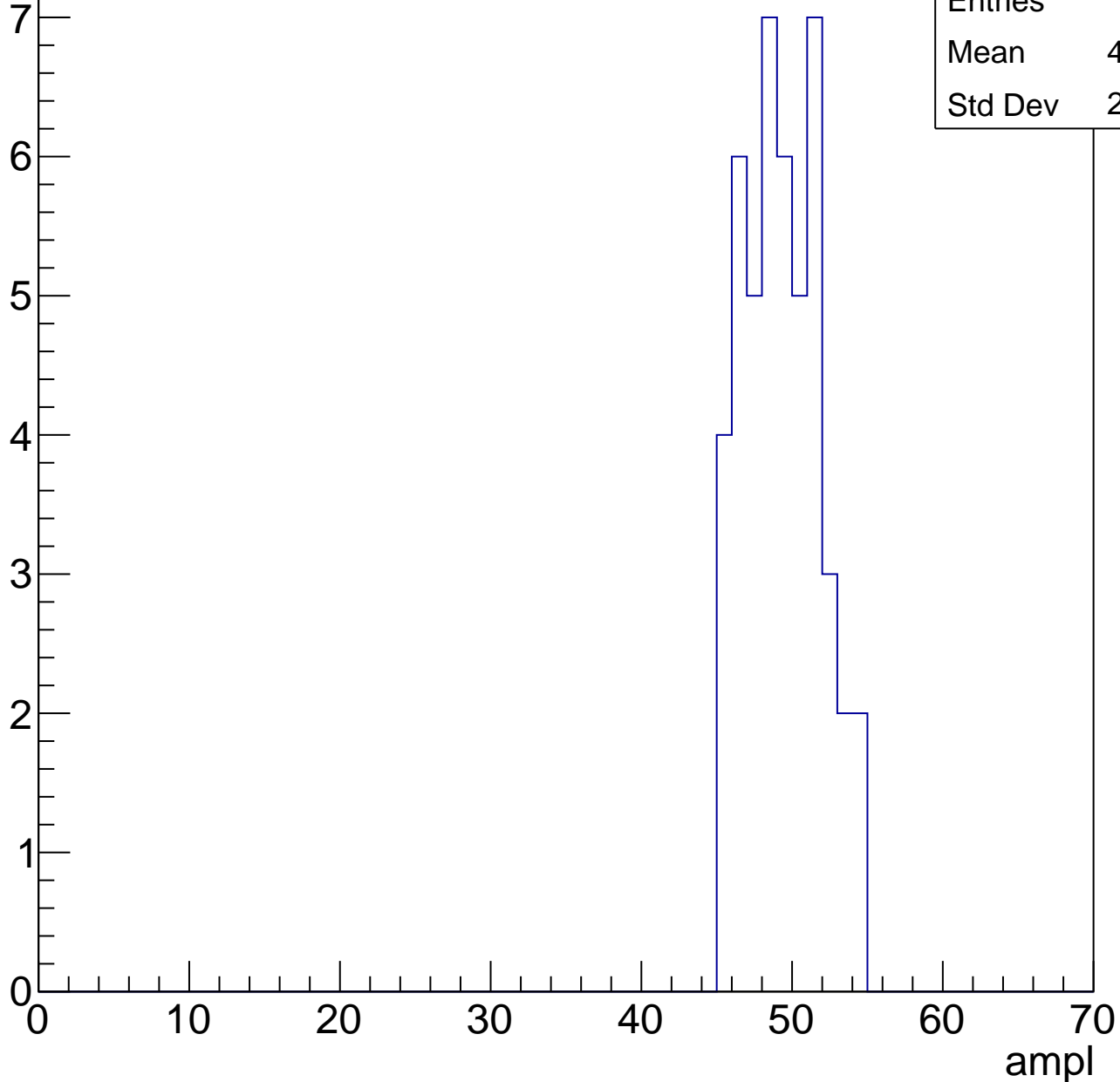
**Gaus Width: 2.9972**



# B1L002S, U8-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

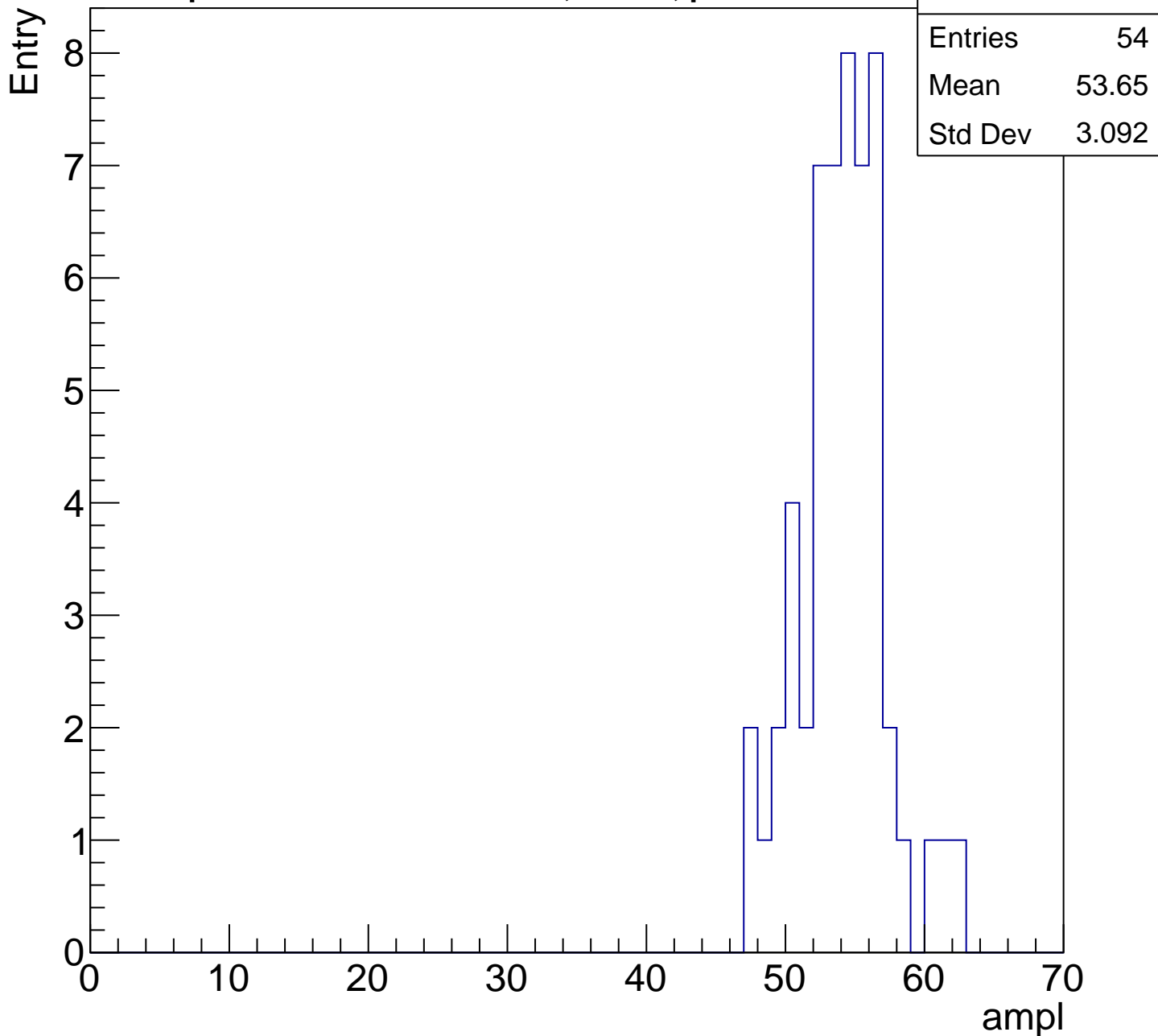
Entry



Entries	47
Mean	48.89
Std Dev	2.469

# B1L002S, U8-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	59.4
Std Dev	2.365

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

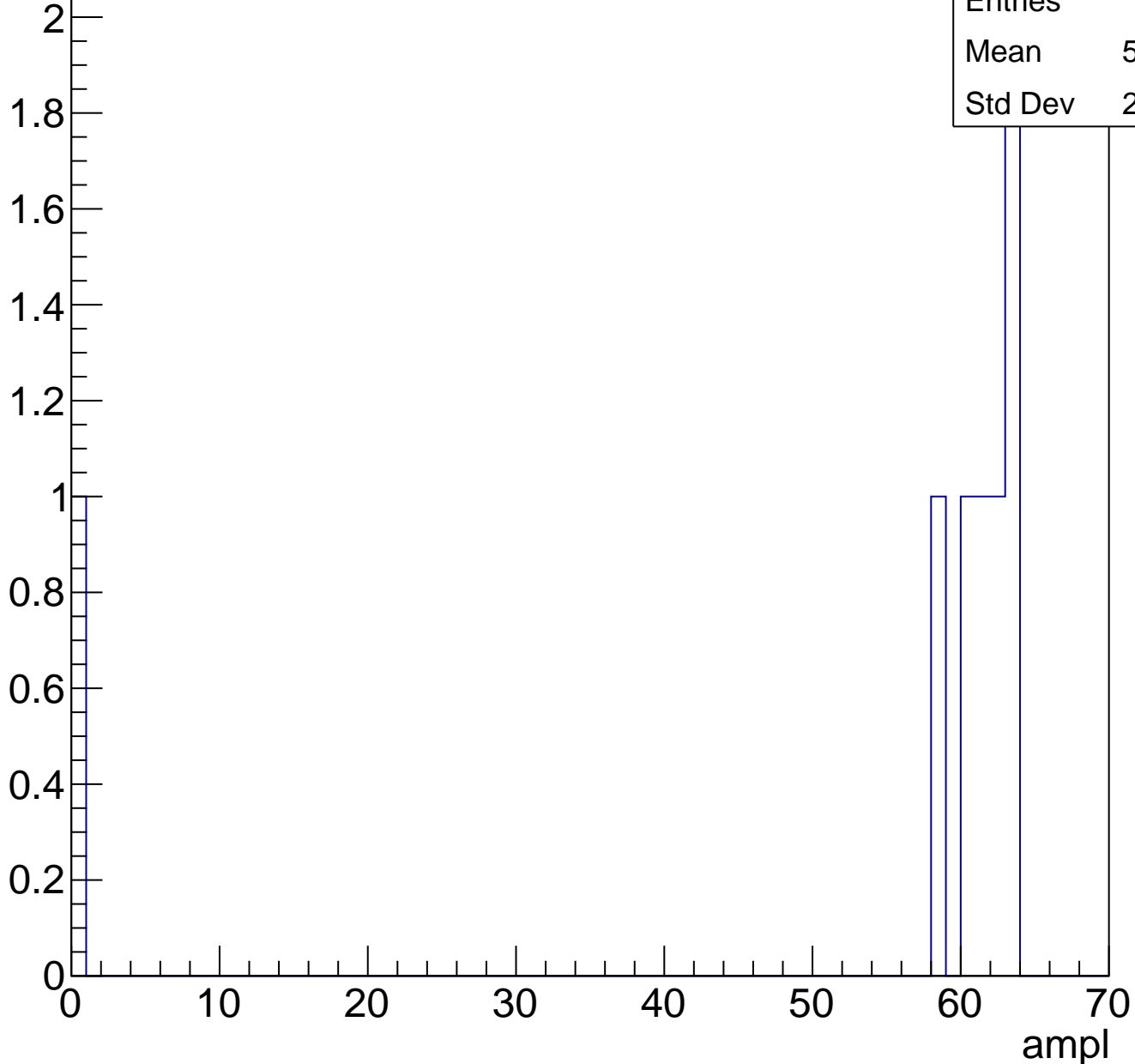
ampl

70

# B1L002S, U8-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



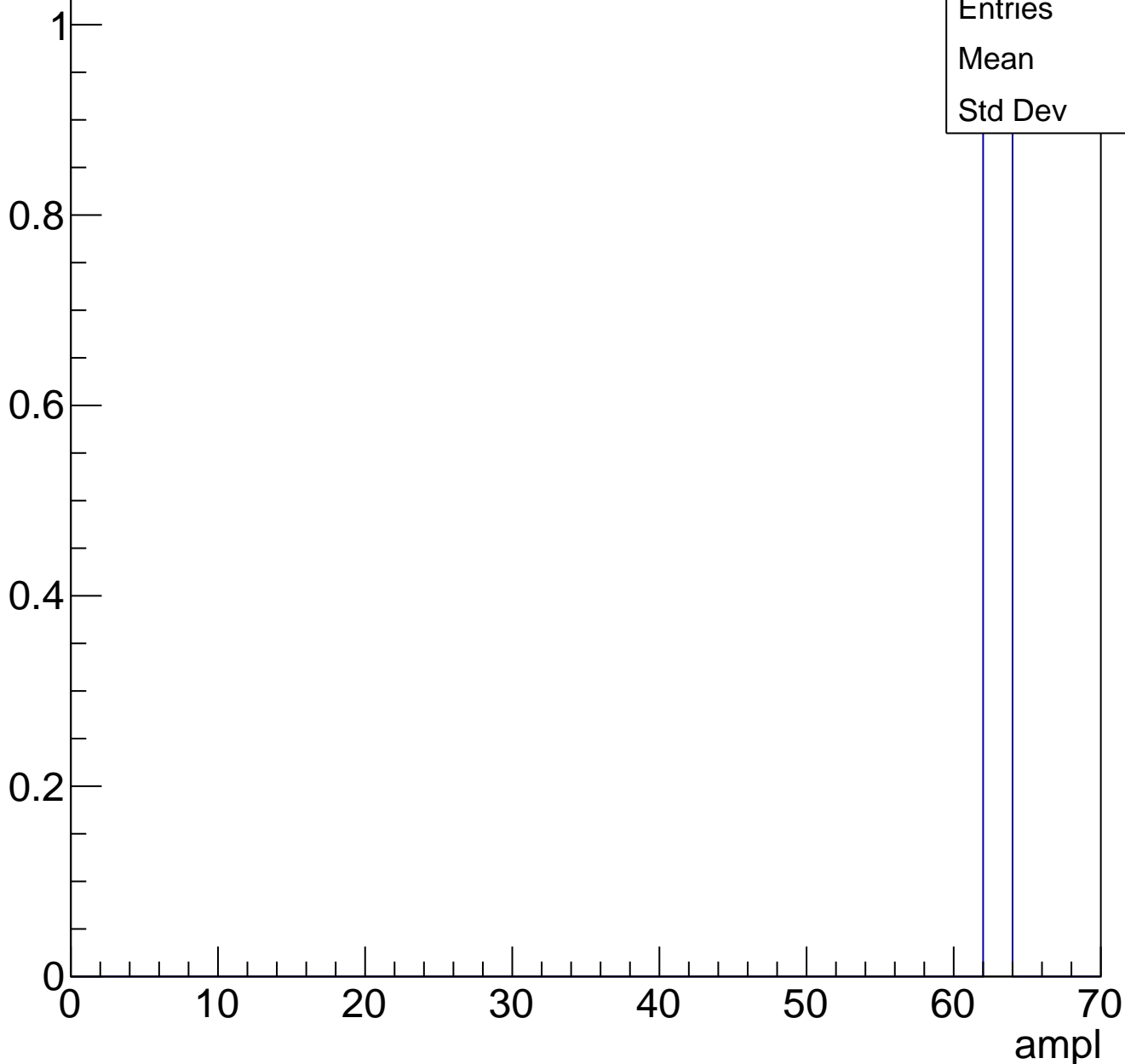
Entries	7
Mean	52.43
Std Dev	21.47



# B1L002S, U8-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch52, adc0

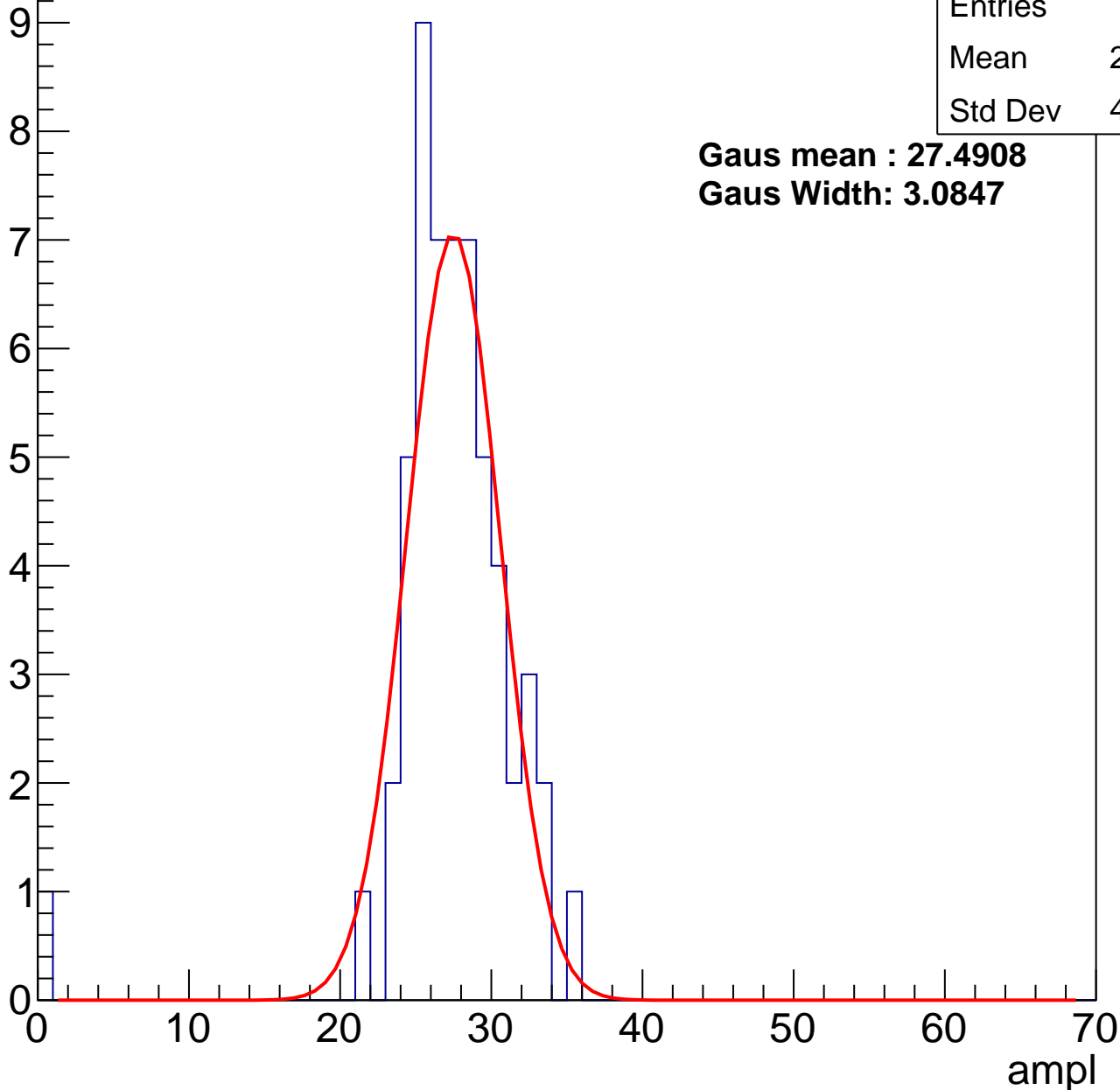
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	26.84
Std Dev	4.609

**Gaus mean : 27.4908**

**Gaus Width: 3.0847**



# B1L002S, U8-ch52, adc1

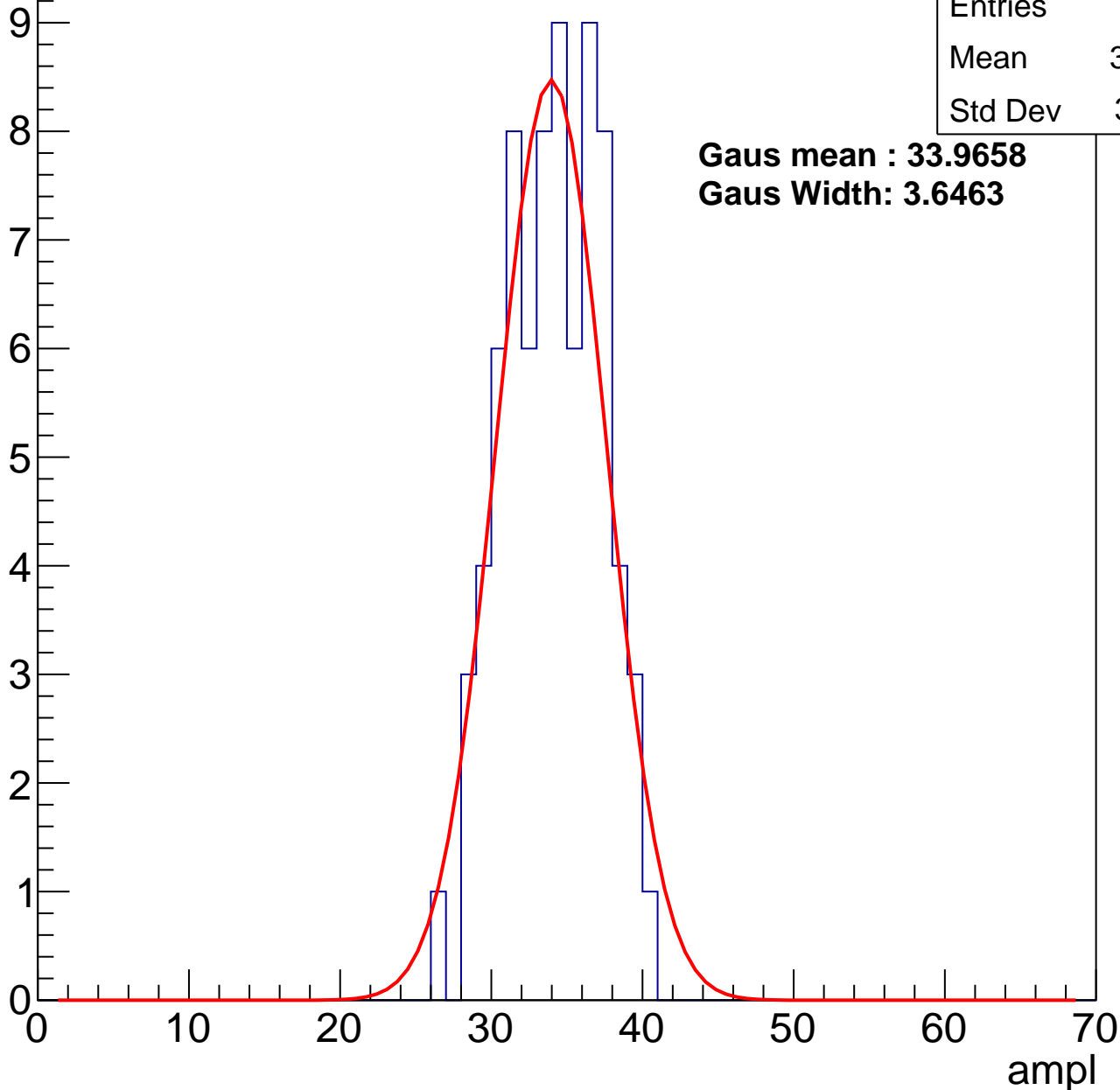
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	33.62
Std Dev	3.141

**Gaus mean : 33.9658**

**Gaus Width: 3.6463**



# B1L002S, U8-ch52, adc2

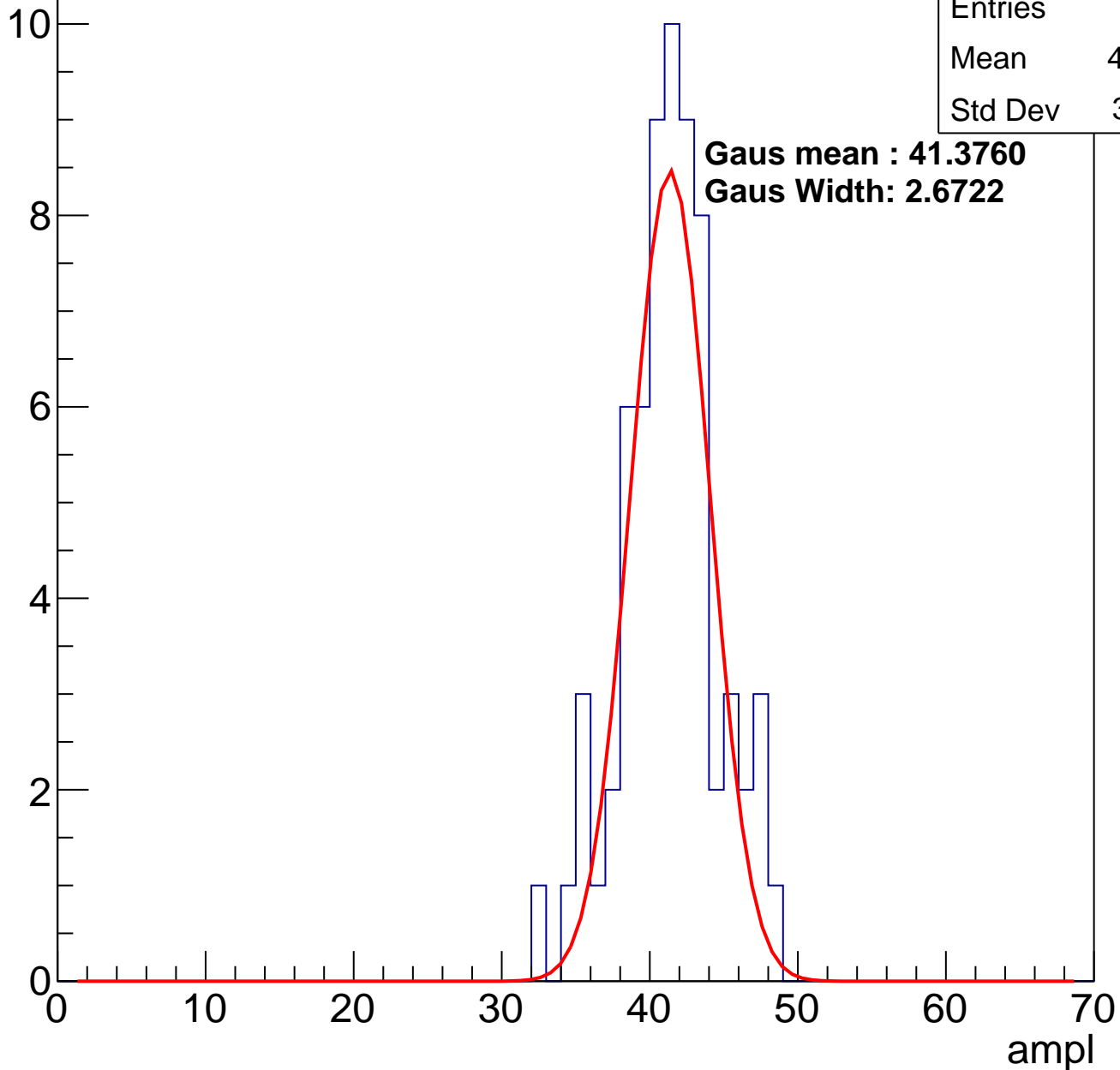
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	40.88
Std Dev	3.221

**Gaus mean : 41.3760**

**Gaus Width: 2.6722**

Entry

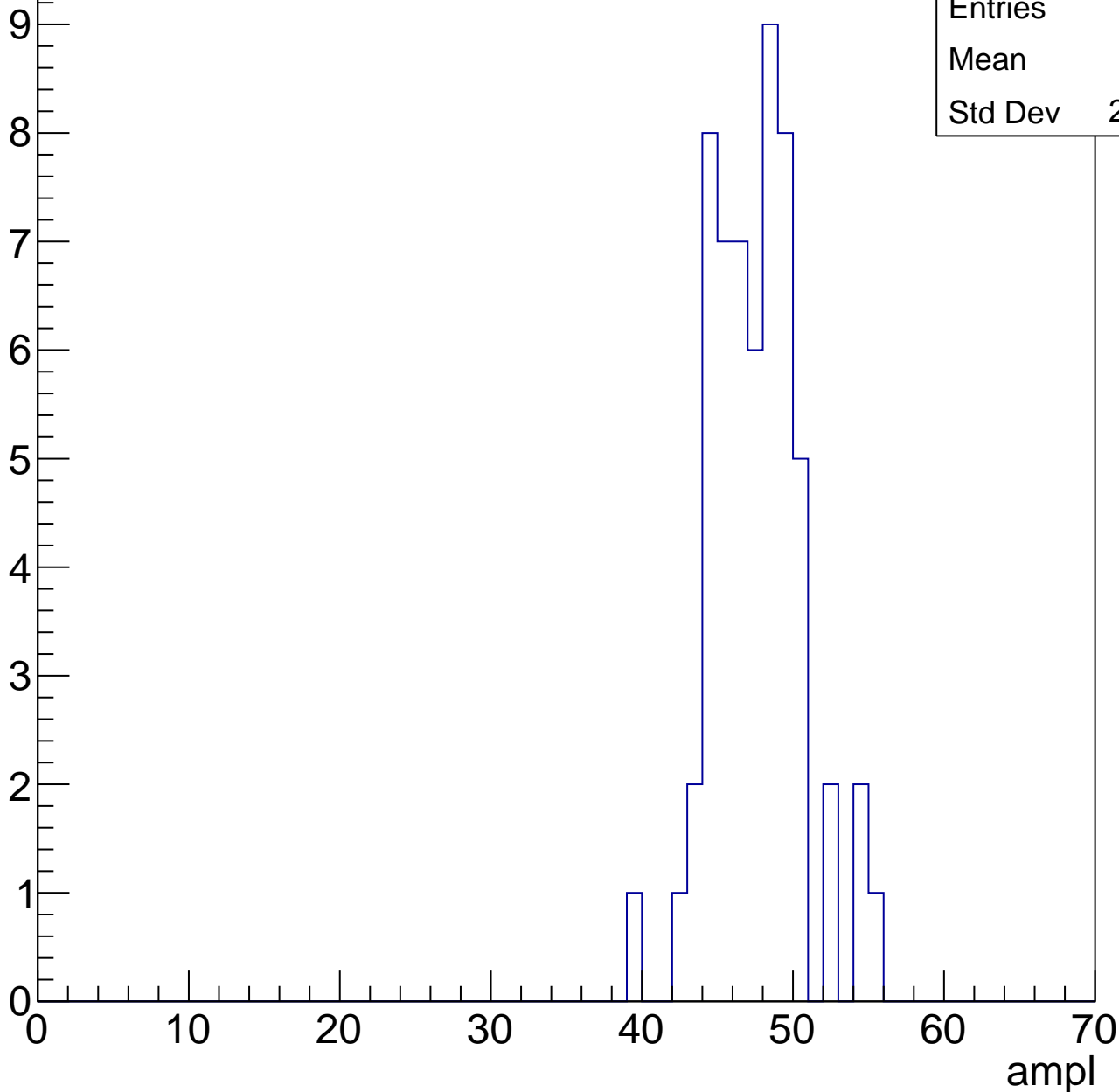


# B1L002S, U8-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.1
Std Dev	2.984

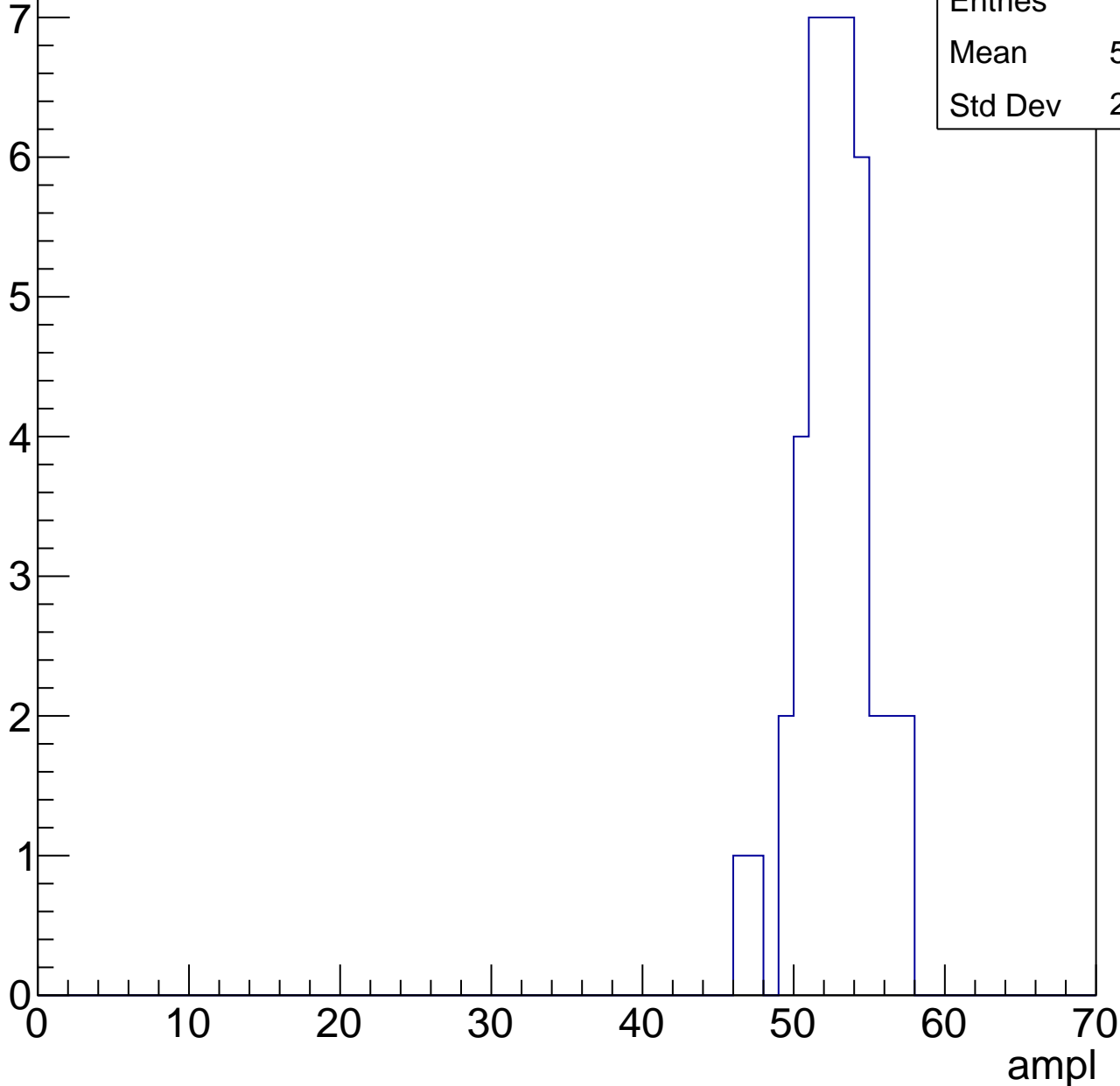


# B1L002S, U8-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	52.27
Std Dev	2.369



# B1L002S, U8-ch52, adc5

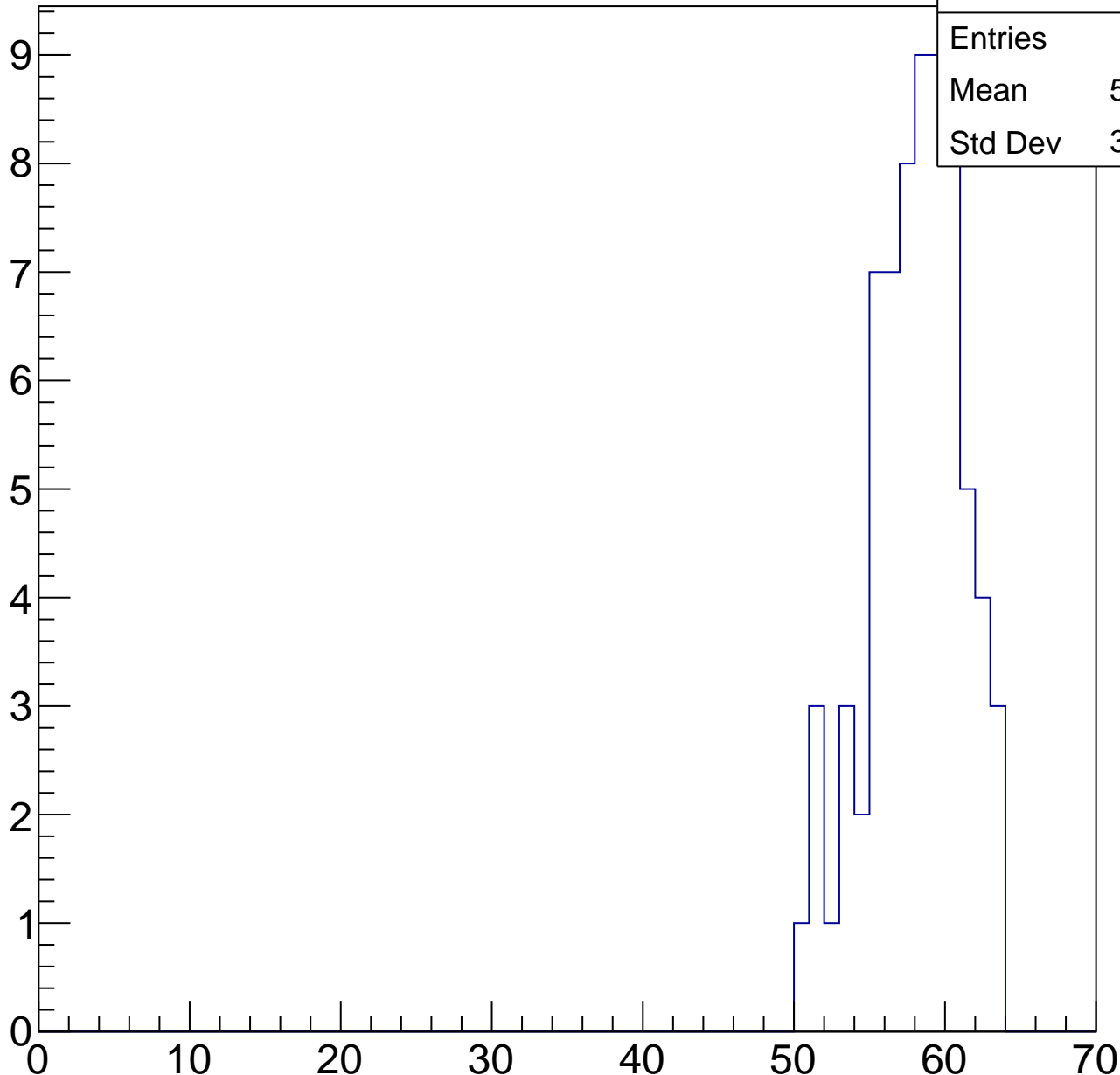
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	70
Mean	57.57
Std Dev	3.083

ampl

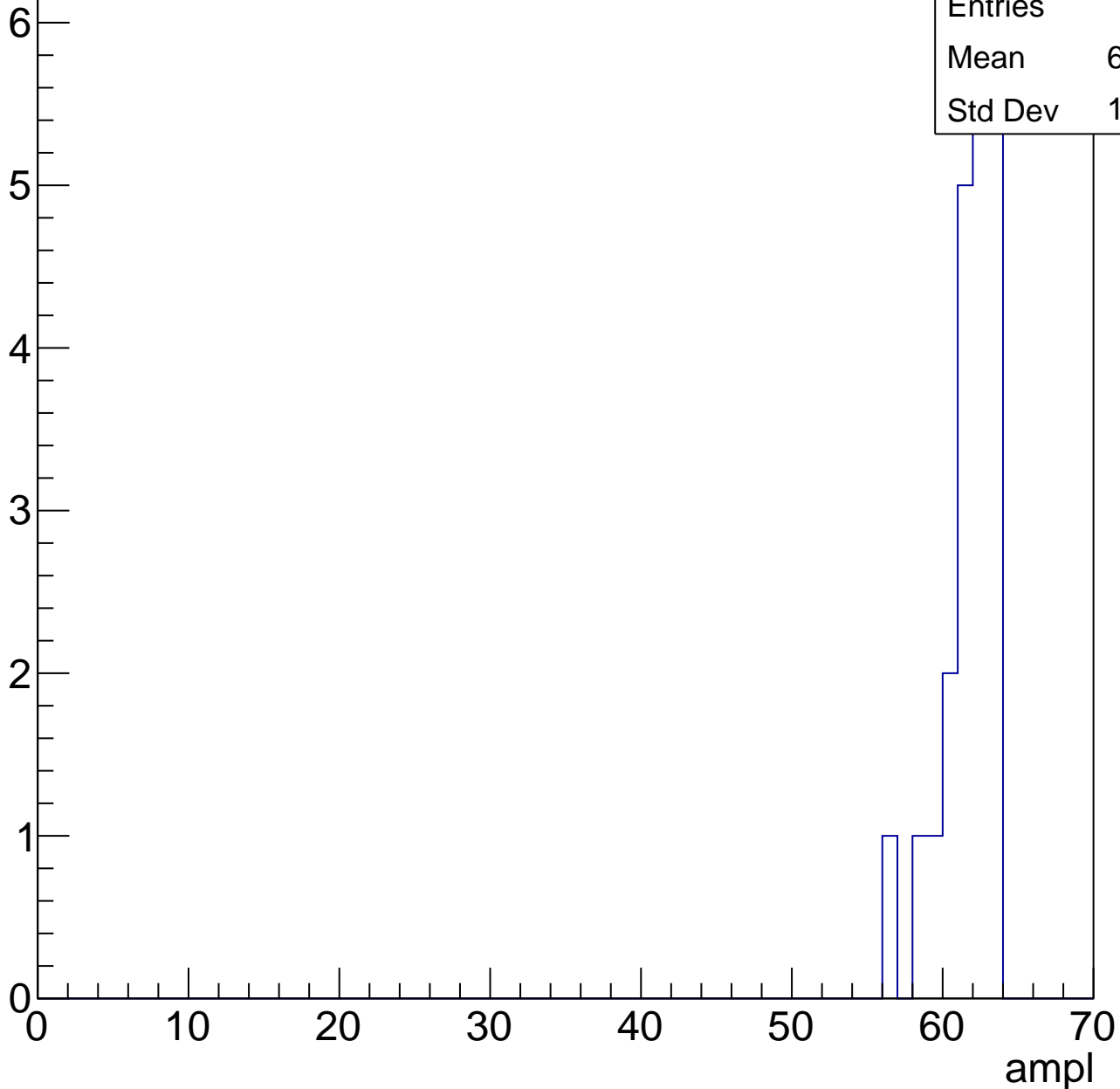


# B1L002S, U8-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	61.27
Std Dev	1.763





# B1L002S, U8-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch53, adc0

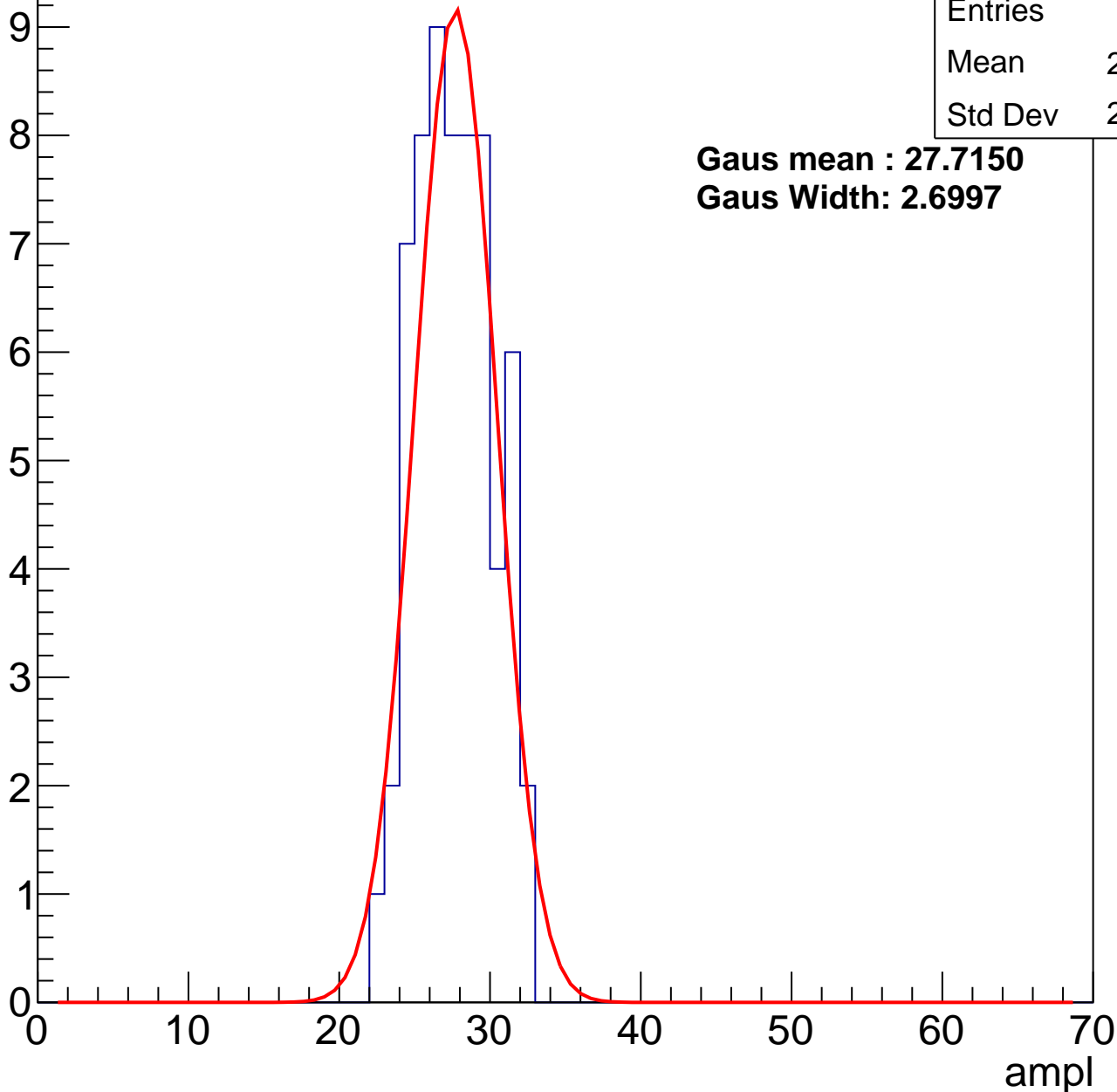
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.17
Std Dev	2.459

**Gaus mean : 27.7150**

**Gaus Width: 2.6997**



# B1L002S, U8-ch53, adc1

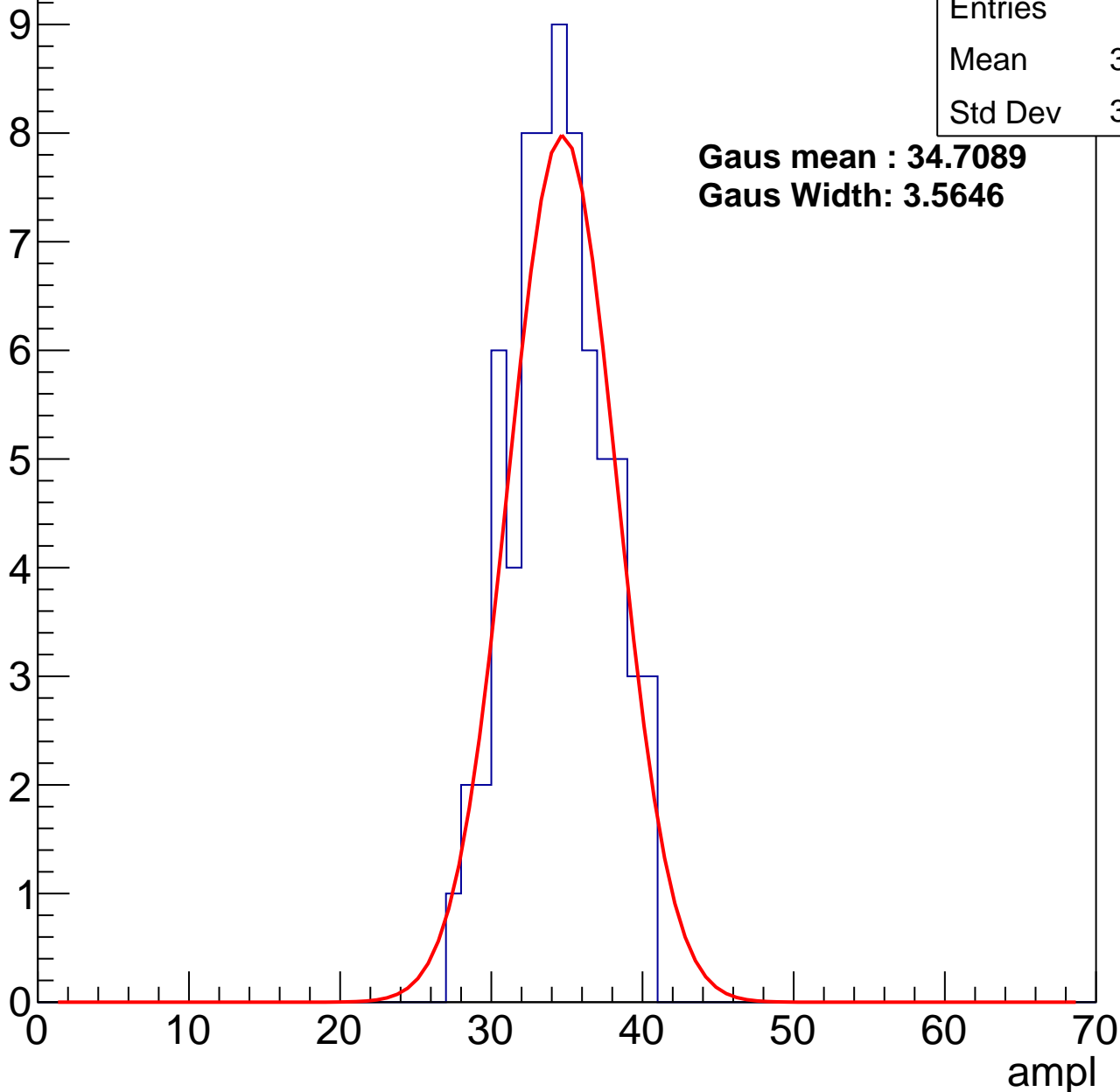
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	33.99
Std Dev	3.124

**Gaus mean : 34.7089**

**Gaus Width: 3.5646**



# B1L002S, U8-ch53, adc2

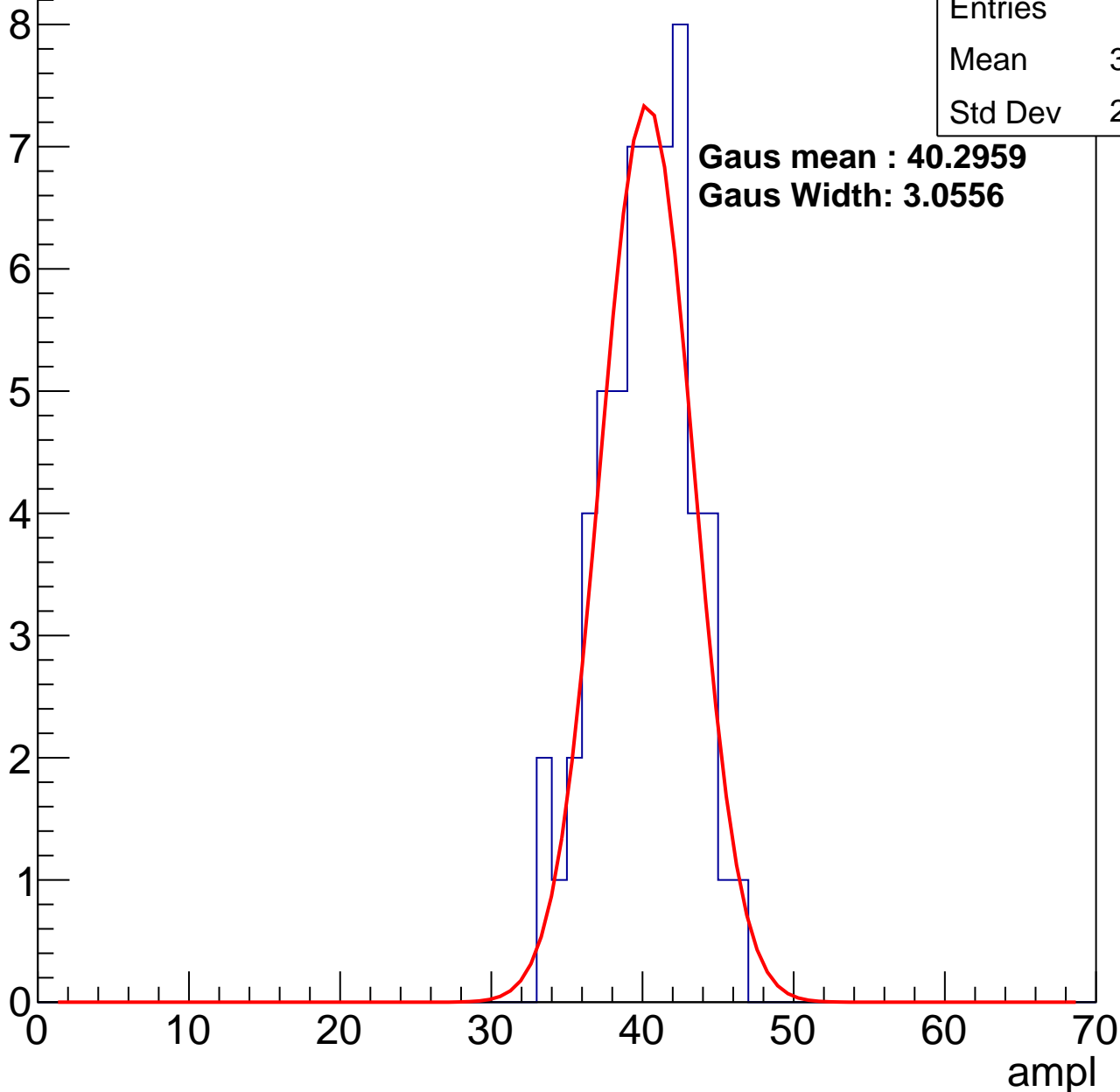
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.72
Std Dev	2.982

**Gaus mean : 40.2959**

**Gaus Width: 3.0556**

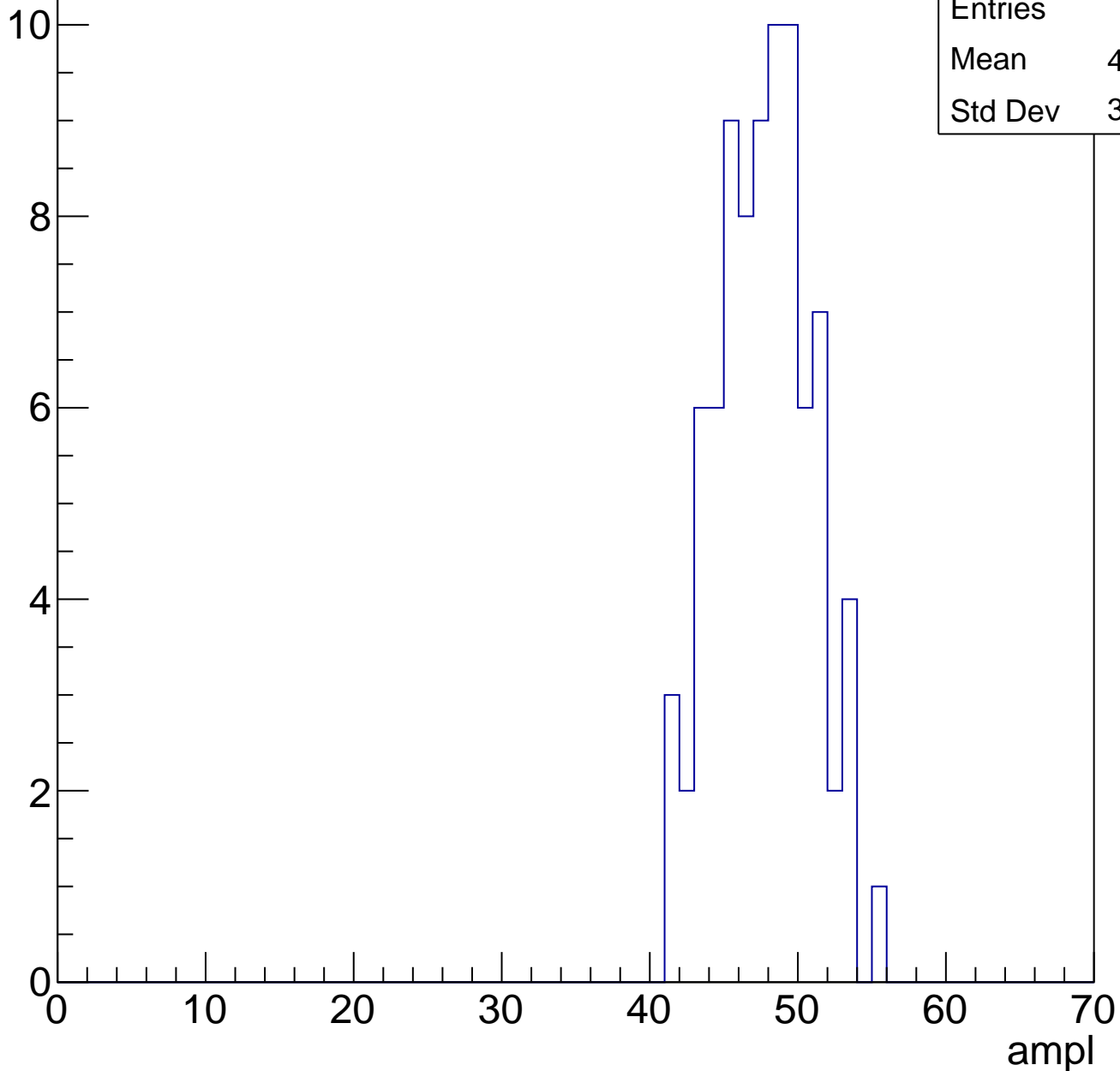


# B1L002S, U8-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	47.27
Std Dev	3.143

Entry

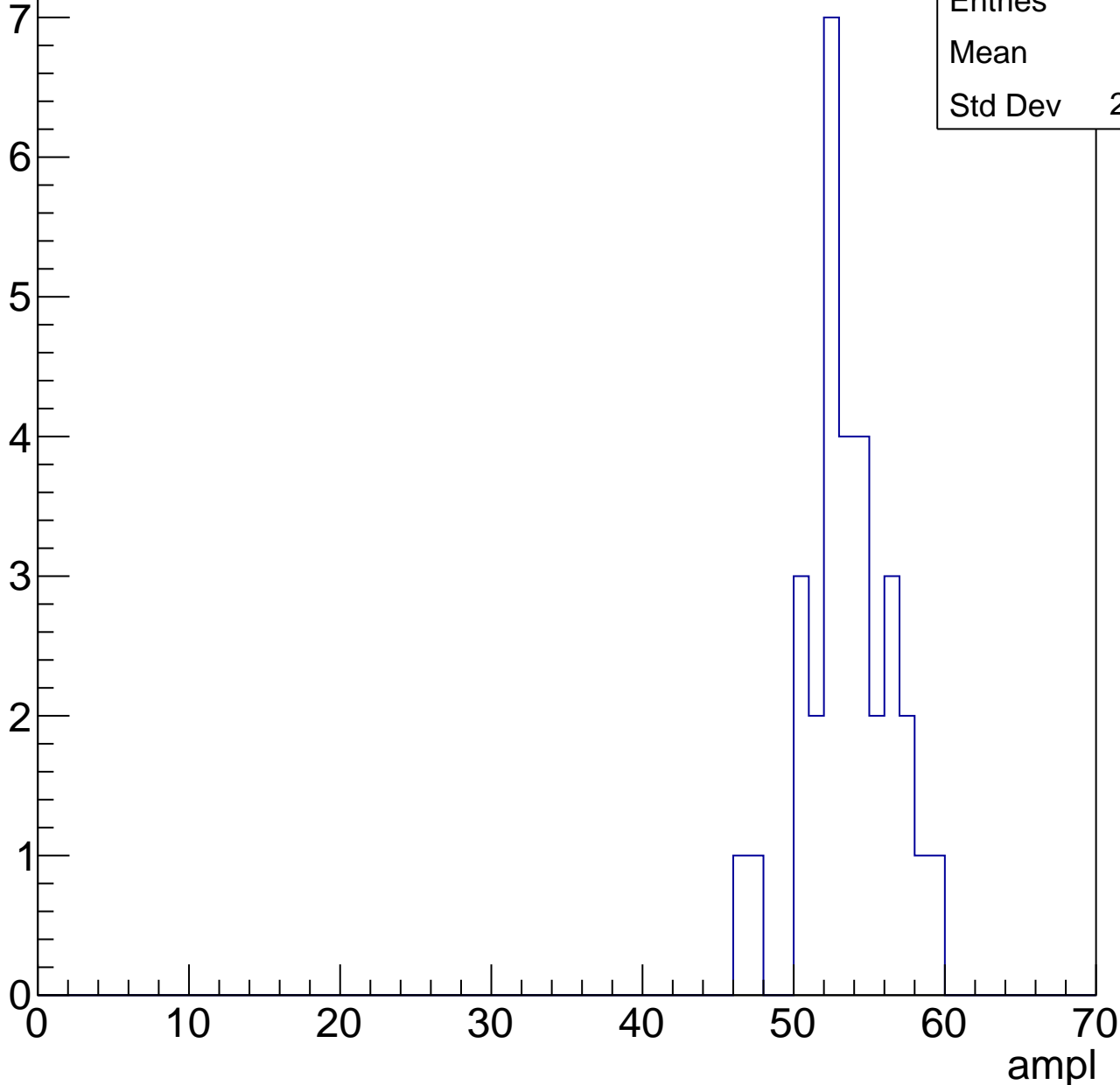


# B1L002S, U8-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	53.1
Std Dev	2.889

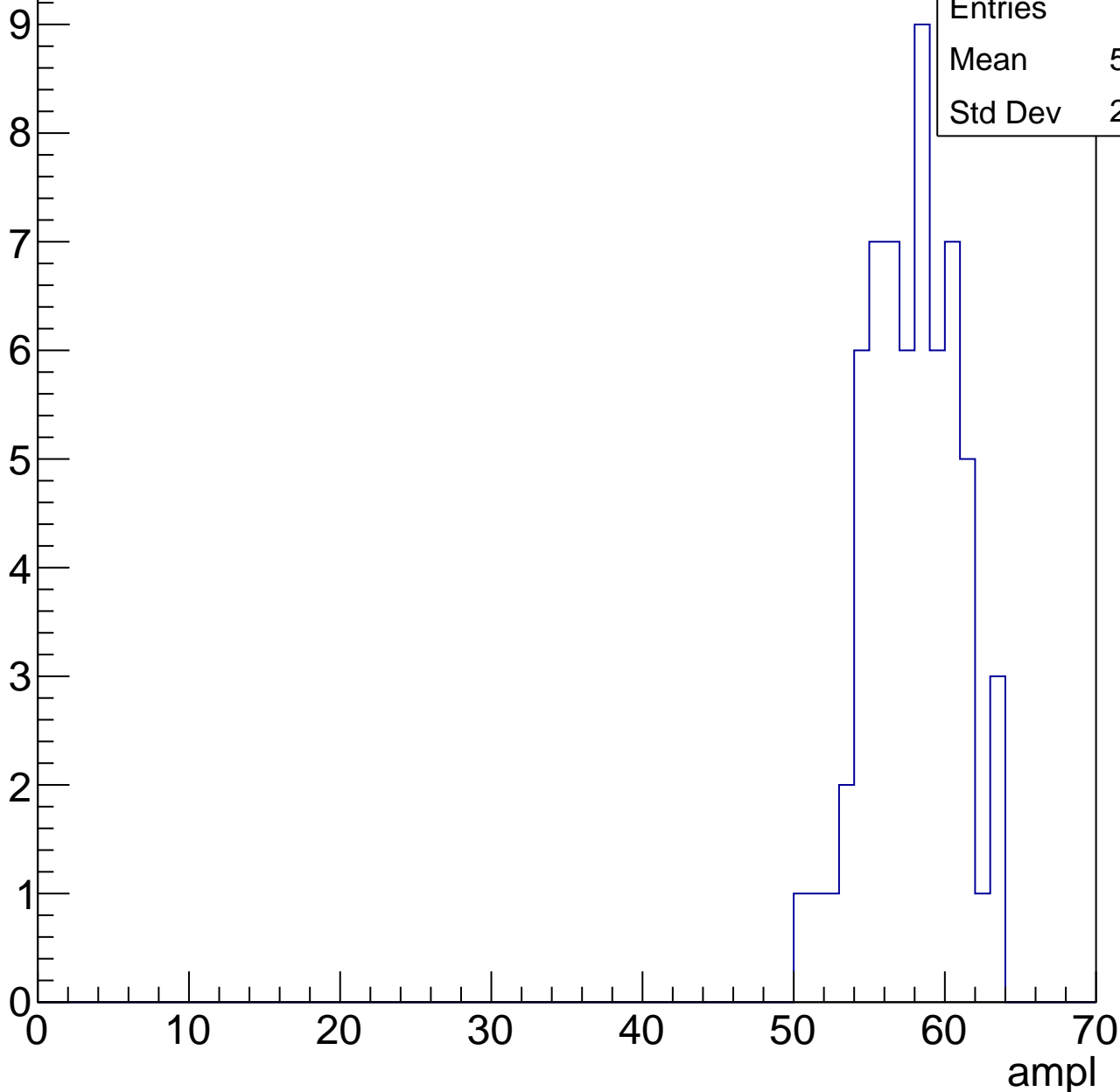


# B1L002S, U8-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	57.32
Std Dev	2.934

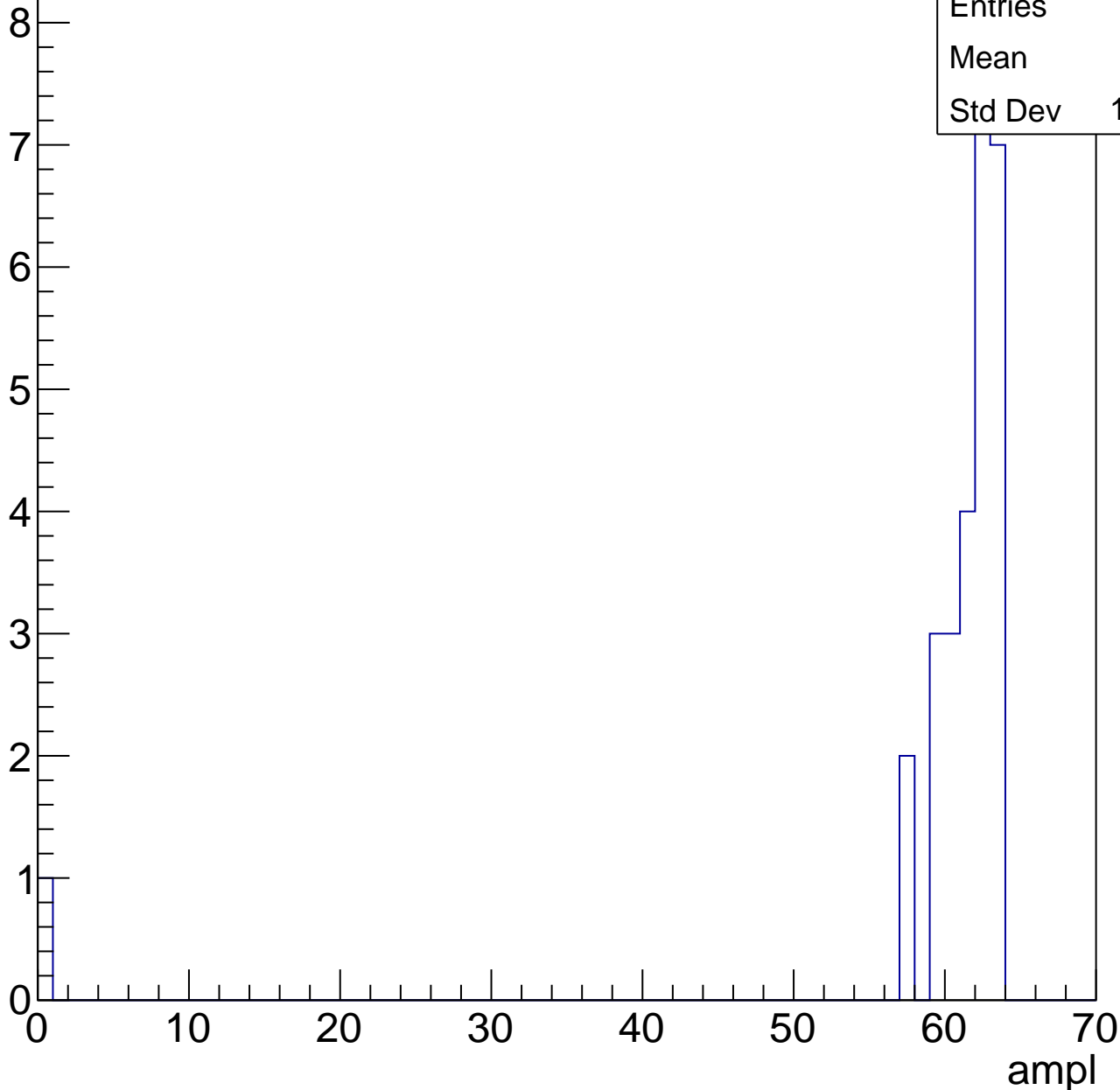


# B1L002S, U8-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59
Std Dev	11.48

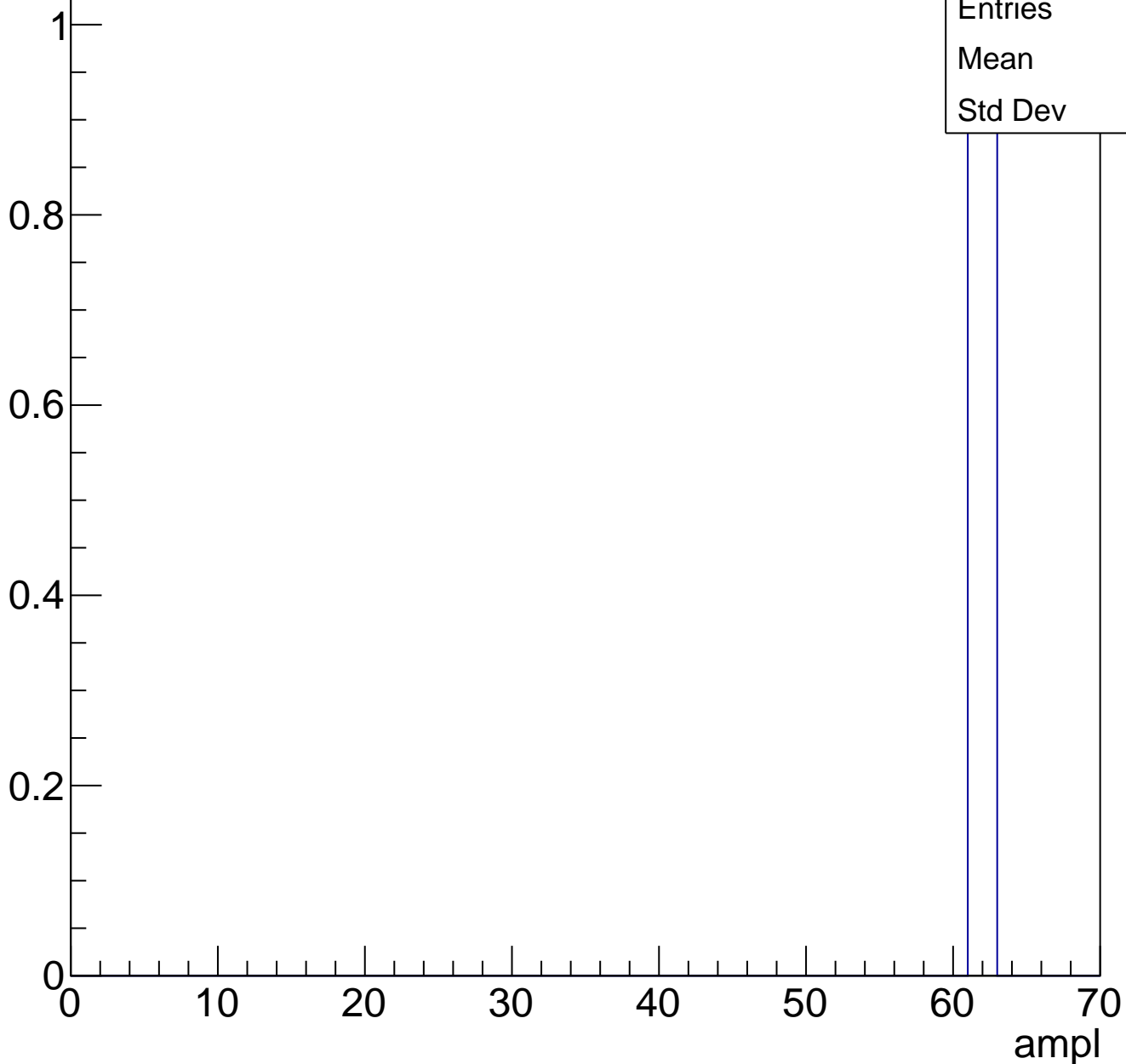




# B1L002S, U8-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch54, adc0

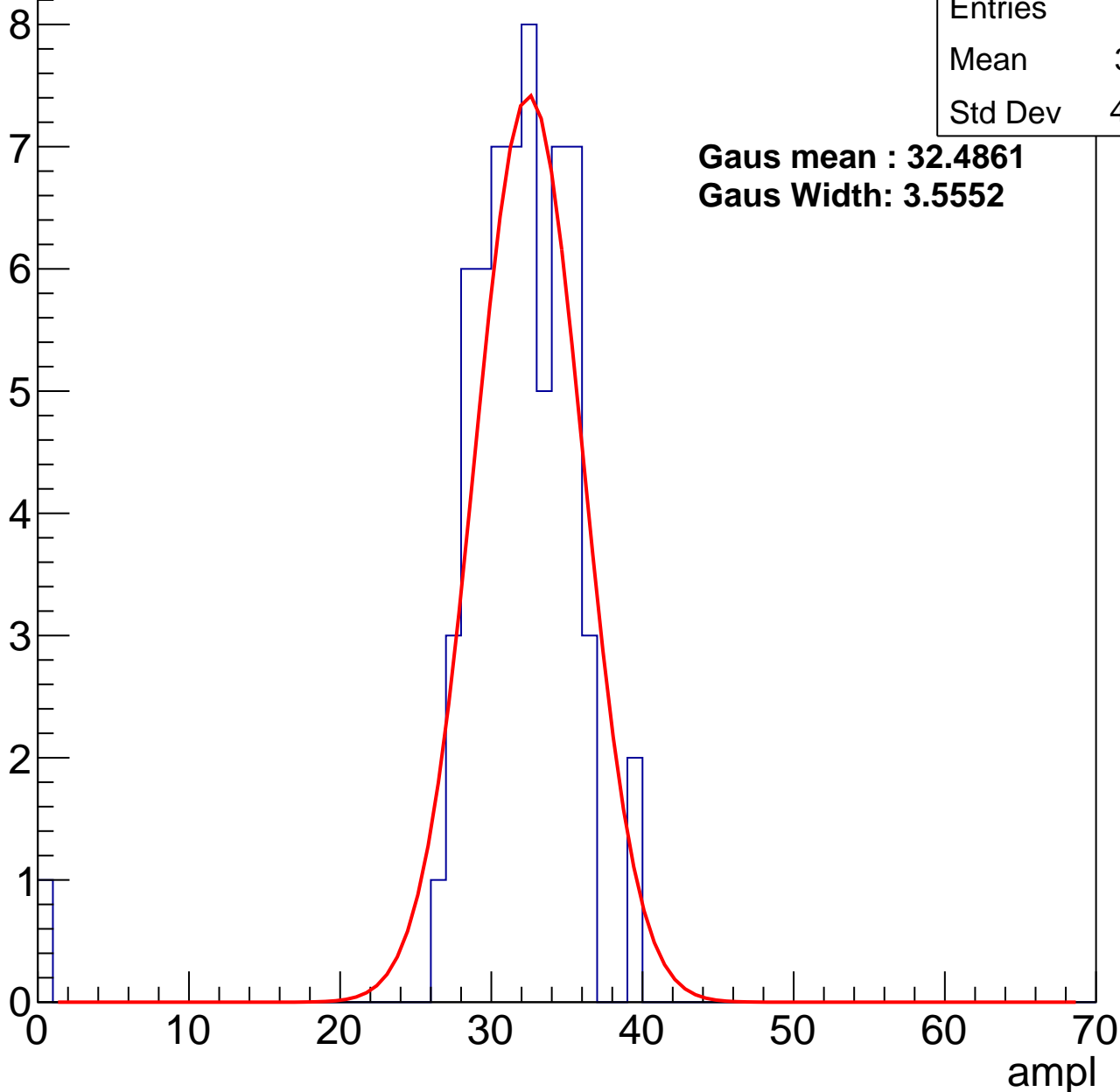
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	31.21
Std Dev	4.916

**Gaus mean : 32.4861**

**Gaus Width: 3.5552**



# B1L002S, U8-ch54, adc1

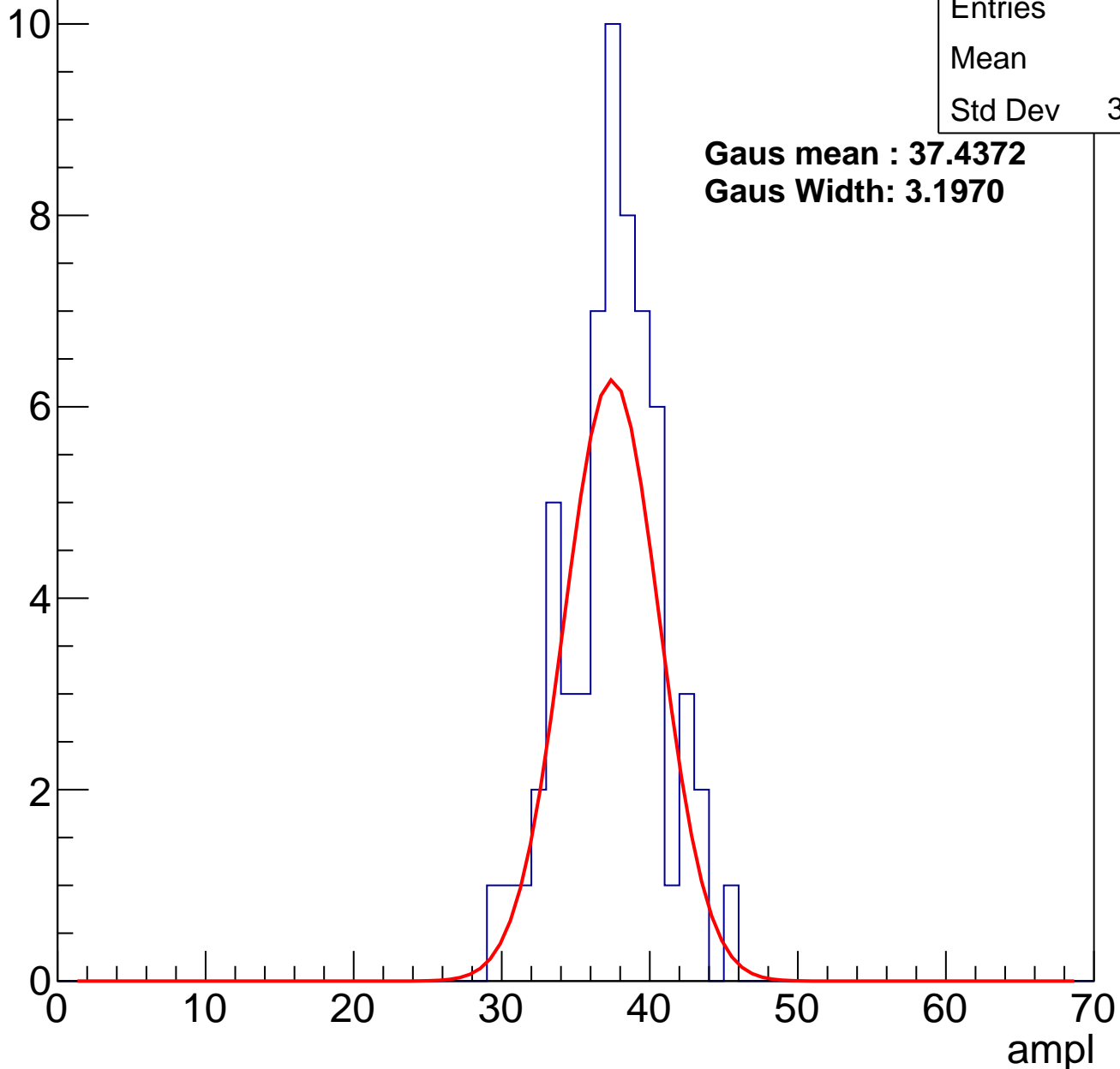
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	37.1
Std Dev	3.243

**Gaus mean : 37.4372**

**Gaus Width: 3.1970**

Entry



# B1L002S, U8-ch54, adc2

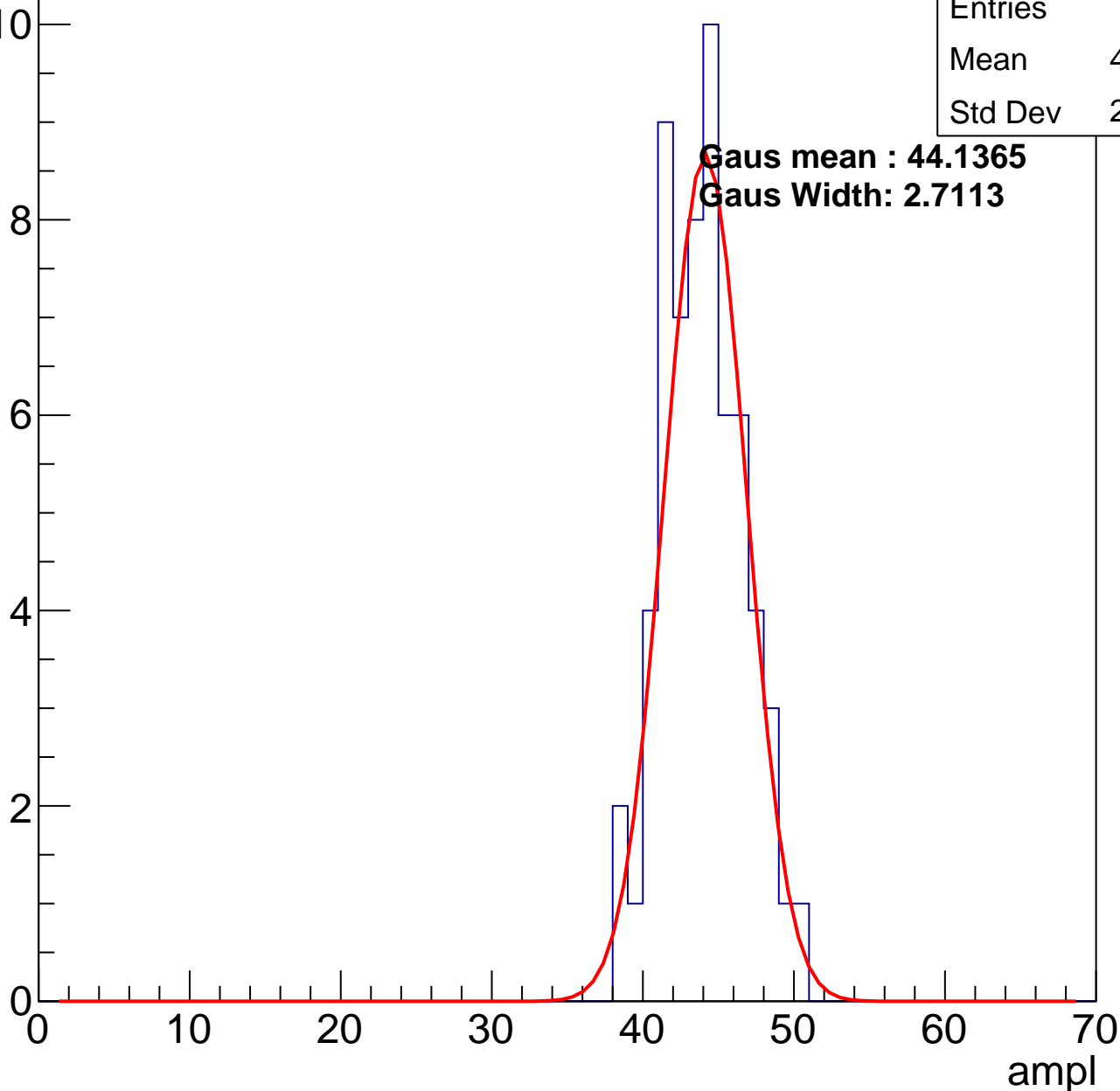
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	43.53
Std Dev	2.662

**Gaus mean : 44.1365**

**Gaus Width: 2.7113**

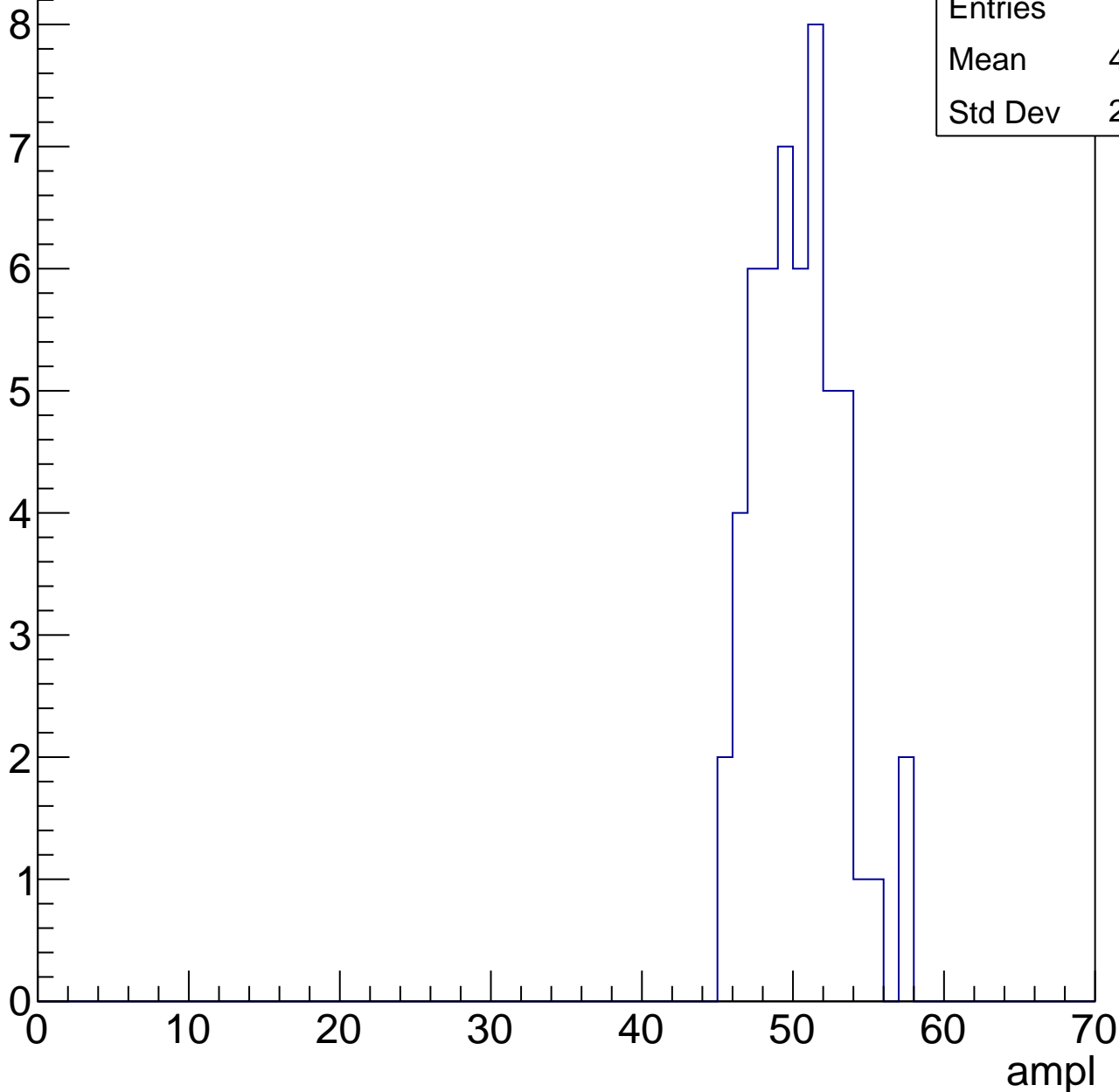


# B1L002S, U8-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	49.87
Std Dev	2.782

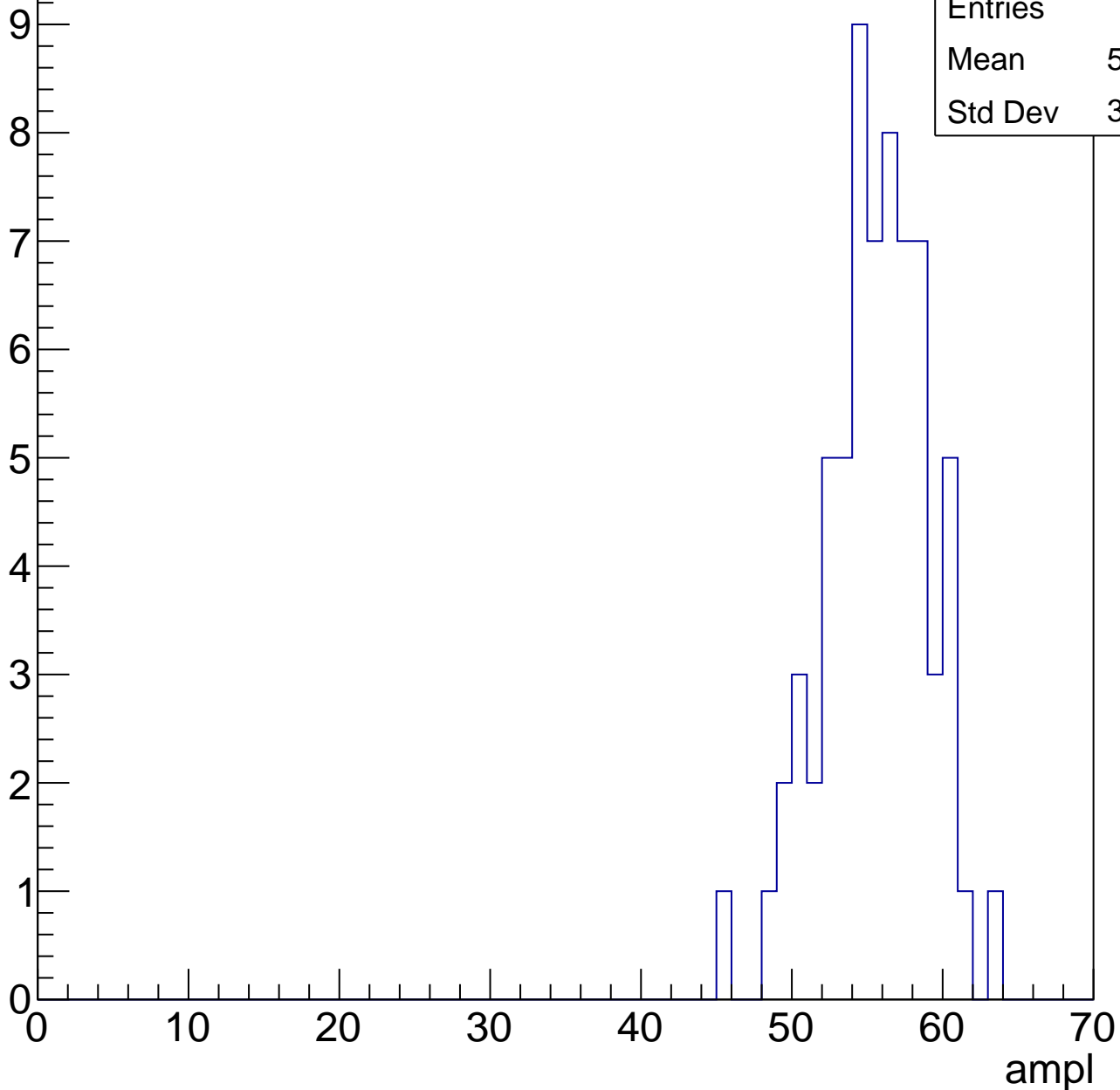


# B1L002S, U8-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	55.12
Std Dev	3.397



# B1L002S, U8-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

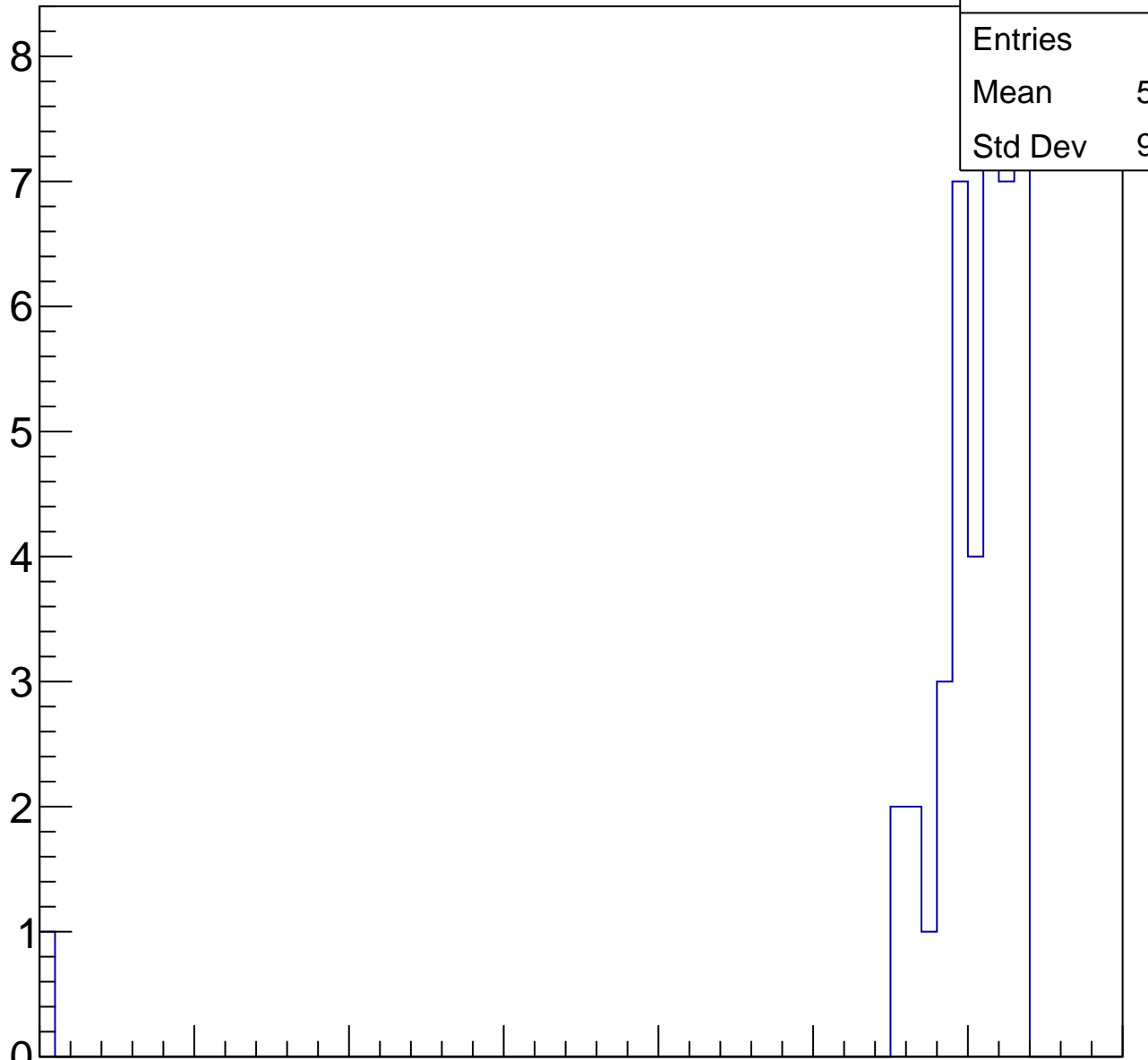
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	58.88
Std Dev	9.357

ampl

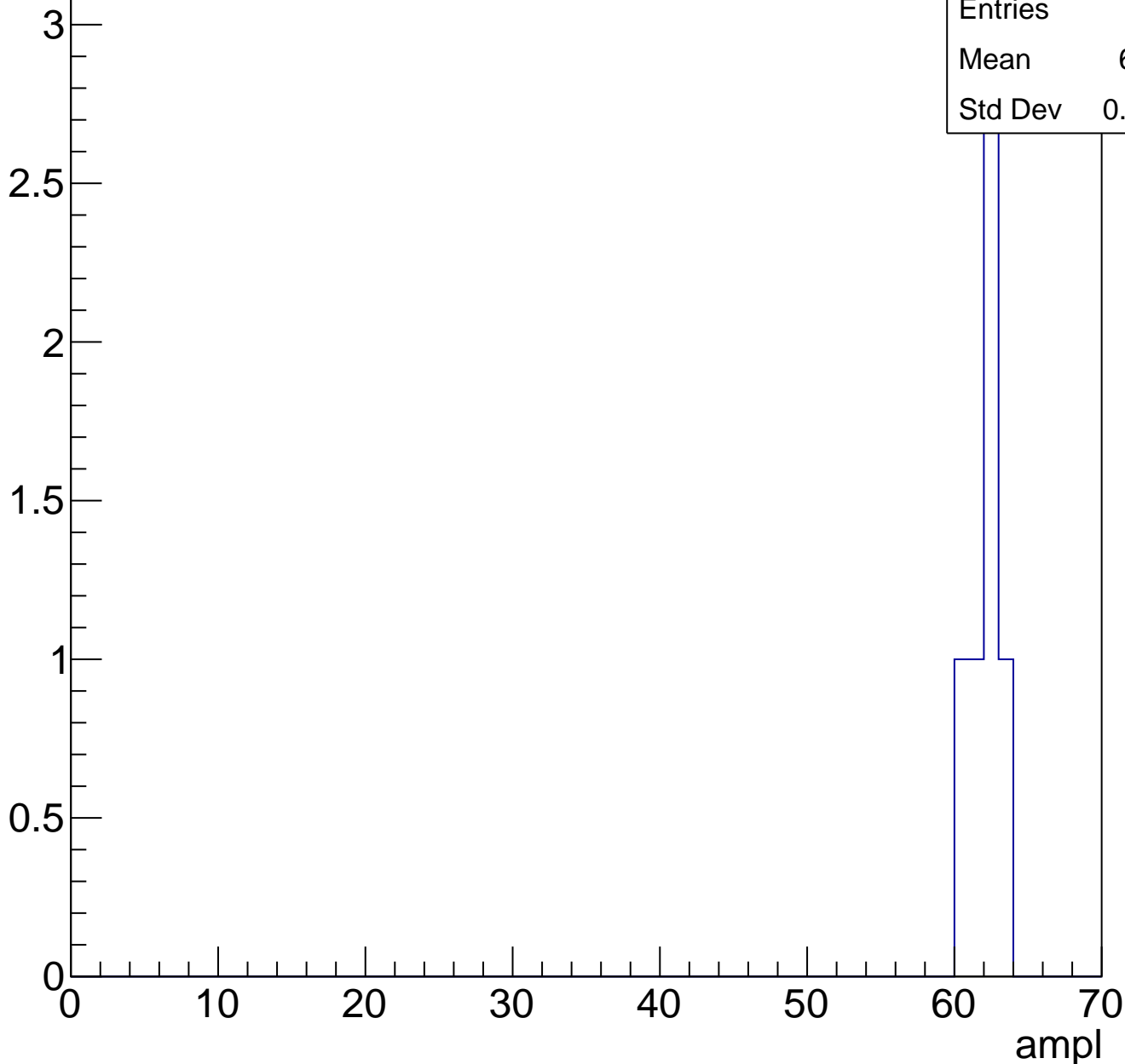
0 10 20 30 40 50 60 70



# B1L002S, U8-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

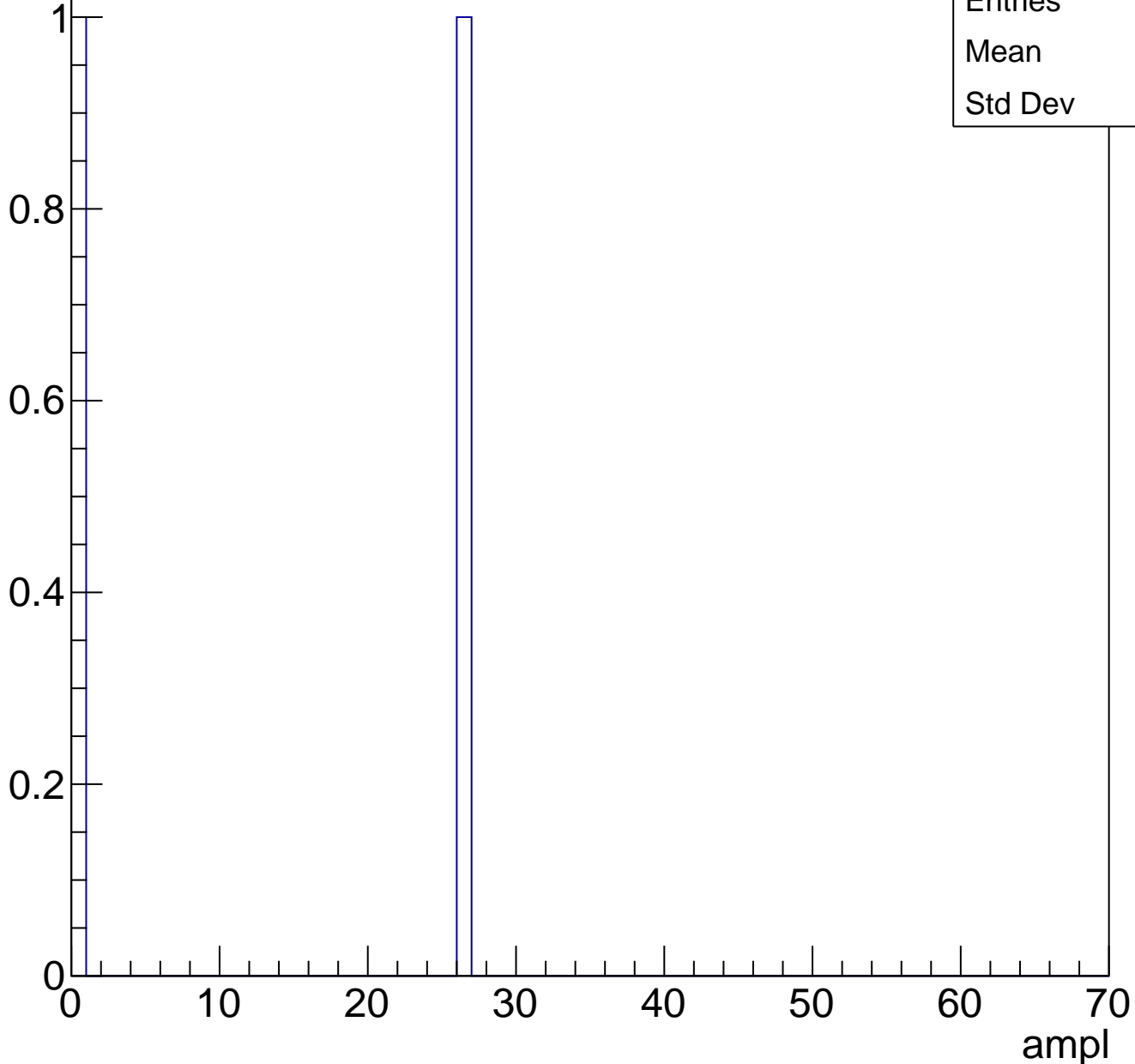




# B1L002S, U8-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	13
Std Dev	13

# B1L002S, U8-ch55, adc0

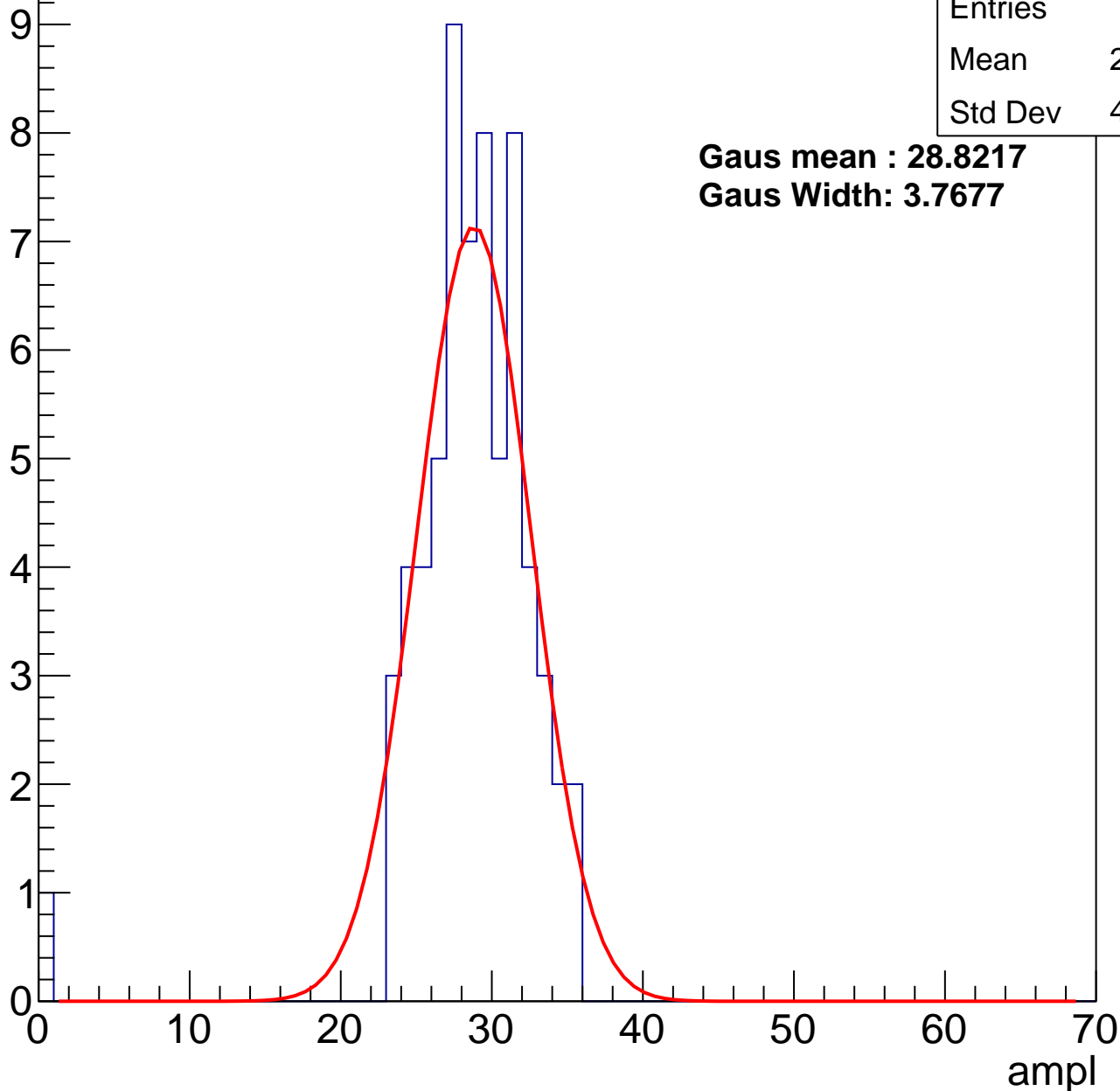
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.14
Std Dev	4.634

**Gaus mean : 28.8217**

**Gaus Width: 3.7677**



# B1L002S, U8-ch55, adc1

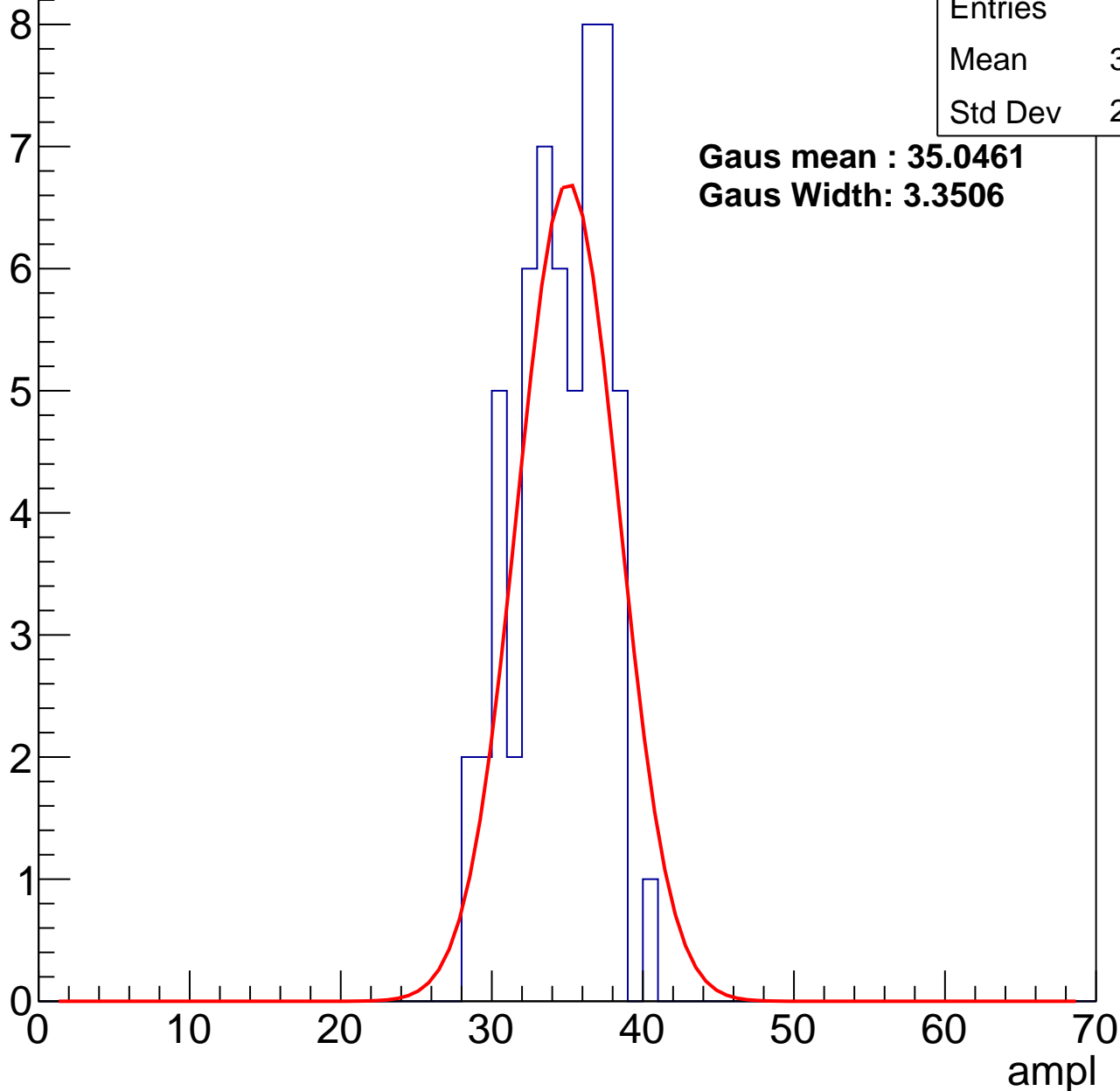
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.07
Std Dev	2.889

**Gaus mean : 35.0461**

**Gaus Width: 3.3506**



# B1L002S, U8-ch55, adc2

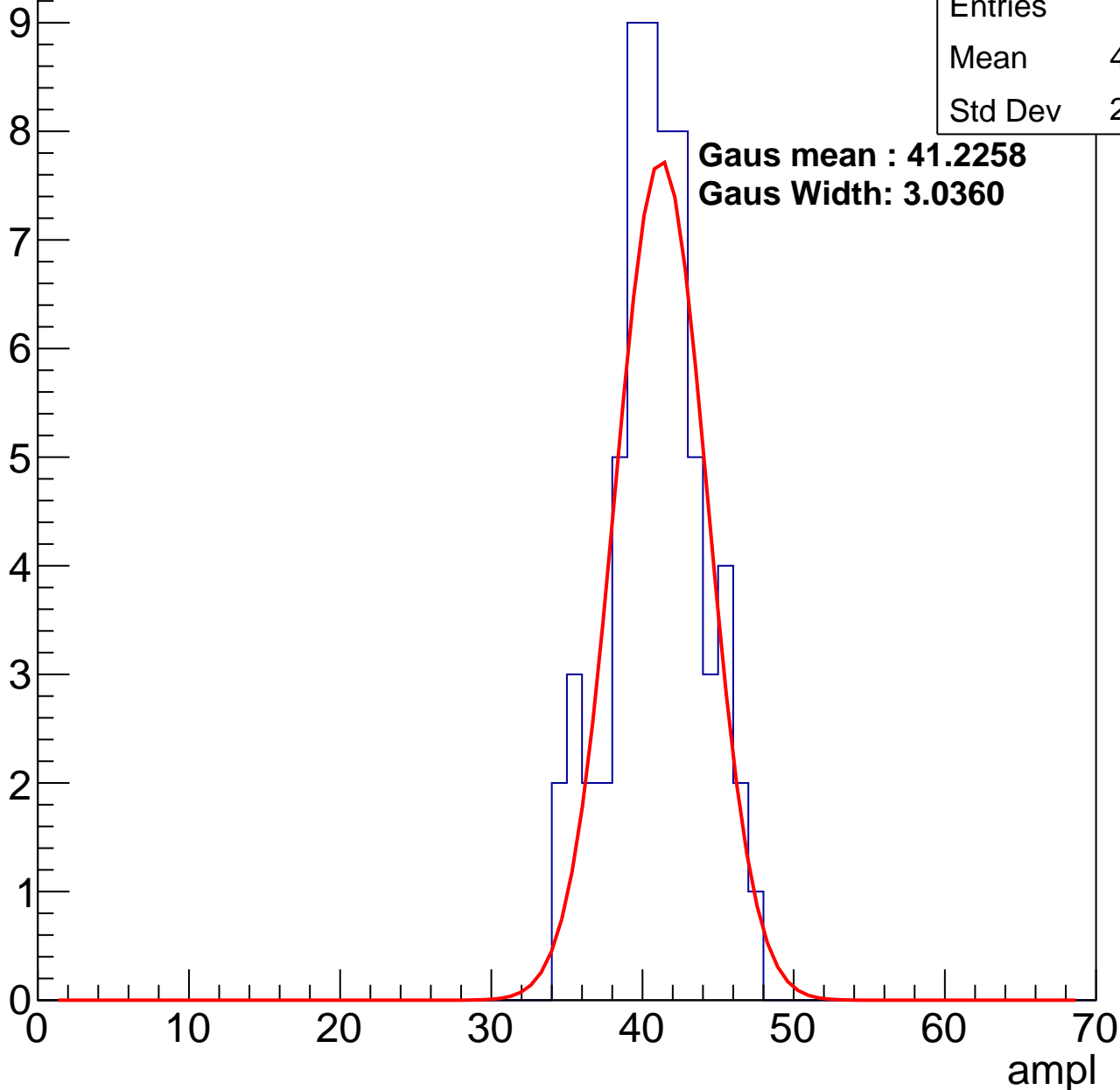
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.48
Std Dev	2.997

**Gaus mean : 41.2258**

**Gaus Width: 3.0360**

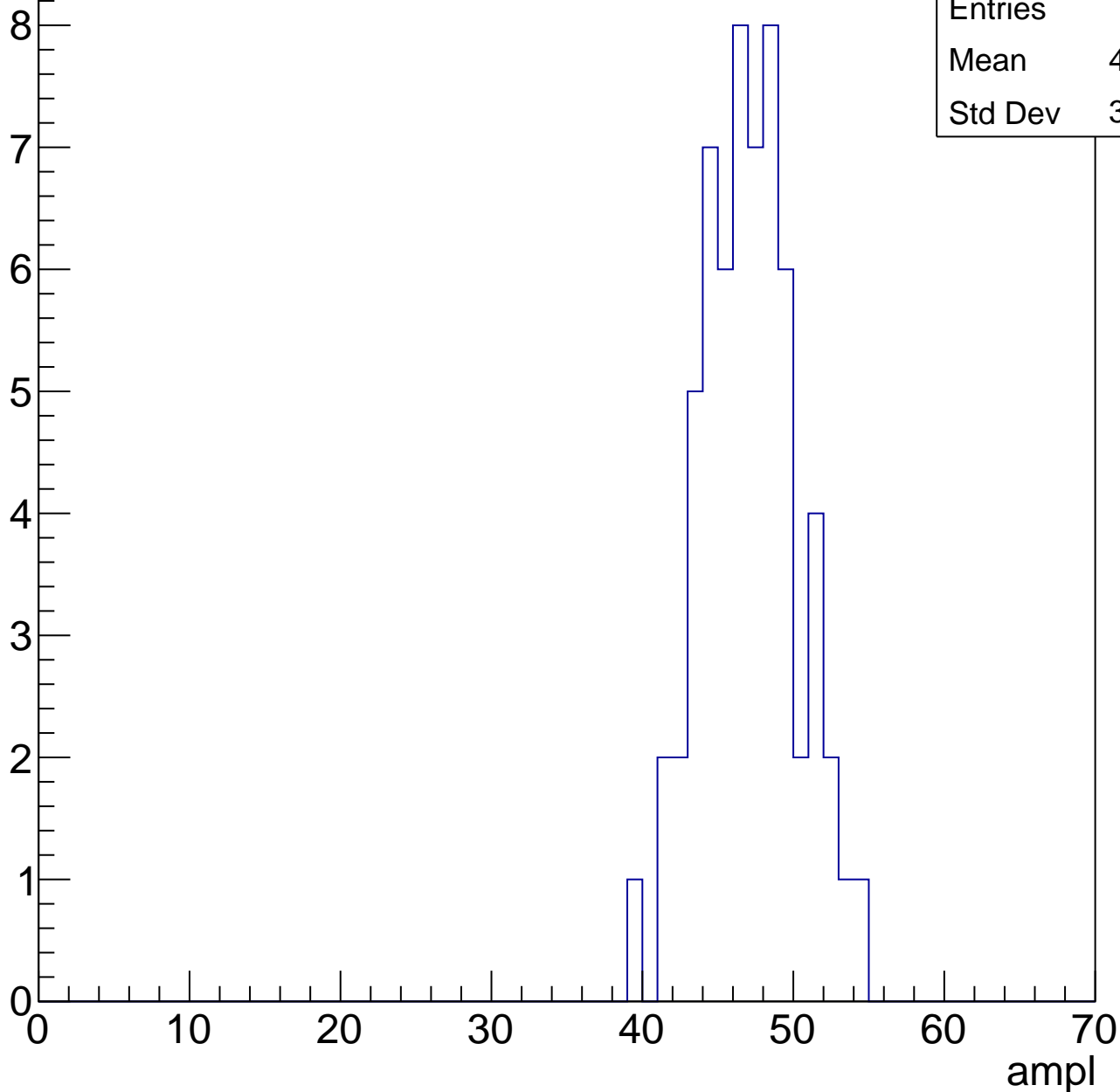


# B1L002S, U8-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

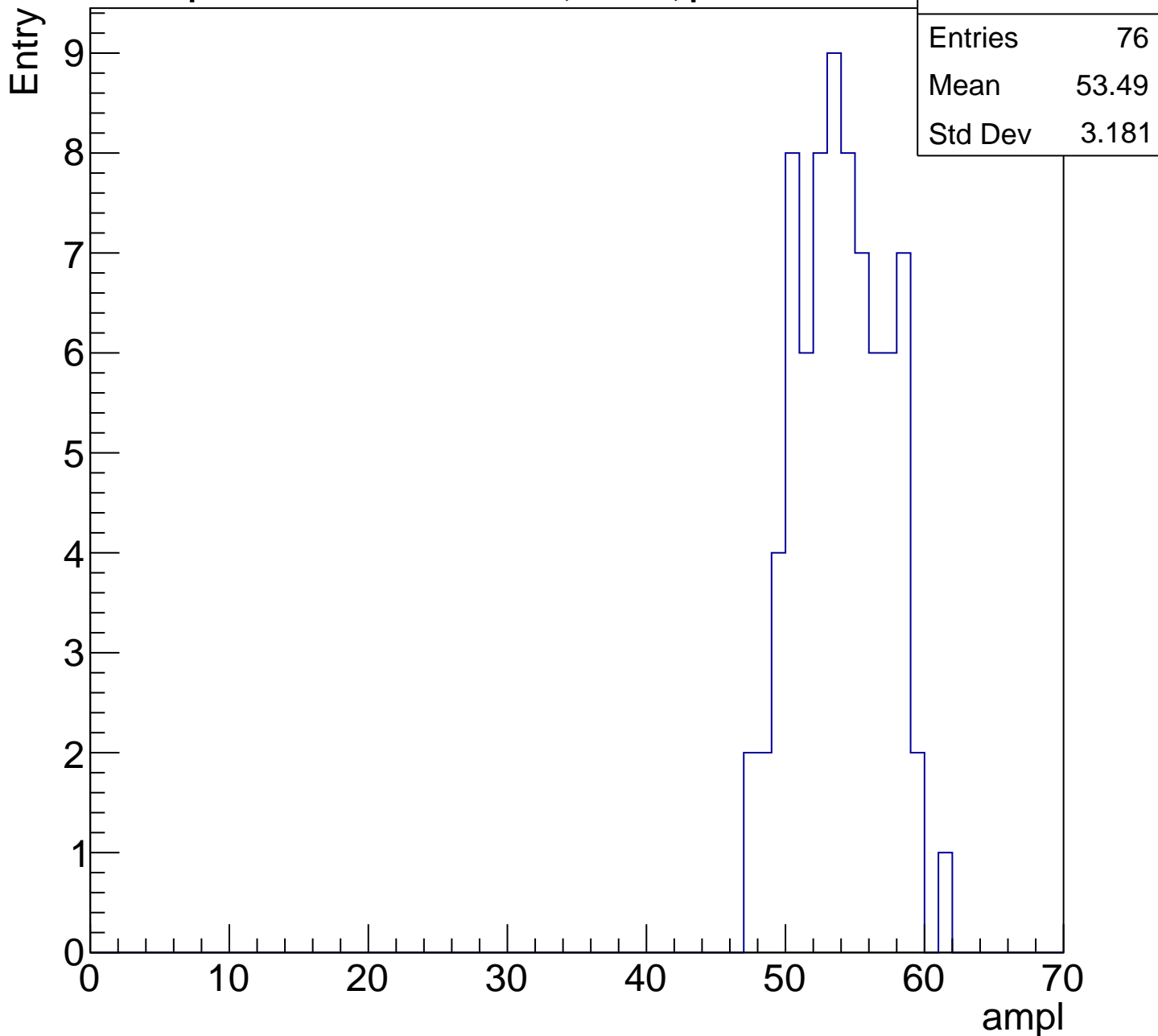
Entry

Entries	62
Mean	46.58
Std Dev	3.109



# B1L002S, U8-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.46
Std Dev	2.435

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

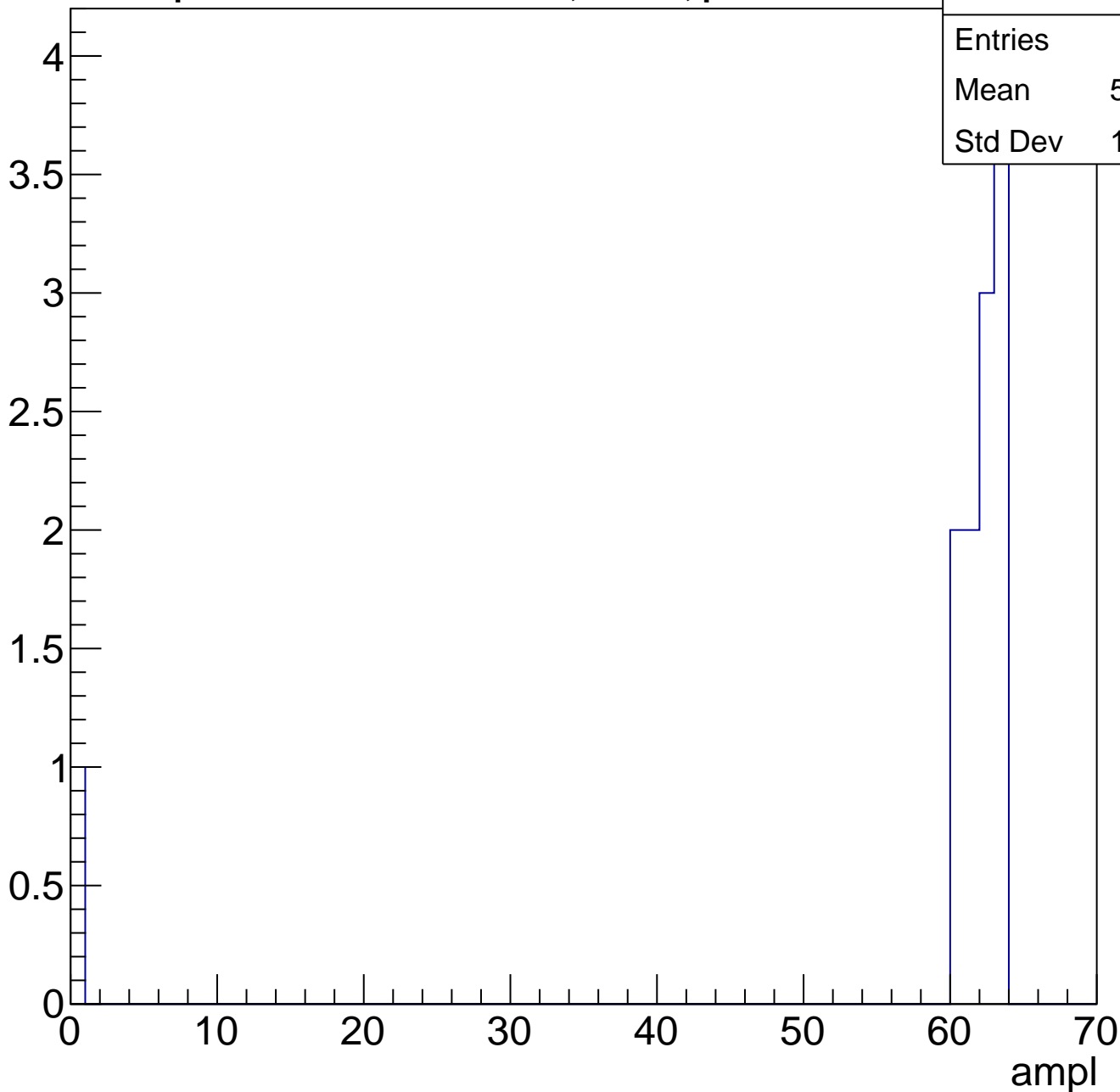
7

8

# B1L002S, U8-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

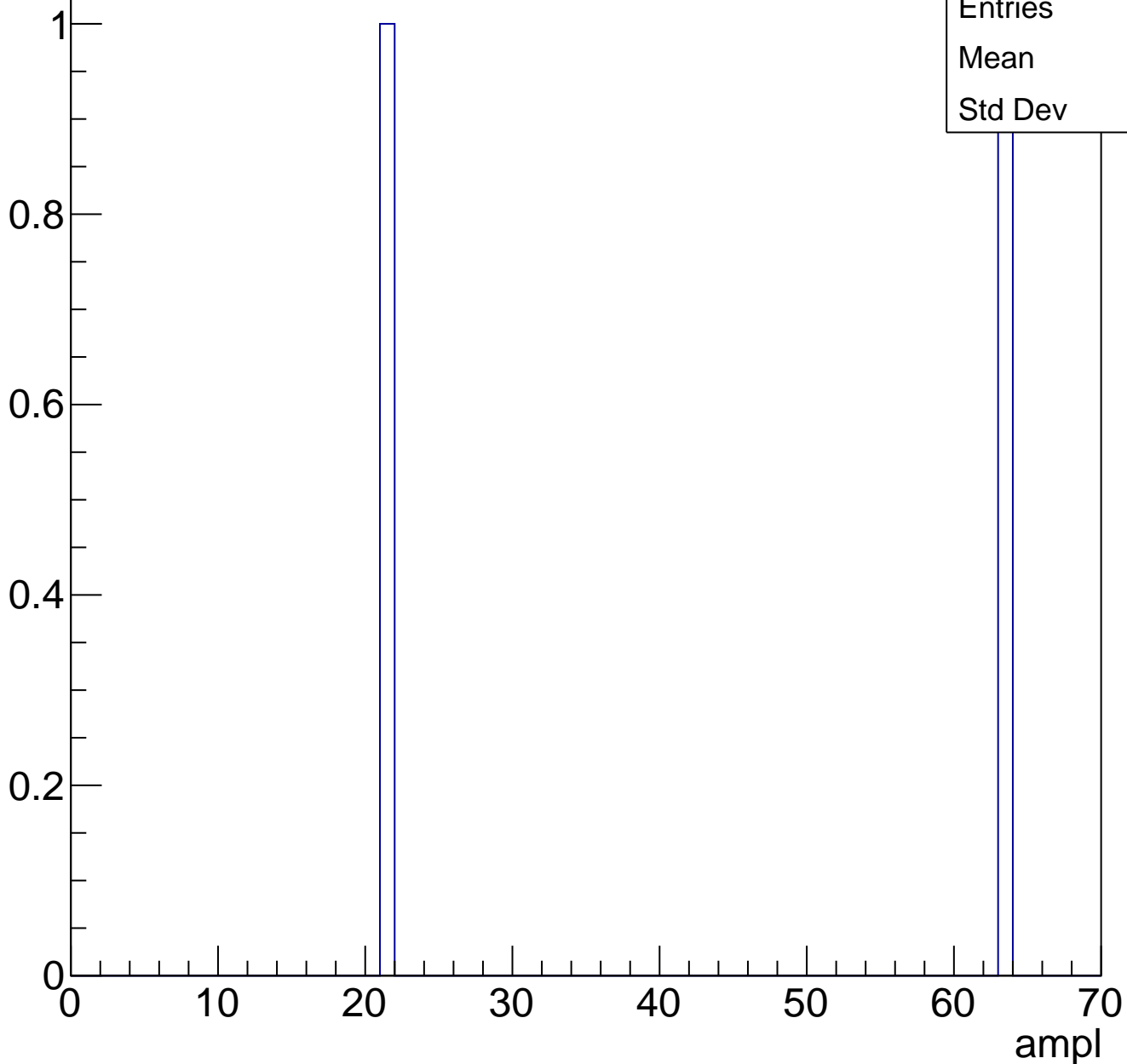




# B1L002S, U8-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	42
Std Dev	21

# B1L002S, U8-ch56, adc0

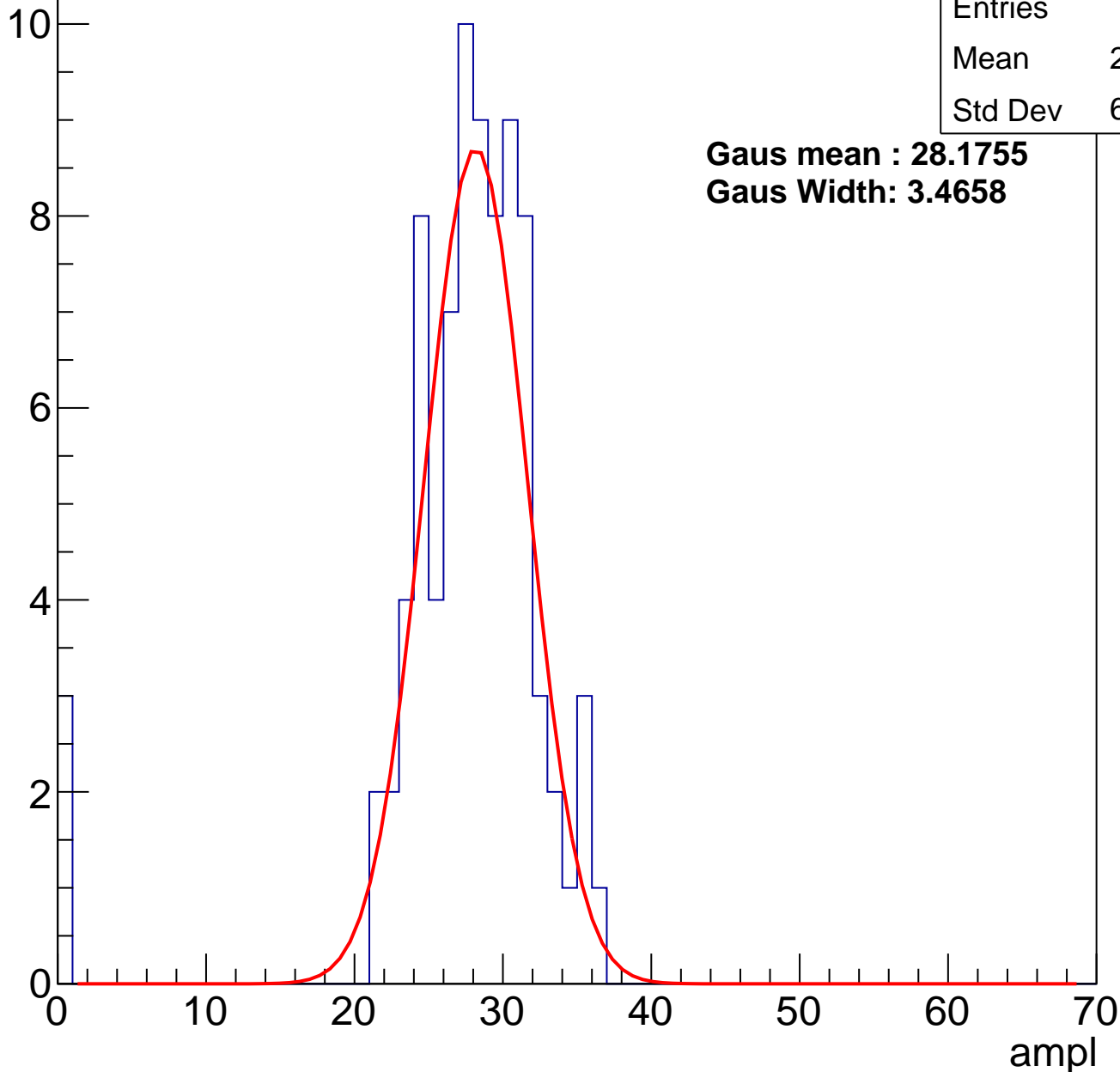
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	84
Mean	26.92
Std Dev	6.159

**Gaus mean : 28.1755**

**Gaus Width: 3.4658**

Entry



# B1L002S, U8-ch56, adc1

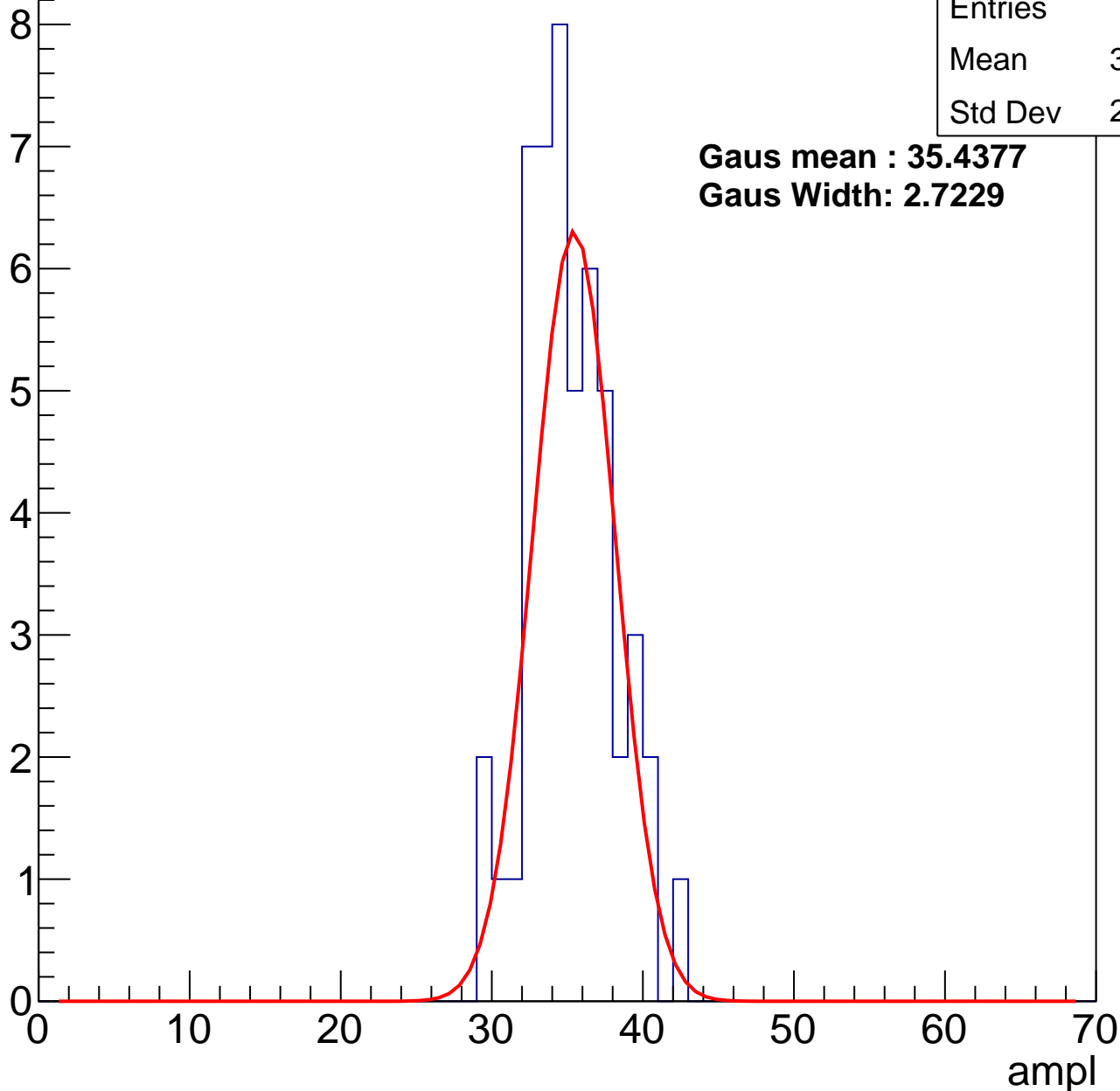
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	34.74
Std Dev	2.827

**Gaus mean : 35.4377**

**Gaus Width: 2.7229**



# B1L002S, U8-ch56, adc2

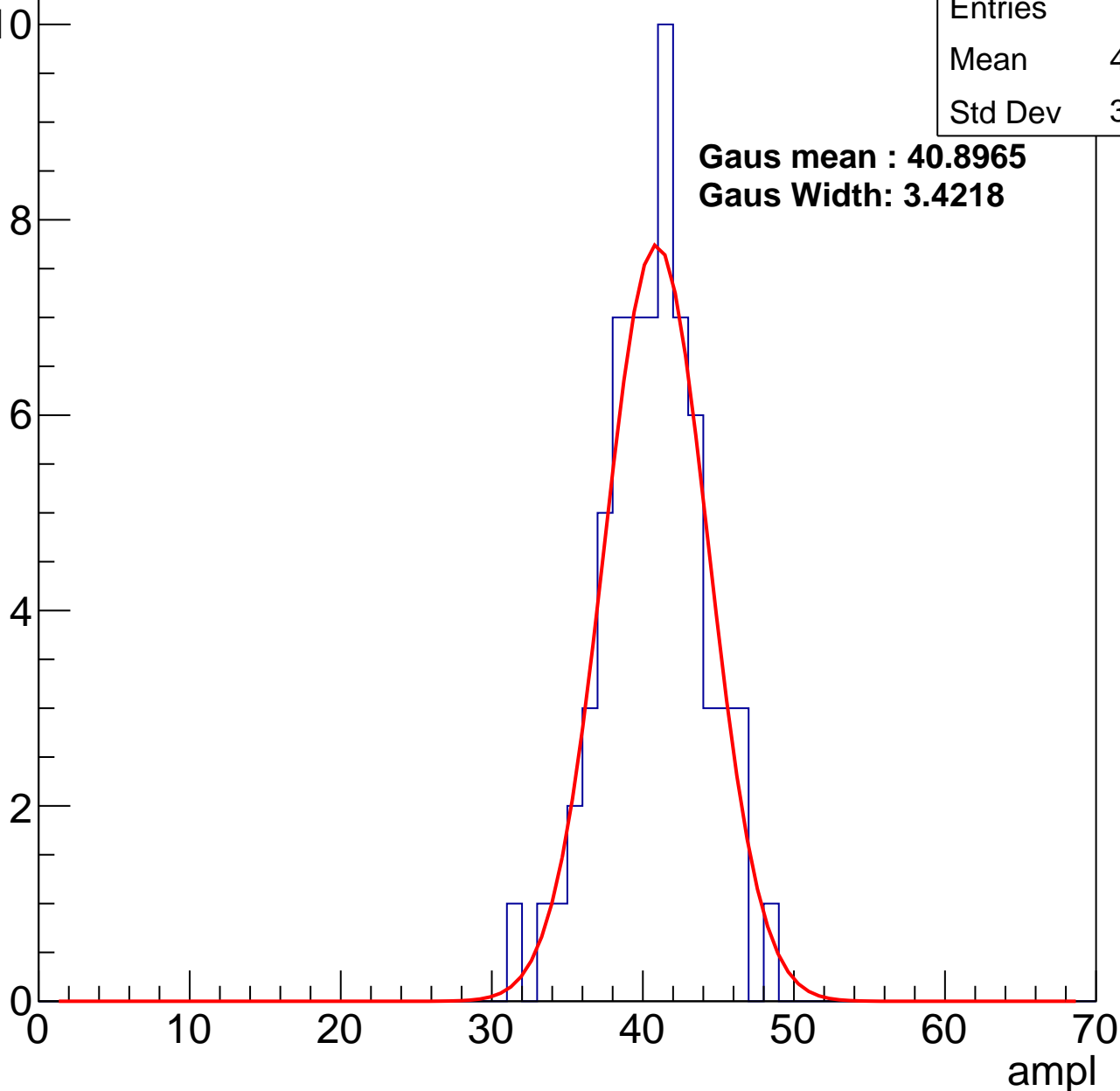
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.22
Std Dev	3.296

**Gaus mean : 40.8965**

**Gaus Width: 3.4218**

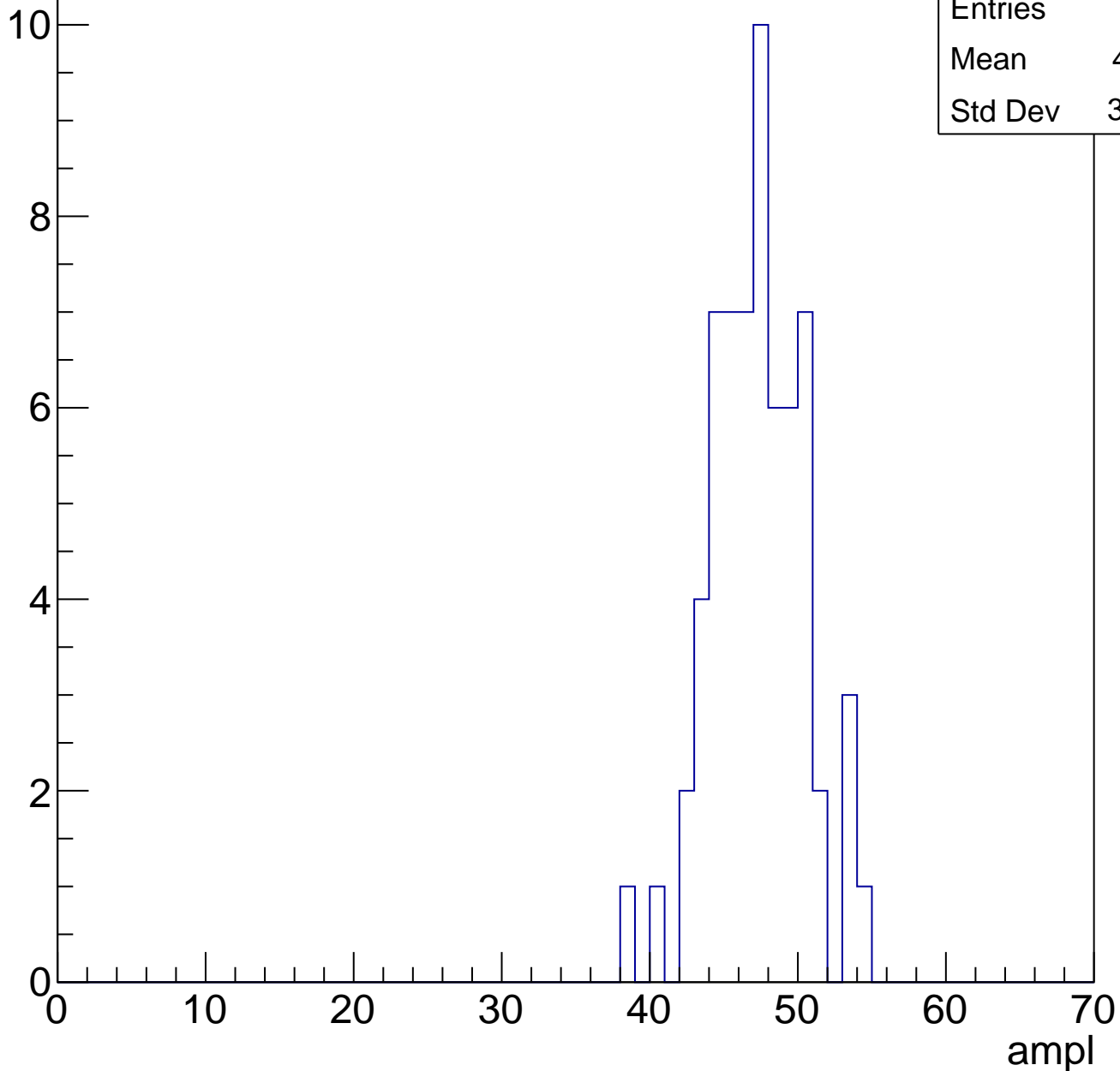


# B1L002S, U8-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

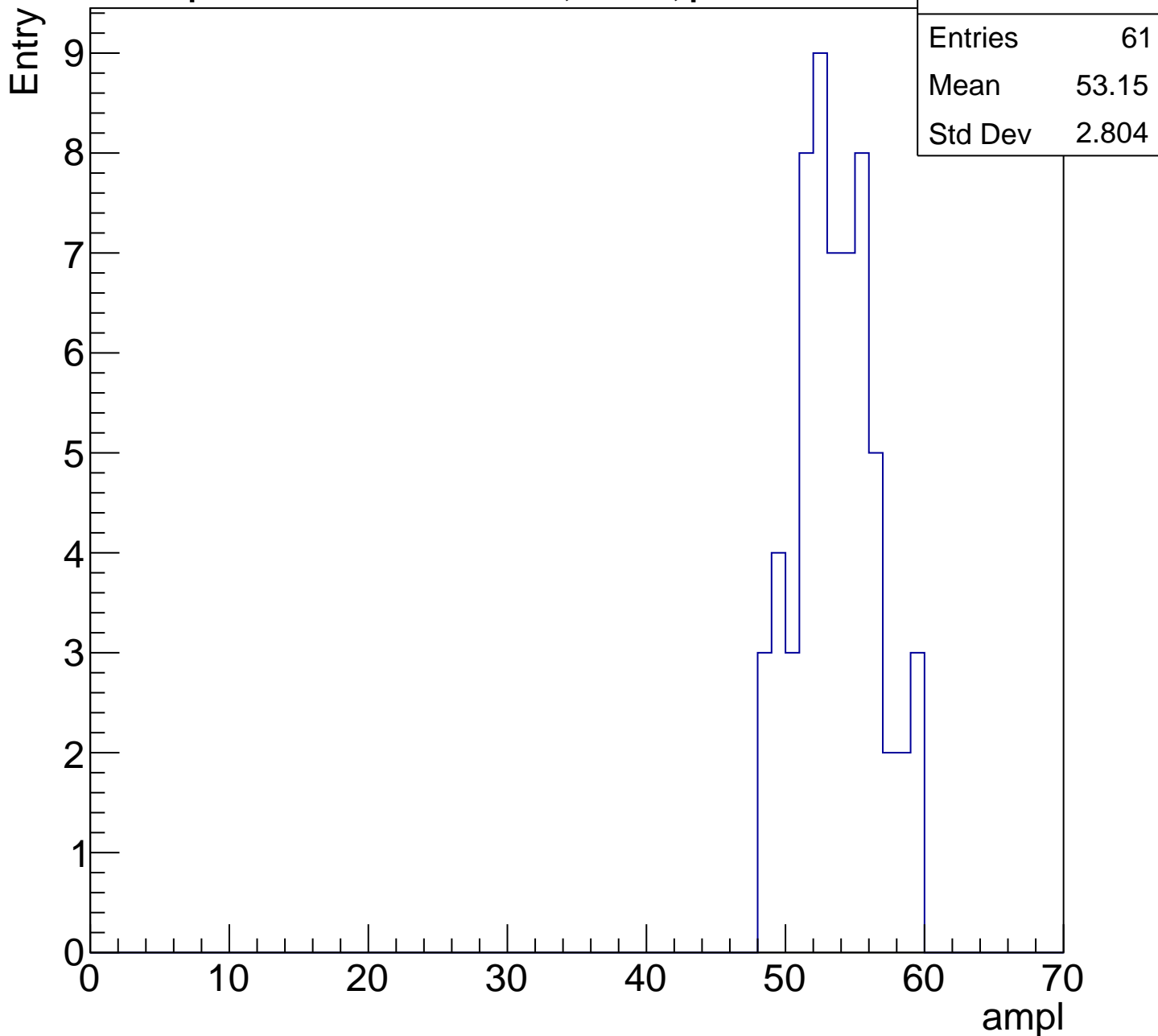
Entries	64
Mean	46.81
Std Dev	3.117

Entry



# B1L002S, U8-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

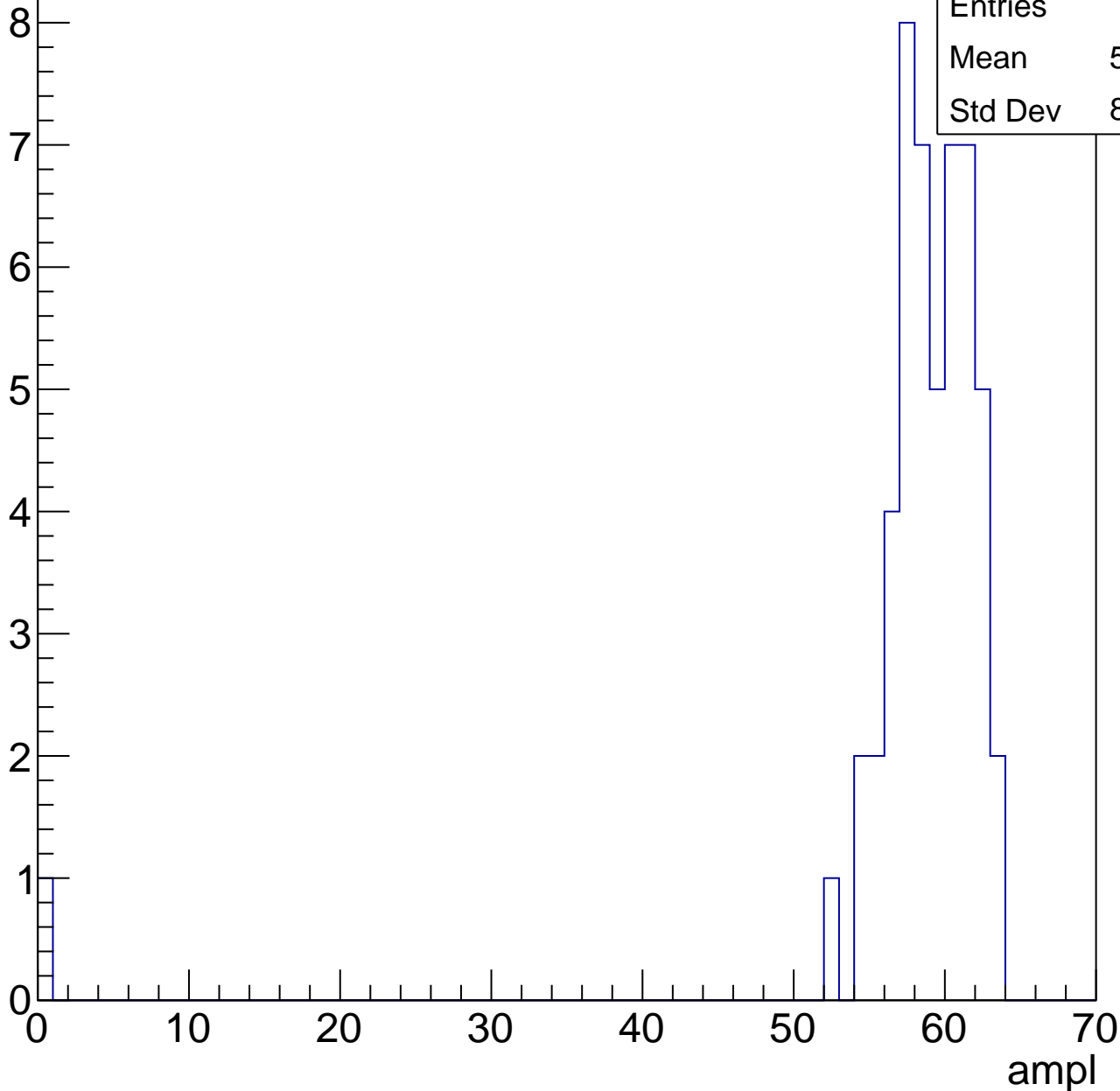


# B1L002S, U8-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.53
Std Dev	8.505

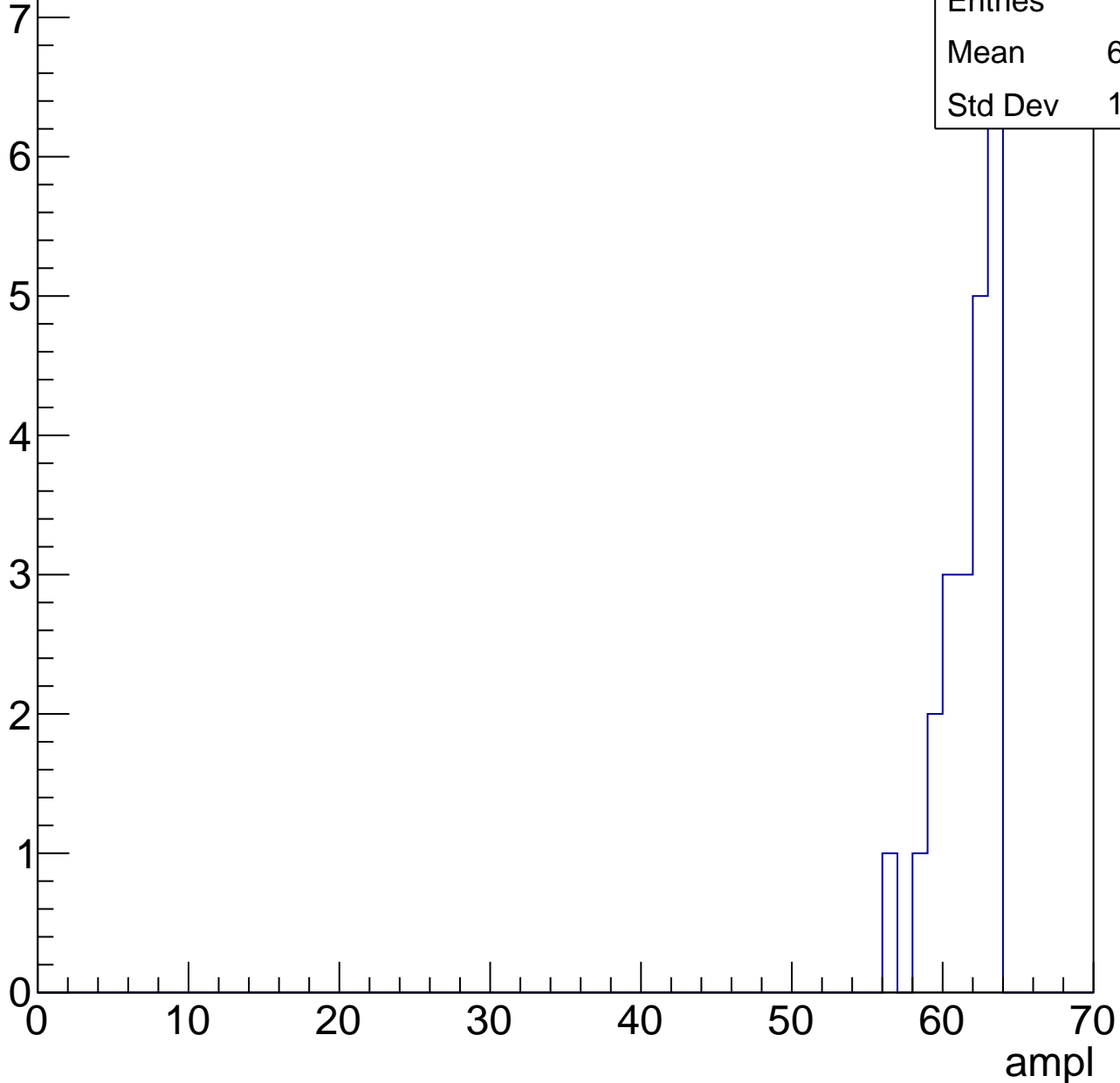


# B1L002S, U8-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	61.18
Std Dev	1.874

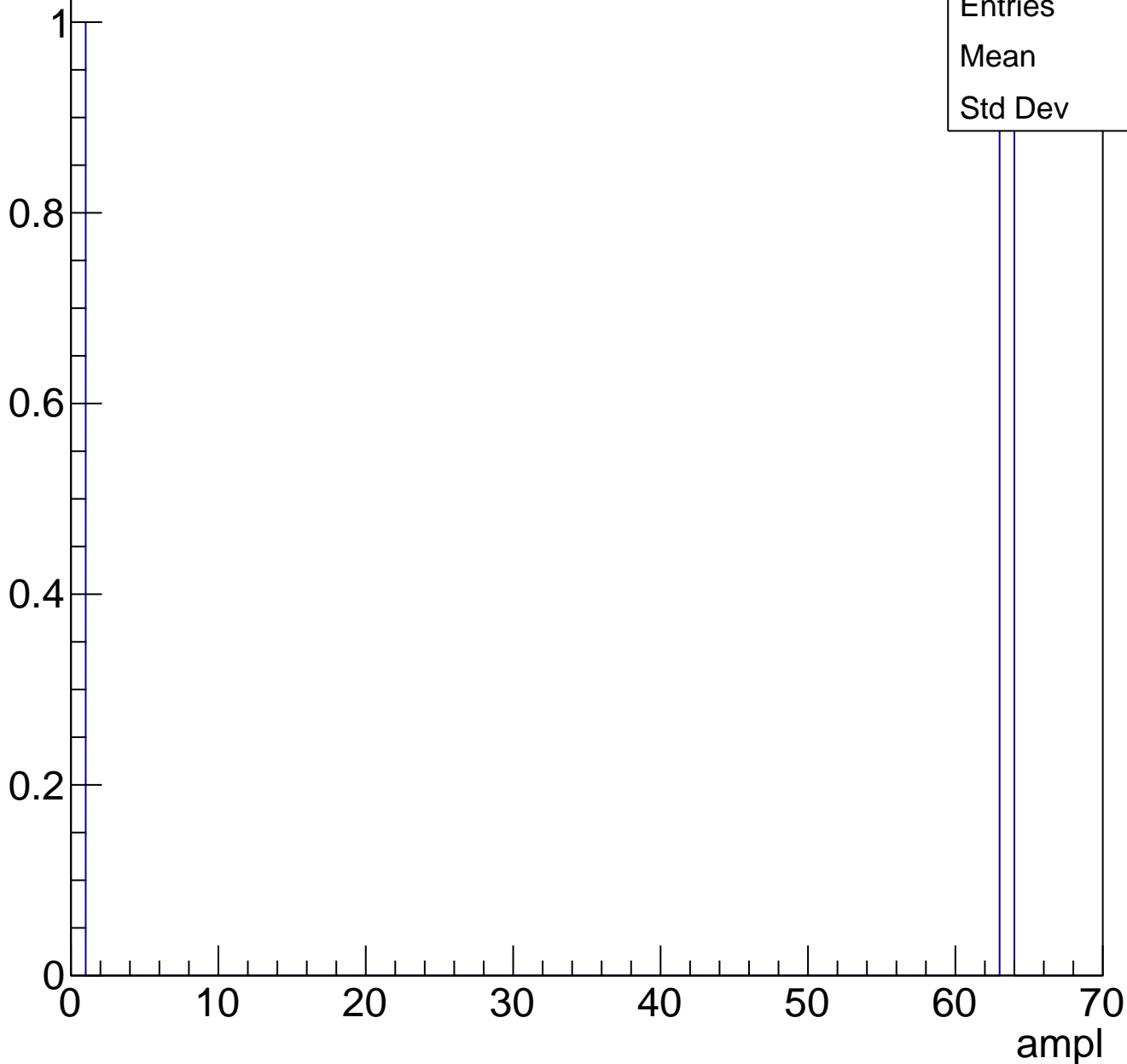




# B1L002S, U8-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch57, adc0

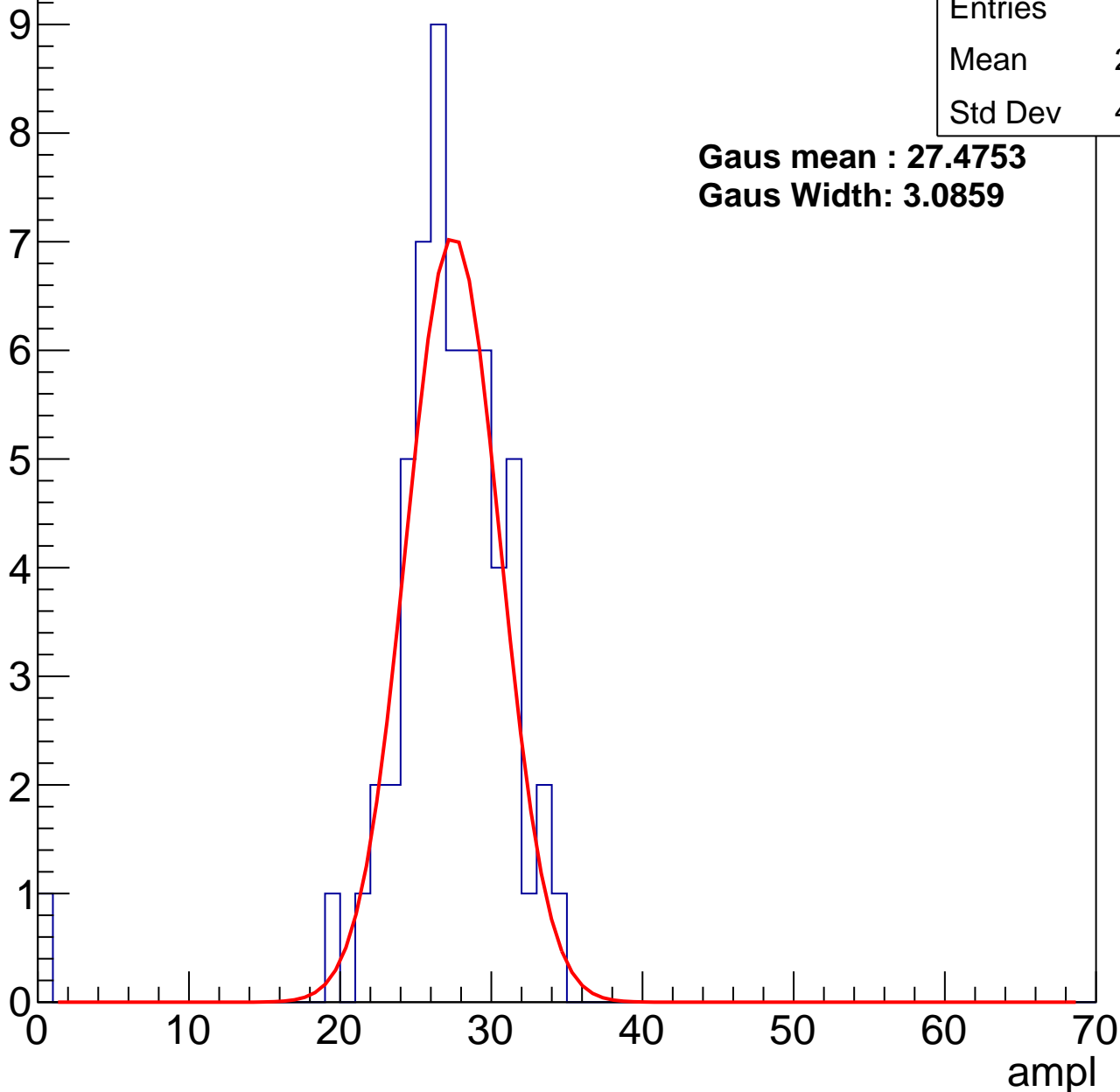
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	26.61
Std Dev	4.651

**Gaus mean : 27.4753**

**Gaus Width: 3.0859**



# B1L002S, U8-ch57, adc1

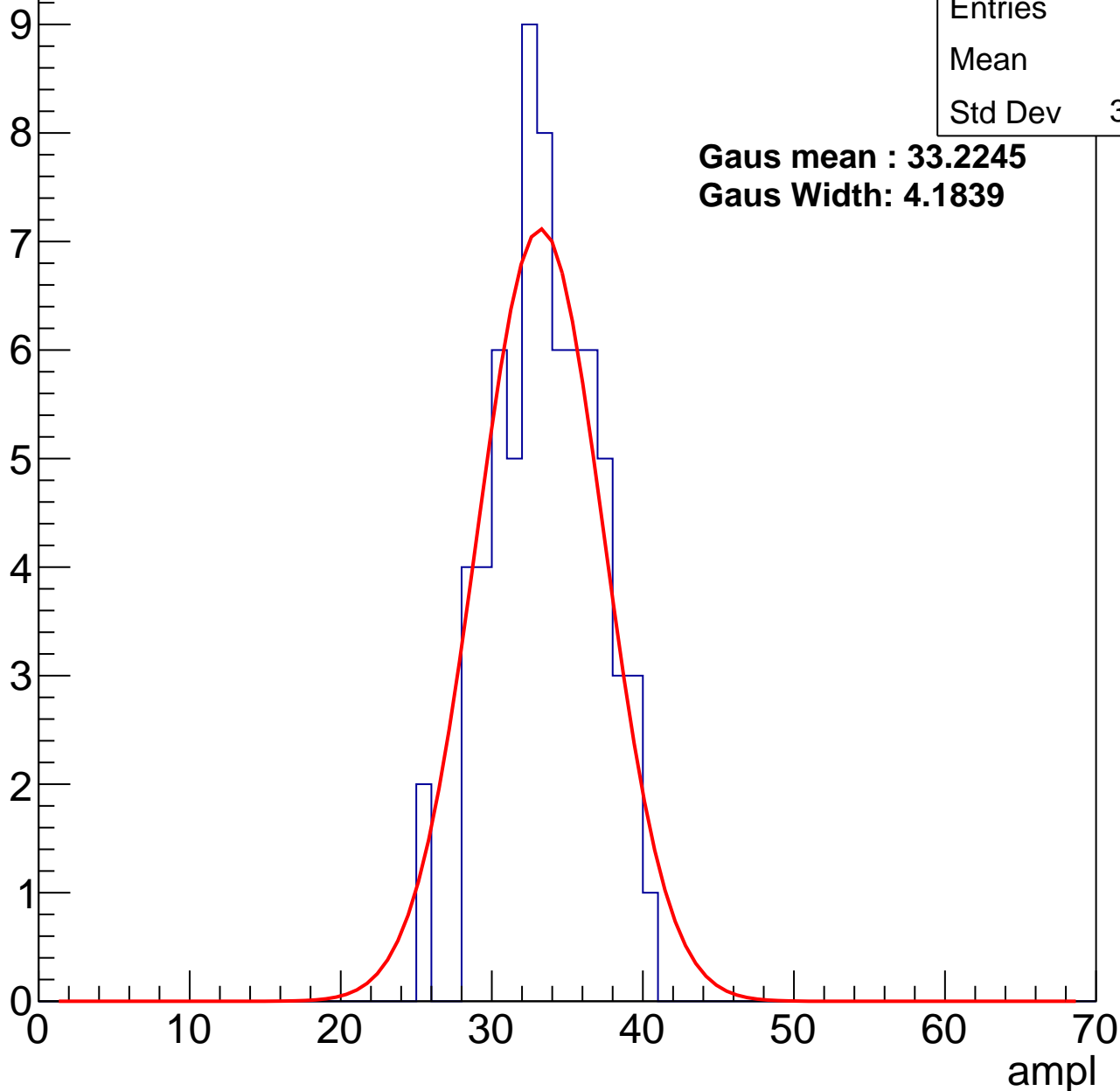
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.1
Std Dev	3.366

**Gaus mean : 33.2245**

**Gaus Width: 4.1839**



# B1L002S, U8-ch57, adc2

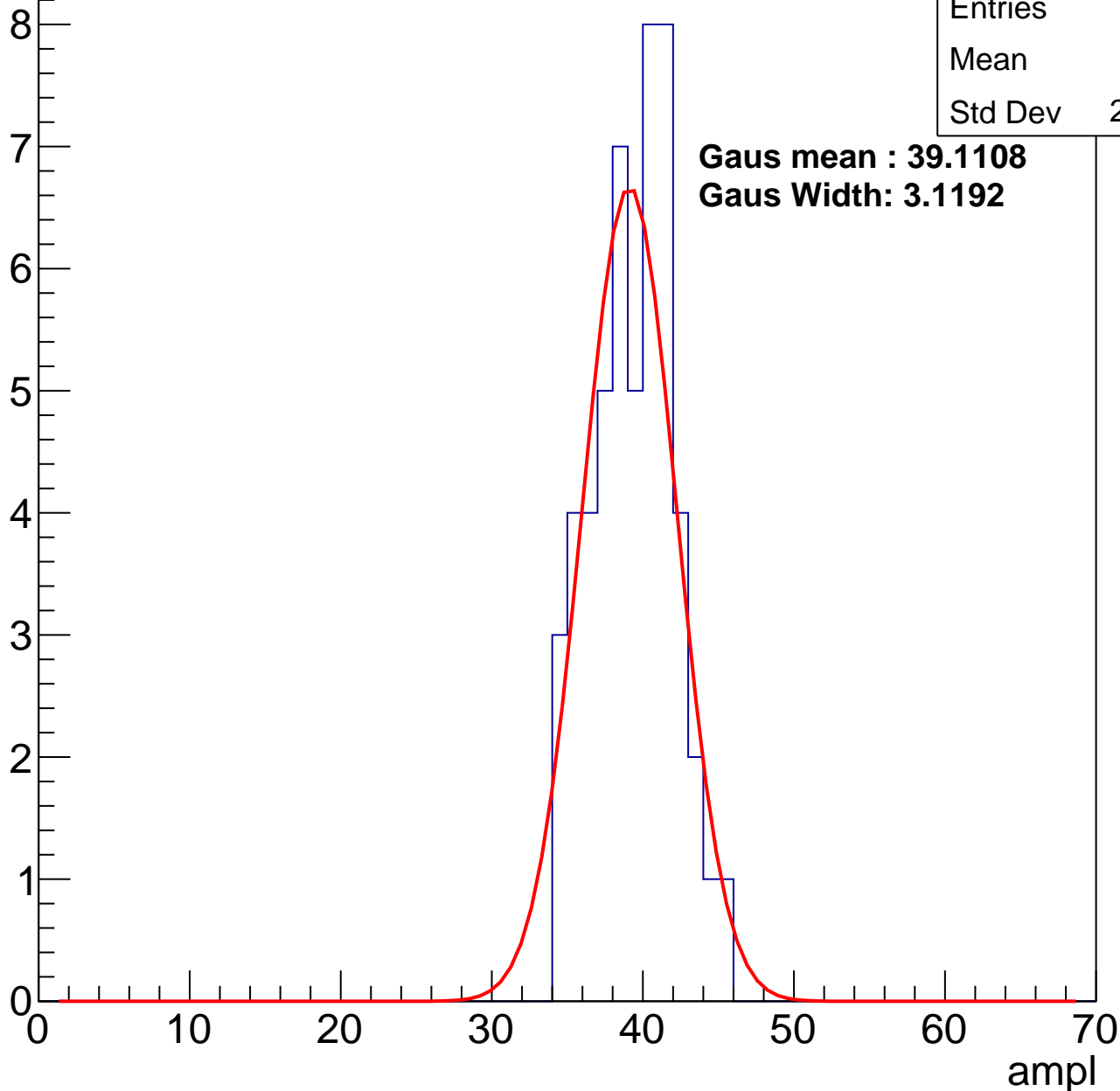
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	38.9
Std Dev	2.669

**Gaus mean : 39.1108**

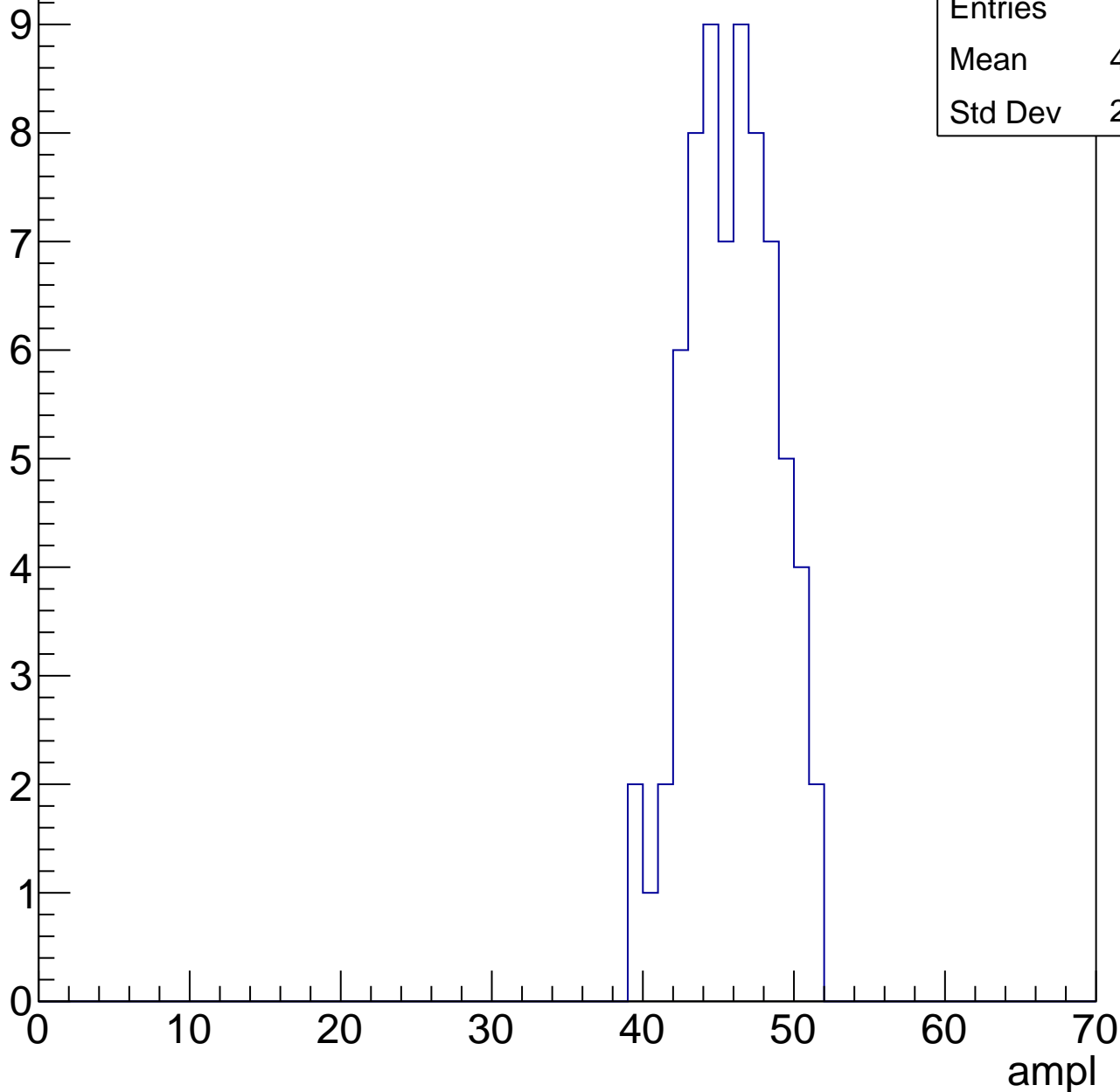
**Gaus Width: 3.1192**



# B1L002S, U8-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

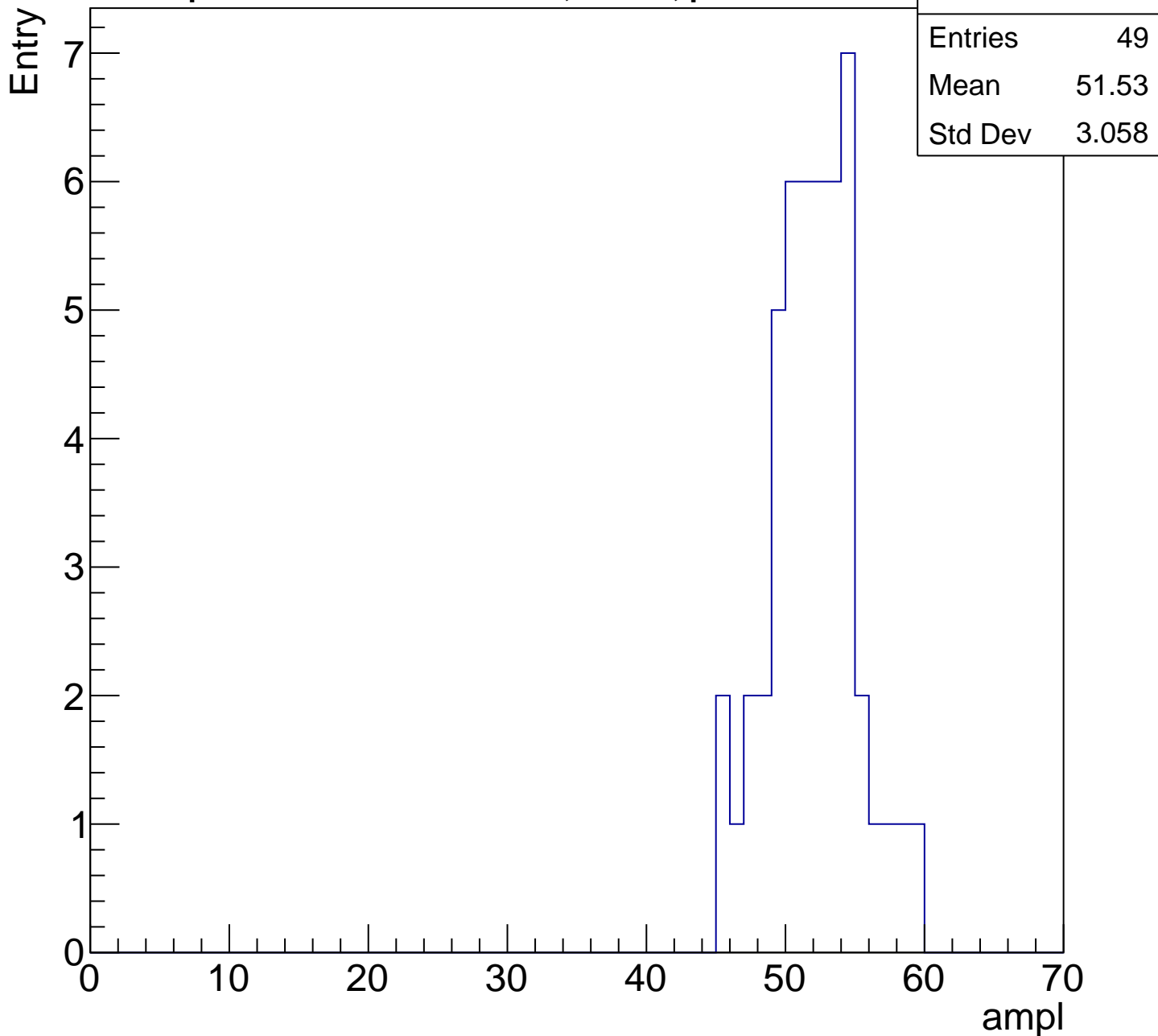
Entry



Entries	70
Mean	45.43
Std Dev	2.846

# B1L002S, U8-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch57, adc5

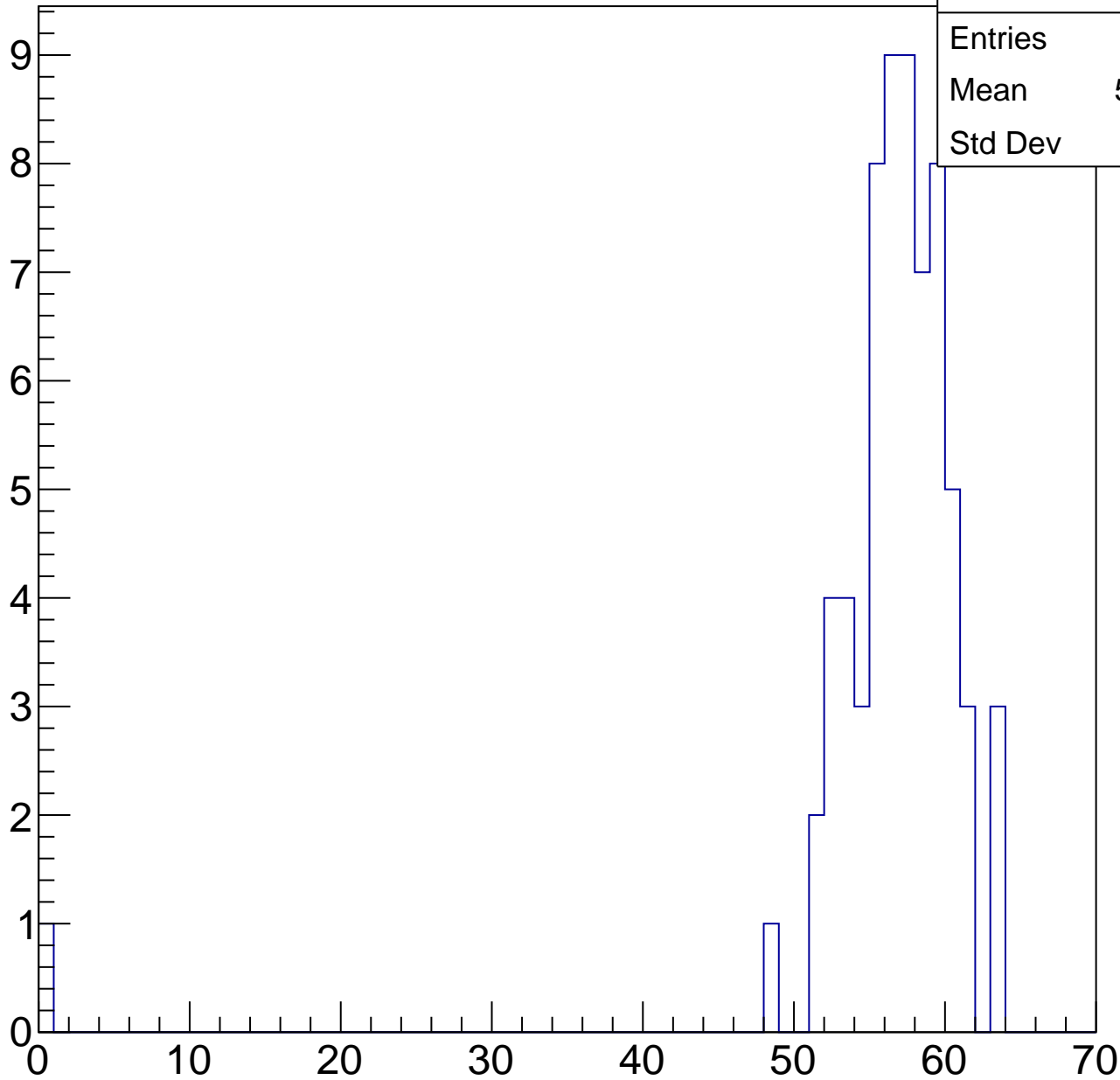
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	67
Mean	55.81
Std Dev	7.51

ampl



# B1L002S, U8-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	31
Mean	61.29
Std Dev	1.372

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10



# B1L002S, U8-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch58, adc0

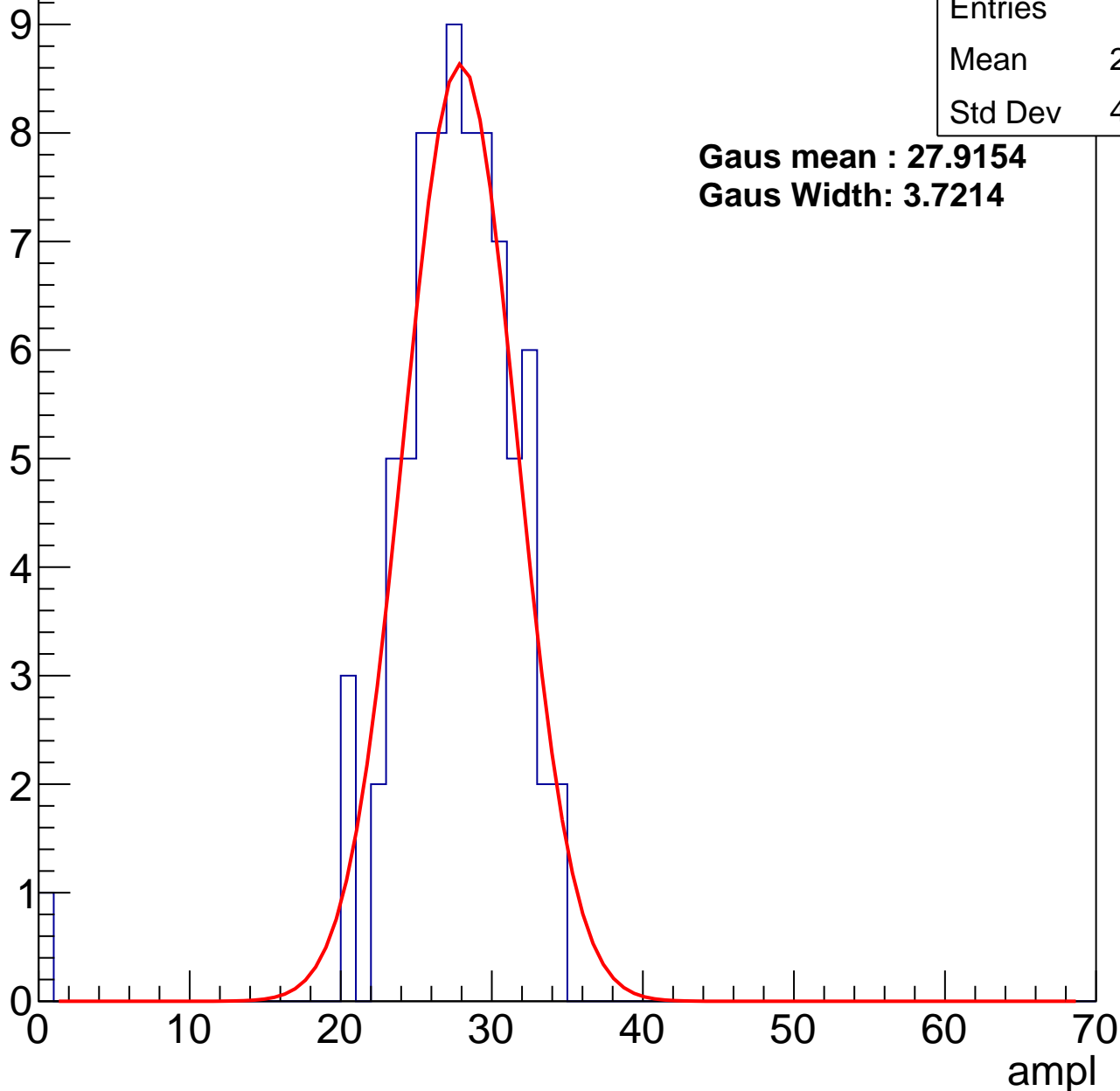
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	27.05
Std Dev	4.492

**Gaus mean : 27.9154**

**Gaus Width: 3.7214**



# B1L002S, U8-ch58, adc1

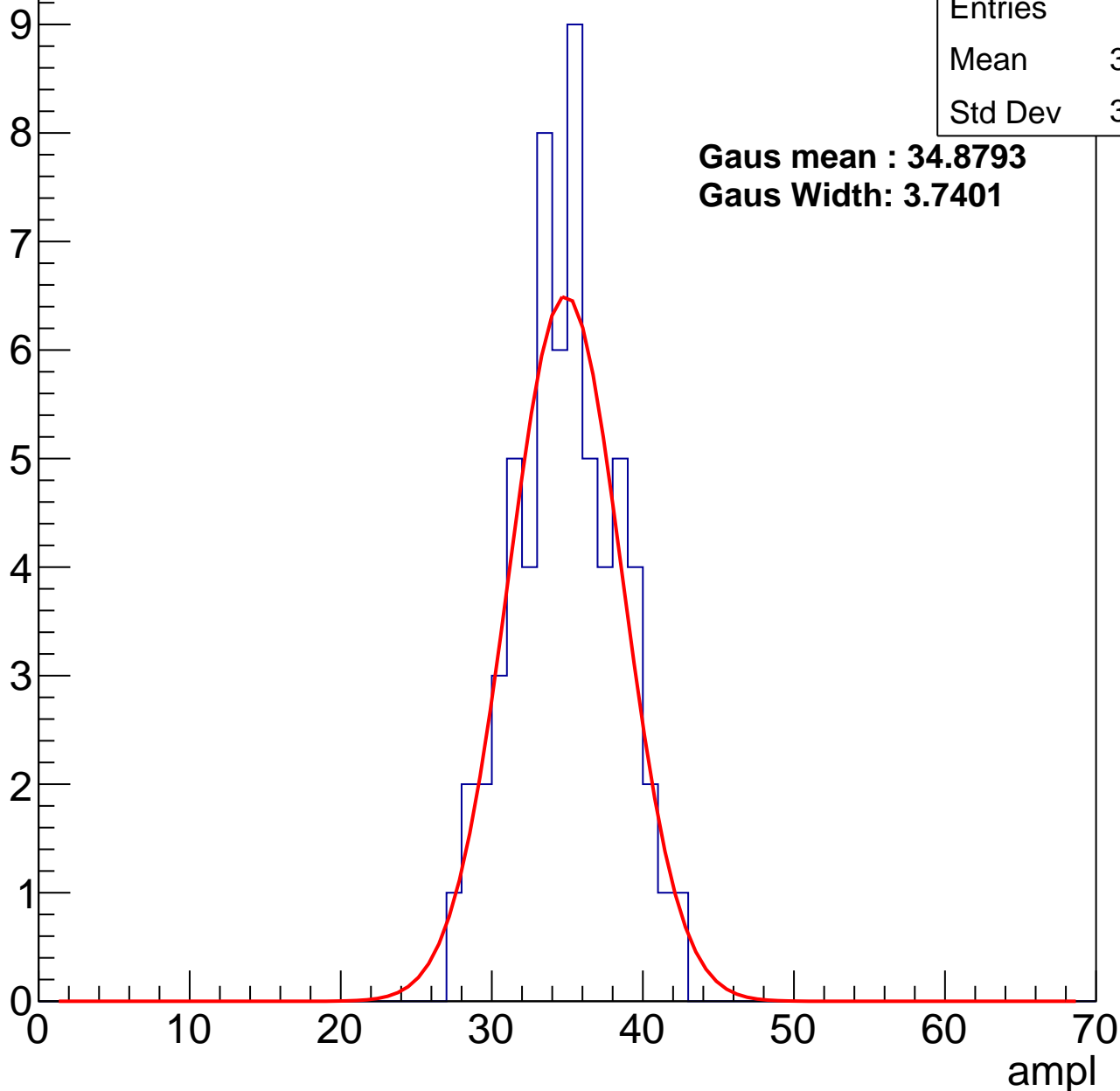
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.42
Std Dev	3.377

**Gaus mean : 34.8793**

**Gaus Width: 3.7401**



# B1L002S, U8-ch58, adc2

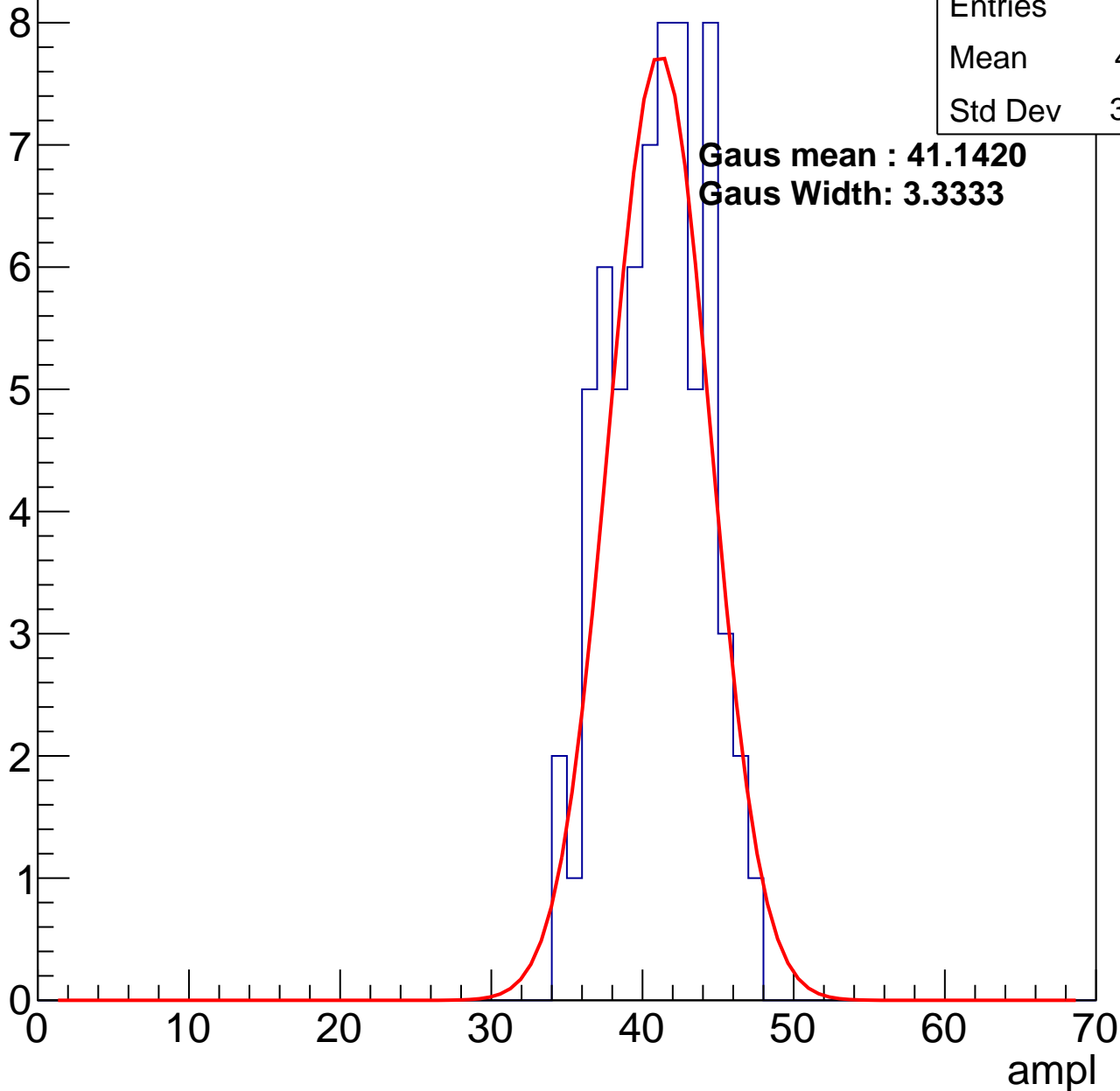
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.51
Std Dev	3.112

**Gaus mean : 41.1420**

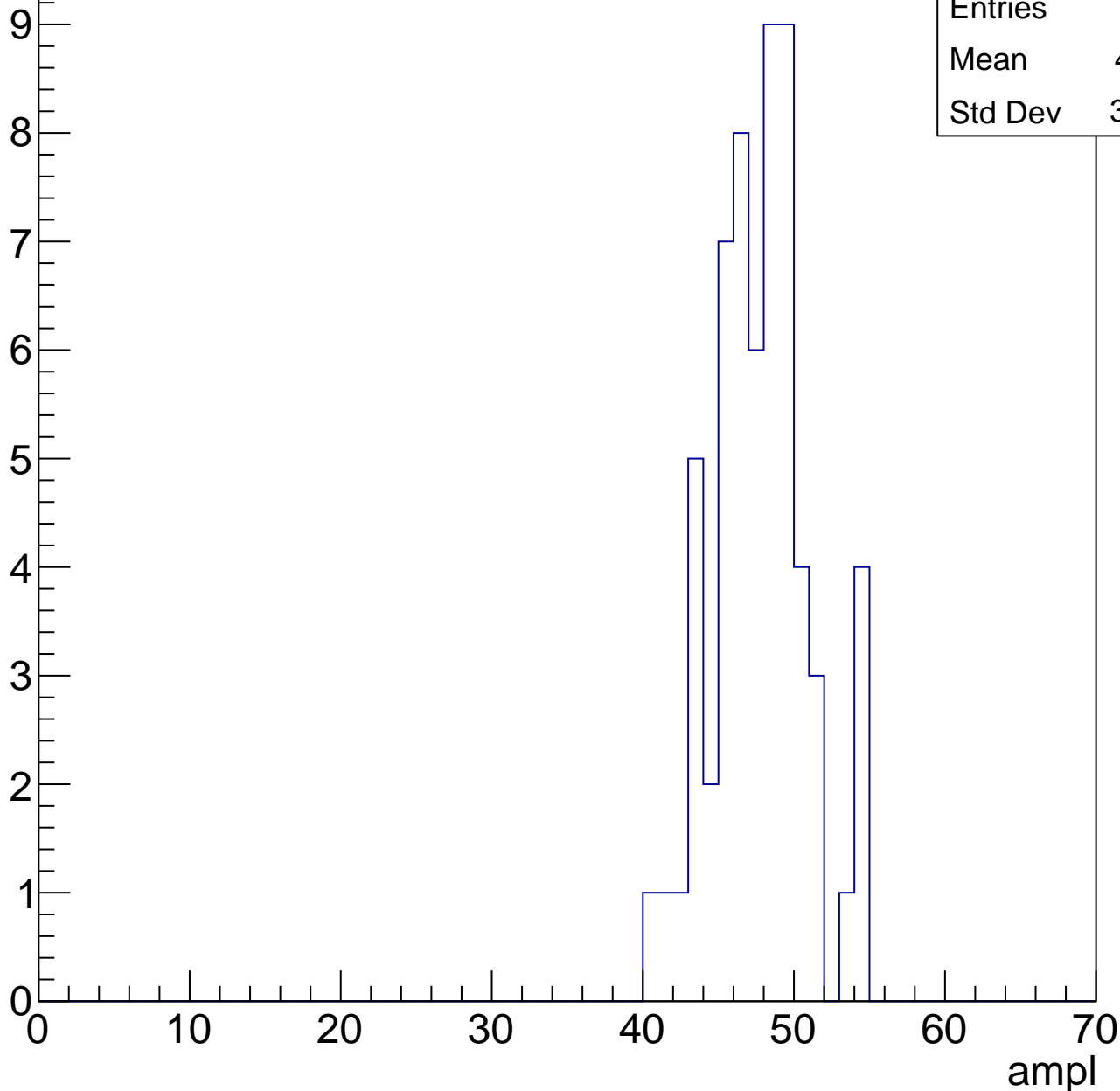
**Gaus Width: 3.3333**



# B1L002S, U8-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

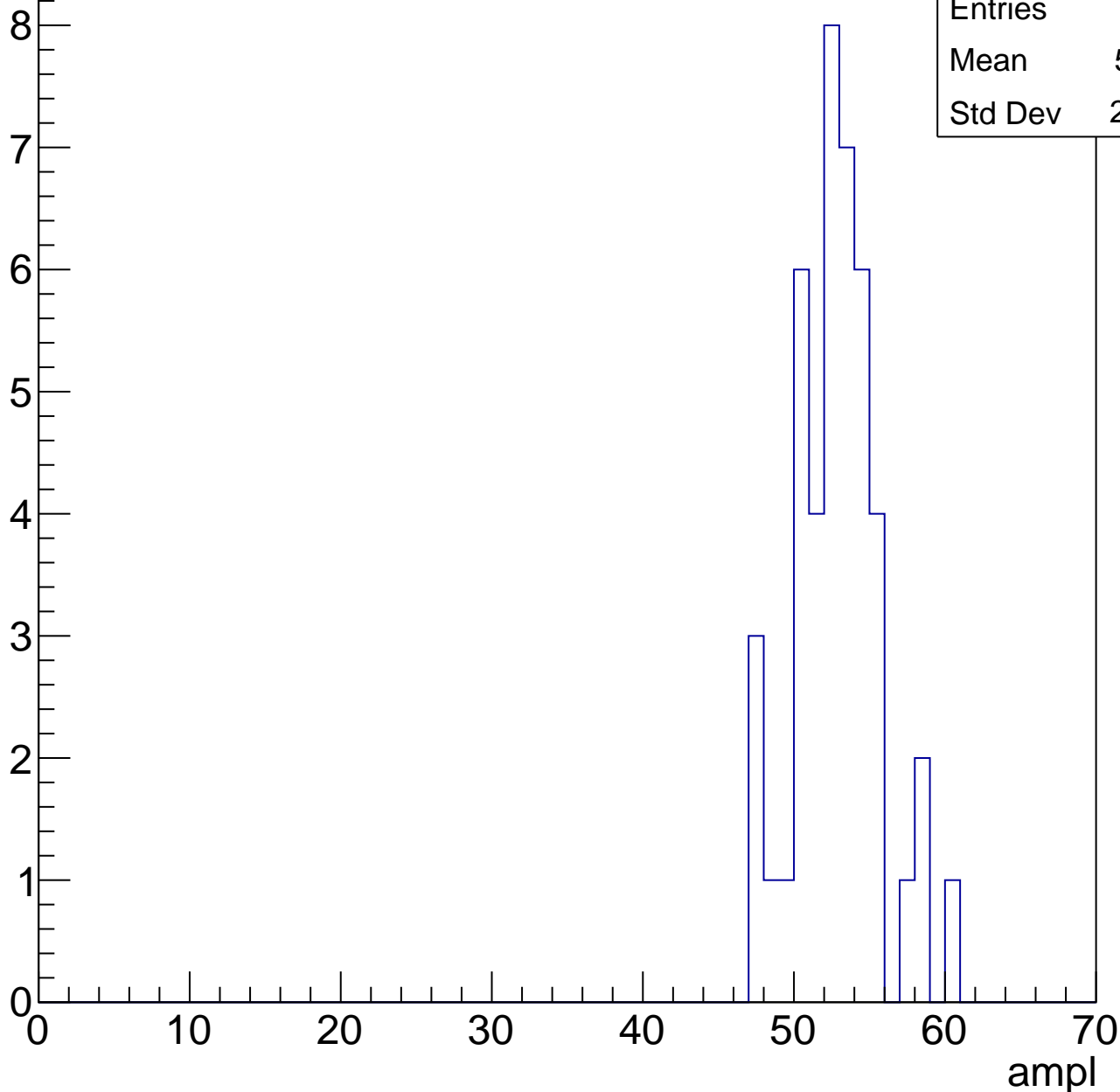


# B1L002S, U8-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	52.41
Std Dev	2.815



# B1L002S, U8-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	56.93
Std Dev	7.43

Entry

10

8

6

4

2

0

0

10

20

30

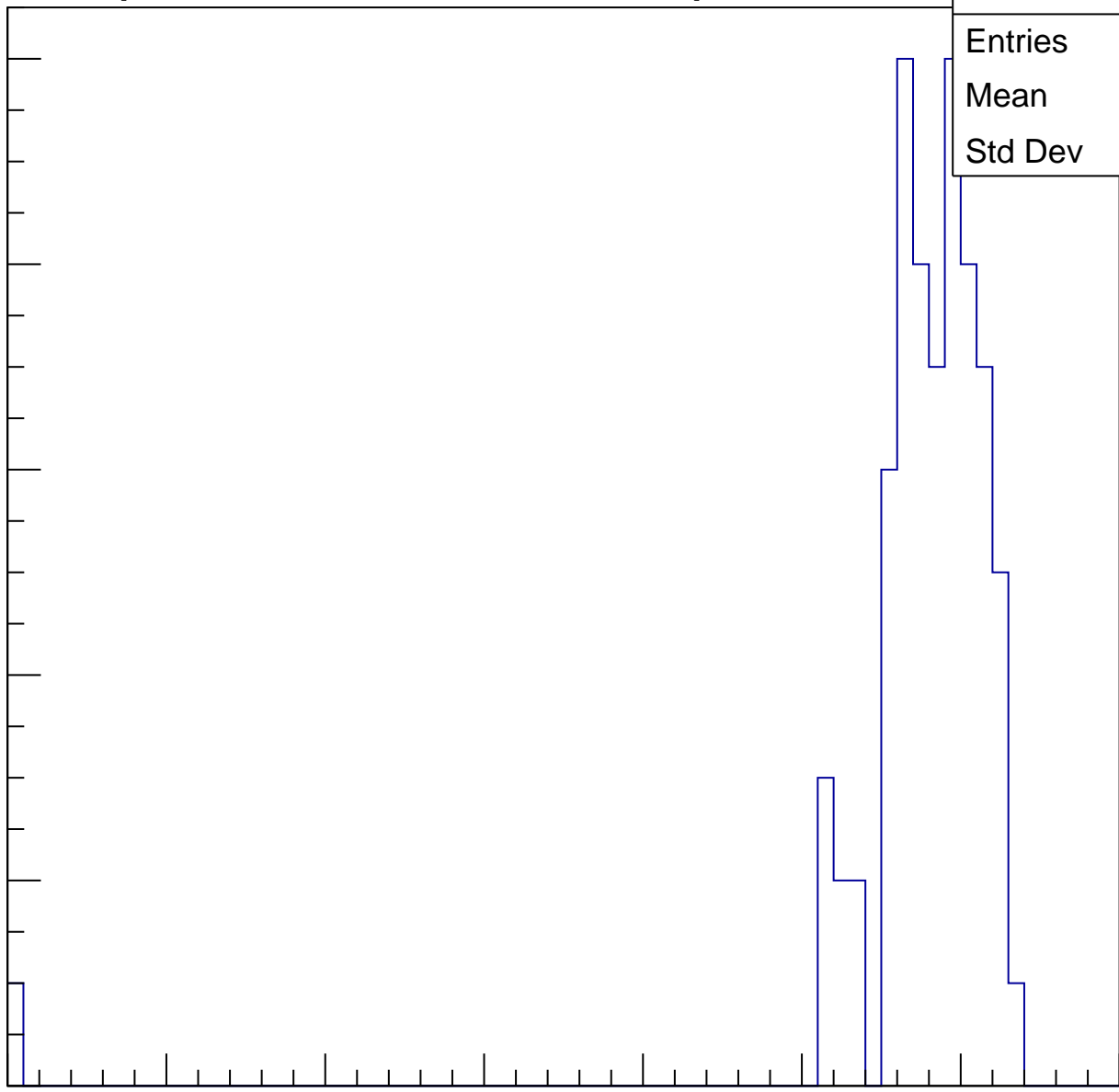
40

50

60

70

ampl

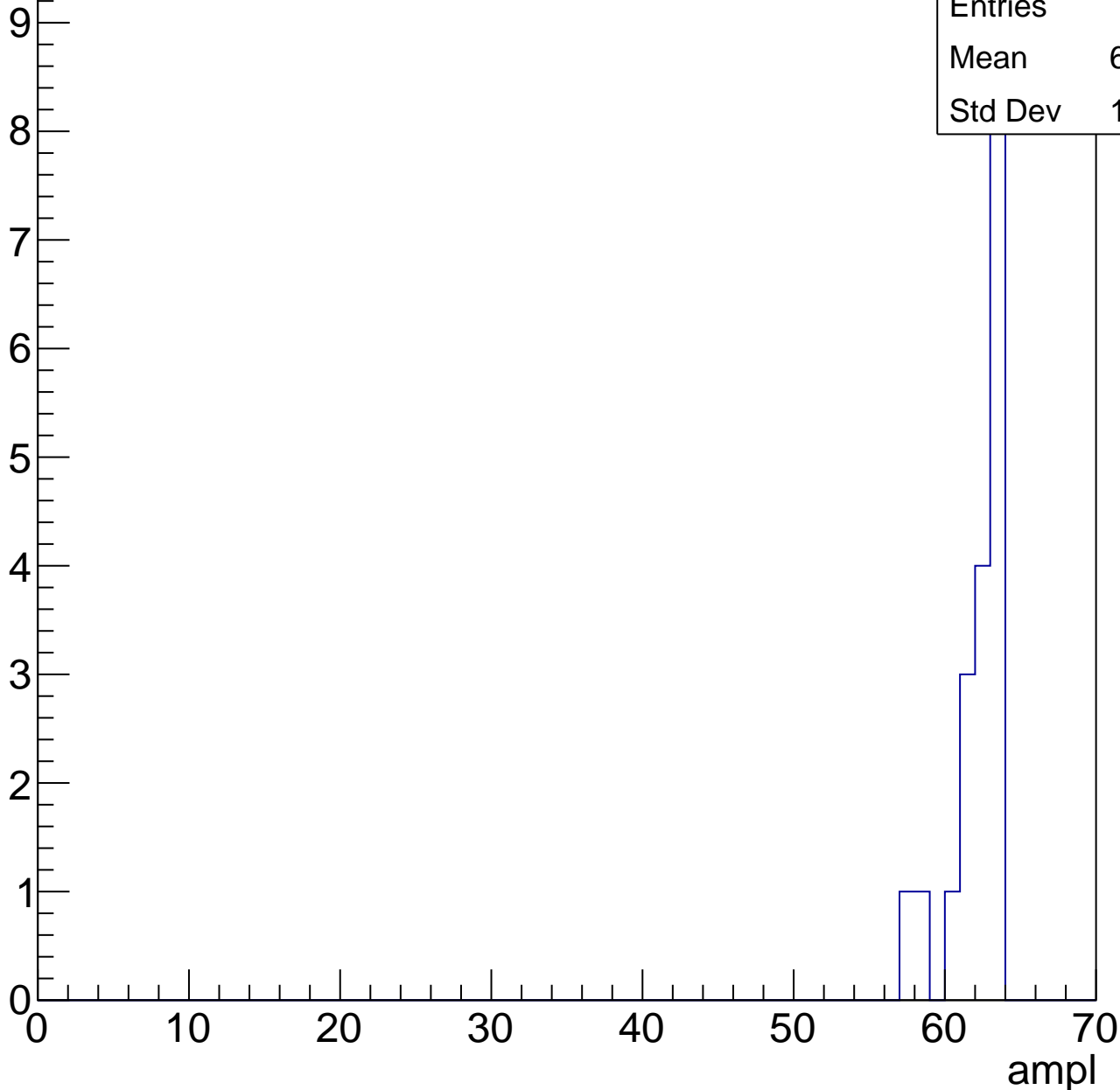


# B1L002S, U8-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.74
Std Dev	1.712

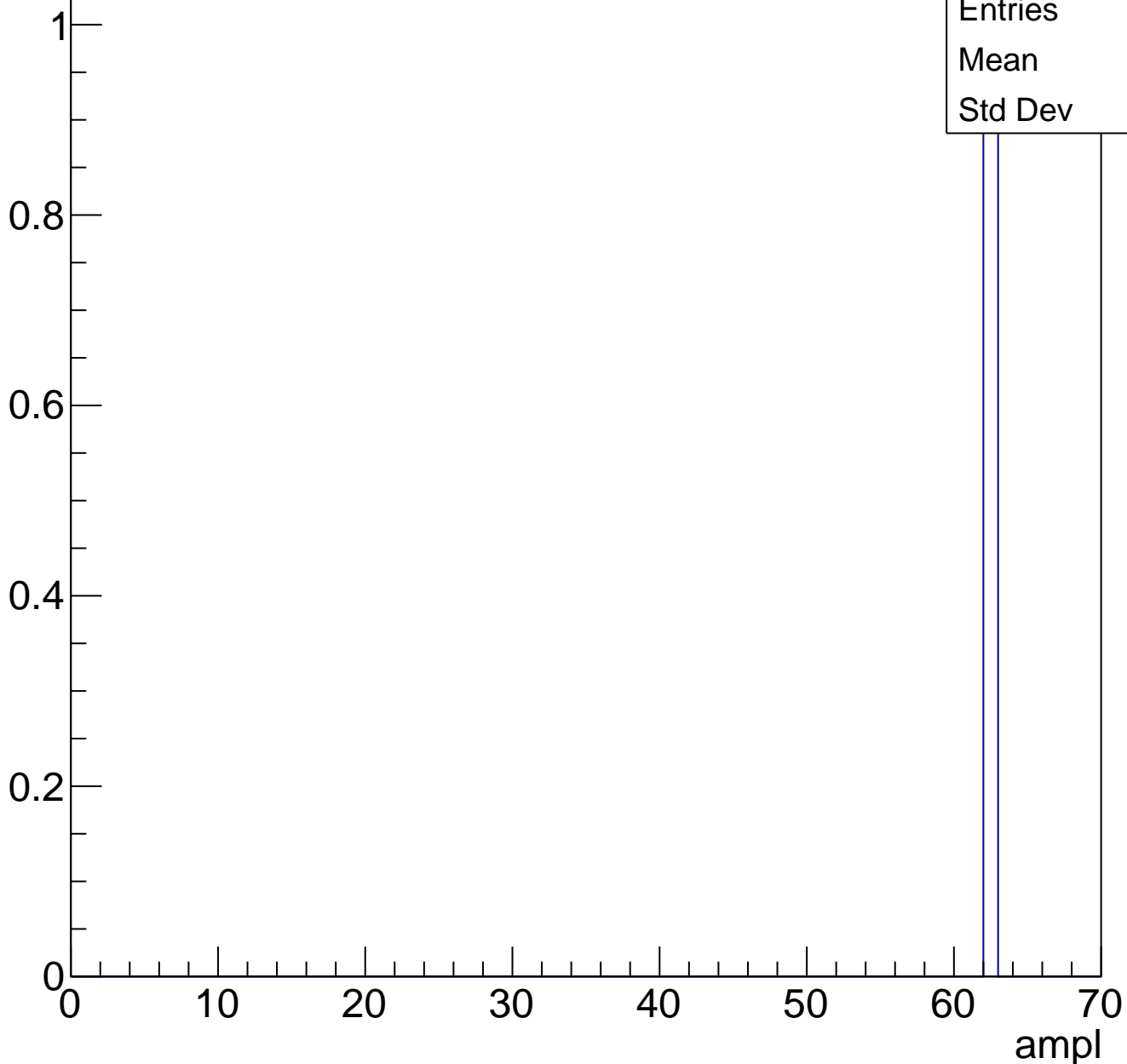




# B1L002S, U8-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch59, adc0

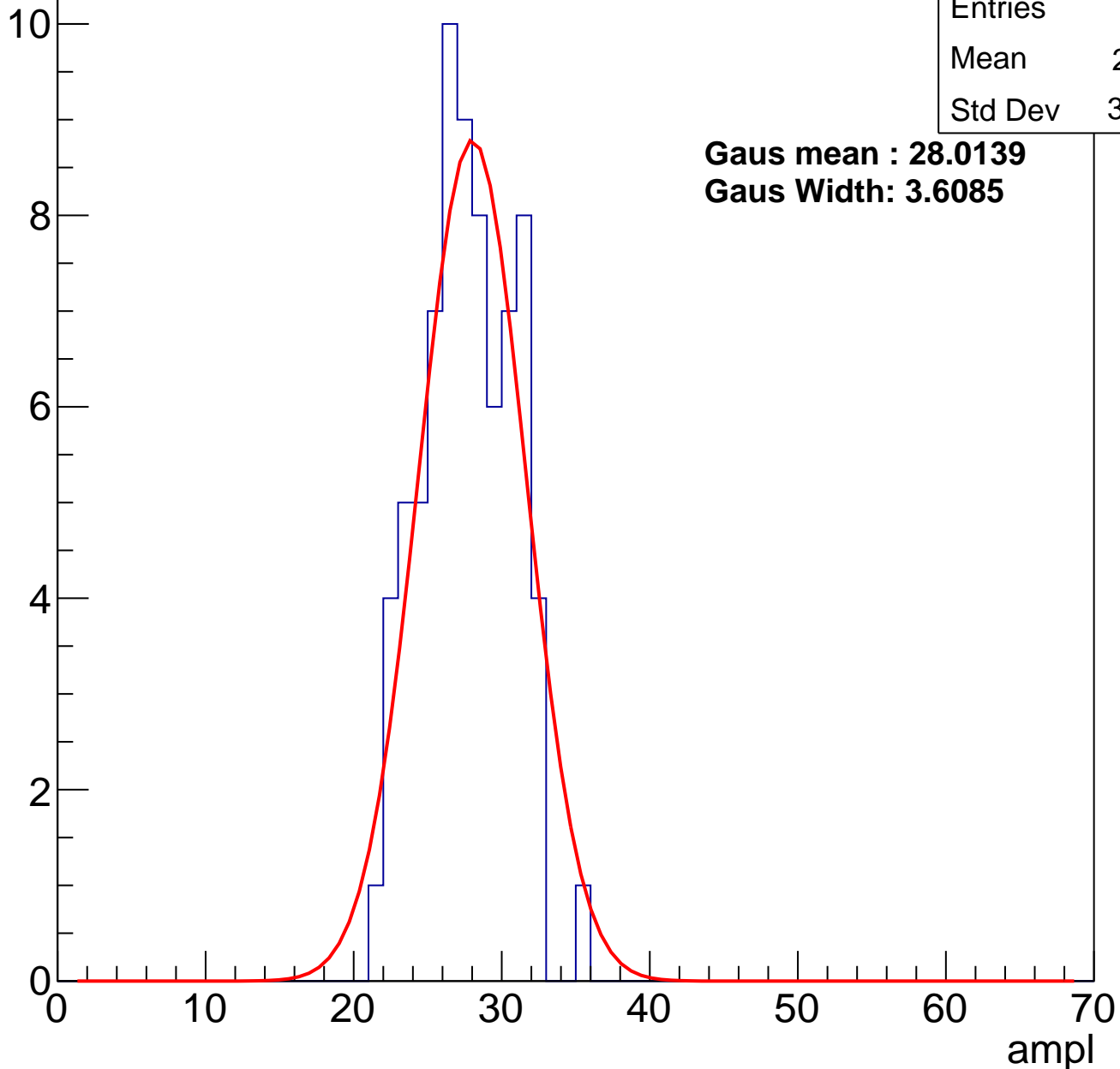
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	27.21
Std Dev	3.017

**Gaus mean : 28.0139**

**Gaus Width: 3.6085**

Entry



# B1L002S, U8-ch59, adc1

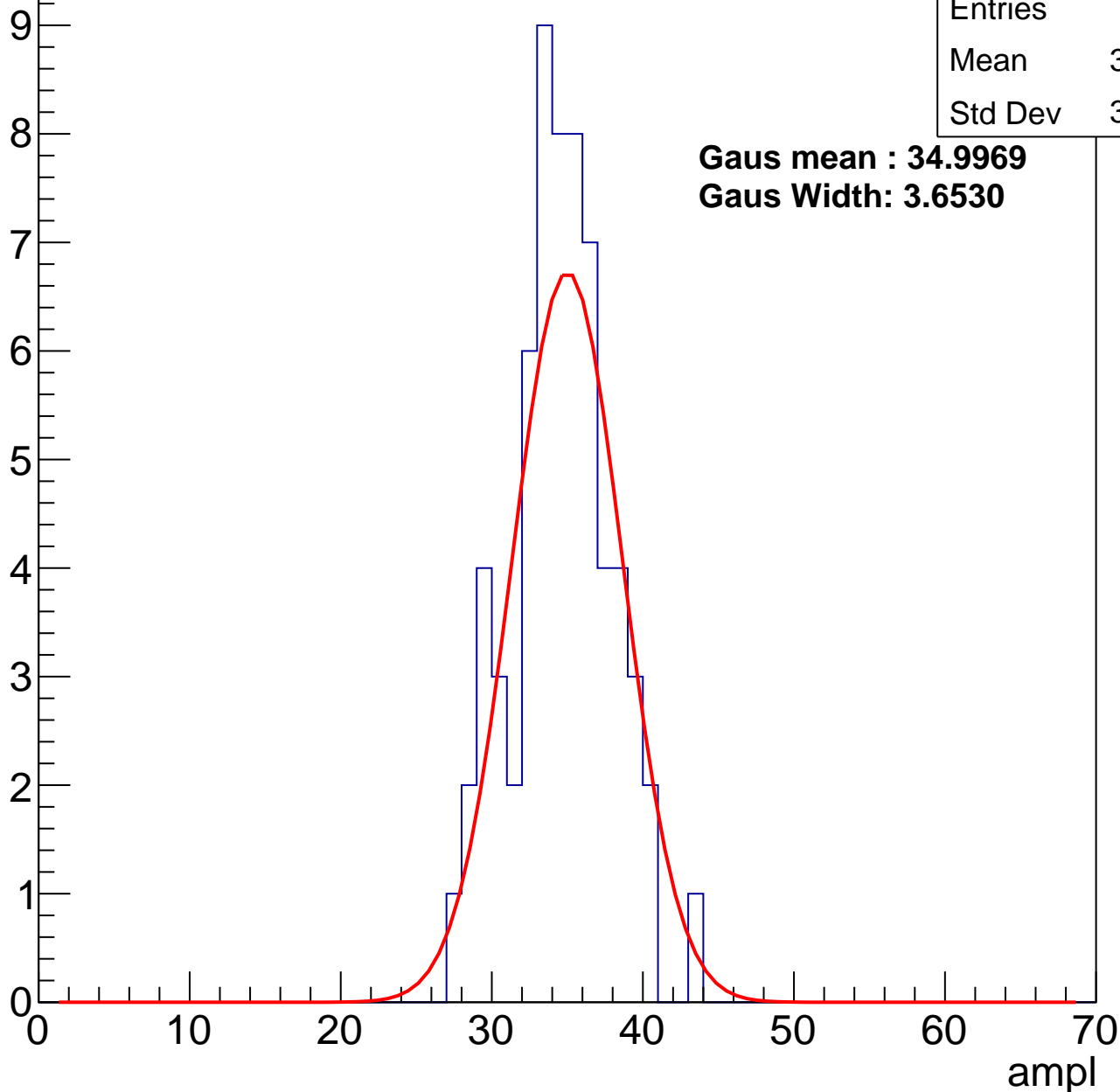
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.12
Std Dev	3.267

**Gaus mean : 34.9969**

**Gaus Width: 3.6530**



# B1L002S, U8-ch59, adc2

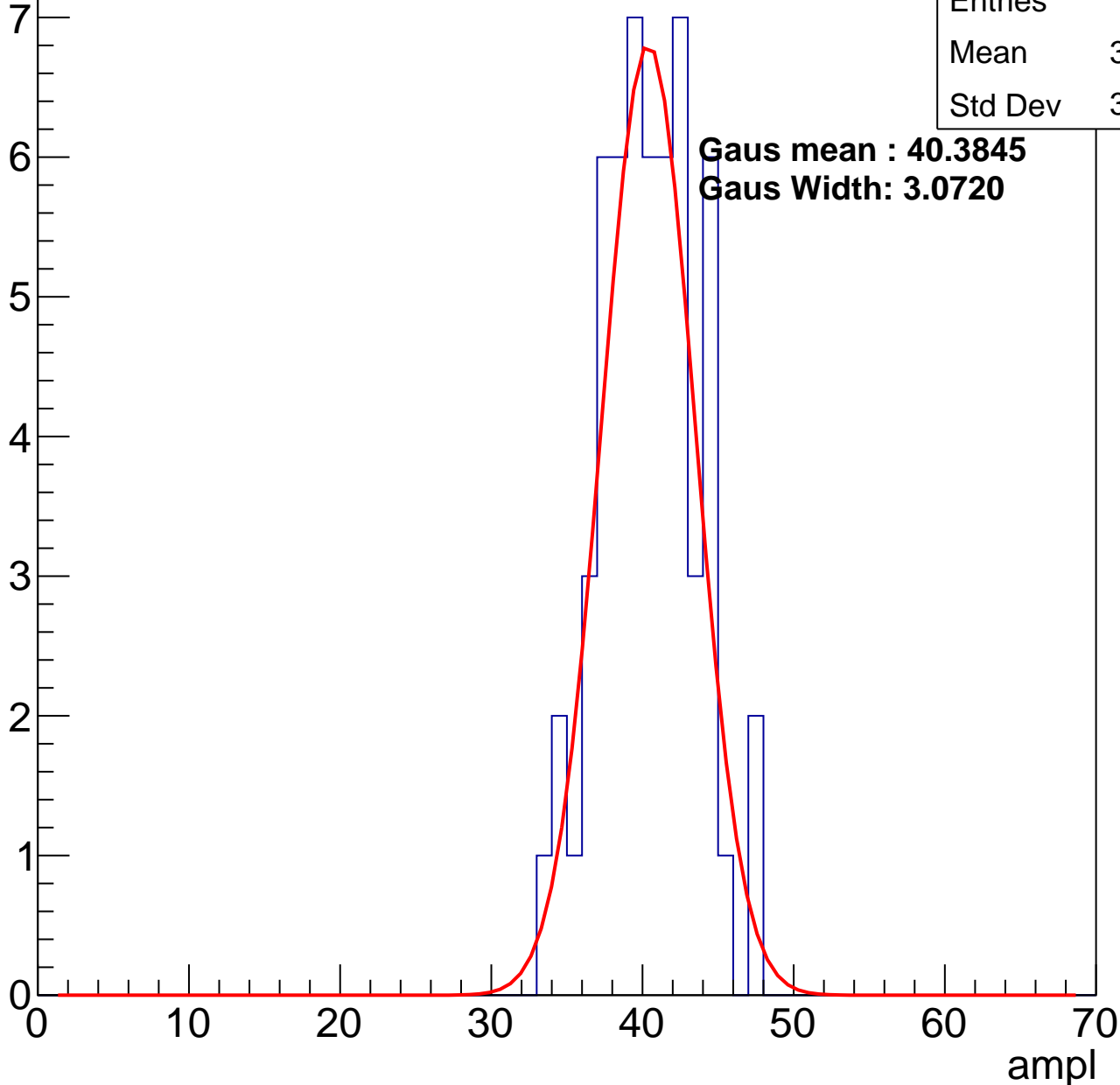
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.98
Std Dev	3.132

**Gaus mean : 40.3845**

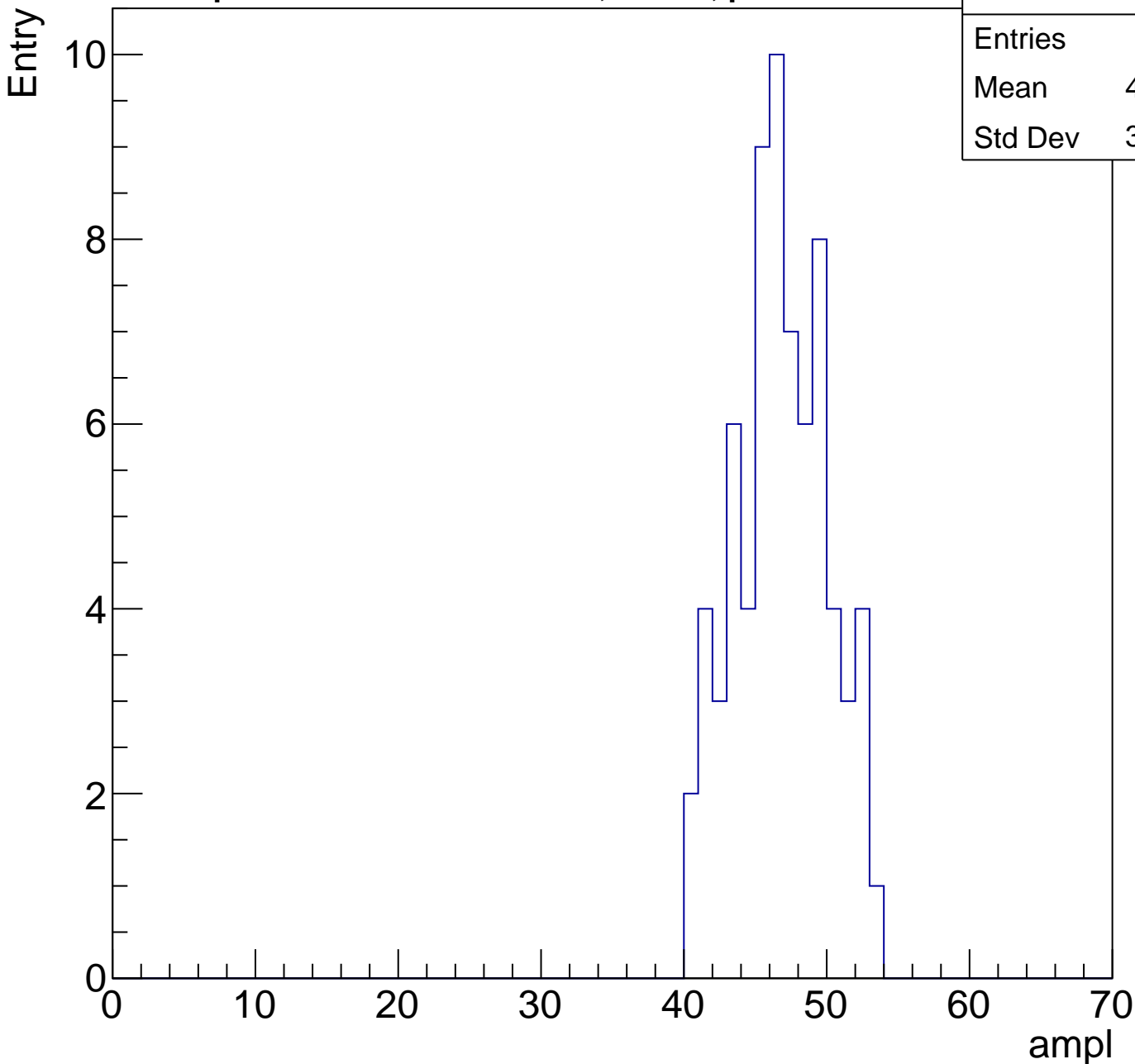
**Gaus Width: 3.0720**



# B1L002S, U8-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	46.37
Std Dev	3.194

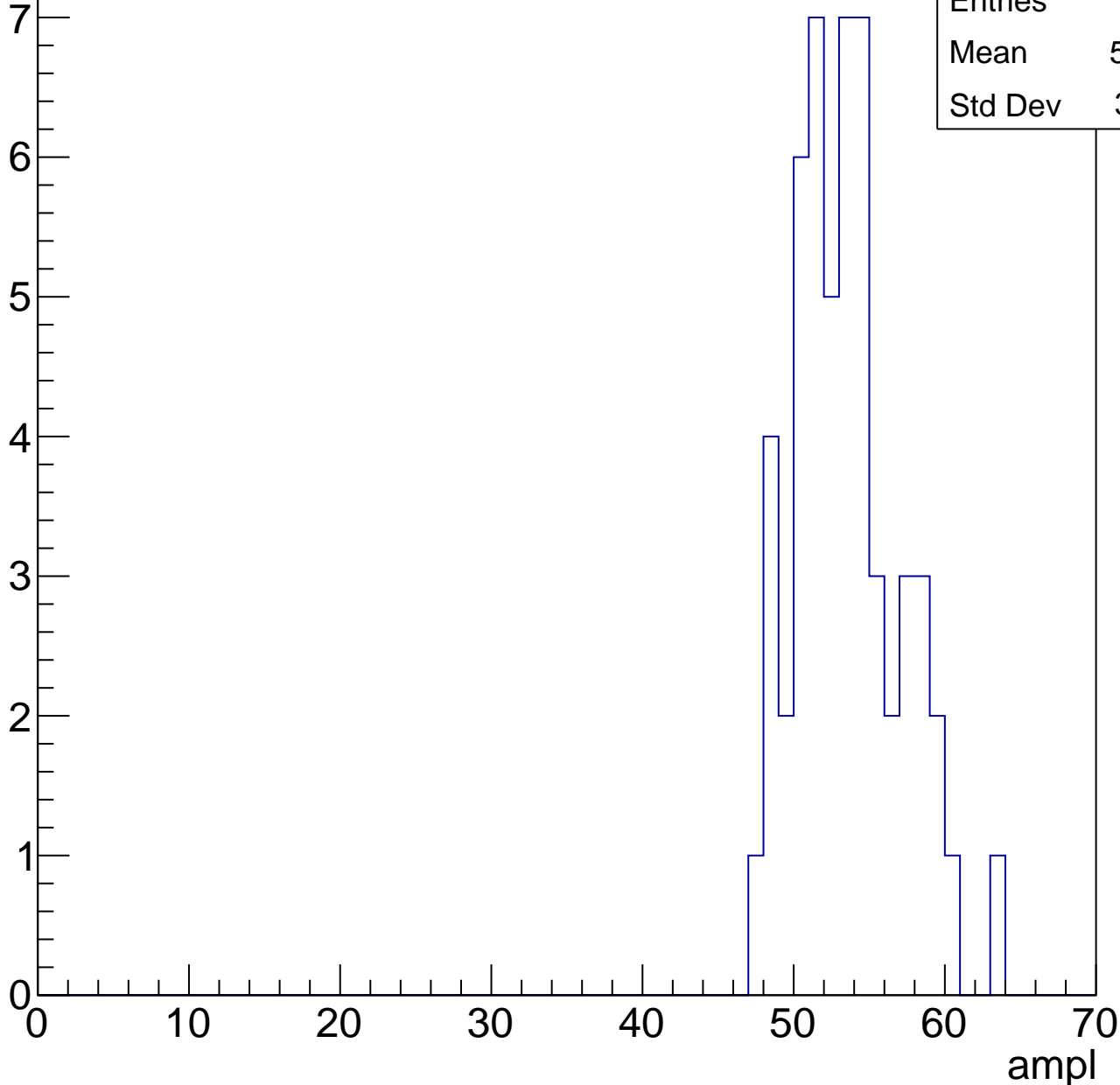


# B1L002S, U8-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	53.07
Std Dev	3.431

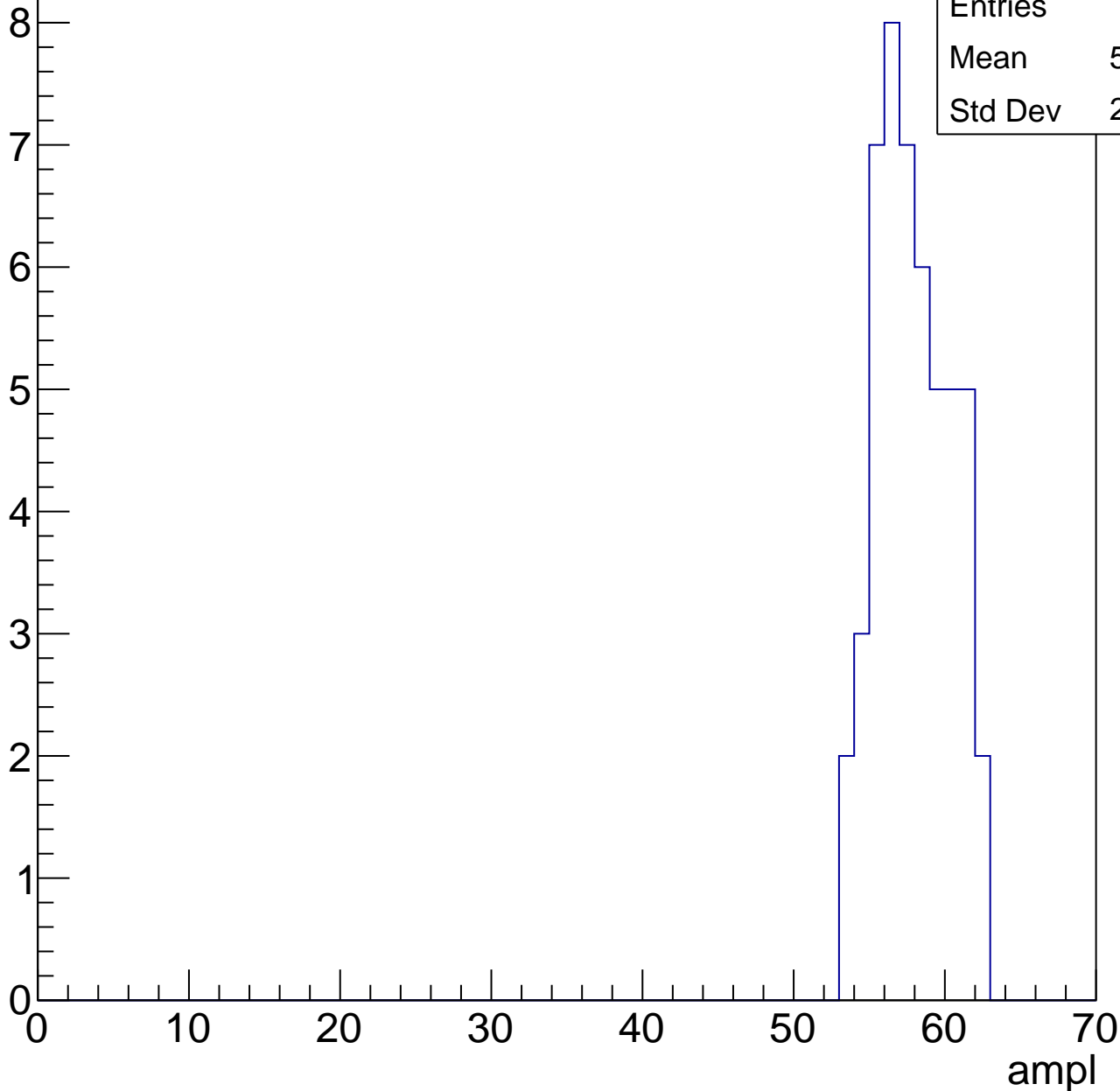


# B1L002S, U8-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	57.44
Std Dev	2.393

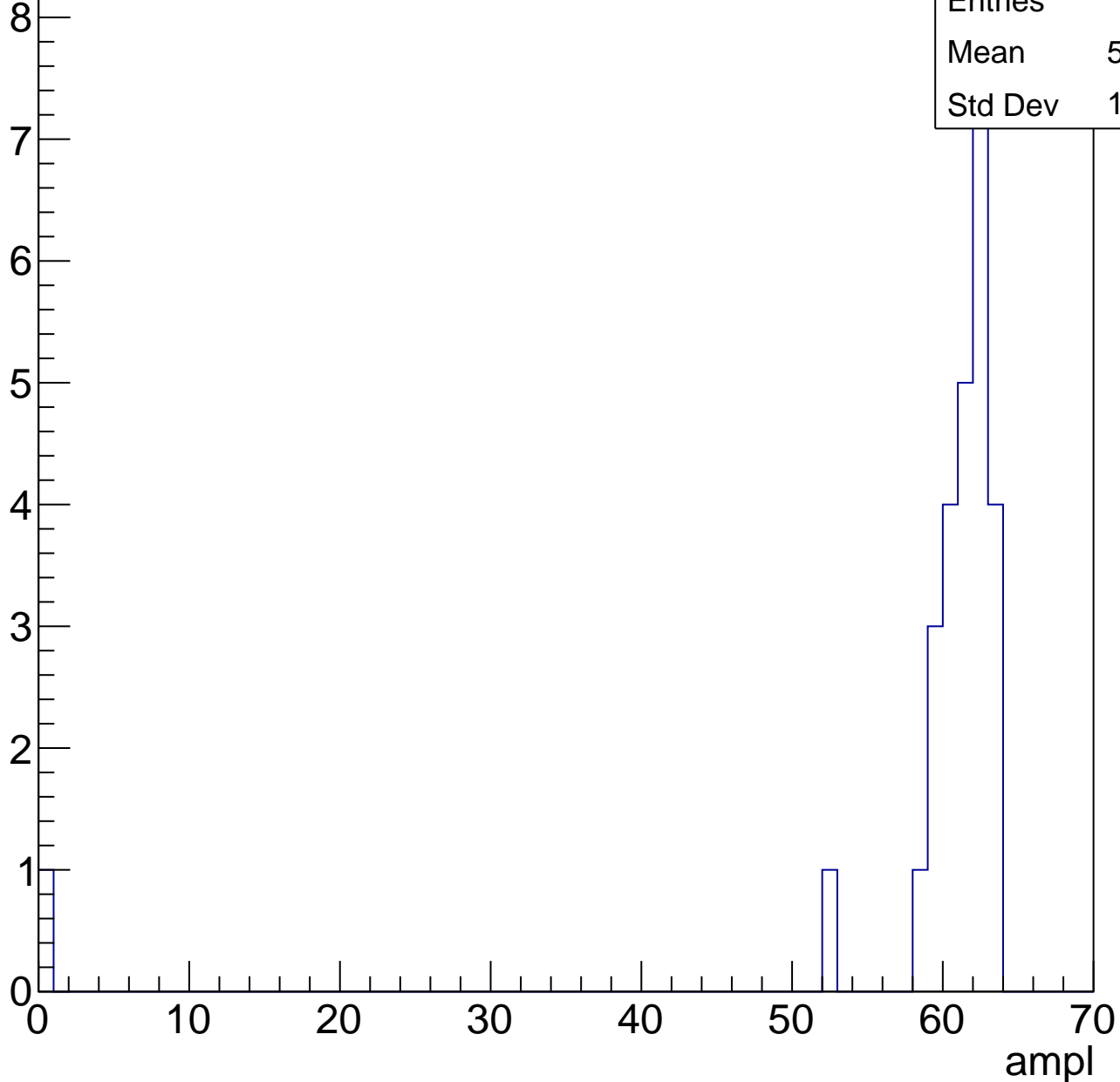


# B1L002S, U8-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.52
Std Dev	11.68



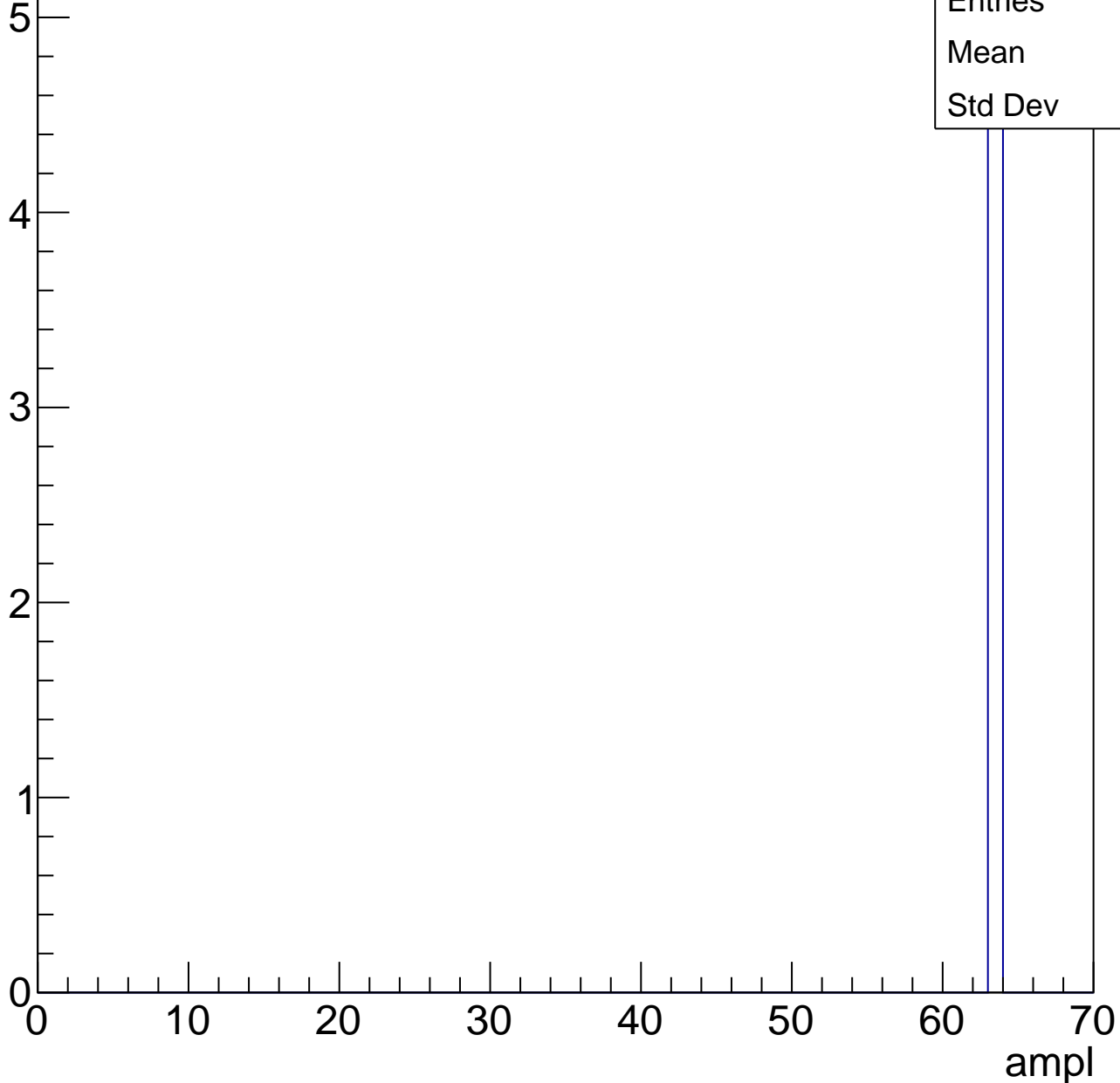


# B1L002S, U8-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	5
Mean	63
Std Dev	0



# B1L002S, U8-ch60, adc0

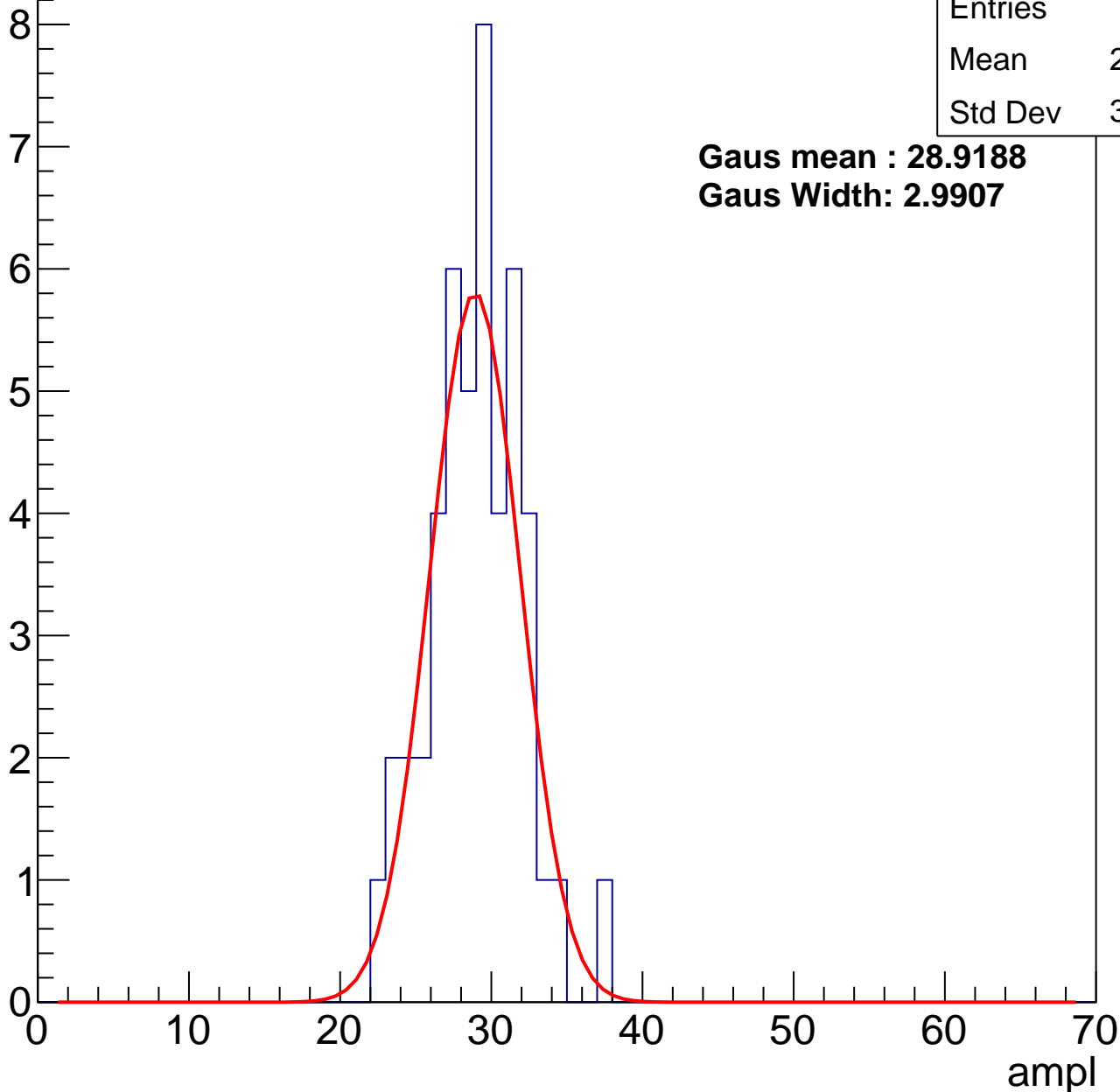
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	28.55
Std Dev	3.016

**Gaus mean : 28.9188**

**Gaus Width: 2.9907**



# B1L002S, U8-ch60, adc1

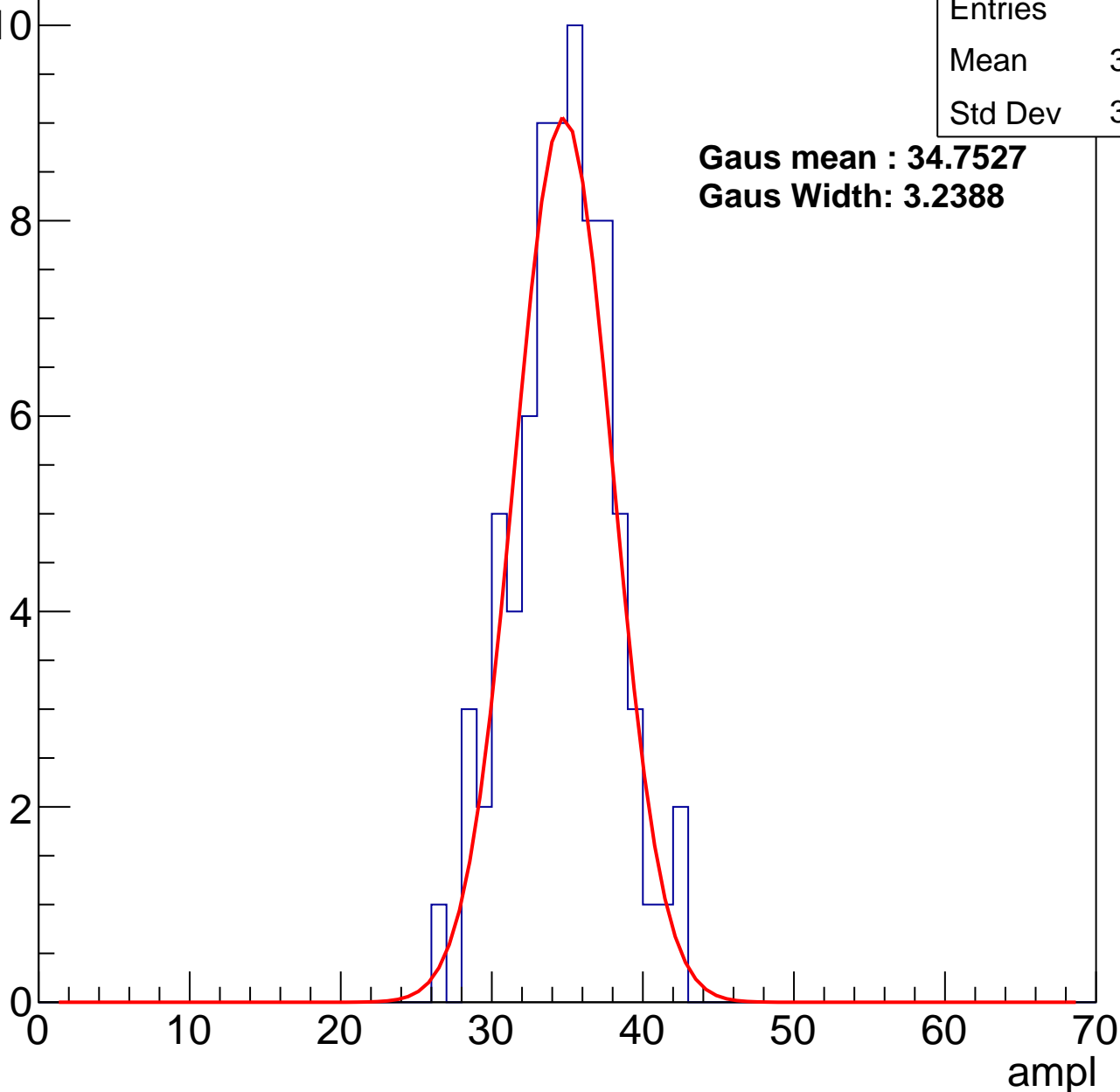
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	34.32
Std Dev	3.312

**Gaus mean : 34.7527**

**Gaus Width: 3.2388**



# B1L002S, U8-ch60, adc2

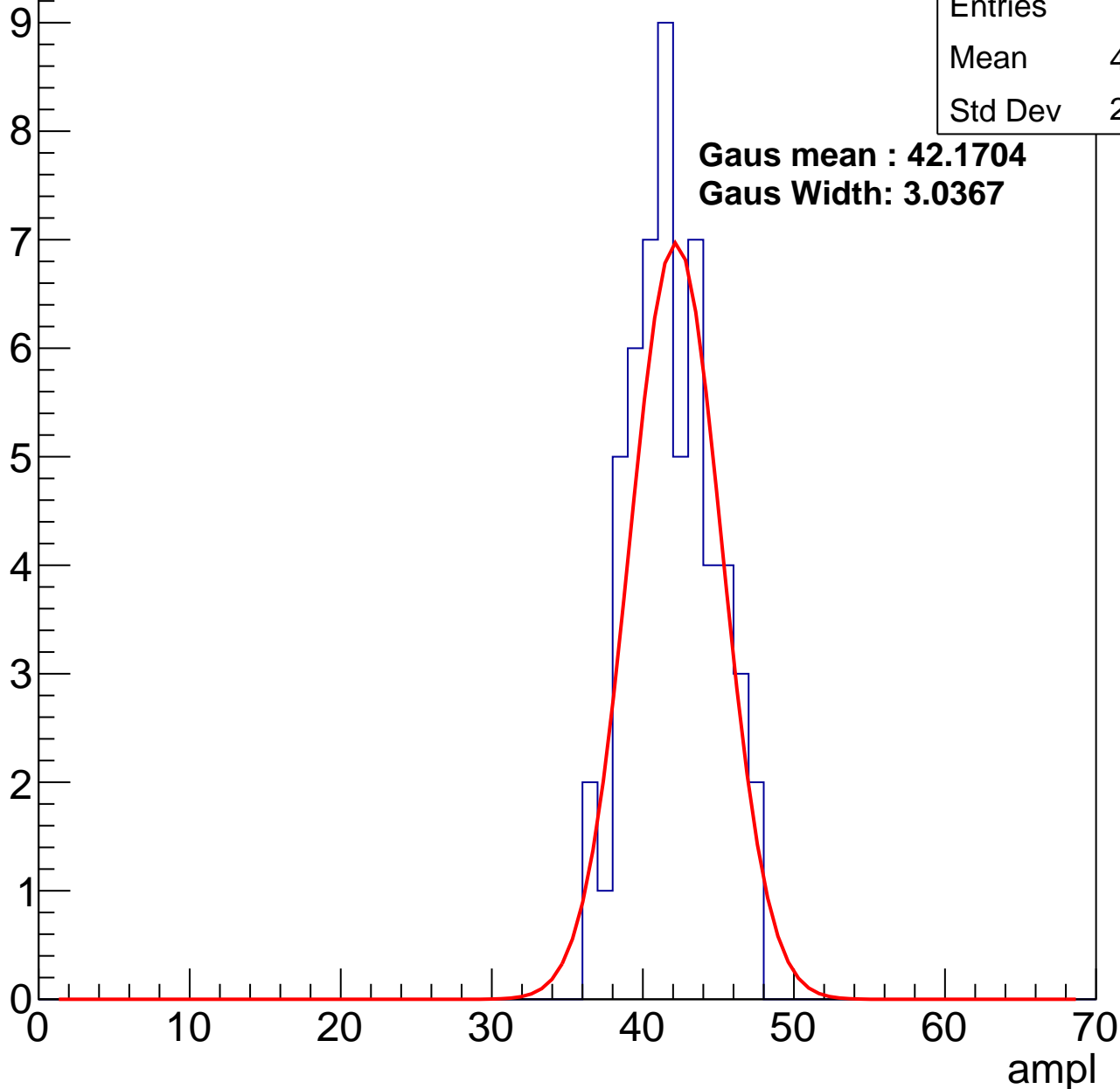
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.47
Std Dev	2.729

**Gaus mean : 42.1704**

**Gaus Width: 3.0367**

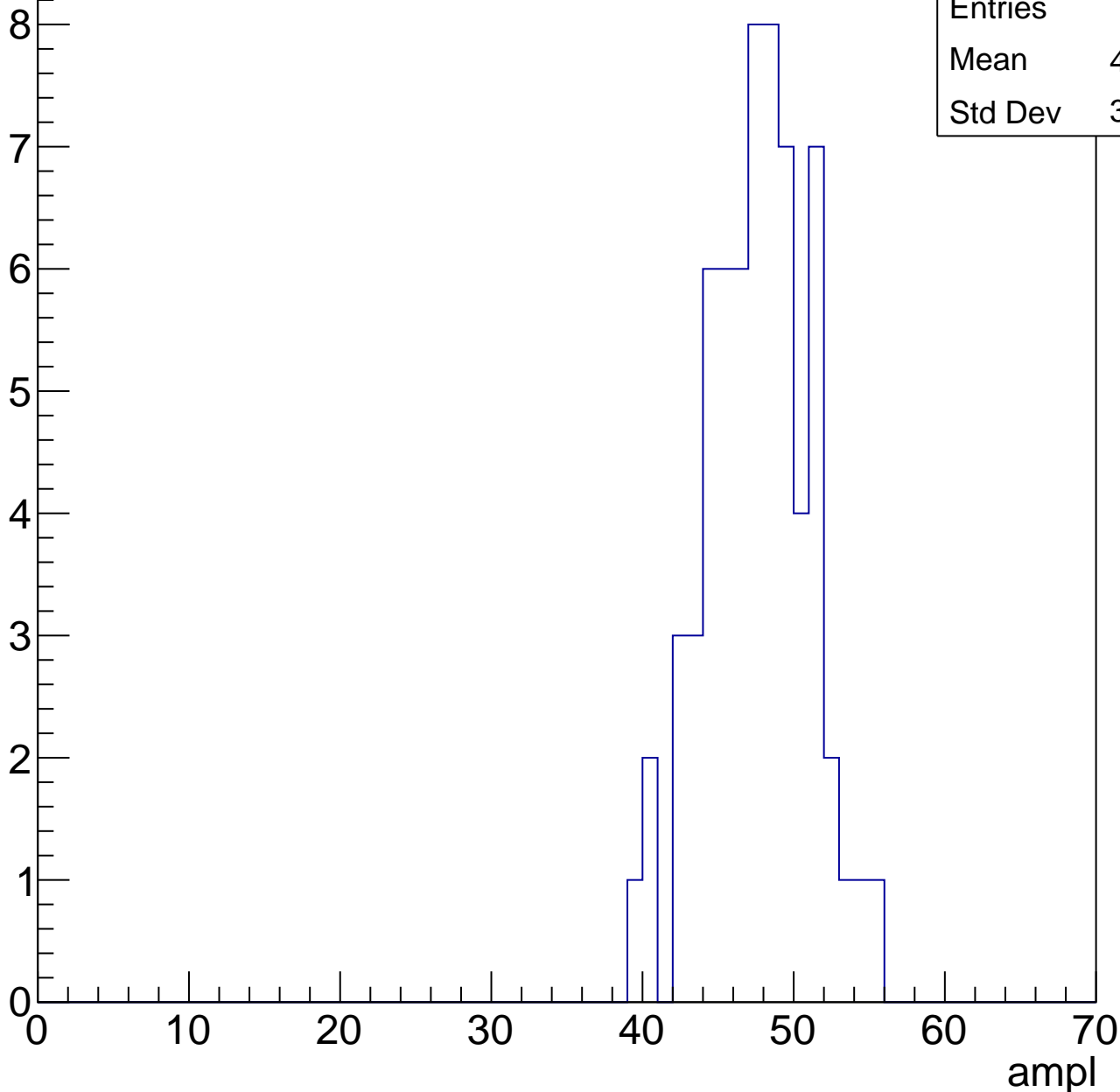


# B1L002S, U8-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	47.12
Std Dev	3.373

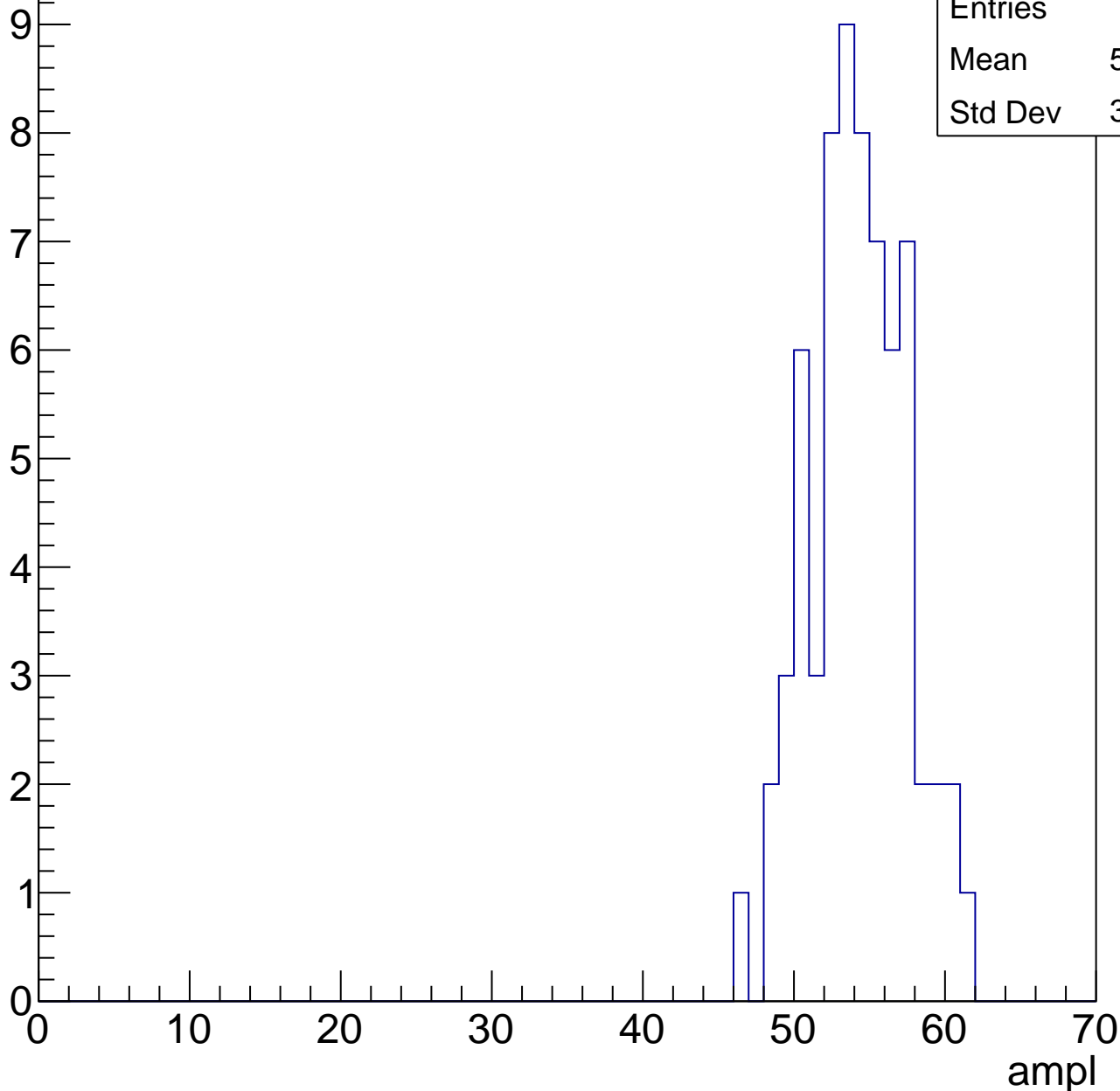


# B1L002S, U8-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	53.76
Std Dev	3.158

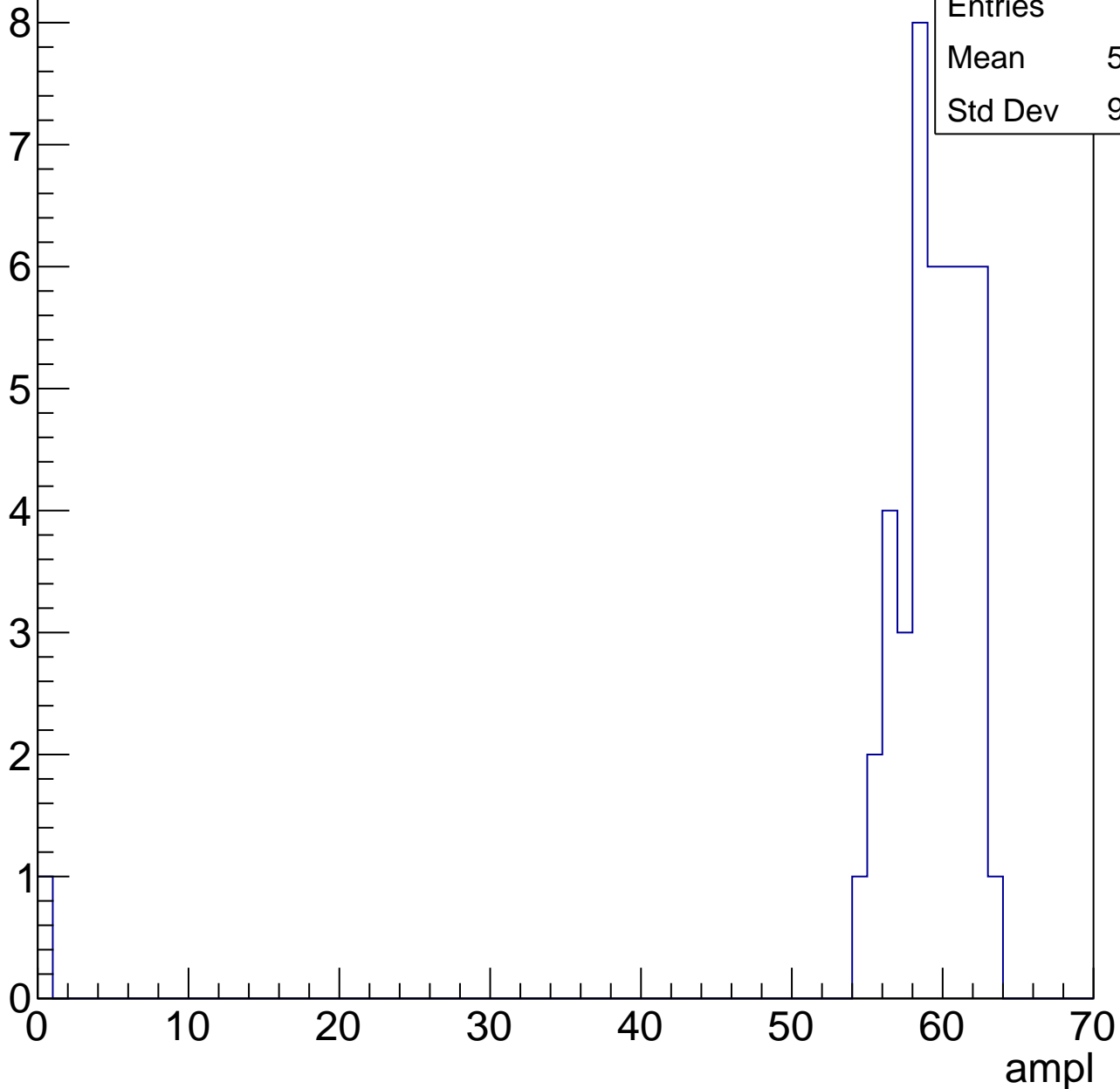


# B1L002S, U8-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	57.68
Std Dev	9.067

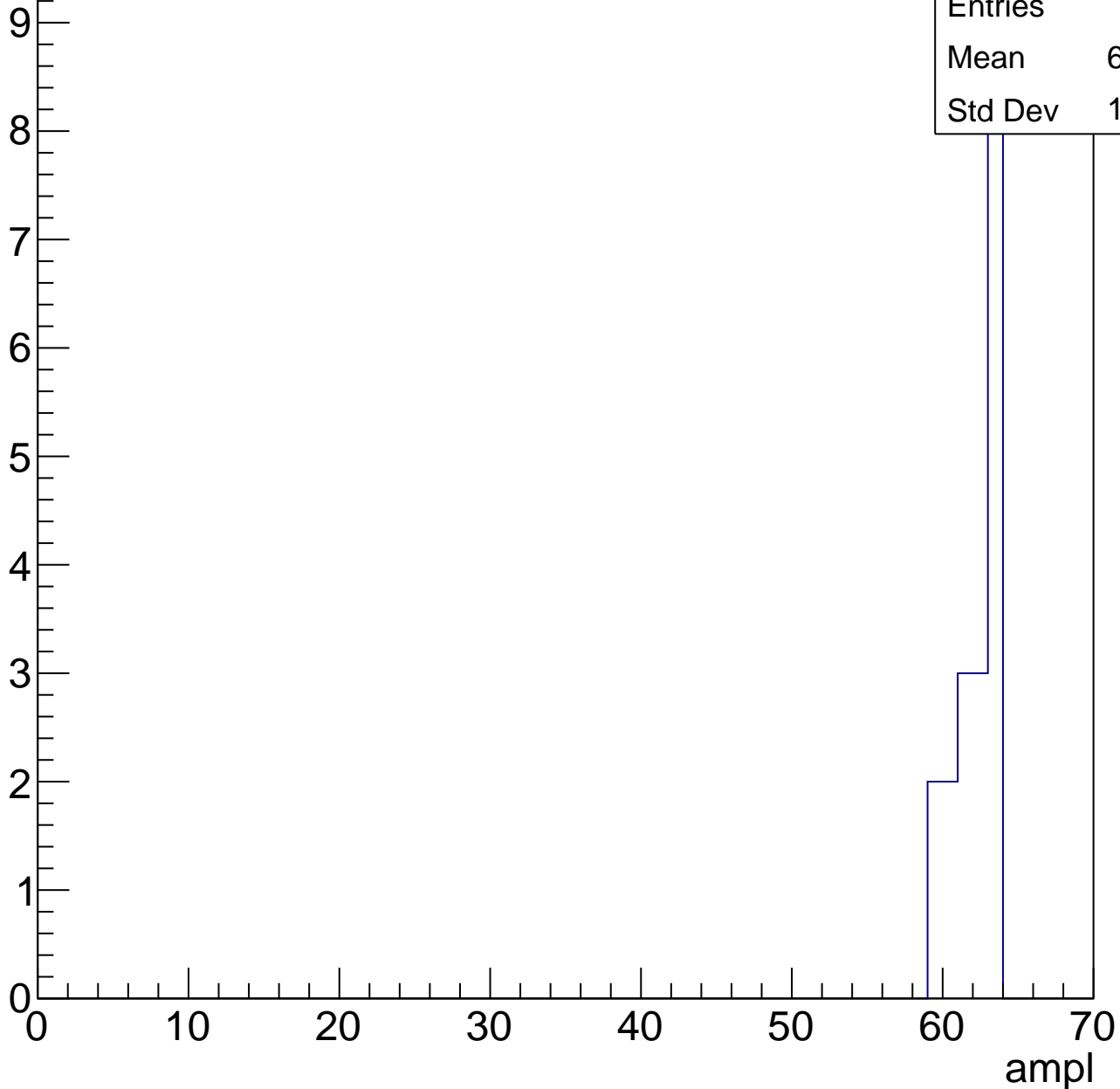


# B1L002S, U8-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.79
Std Dev	1.398

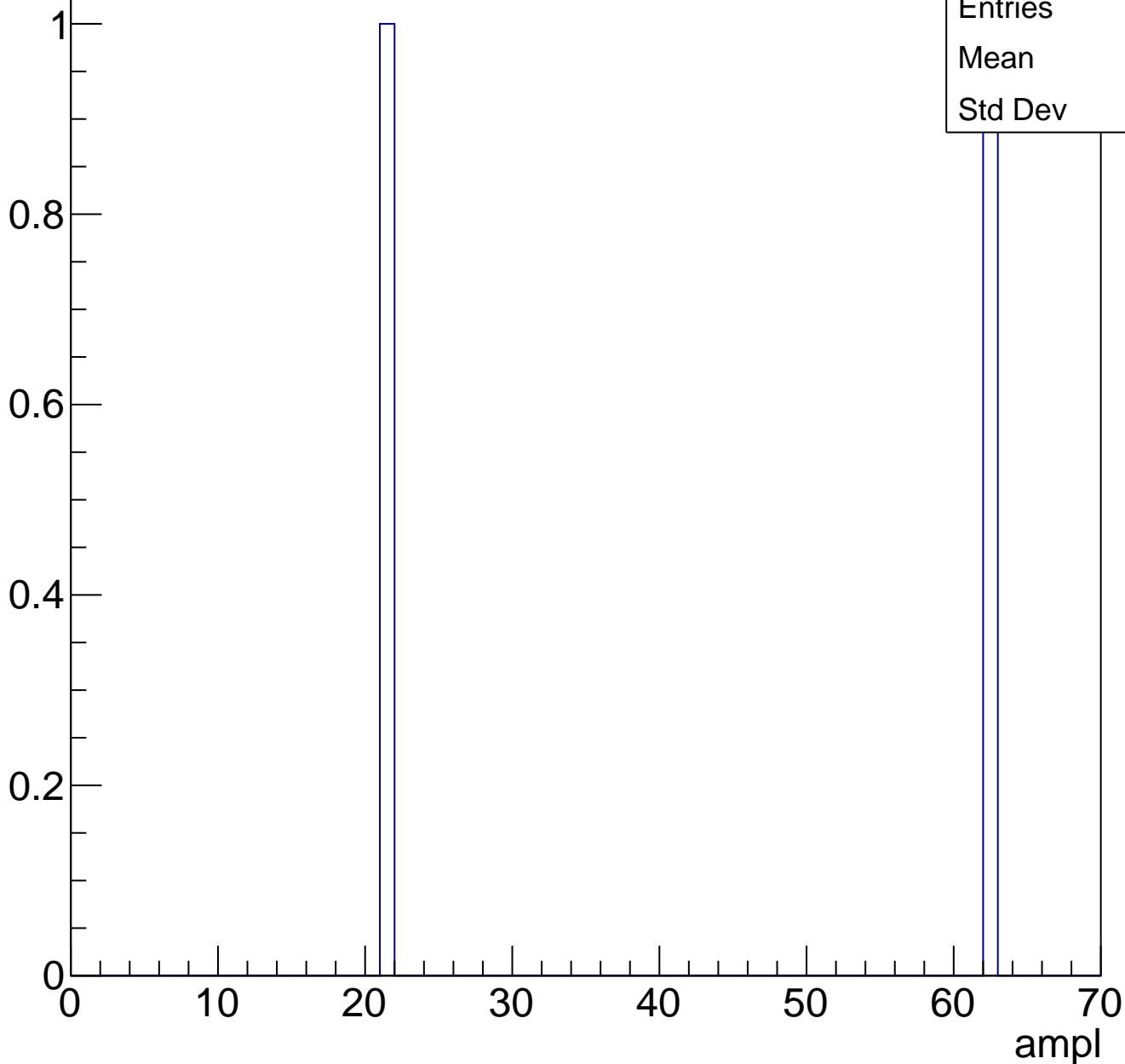




# B1L002S, U8-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch61, adc0

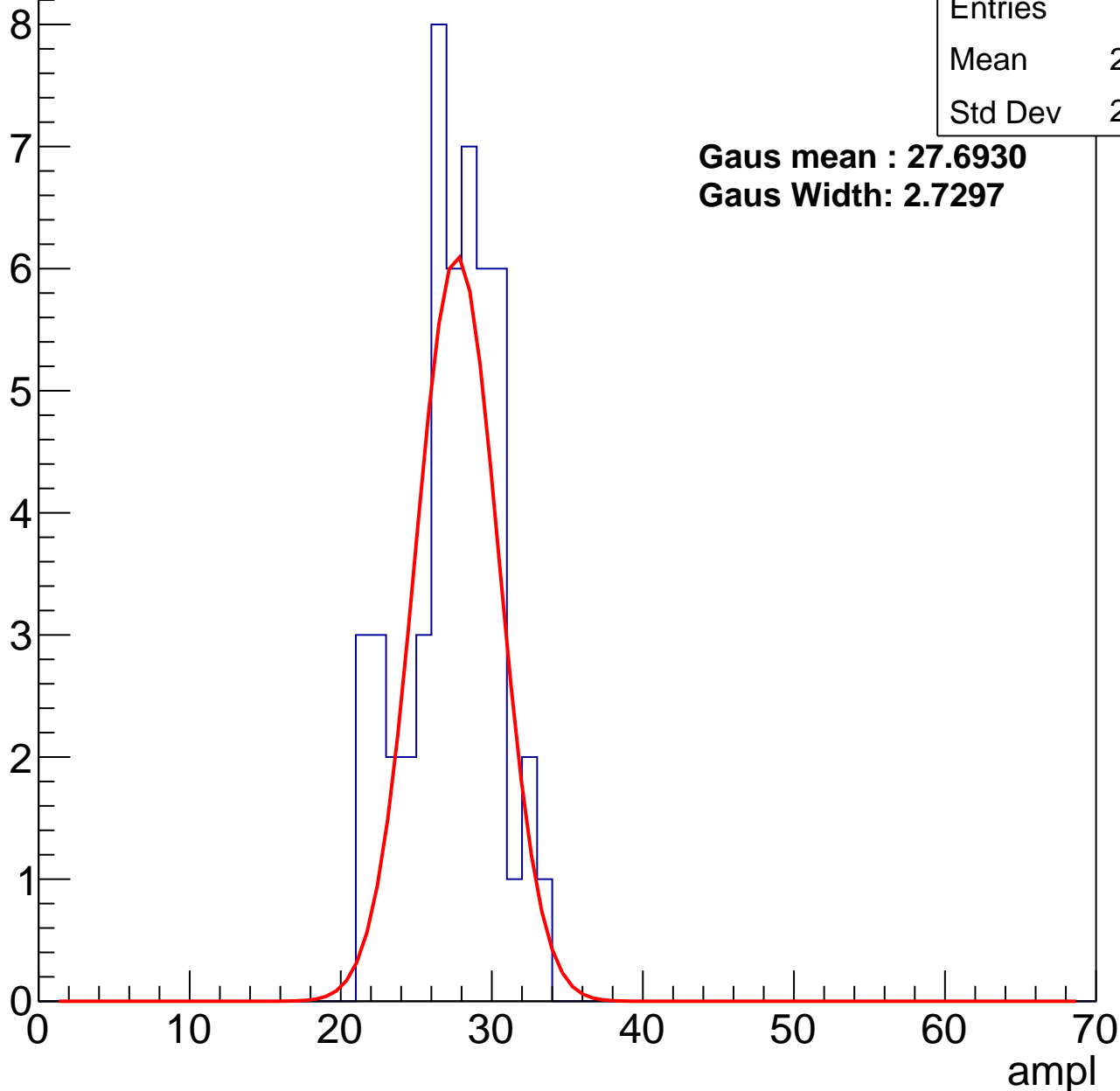
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	26.92
Std Dev	2.965

**Gaus mean : 27.6930**

**Gaus Width: 2.7297**



# B1L002S, U8-ch61, adc1

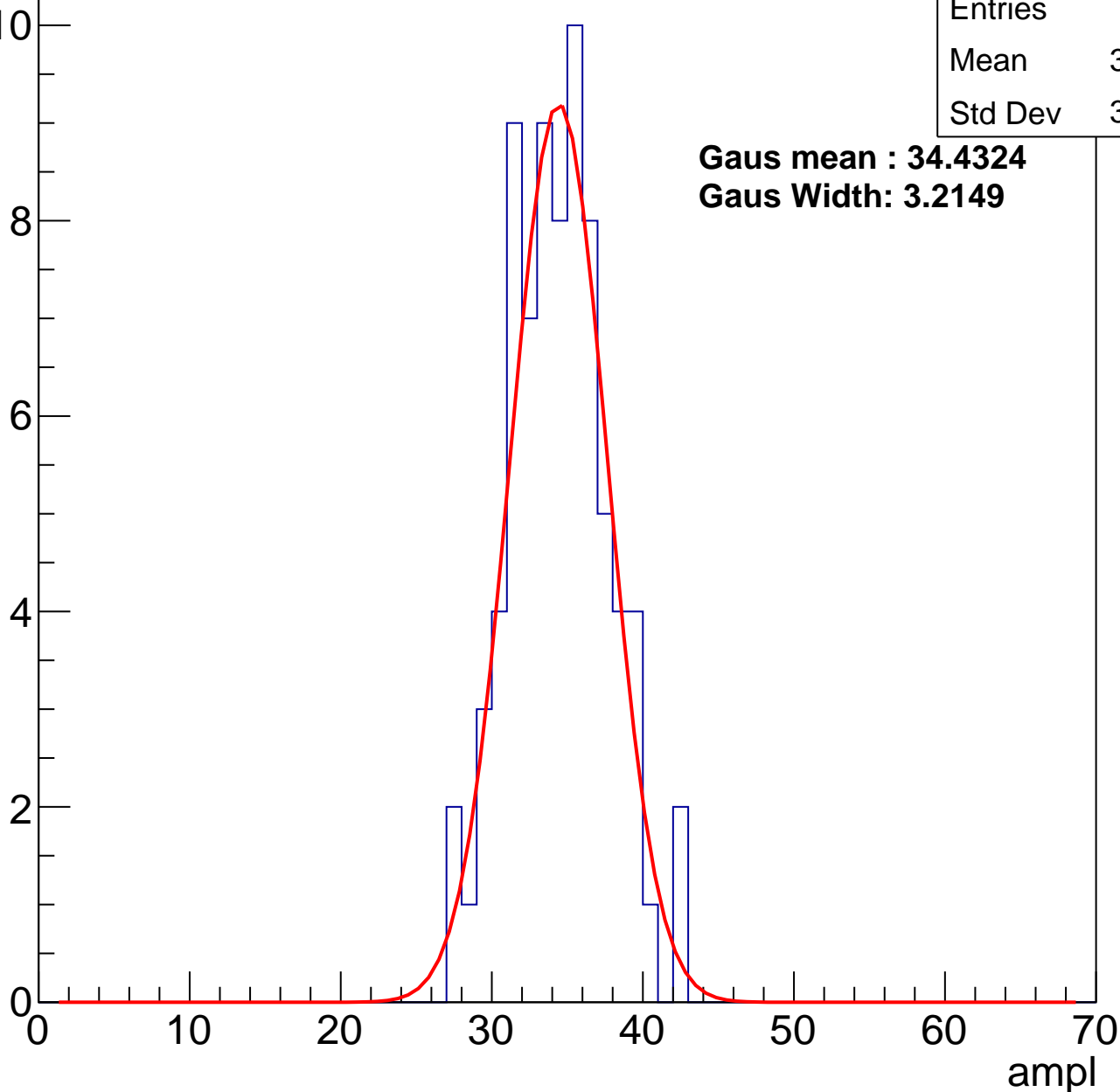
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	33.97
Std Dev	3.235

**Gaus mean : 34.4324**

**Gaus Width: 3.2149**



# B1L002S, U8-ch61, adc2

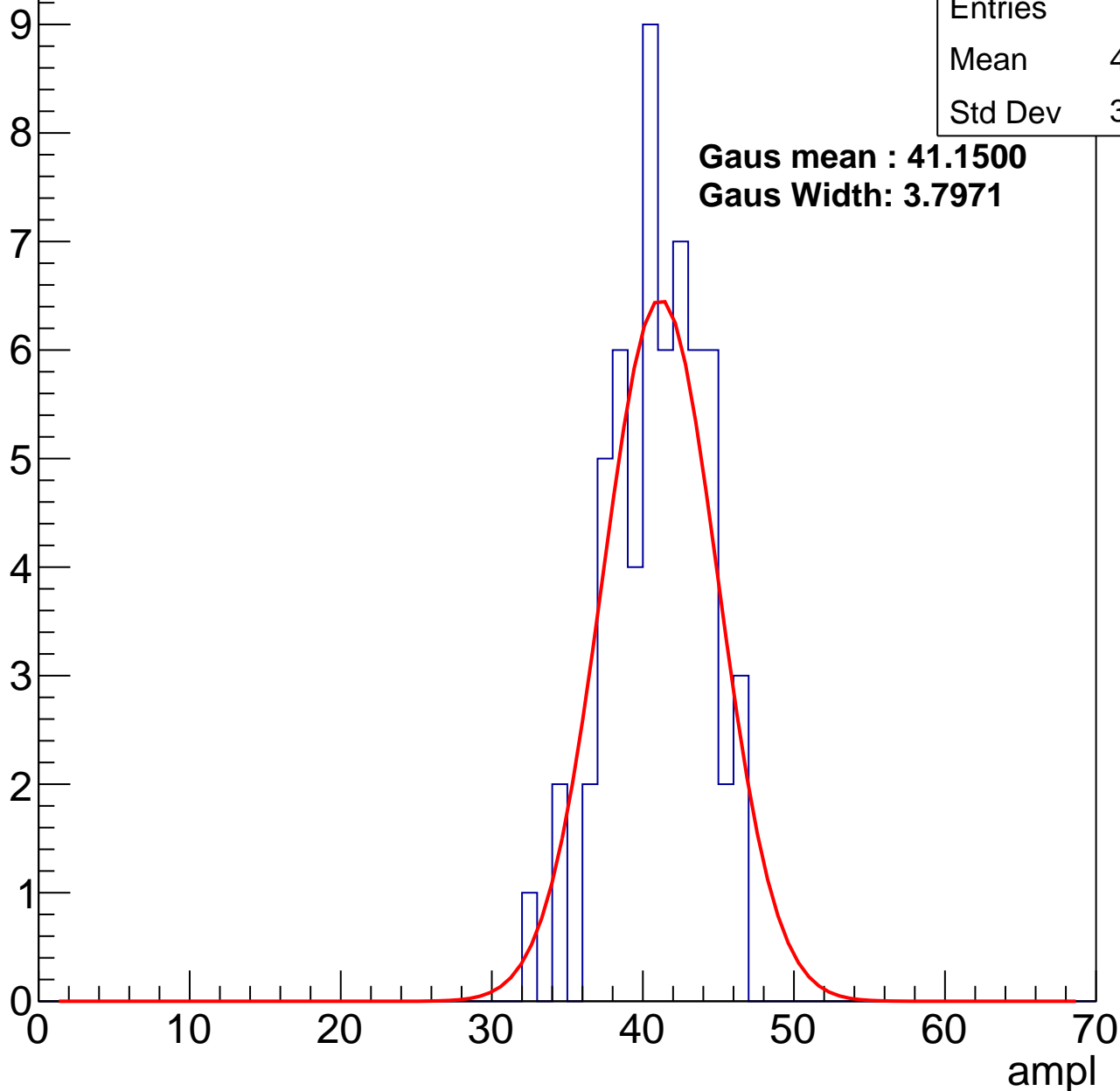
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.53
Std Dev	3.099

**Gaus mean : 41.1500**

**Gaus Width: 3.7971**

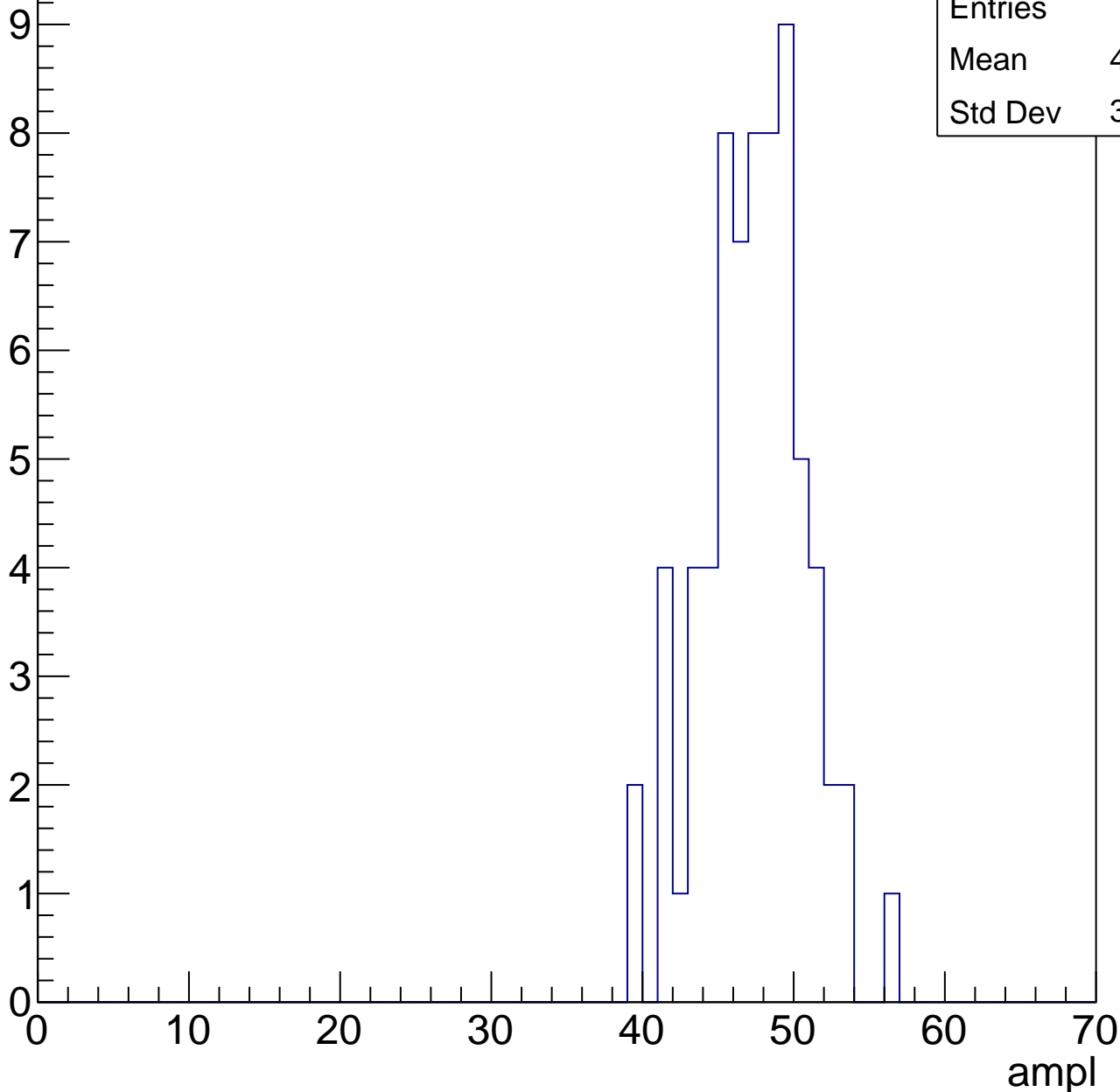


# B1L002S, U8-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

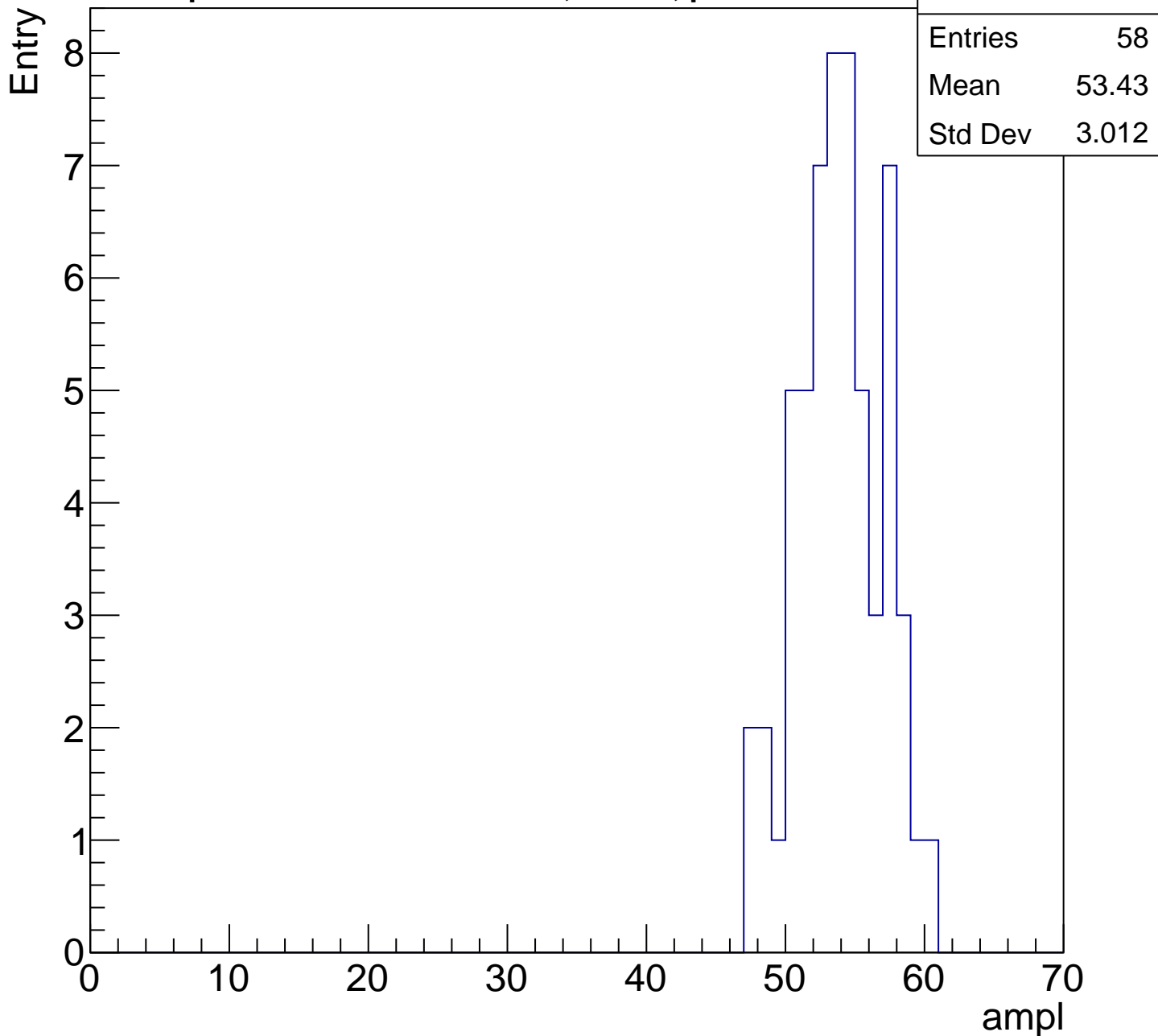
Entry

Entries	69
Mean	46.88
Std Dev	3.386



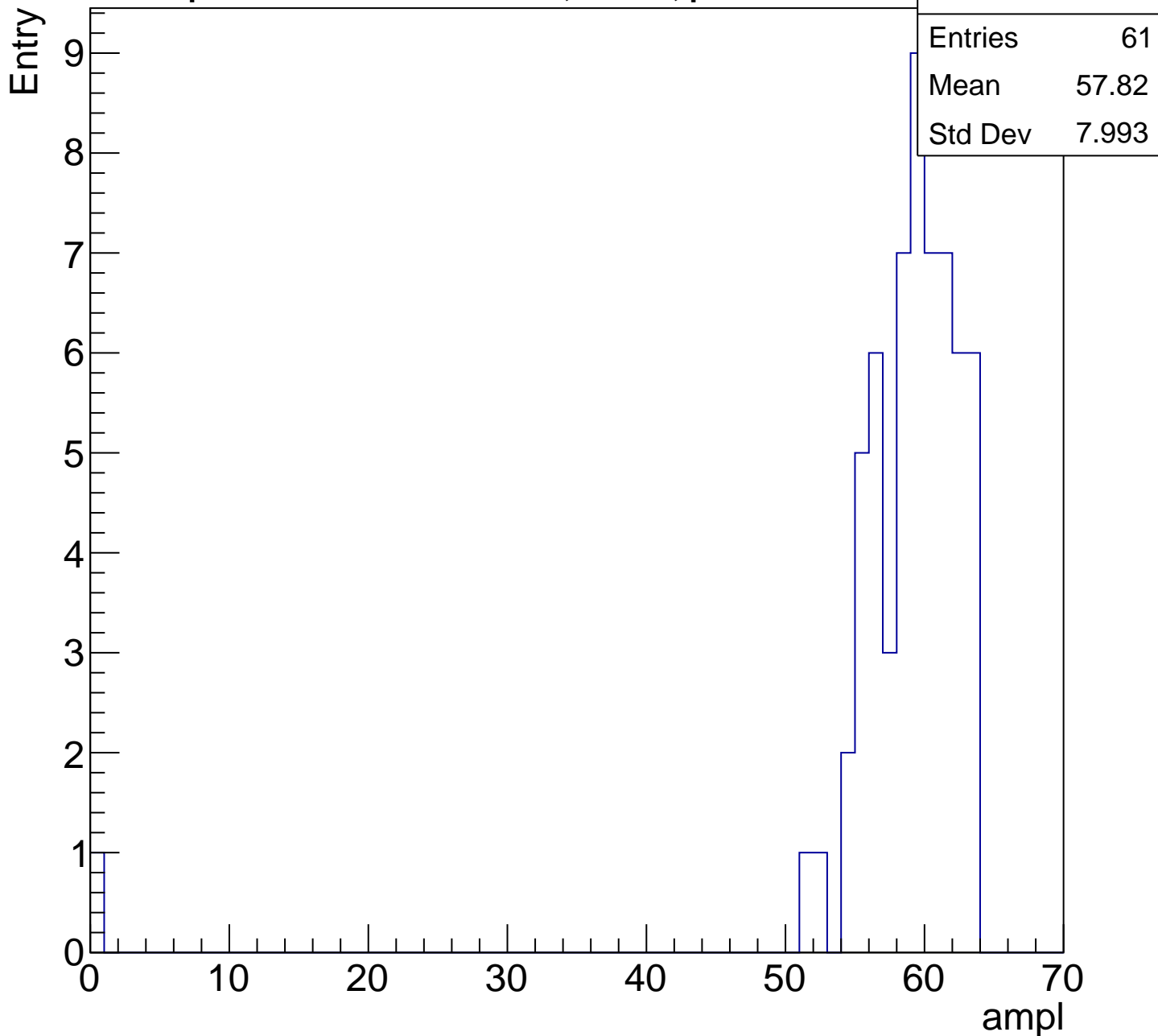
# B1L002S, U8-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch61, adc5

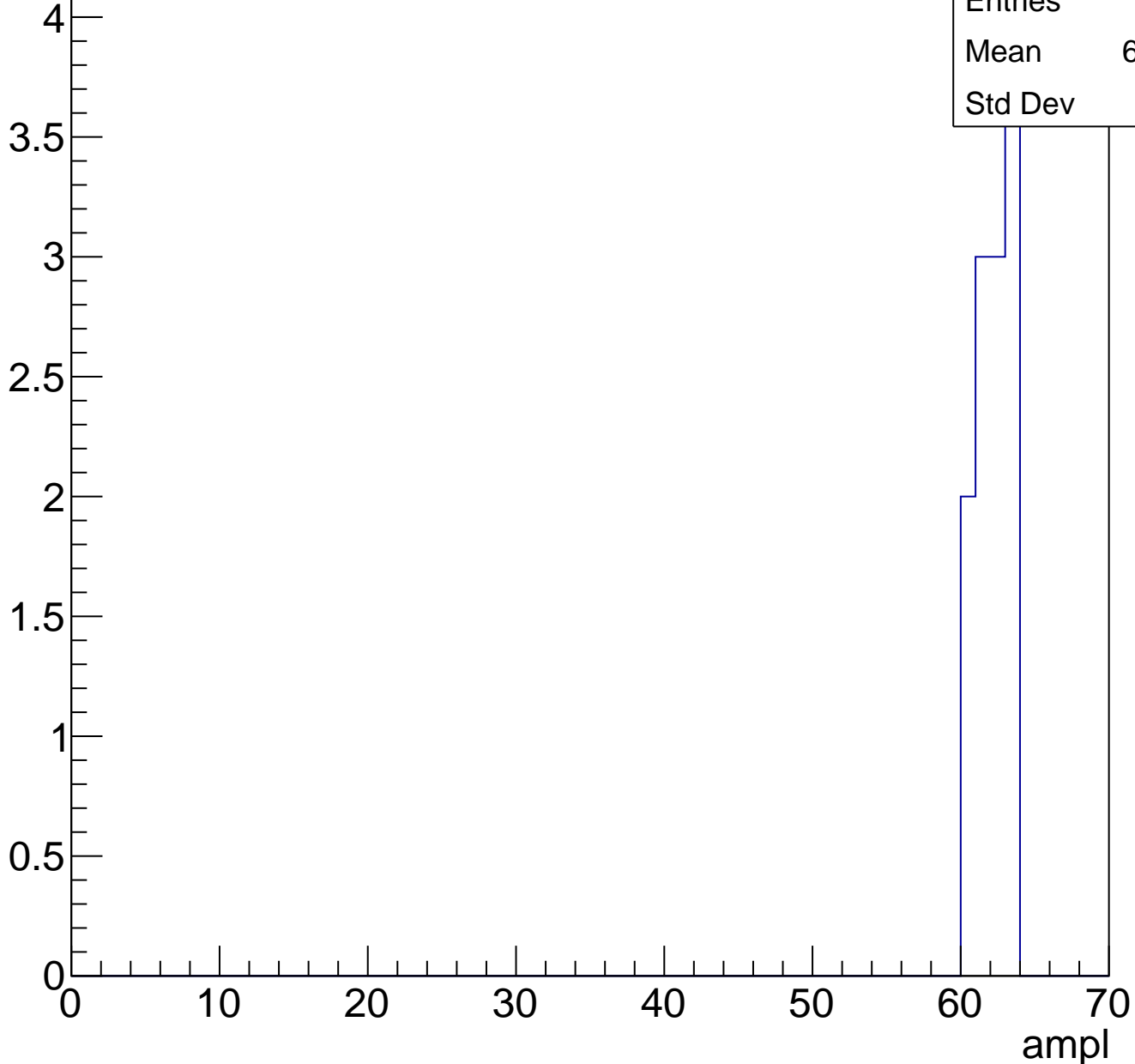
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

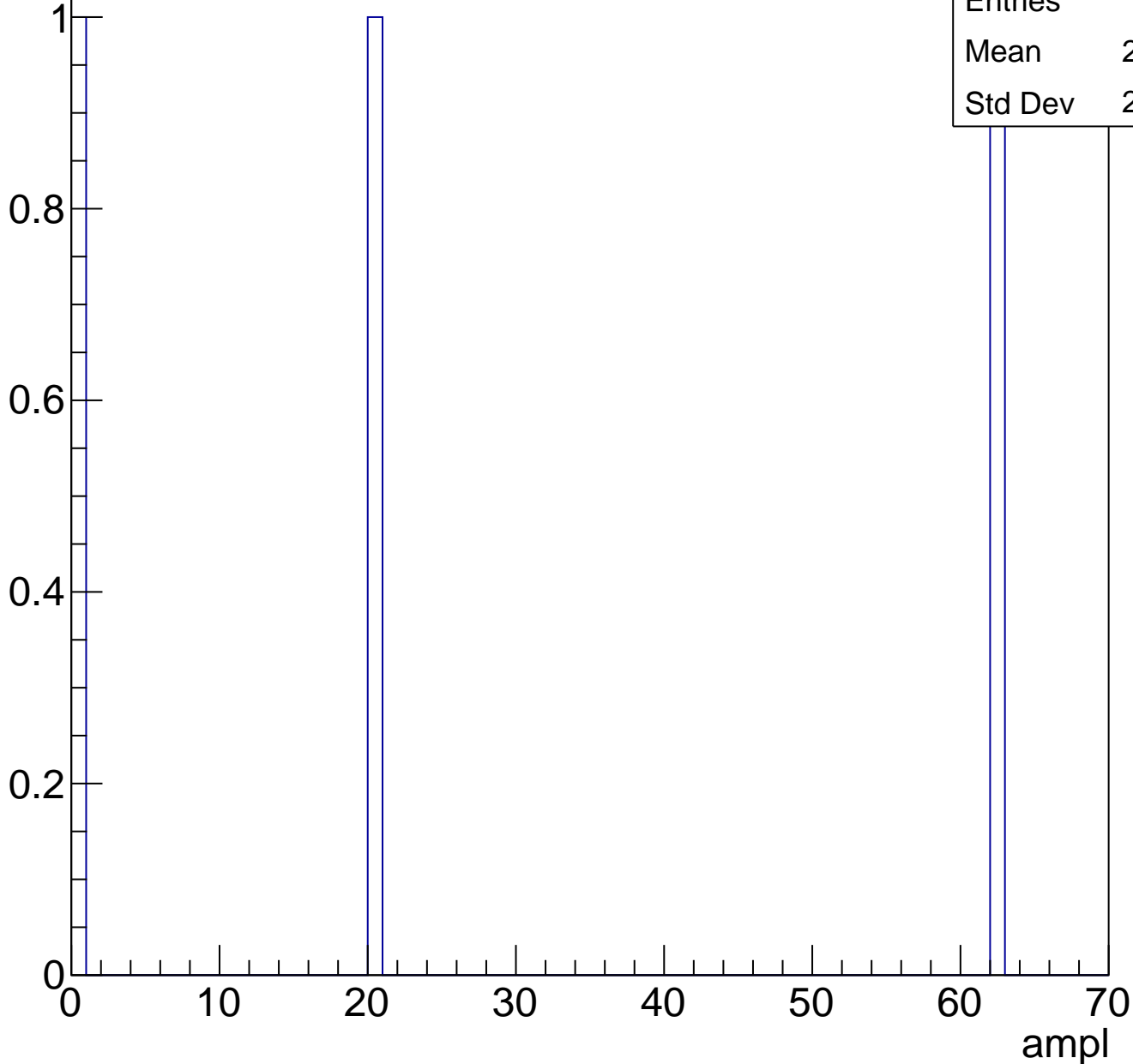




# B1L002S, U8-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.33
Std Dev	25.84

# B1L002S, U8-ch62, adc0

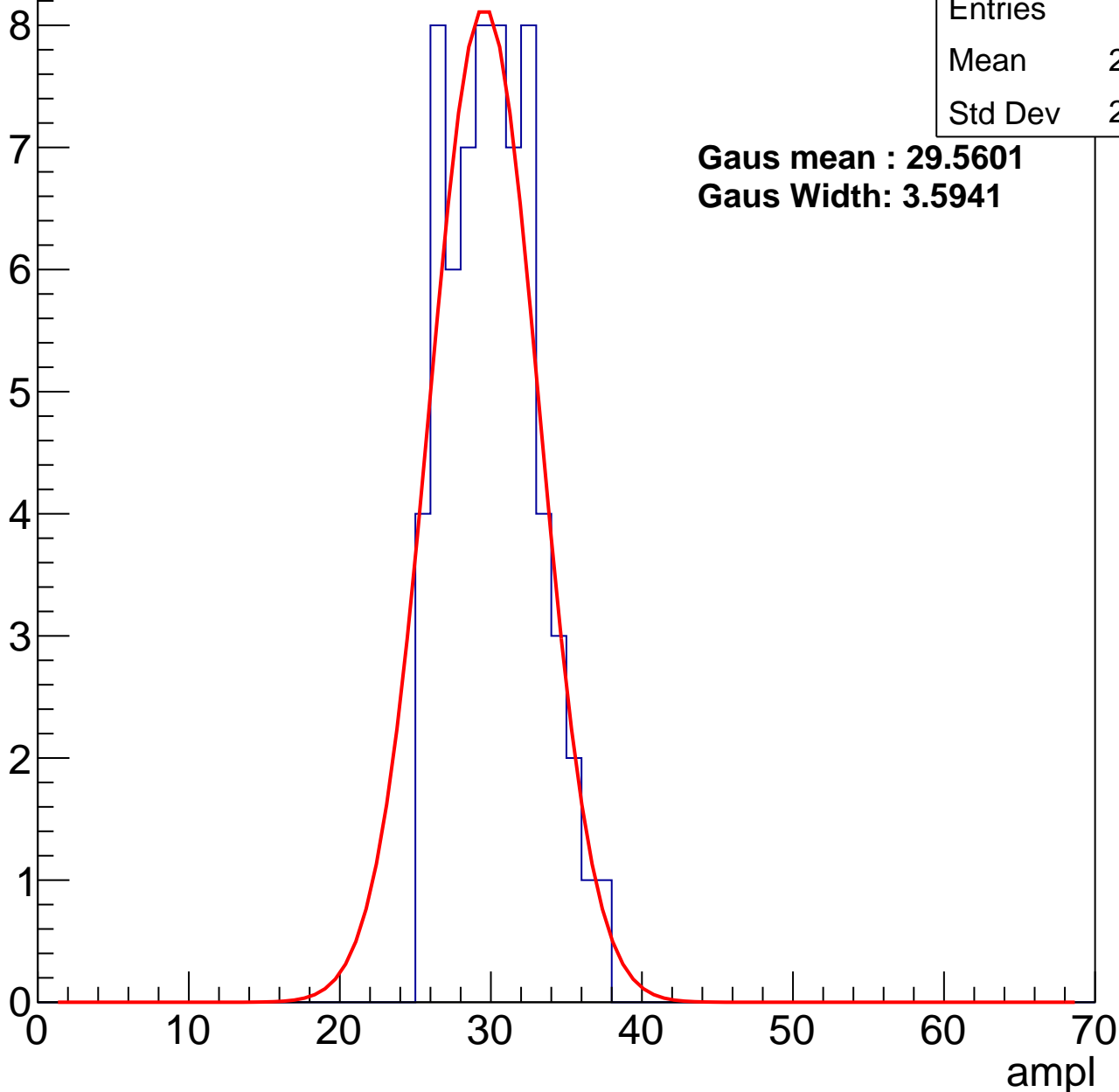
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.67
Std Dev	2.914

**Gaus mean : 29.5601**

**Gaus Width: 3.5941**



# B1L002S, U8-ch62, adc1

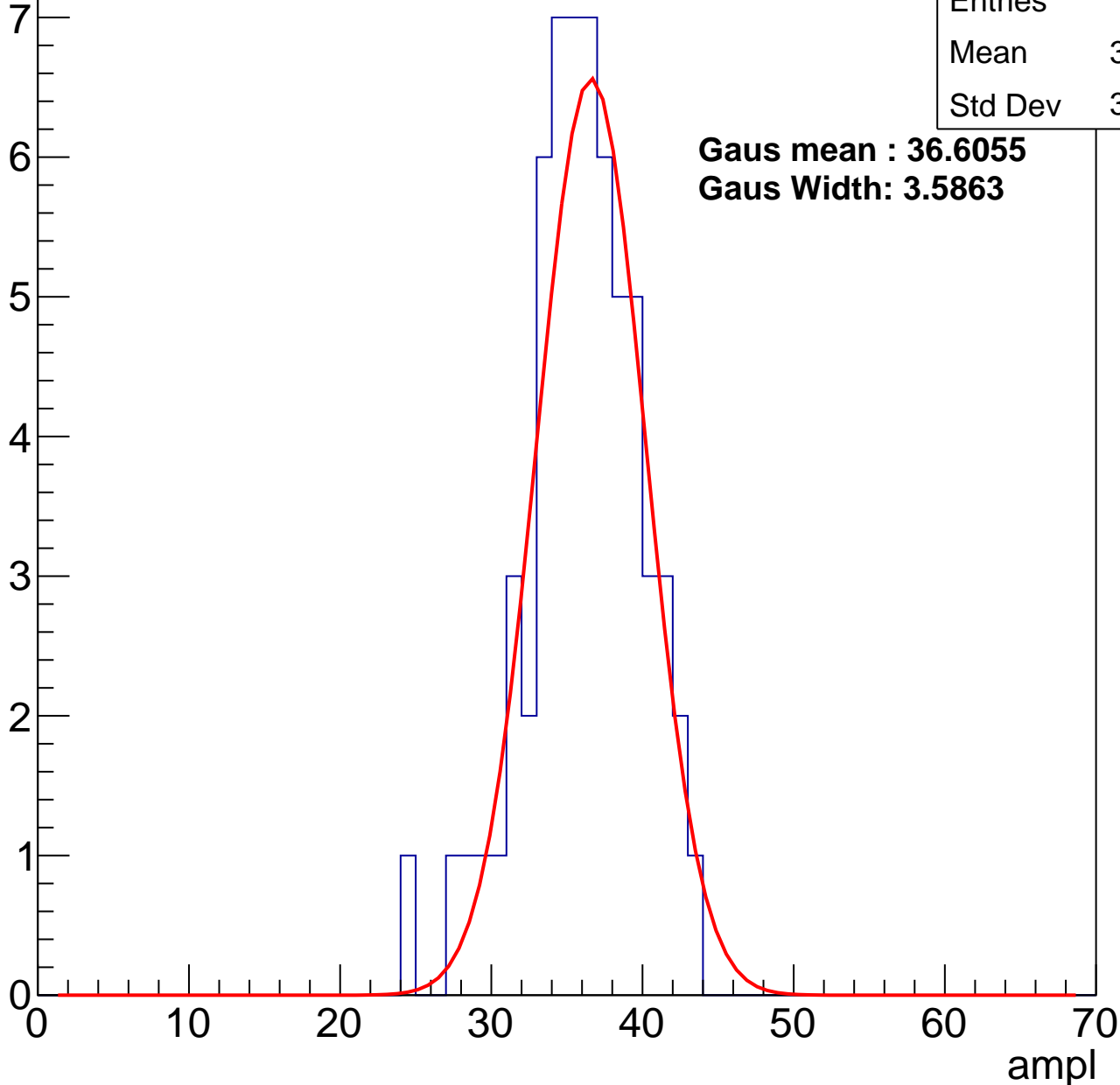
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	35.56
Std Dev	3.753

**Gaus mean : 36.6055**

**Gaus Width: 3.5863**



# B1L002S, U8-ch62, adc2

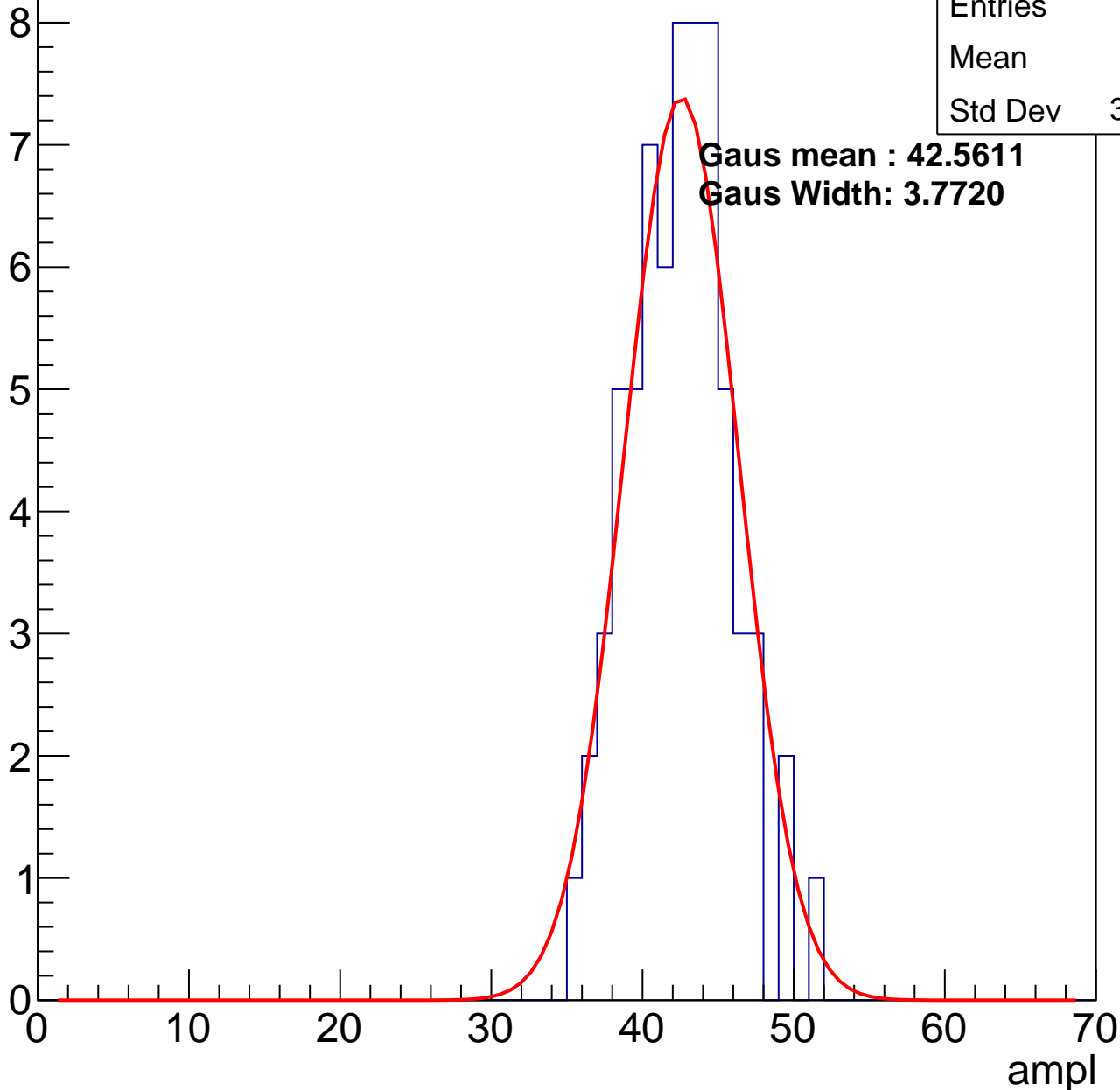
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	42
Std Dev	3.328

**Gaus mean : 42.5611**

**Gaus Width: 3.7720**

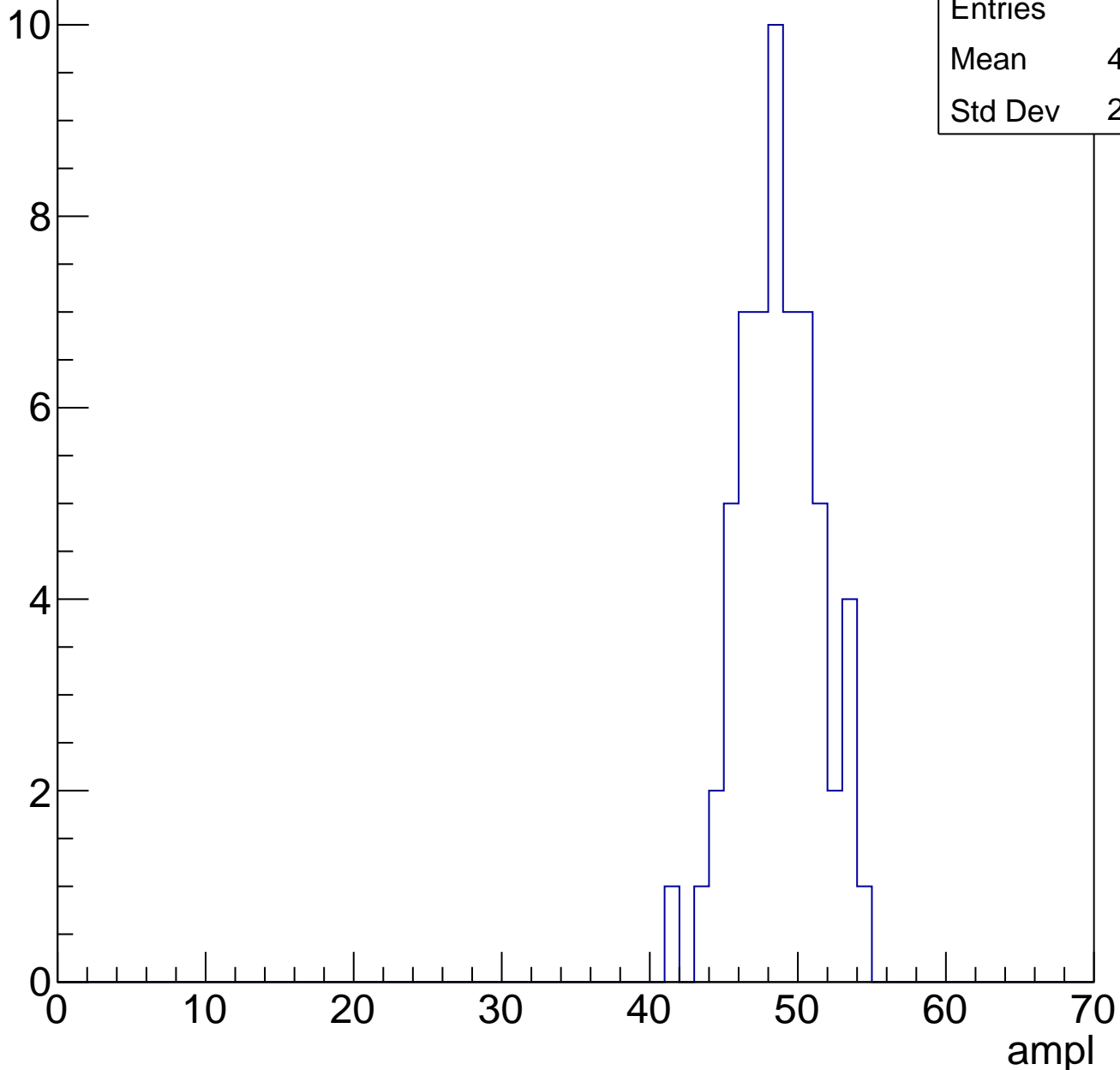


# B1L002S, U8-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	48.24
Std Dev	2.702

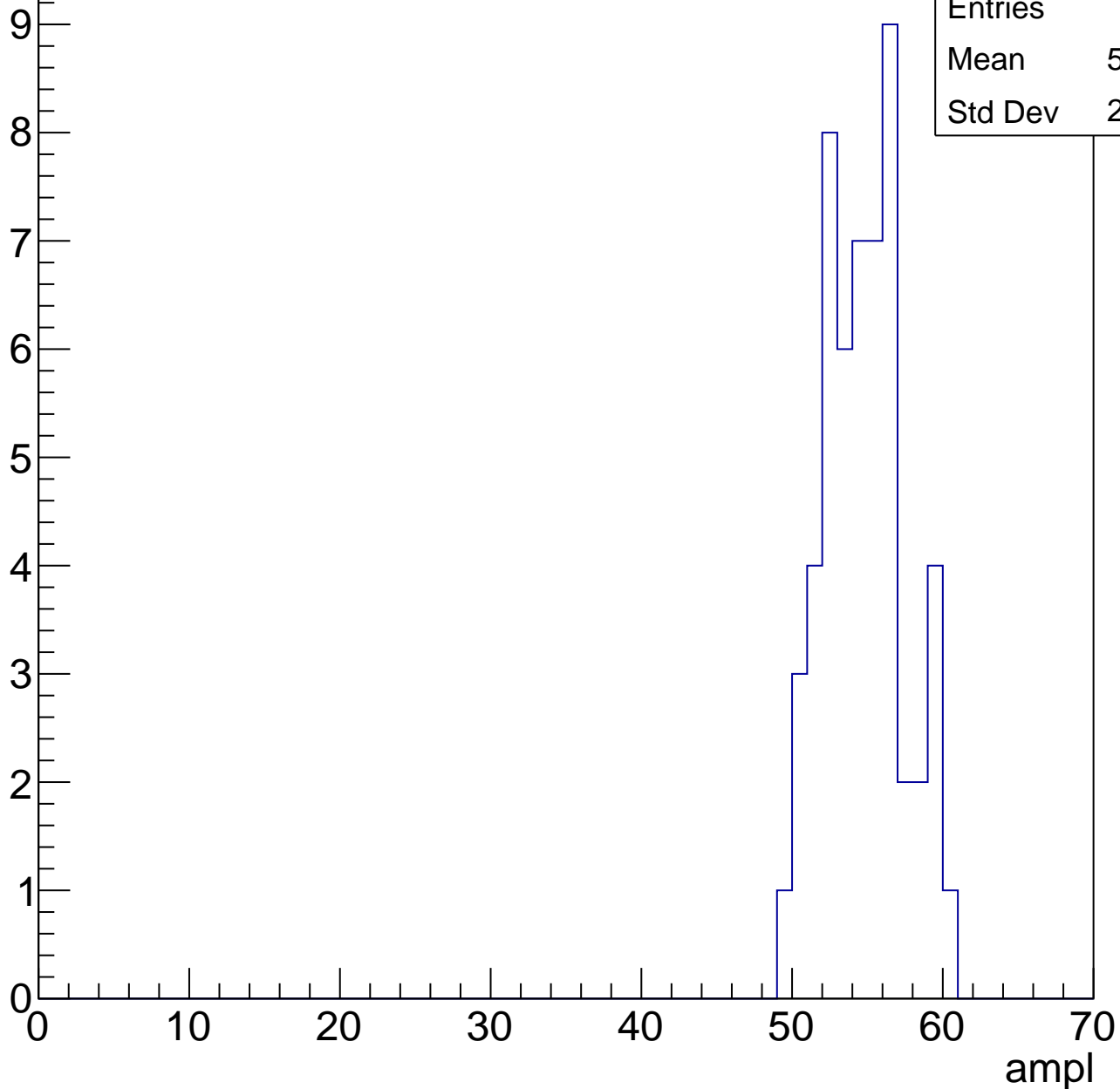
Entry



# B1L002S, U8-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



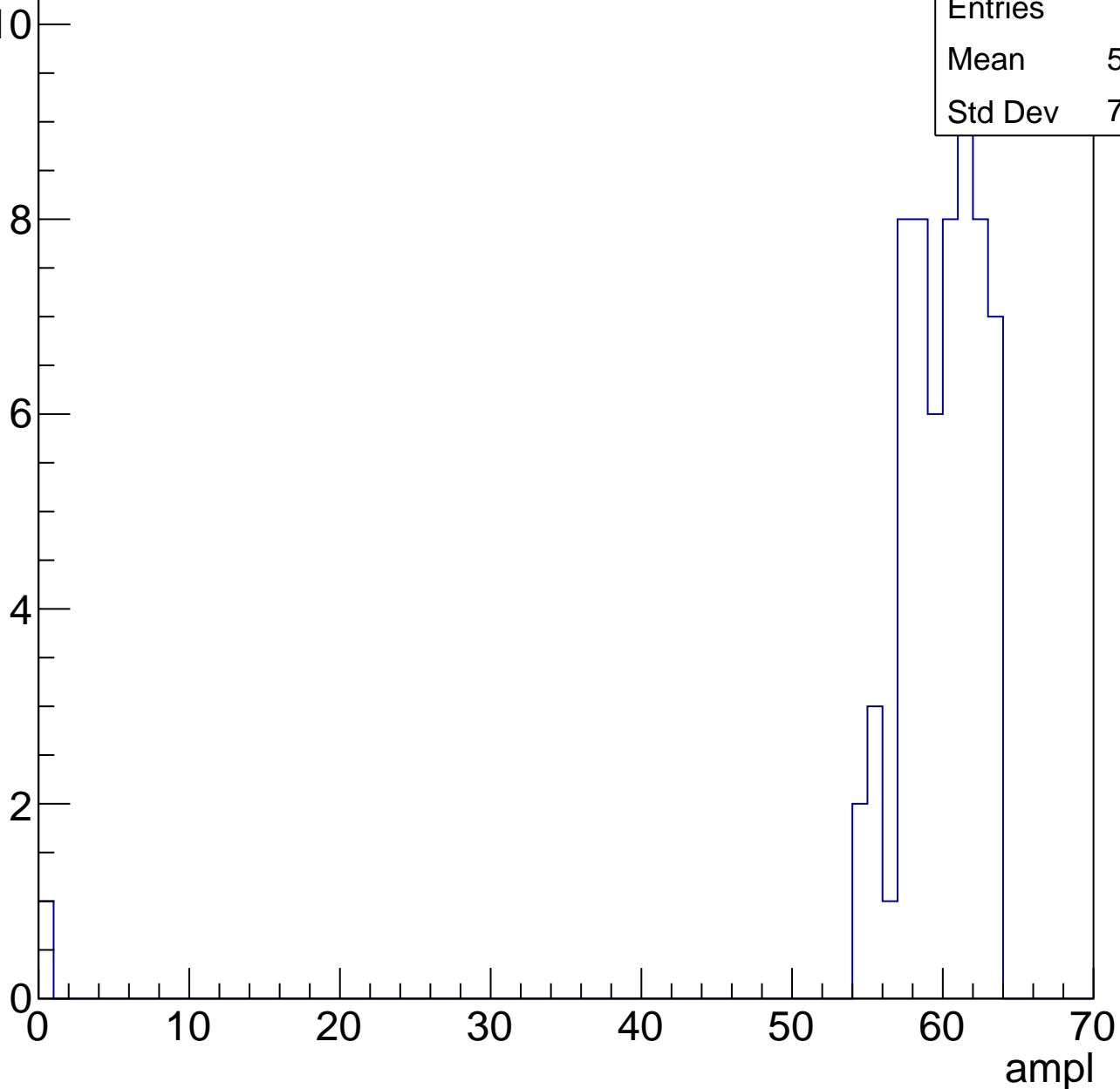
Entries	54
Mean	54.26
Std Dev	2.626

# B1L002S, U8-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

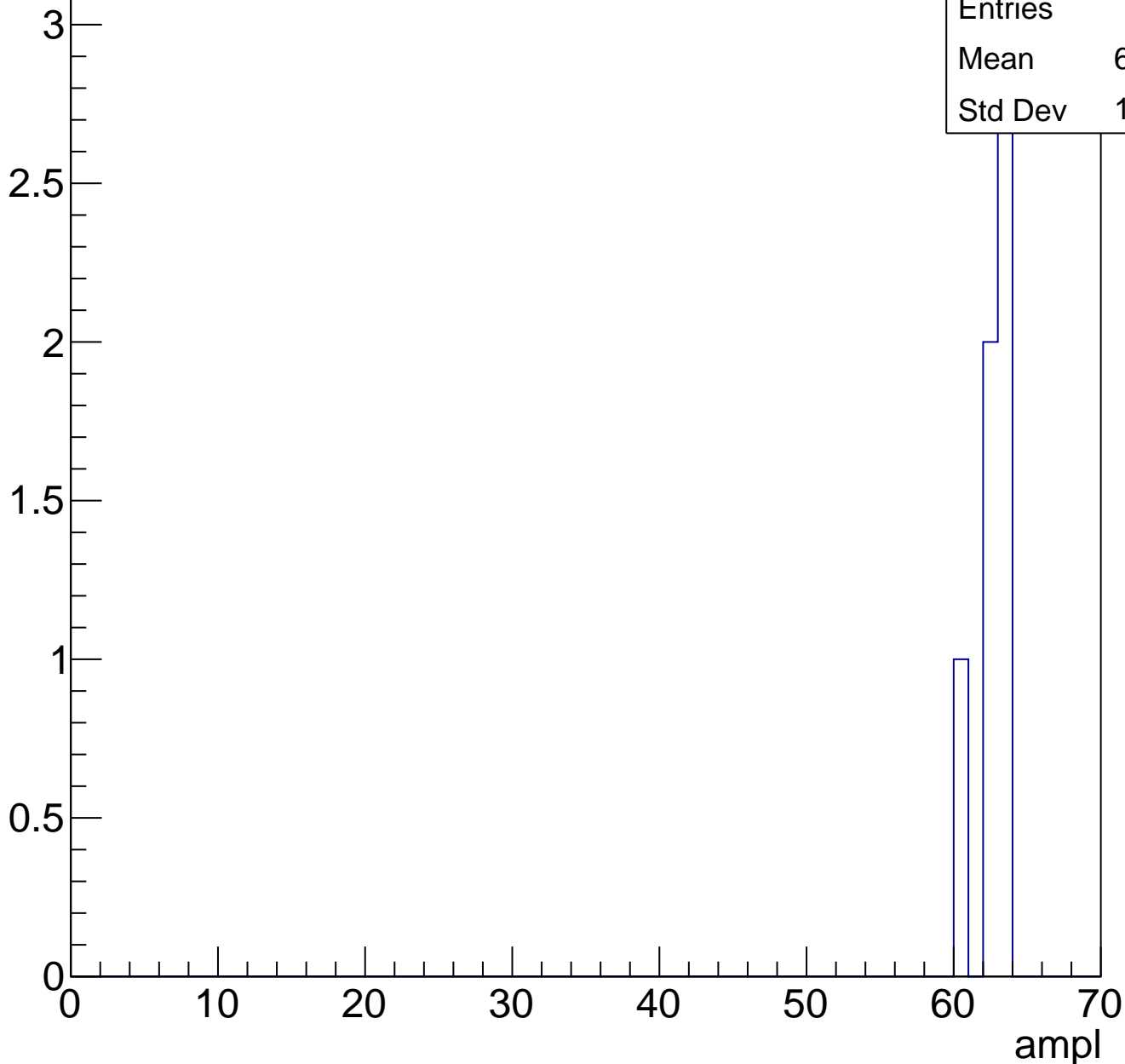
Entries	62
Mean	58.55
Std Dev	7.877



# B1L002S, U8-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L002S, U8-ch63, adc0

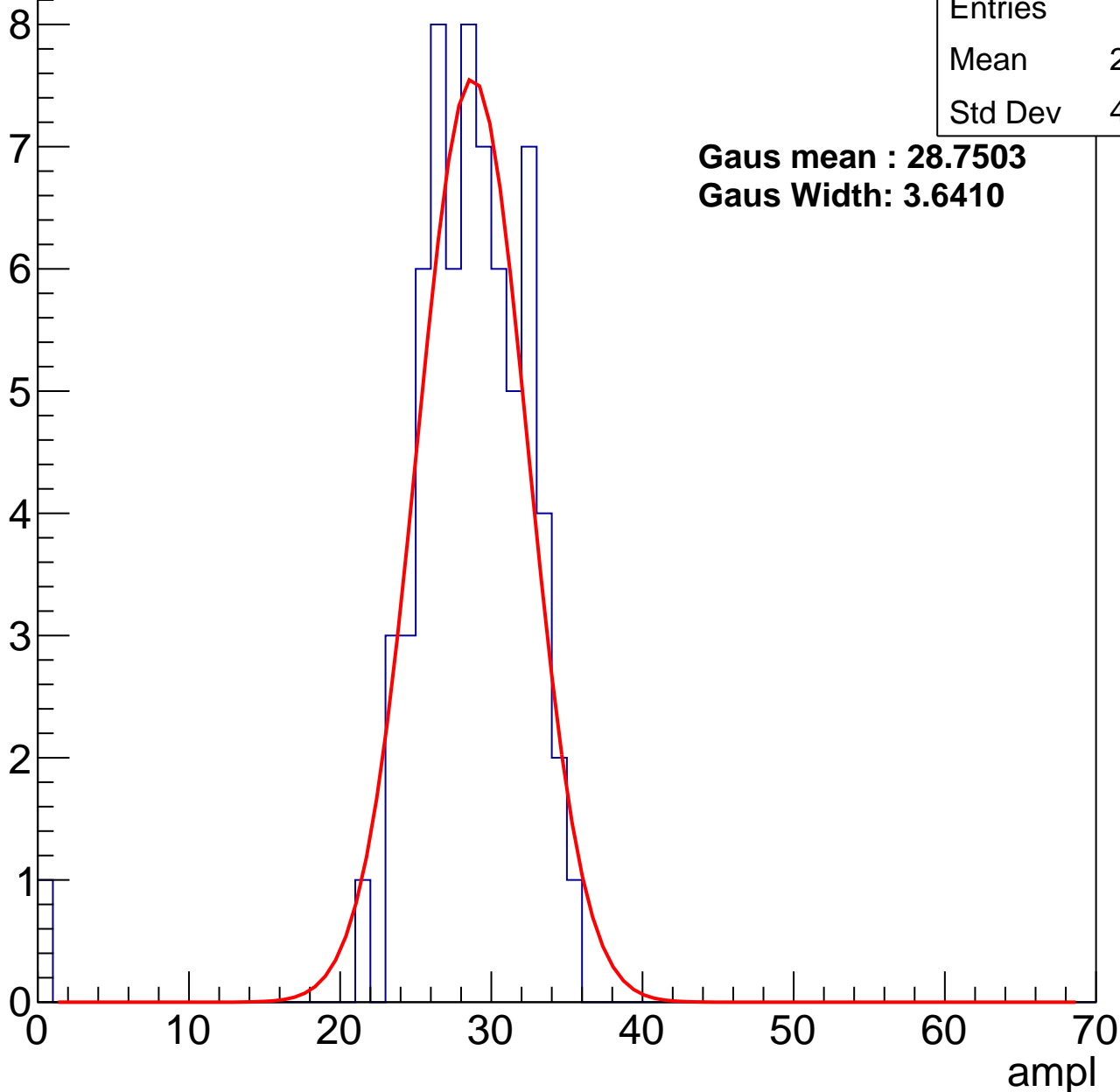
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.99
Std Dev	4.629

**Gaus mean : 28.7503**

**Gaus Width: 3.6410**



# B1L002S, U8-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	35.07
Std Dev	3.209

**Gaus mean : 35.7259**

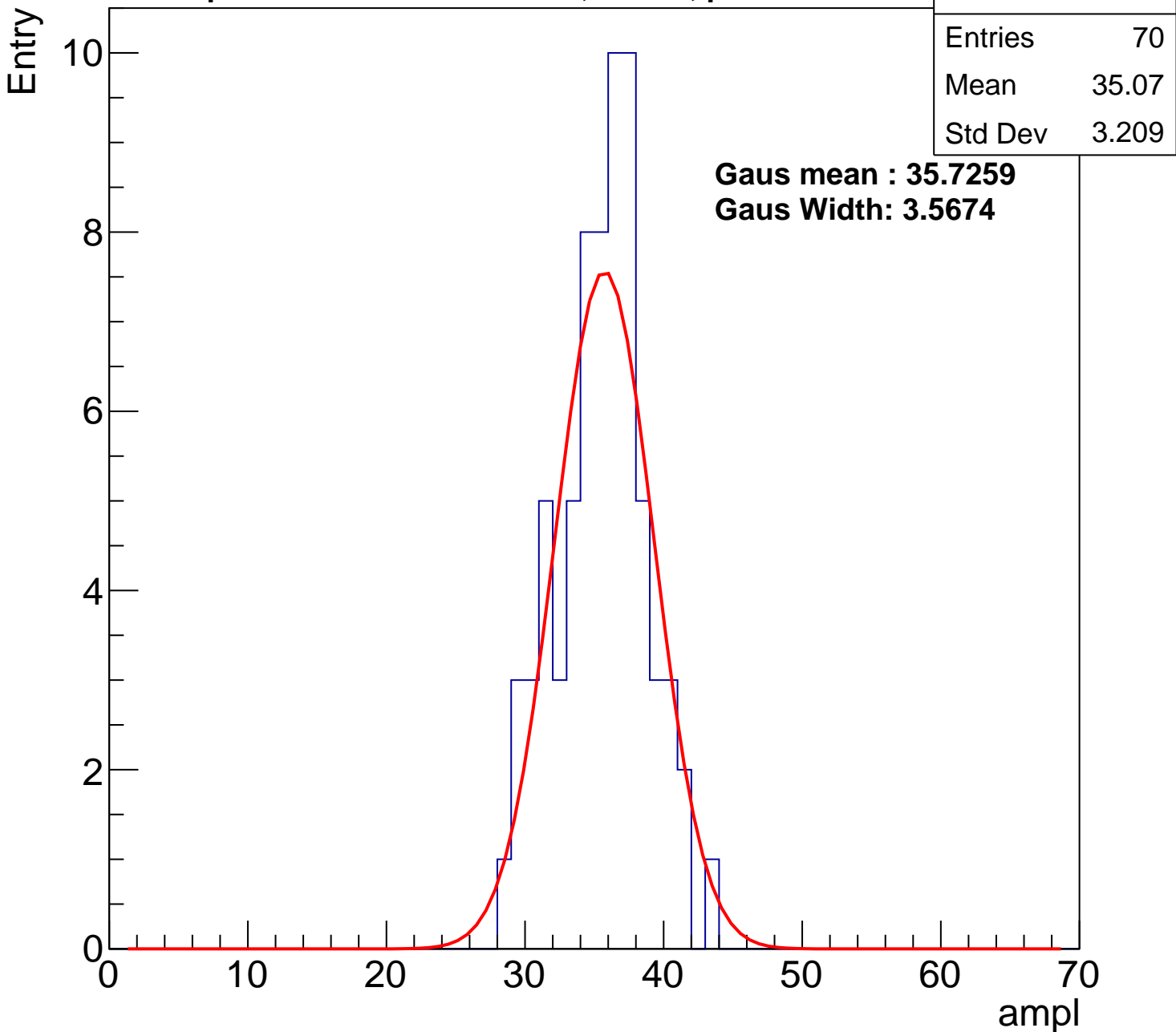
**Gaus Width: 3.5674**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	42.06
Std Dev	2.828

**Gaus mean : 42.3194**

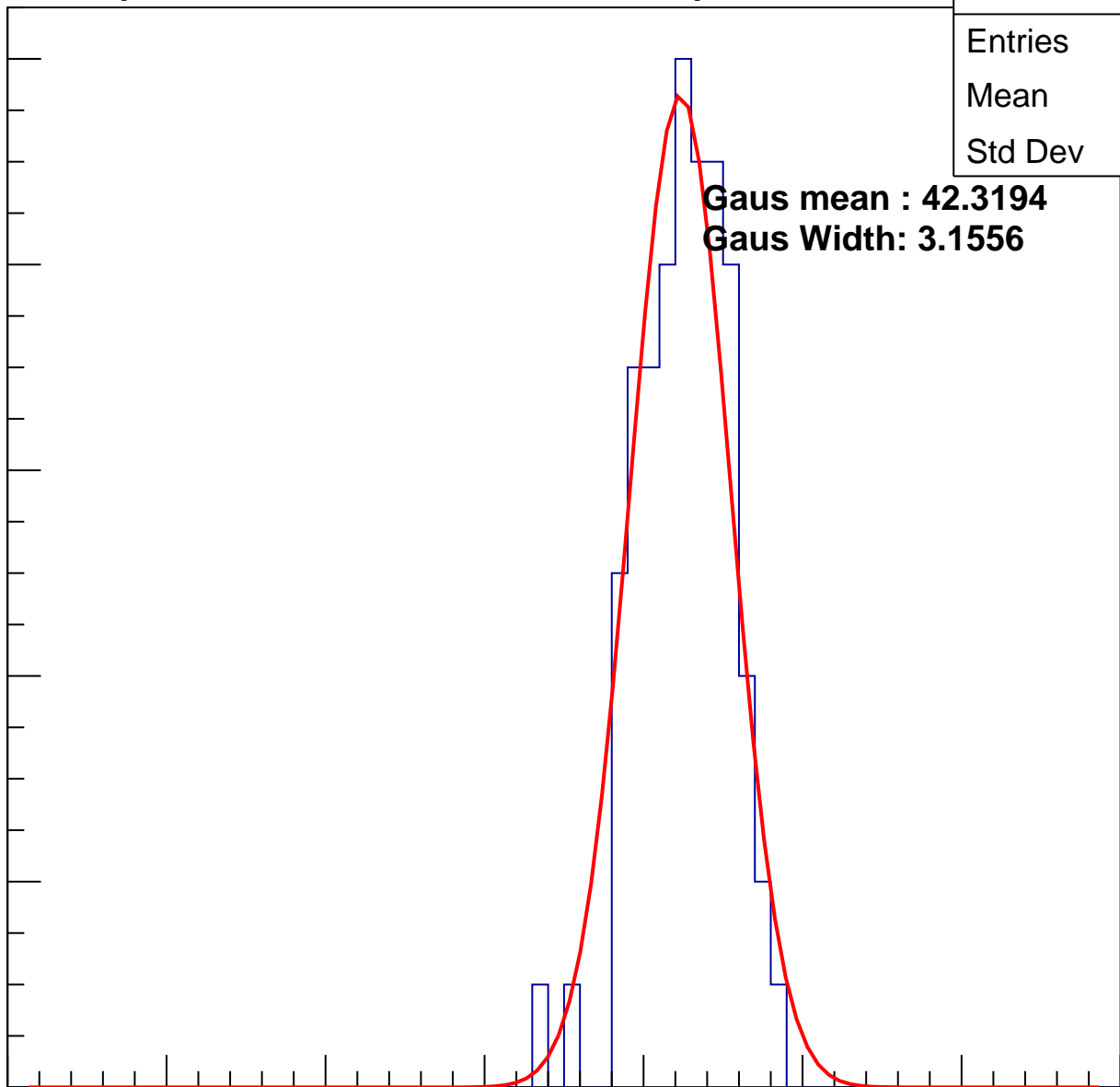
**Gaus Width: 3.1556**

Entry

10  
8  
6  
4  
2  
0

ampl

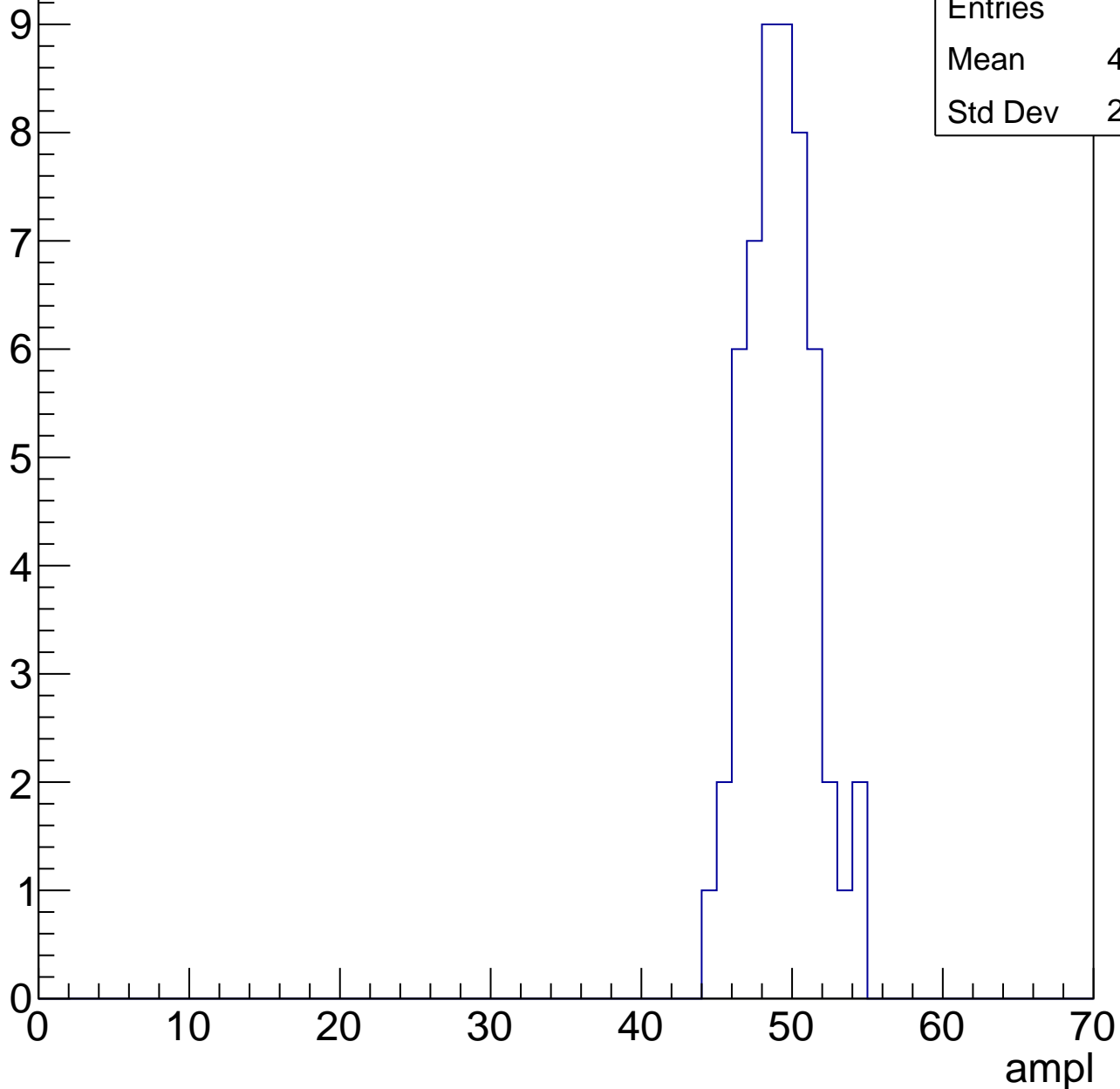
0 10 20 30 40 50 60 70



# B1L002S, U8-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



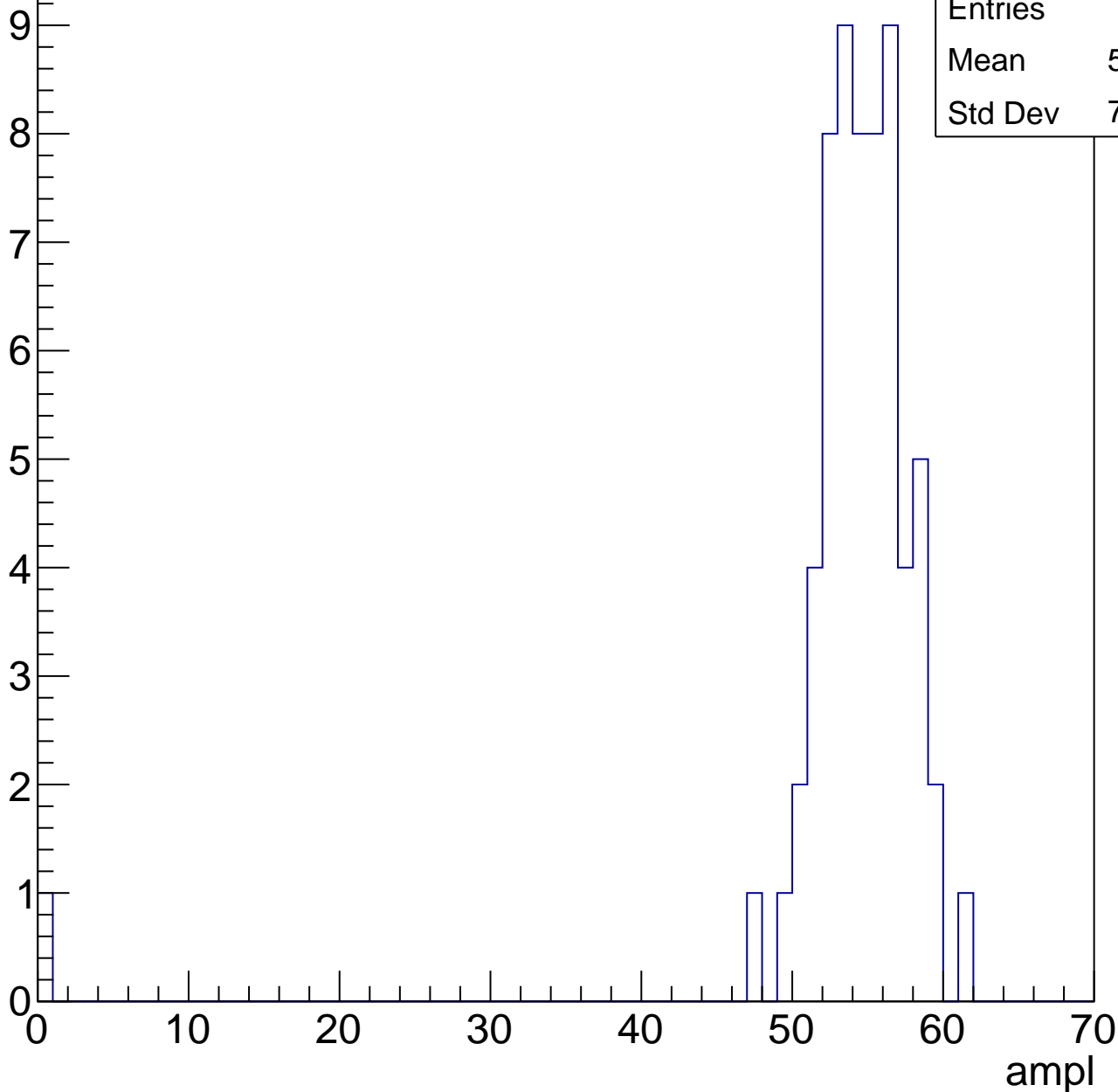
Entries	53
Mean	48.74
Std Dev	2.216

# B1L002S, U8-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

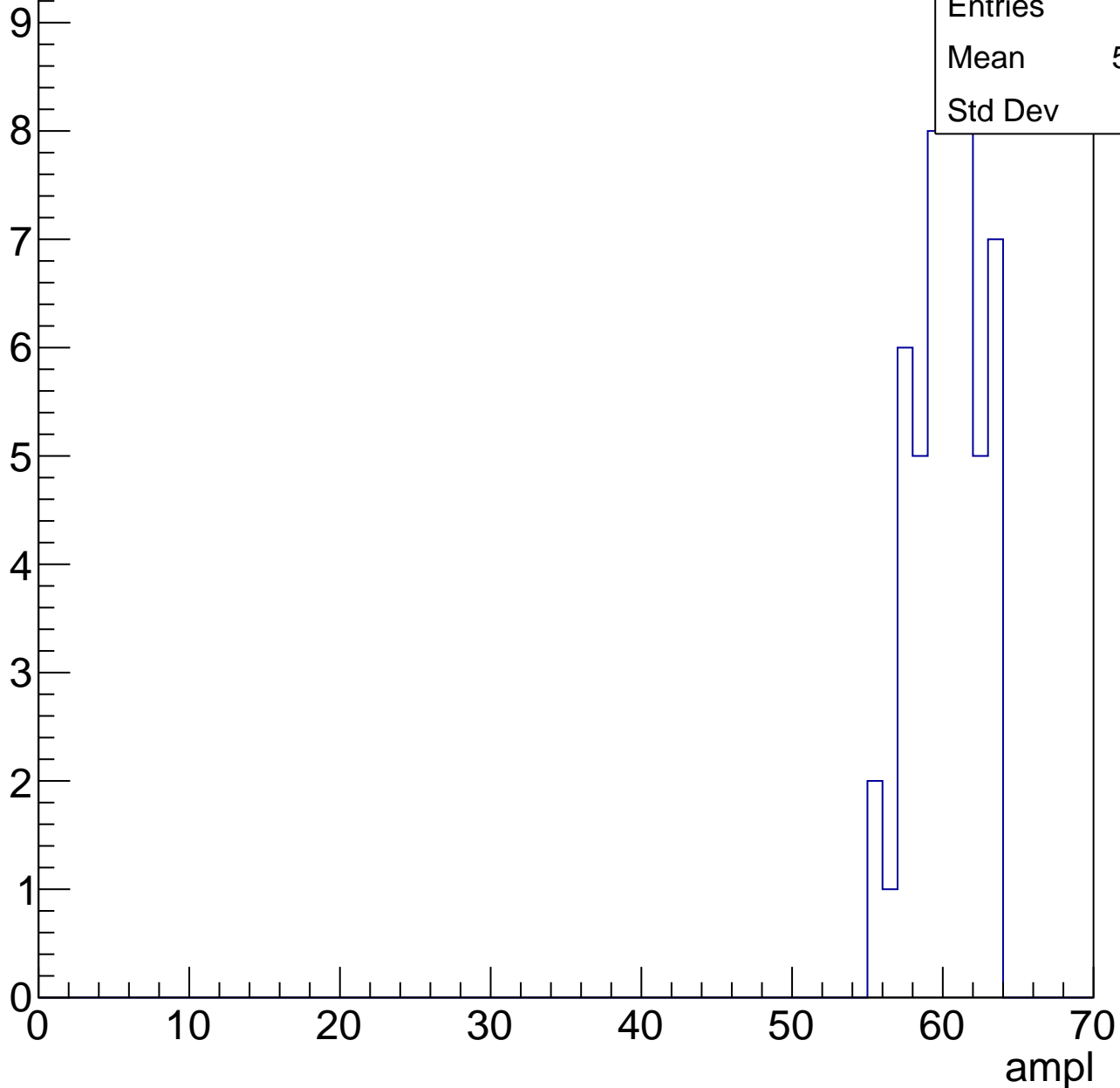
Entries	63
Mean	53.43
Std Dev	7.278



# B1L002S, U8-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

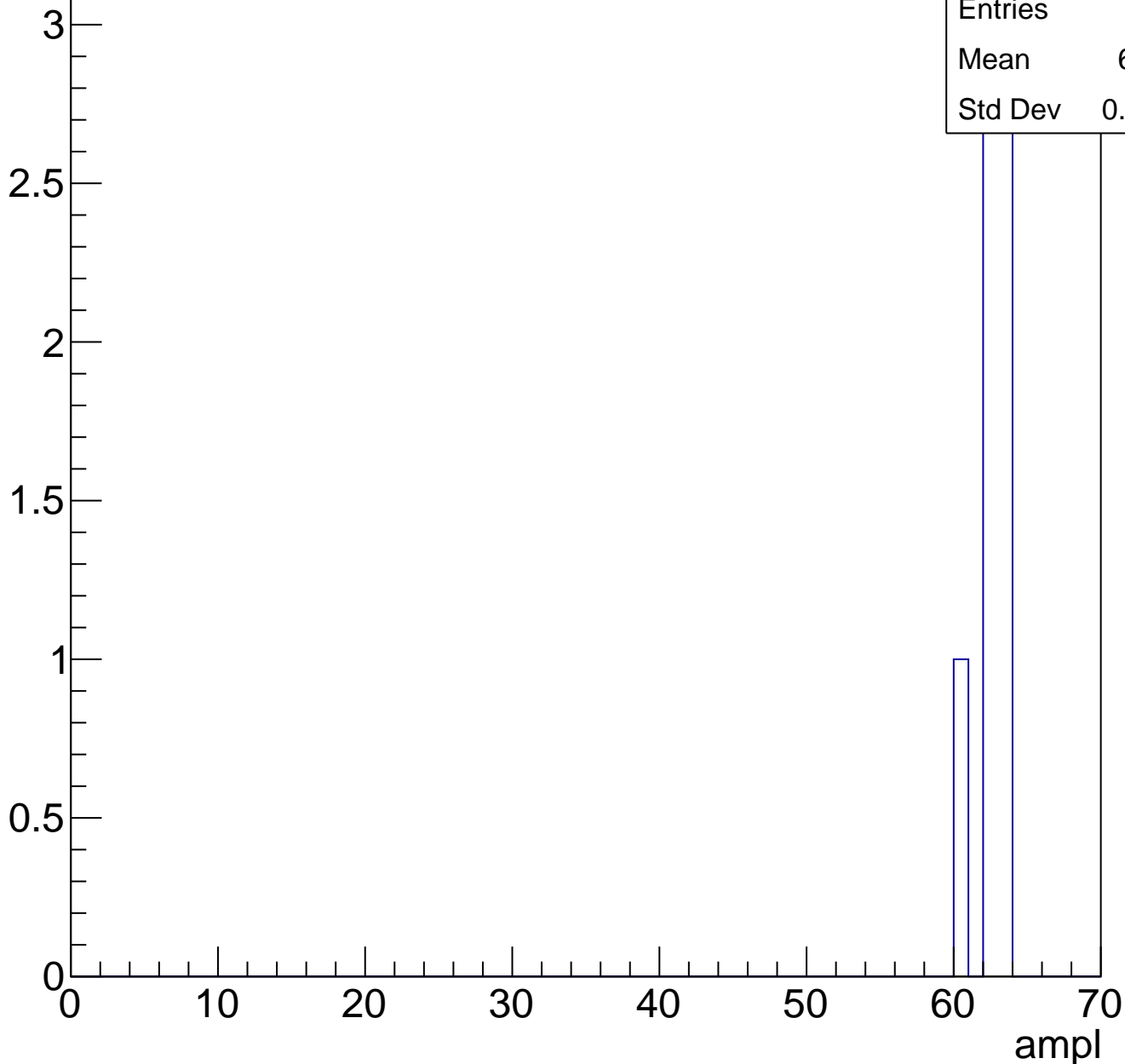
Entry



# B1L002S, U8-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch64, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	26.62
Std Dev	4.259

**Gaus mean : 27.7085**

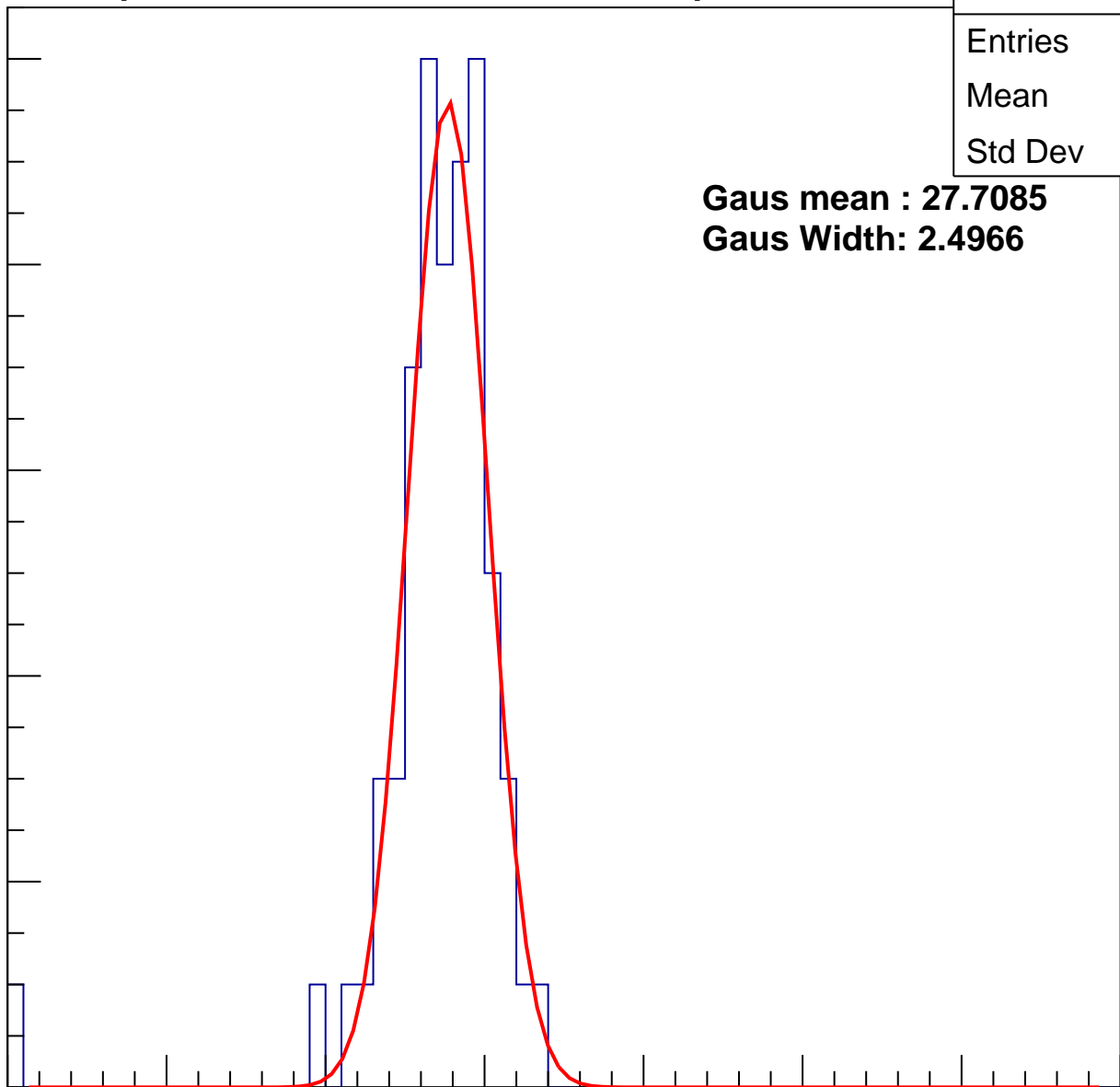
**Gaus Width: 2.4966**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch64, adc1

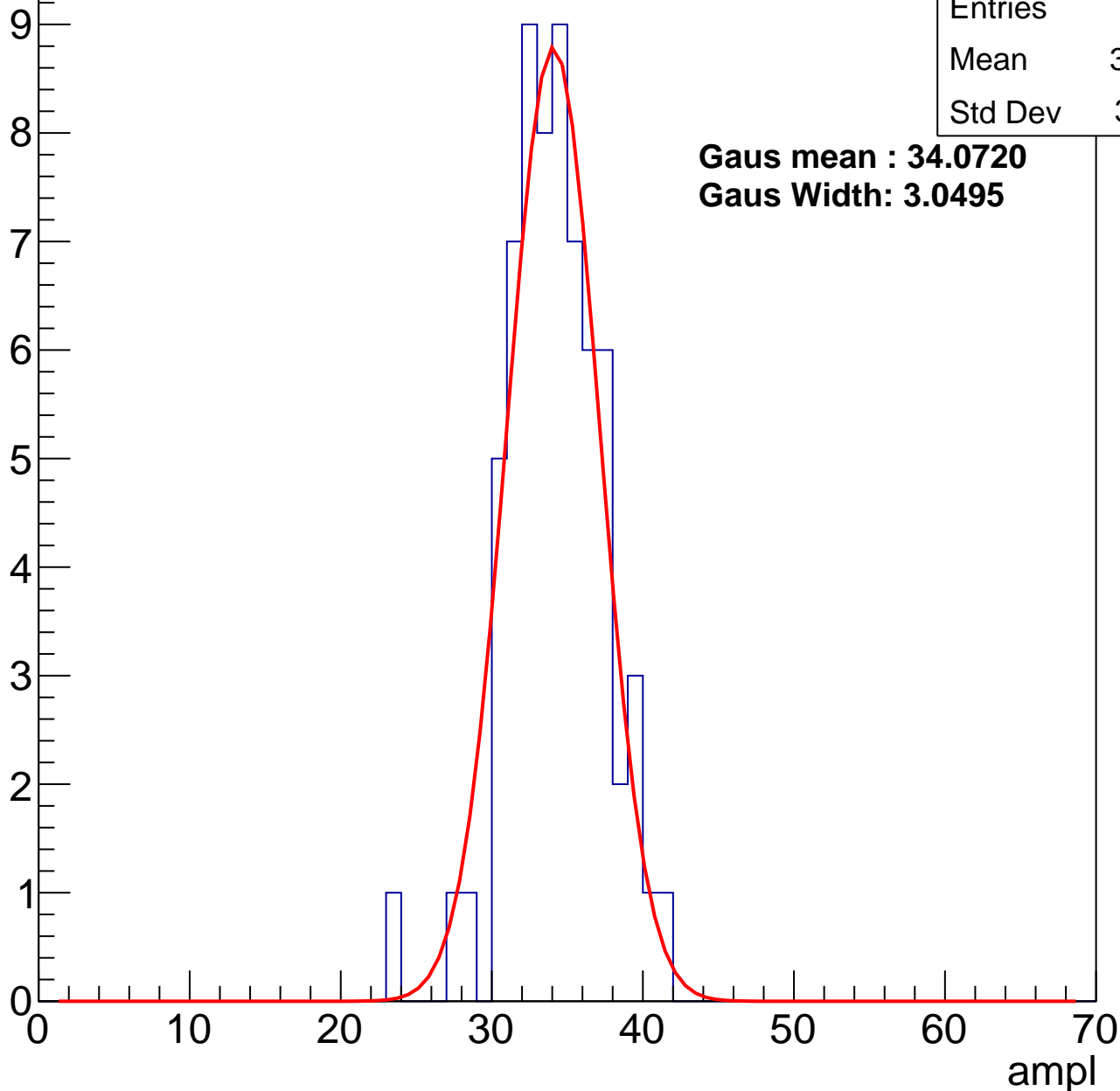
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.73
Std Dev	3.151

**Gaus mean : 34.0720**

**Gaus Width: 3.0495**



# B1L002S, U8-ch64, adc2

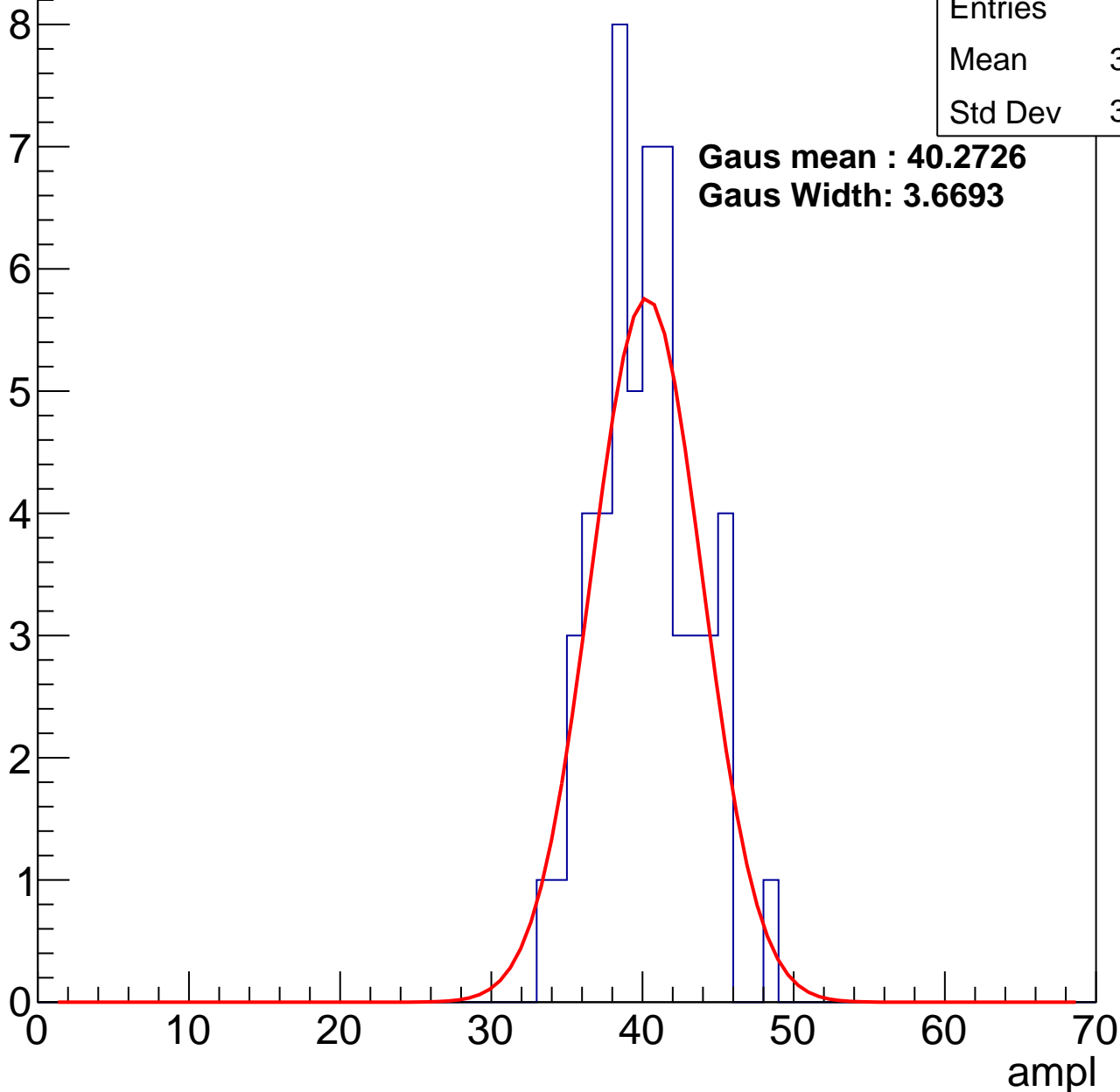
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	39.72
Std Dev	3.194

**Gaus mean : 40.2726**

**Gaus Width: 3.6693**

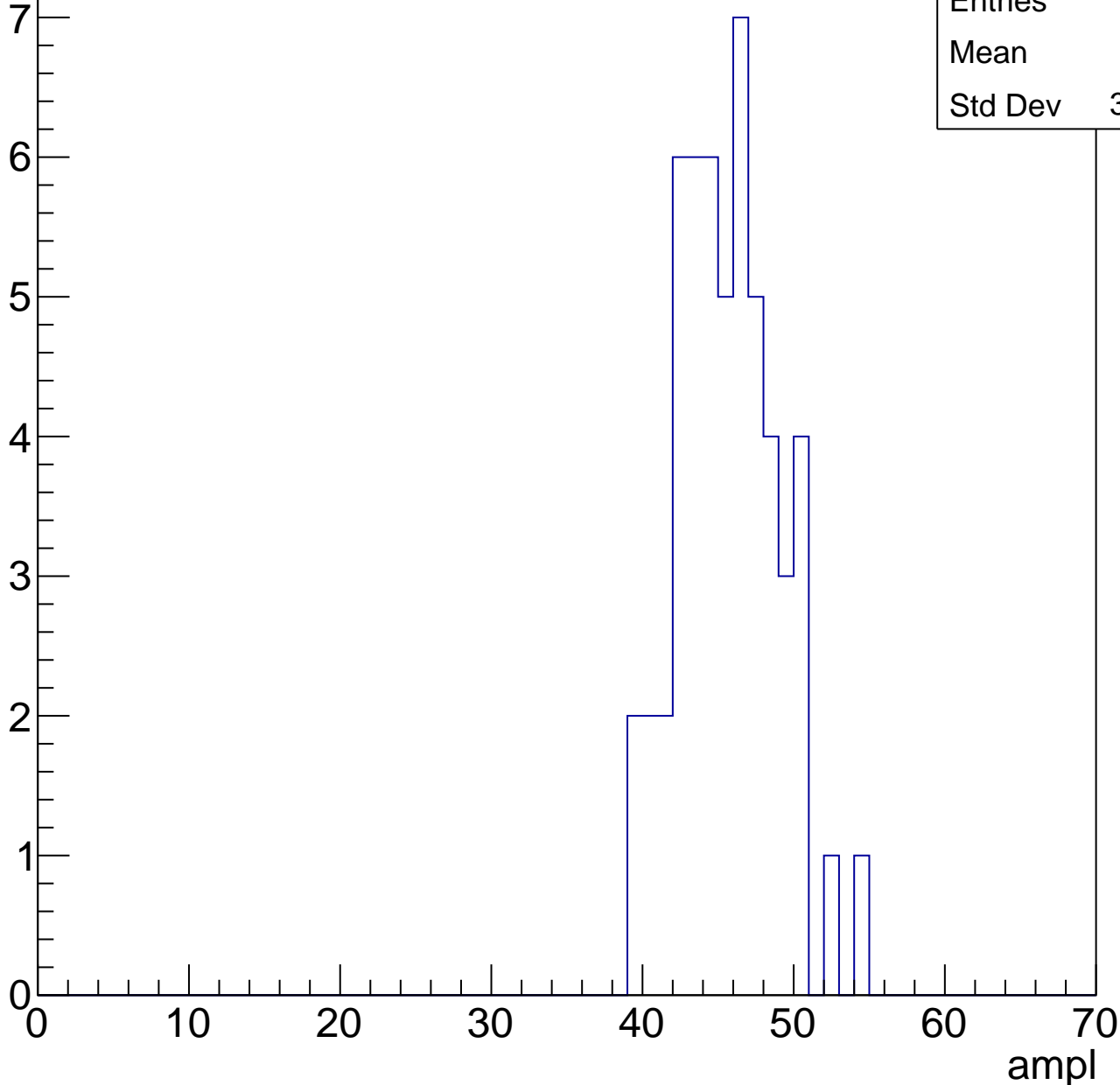


# B1L002S, U8-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

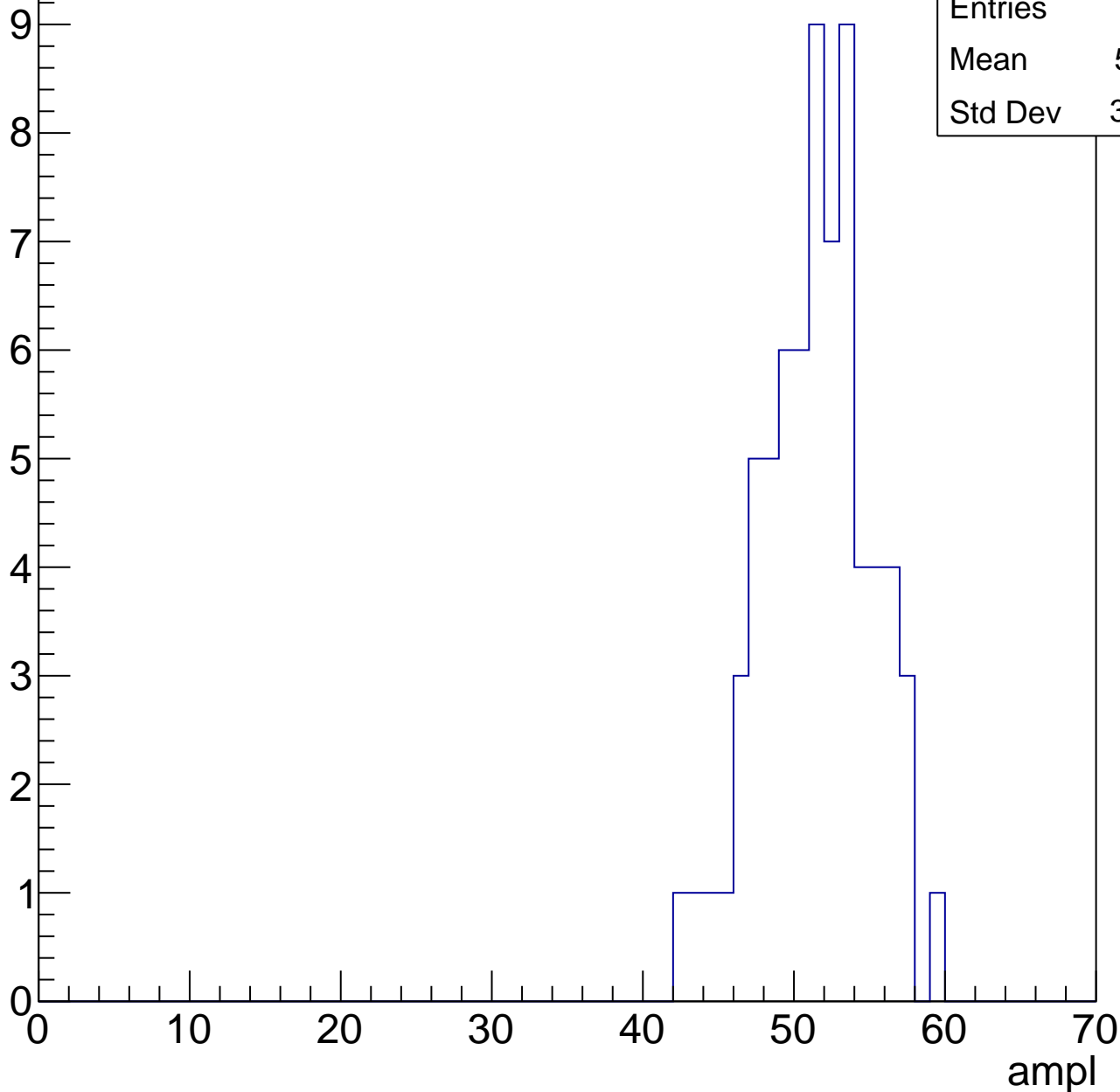
Entries	54
Mean	45.2
Std Dev	3.268



# B1L002S, U8-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



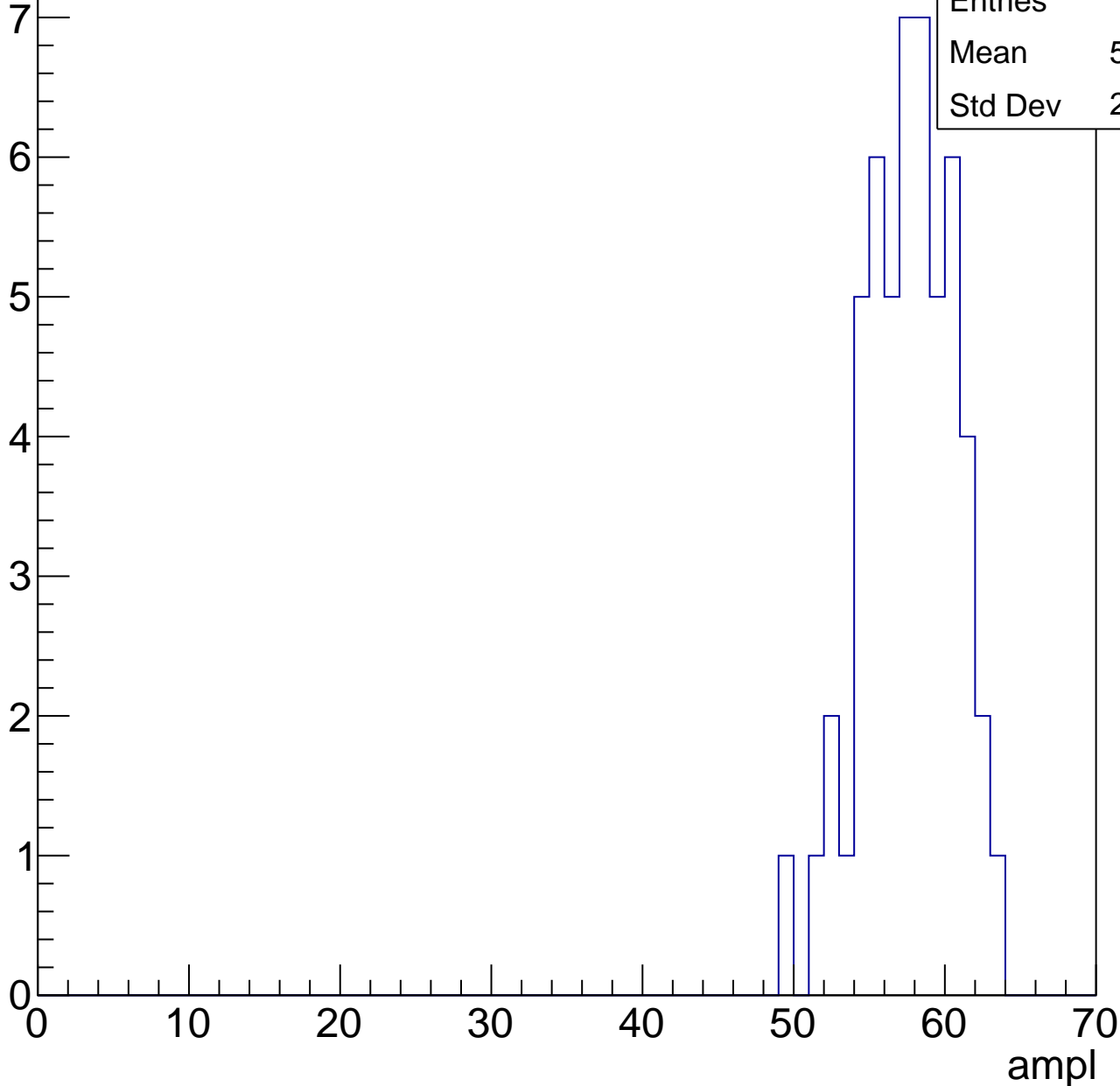
Entries	70
Mean	51.01
Std Dev	3.536

# B1L002S, U8-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.13
Std Dev	2.978

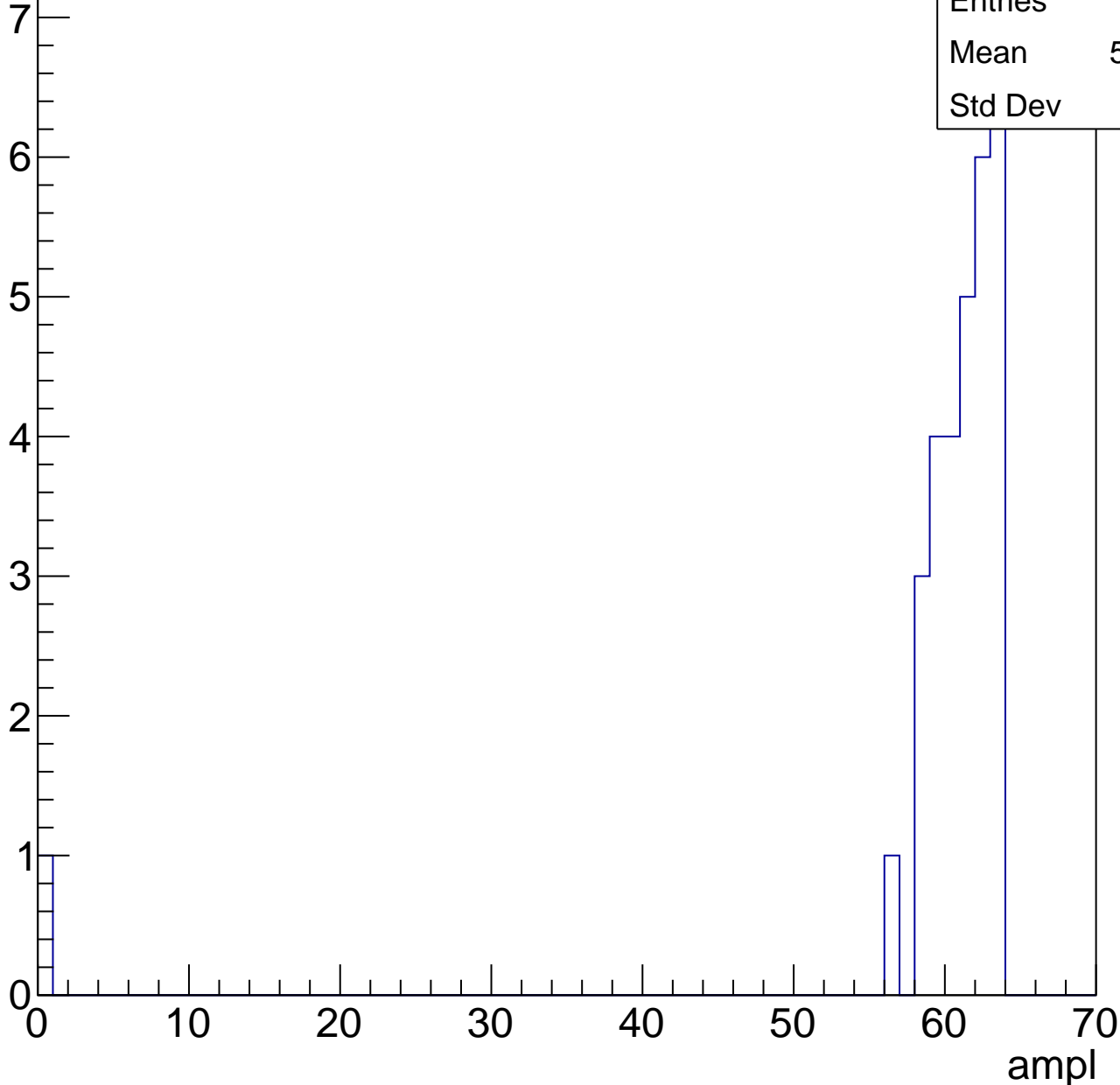


# B1L002S, U8-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.84
Std Dev	10.9

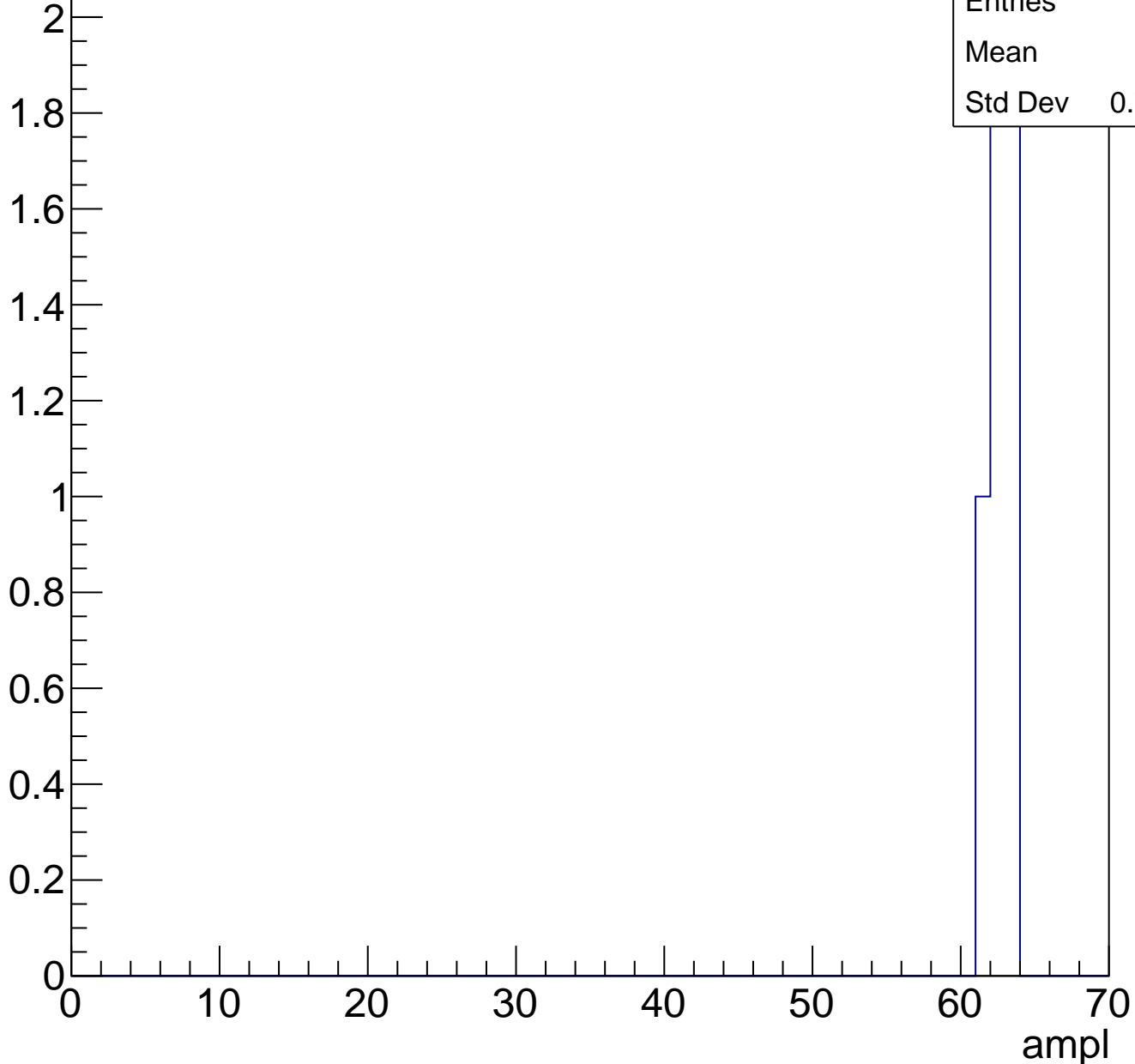




# B1L002S, U8-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch65, adc0

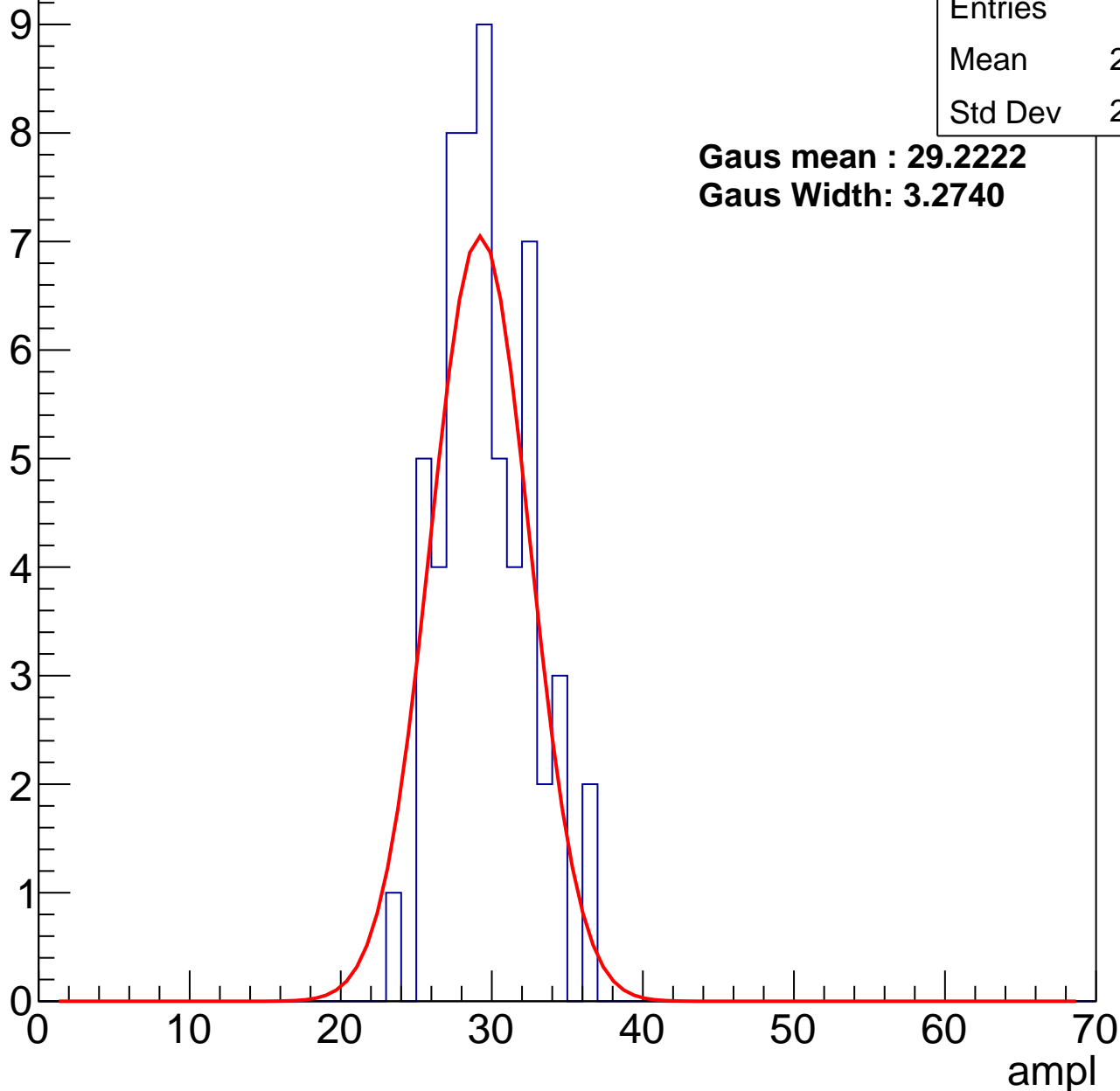
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	29.16
Std Dev	2.876

**Gaus mean : 29.2222**

**Gaus Width: 3.2740**



# B1L002S, U8-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	35.18
Std Dev	3.22

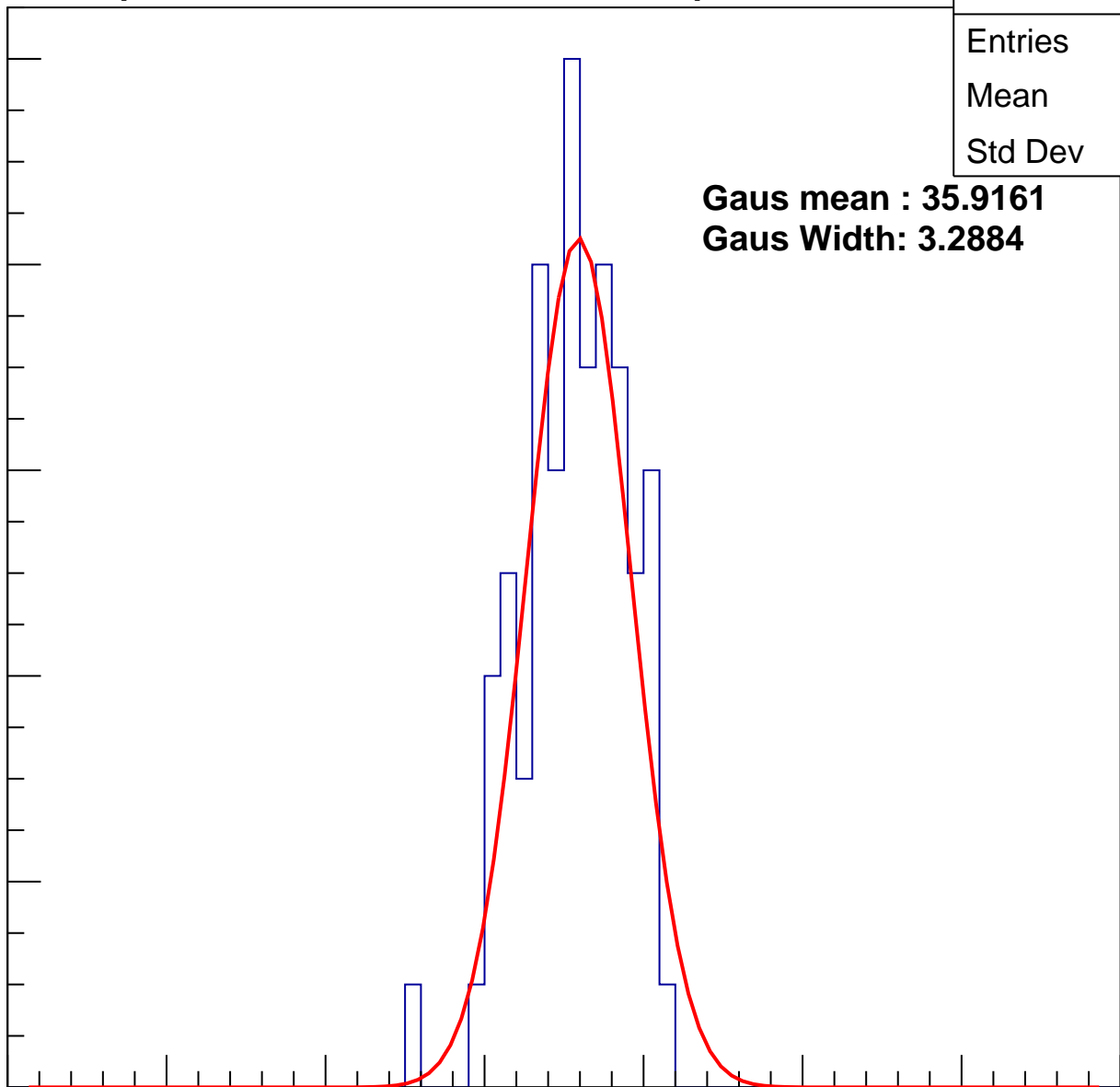
**Gaus mean : 35.9161**

**Gaus Width: 3.2884**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U8-ch65, adc2

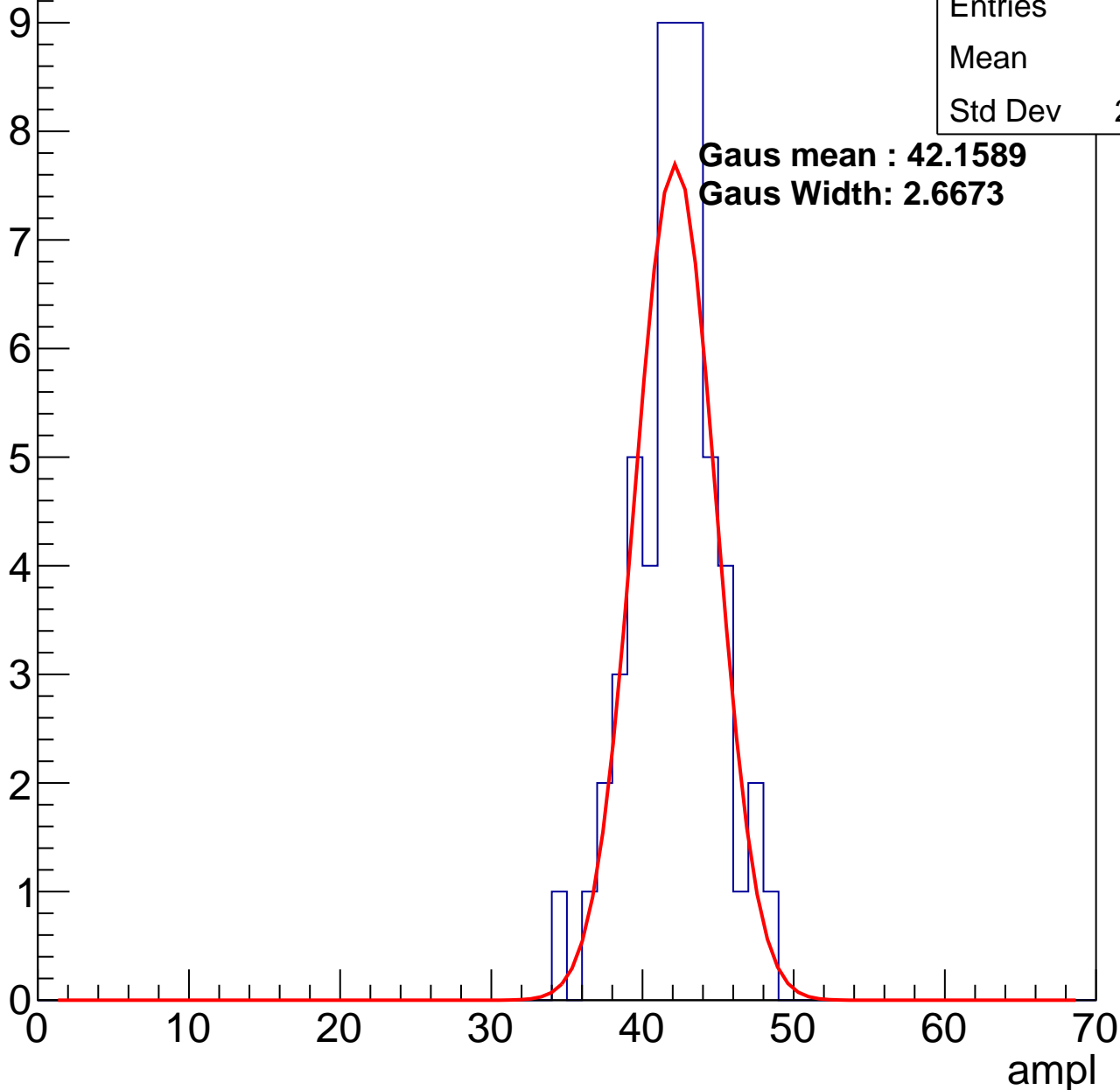
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.7
Std Dev	2.771

**Gaus mean : 42.1589**

**Gaus Width: 2.6673**

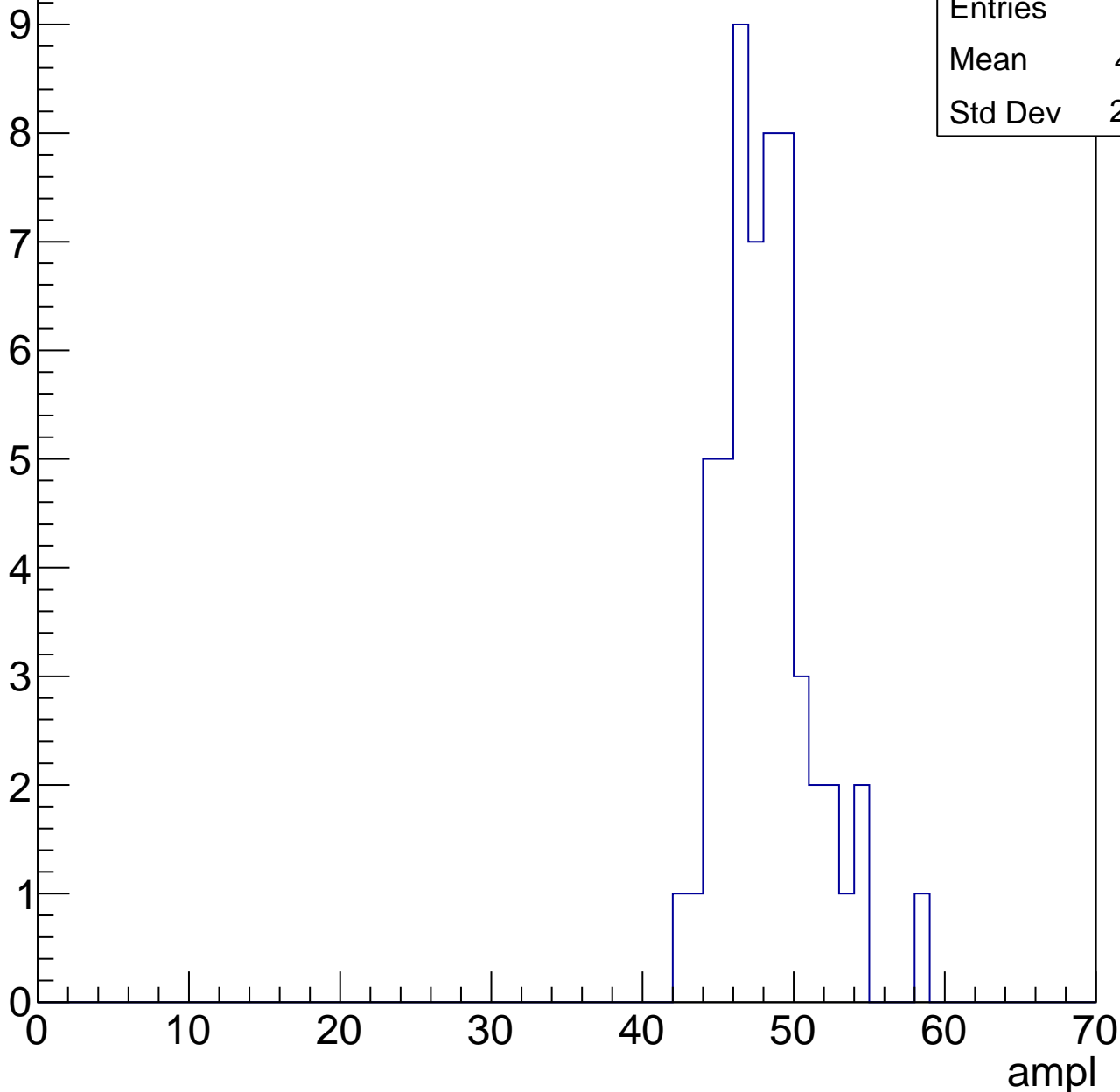


# B1L002S, U8-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

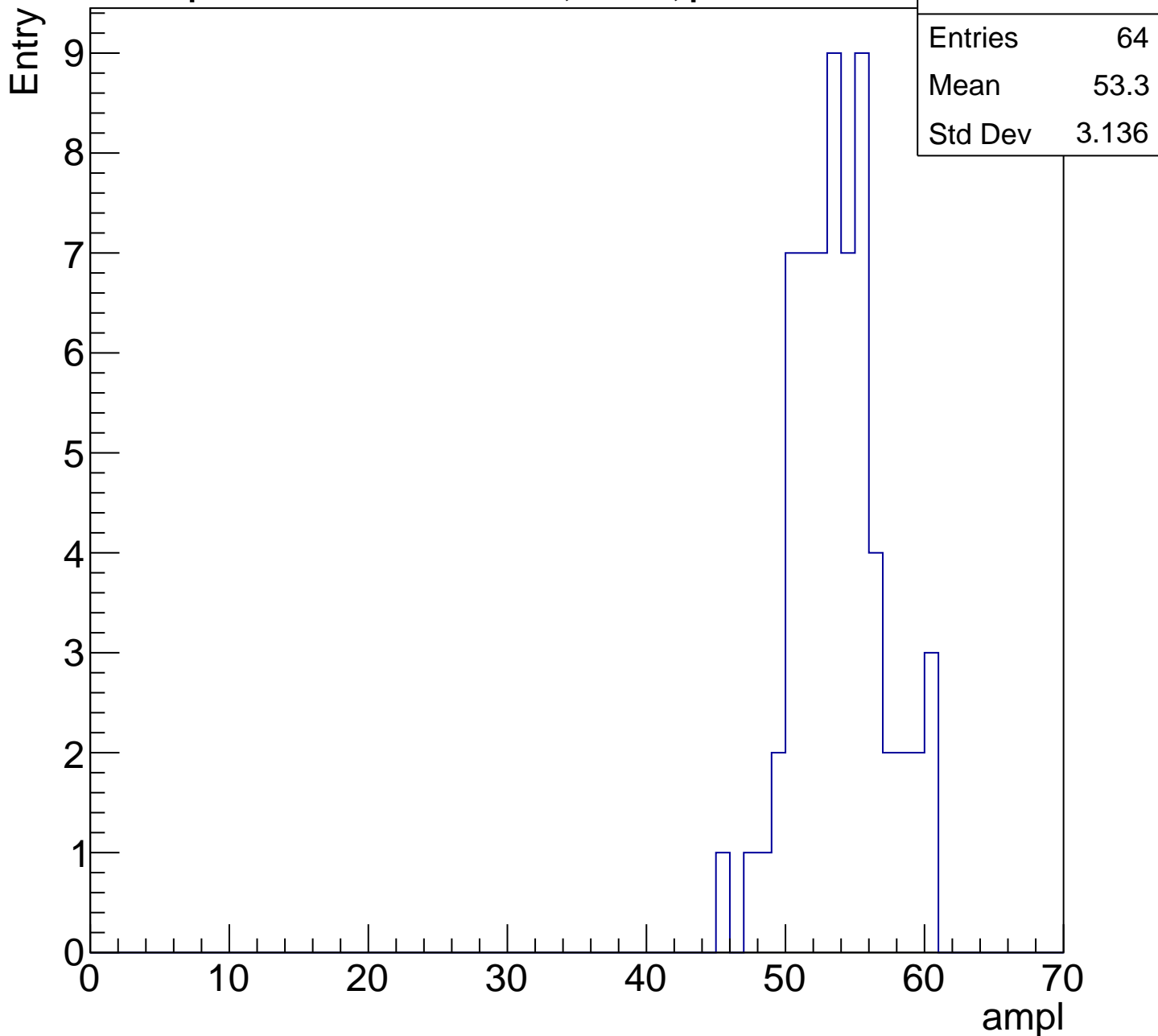
Entry

Entries	55
Mean	47.71
Std Dev	2.989



# B1L002S, U8-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

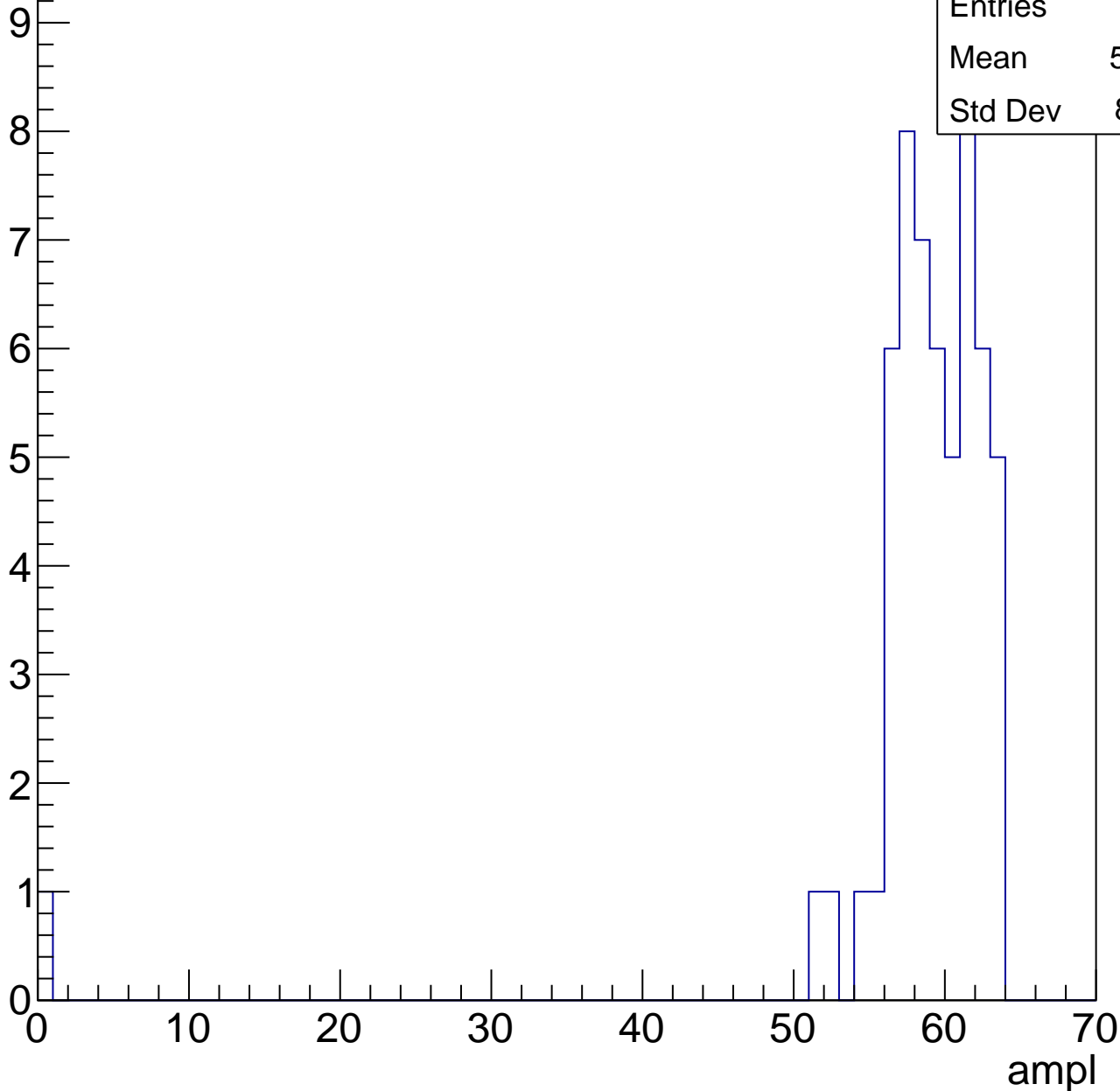


# B1L002S, U8-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.89
Std Dev	8.201

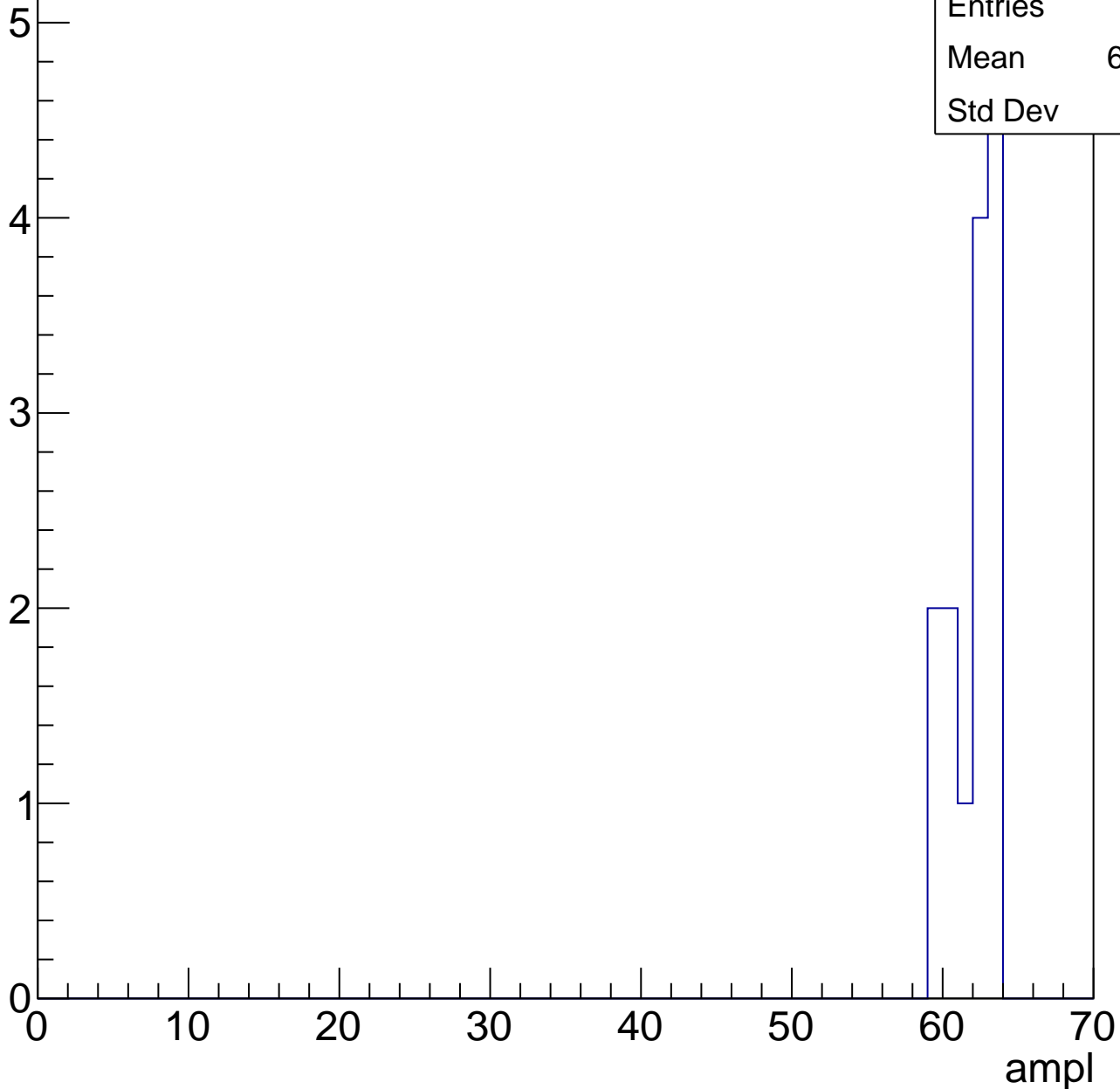


# B1L002S, U8-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.57
Std Dev	1.45





# B1L002S, U8-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch66, adc0

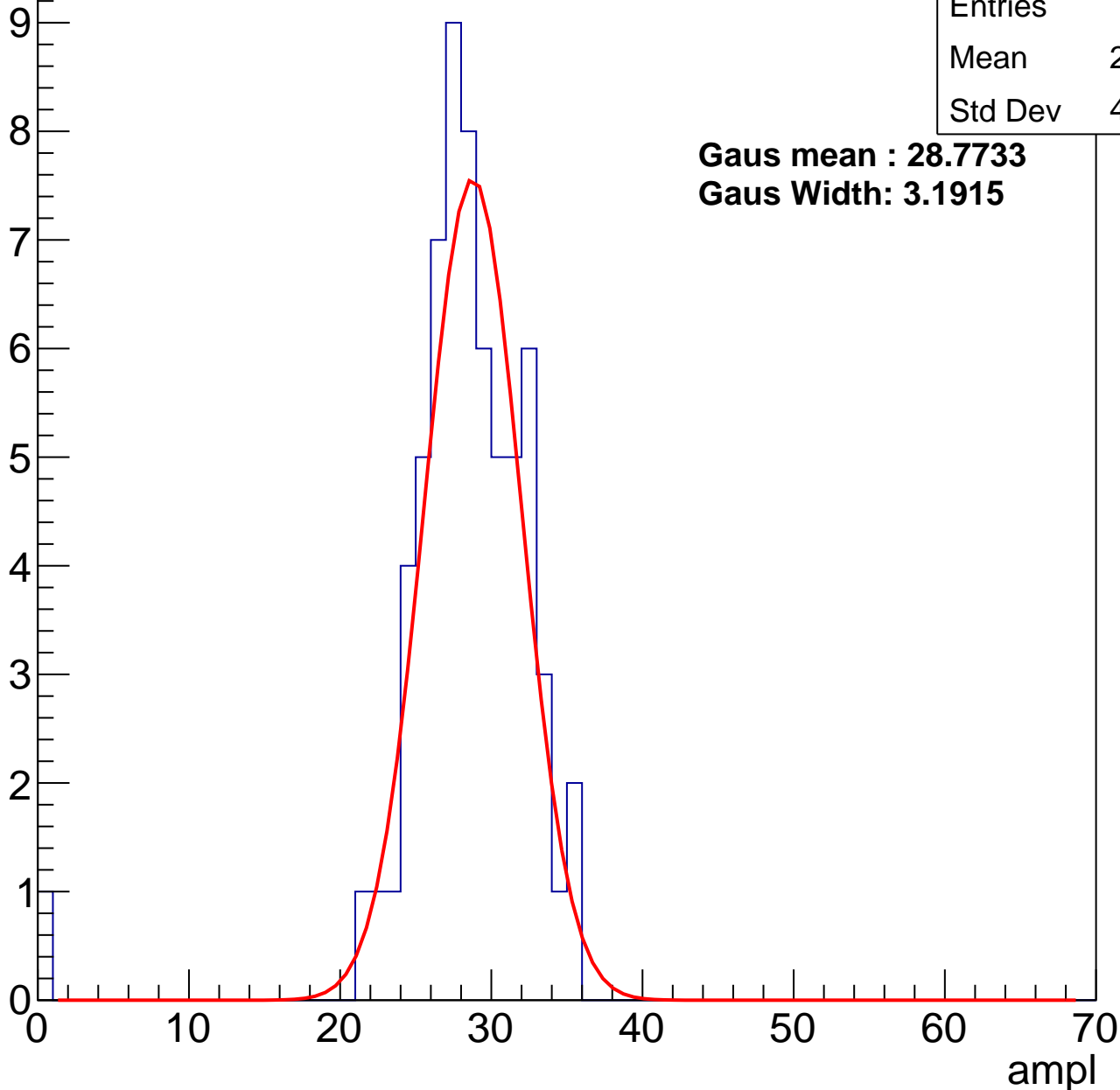
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.85
Std Dev	4.665

**Gaus mean : 28.7733**

**Gaus Width: 3.1915**



# B1L002S, U8-ch66, adc1

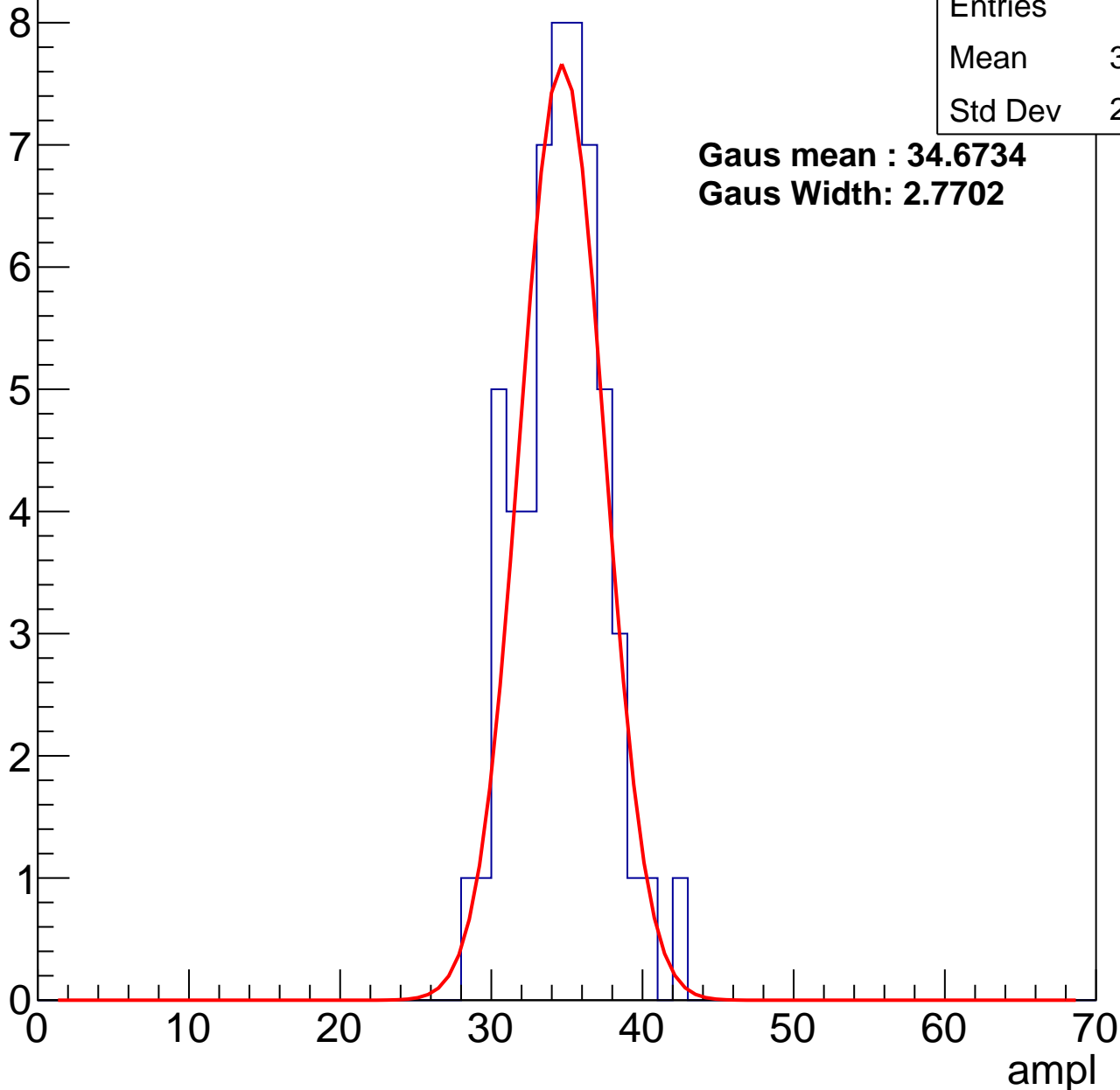
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.18
Std Dev	2.842

**Gaus mean : 34.6734**

**Gaus Width: 2.7702**



# B1L002S, U8-ch66, adc2

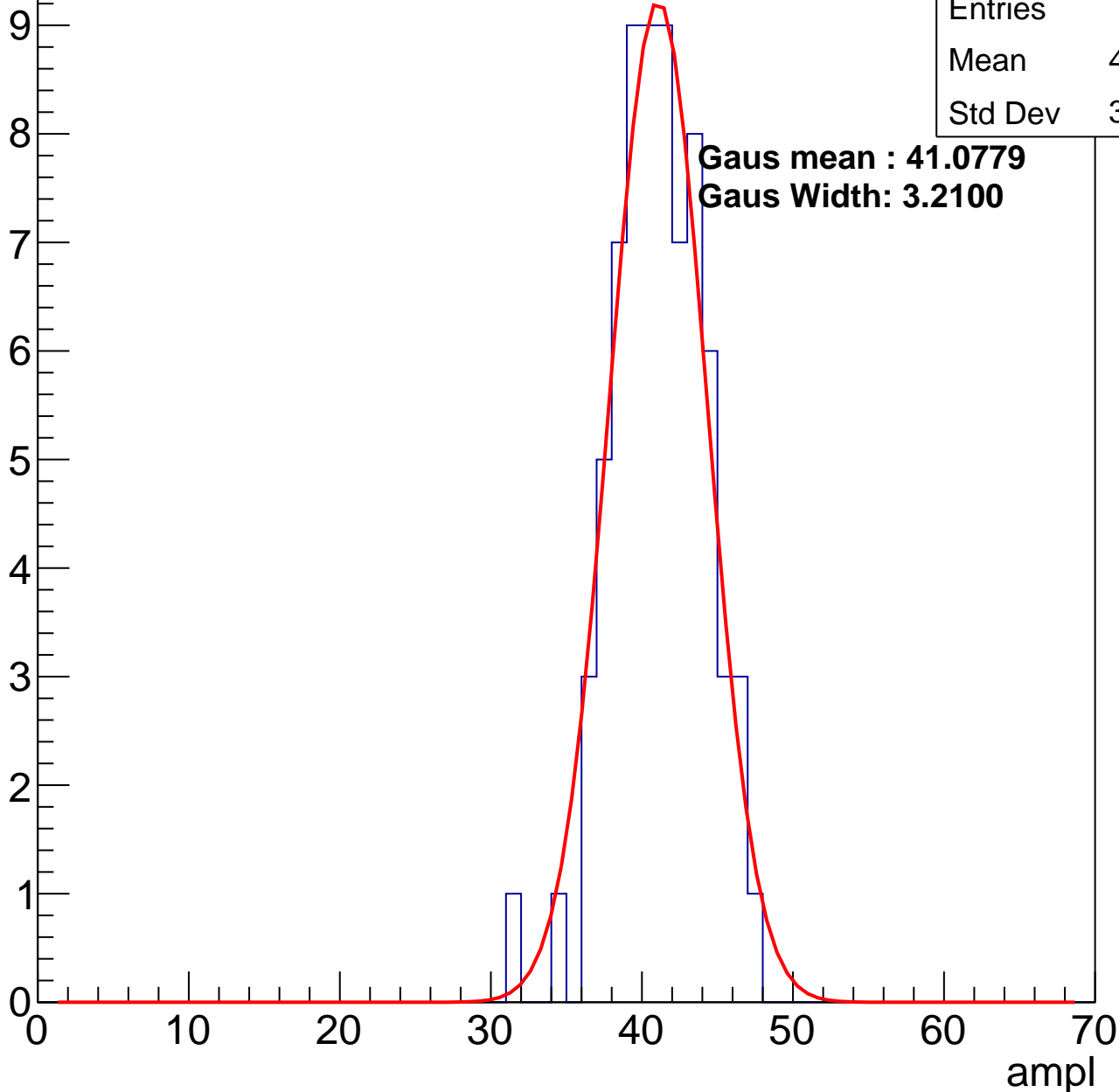
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	40.64
Std Dev	3.015

**Gaus mean : 41.0779**

**Gaus Width: 3.2100**

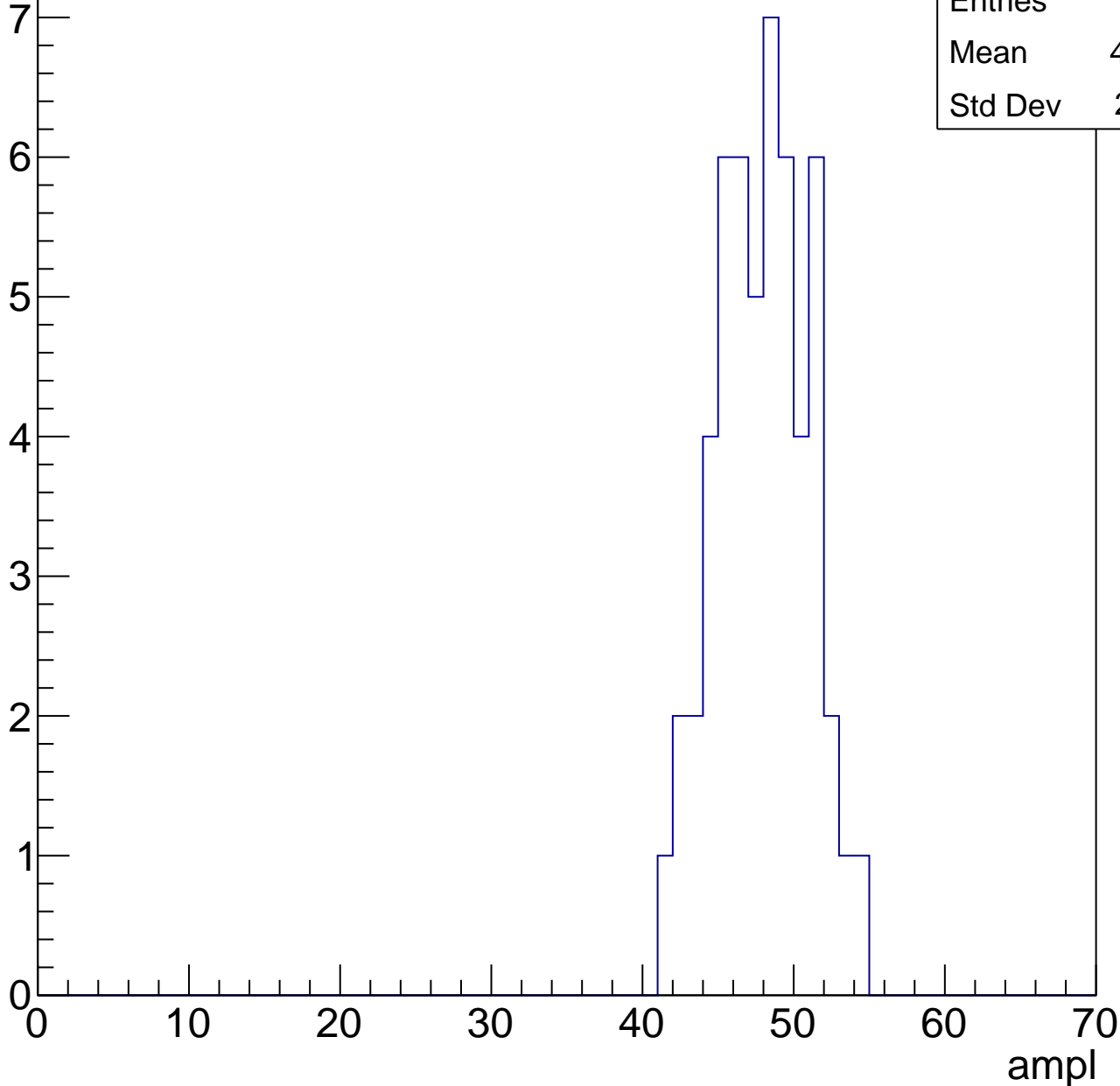


# B1L002S, U8-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

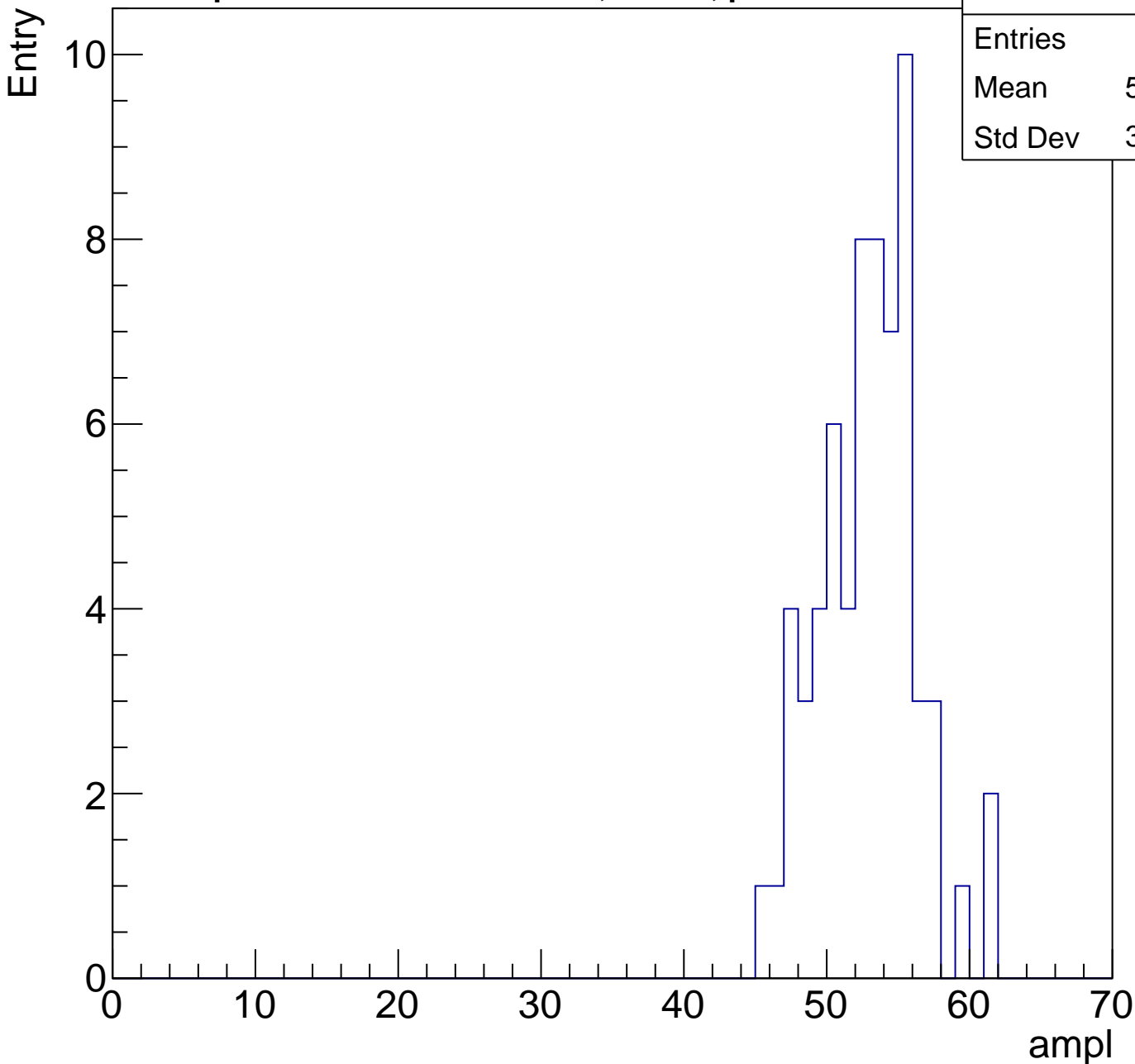
Entries	53
Mean	47.45
Std Dev	2.981



# B1L002S, U8-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

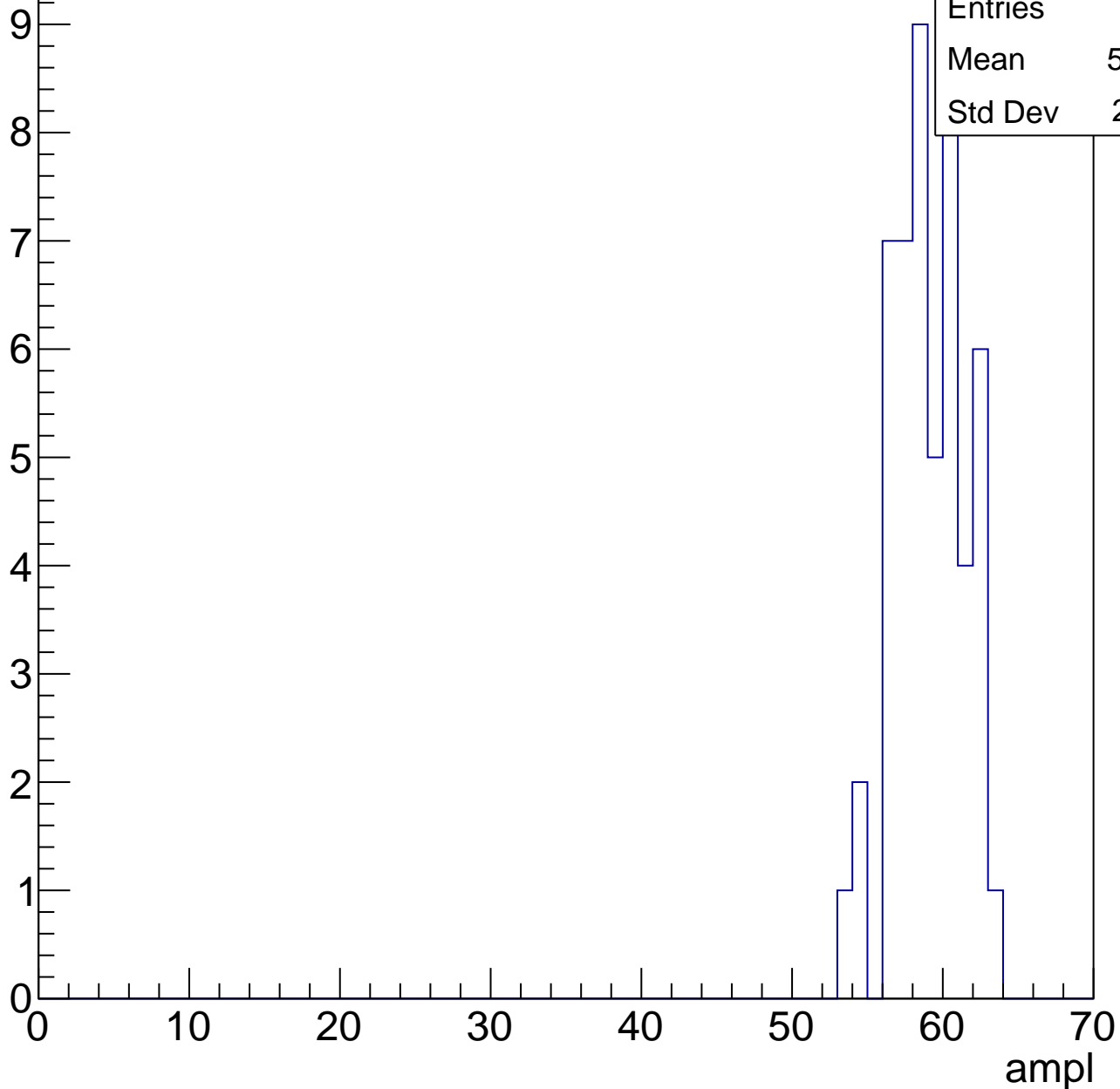
Entries	65
Mean	52.48
Std Dev	3.379



# B1L002S, U8-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

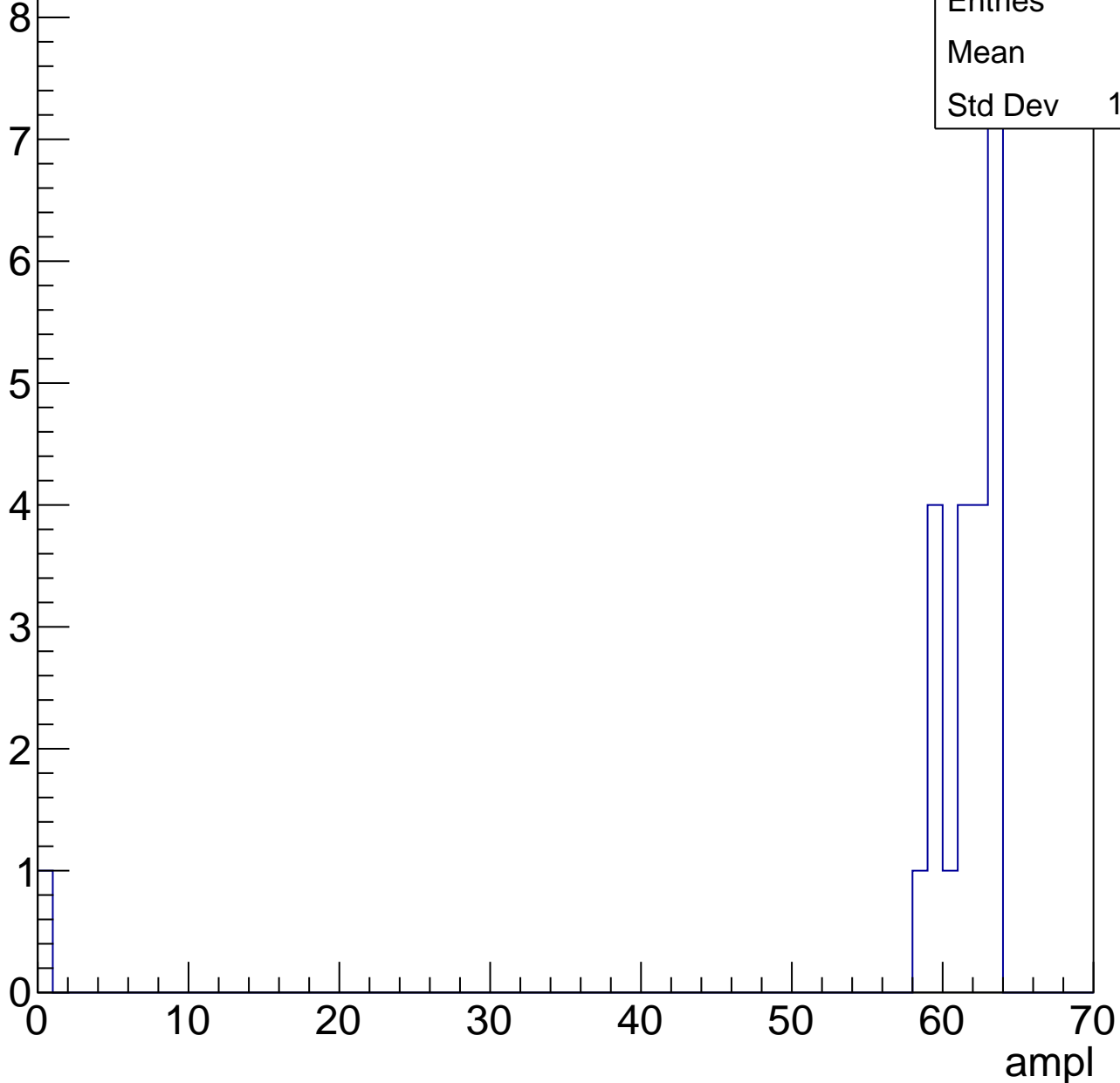


# B1L002S, U8-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.7
Std Dev	12.62

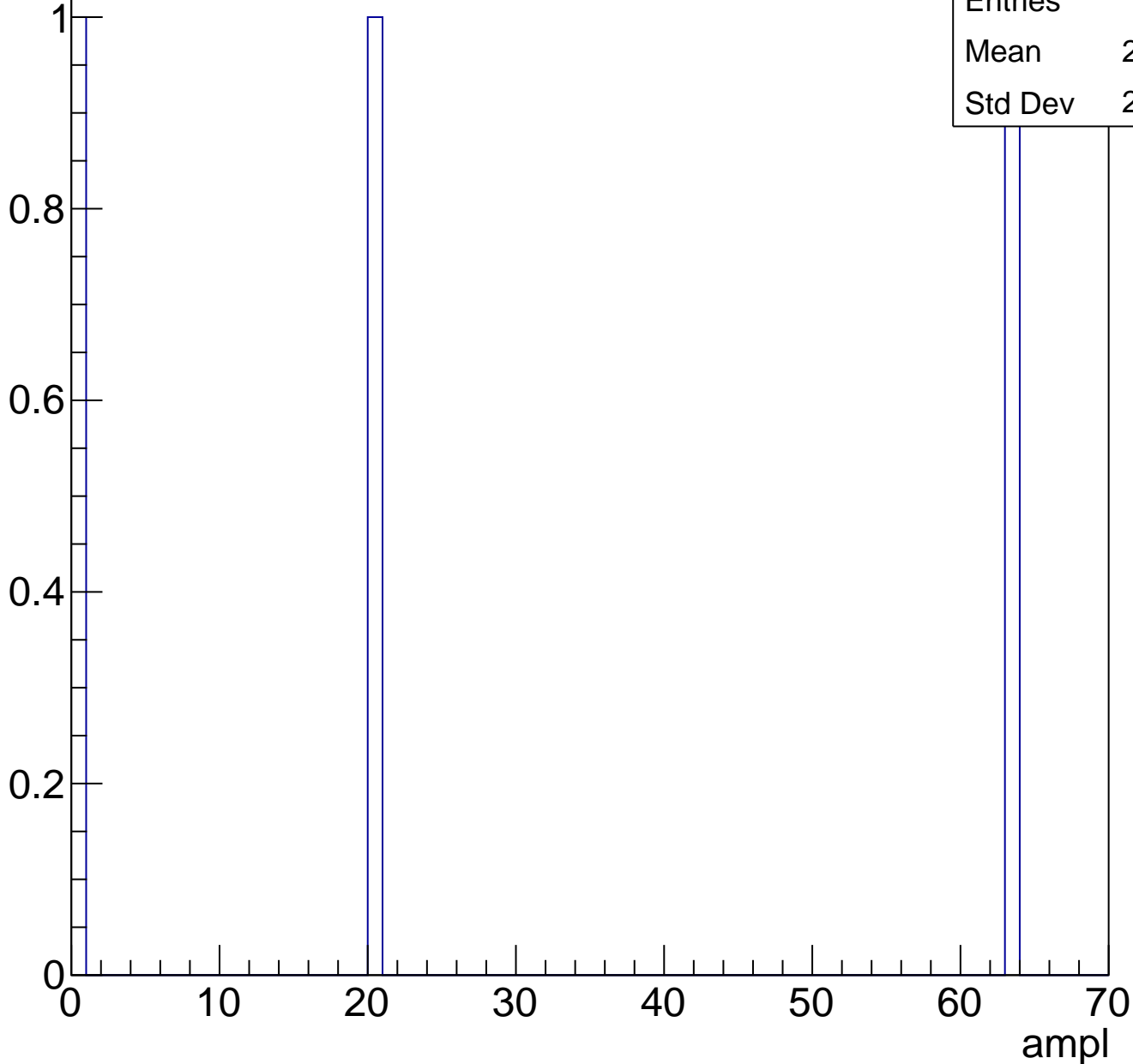




# B1L002S, U8-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	26.28

# B1L002S, U8-ch67, adc0

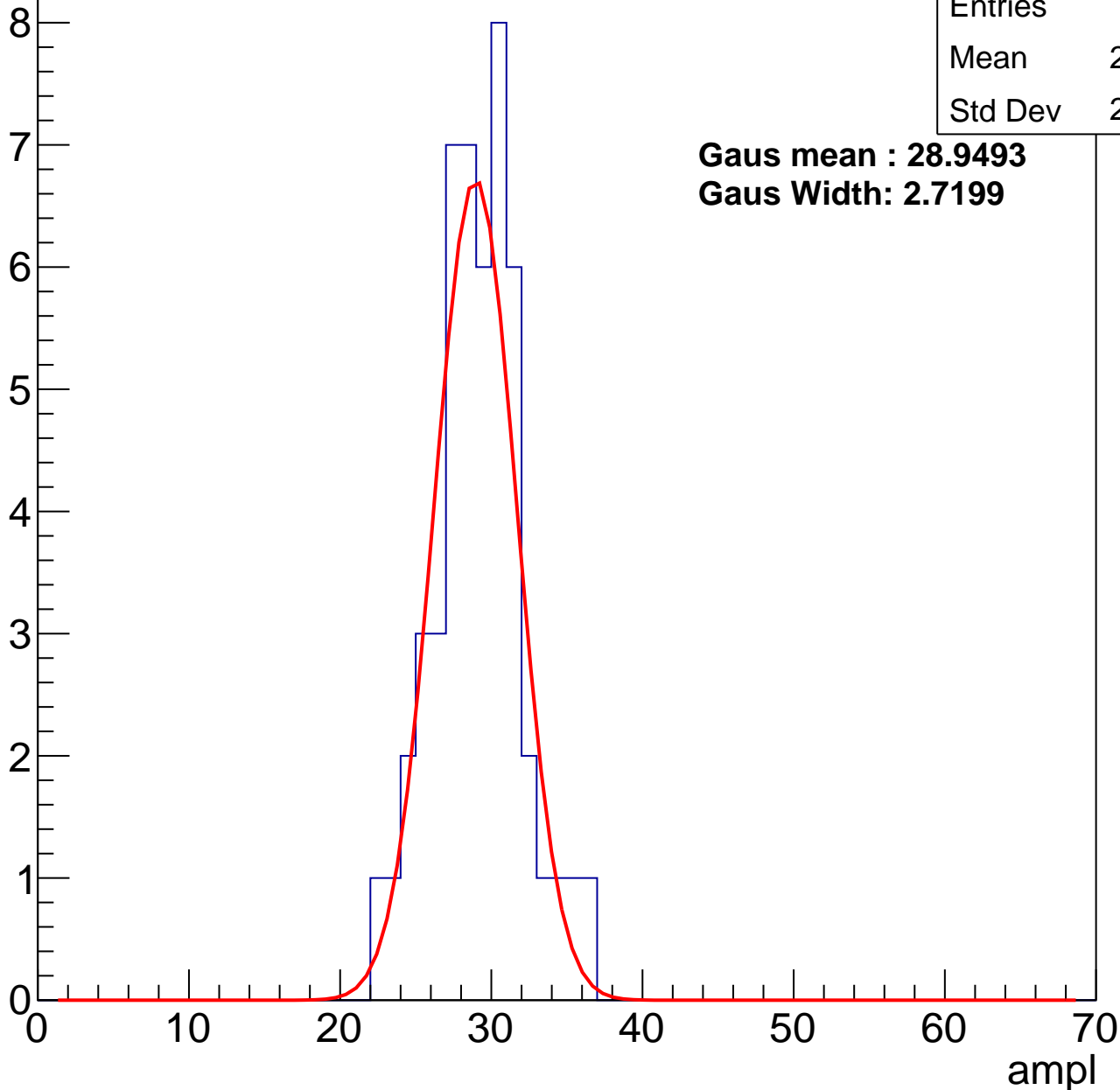
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	28.66
Std Dev	2.882

**Gaus mean : 28.9493**

**Gaus Width: 2.7199**



# B1L002S, U8-ch67, adc1

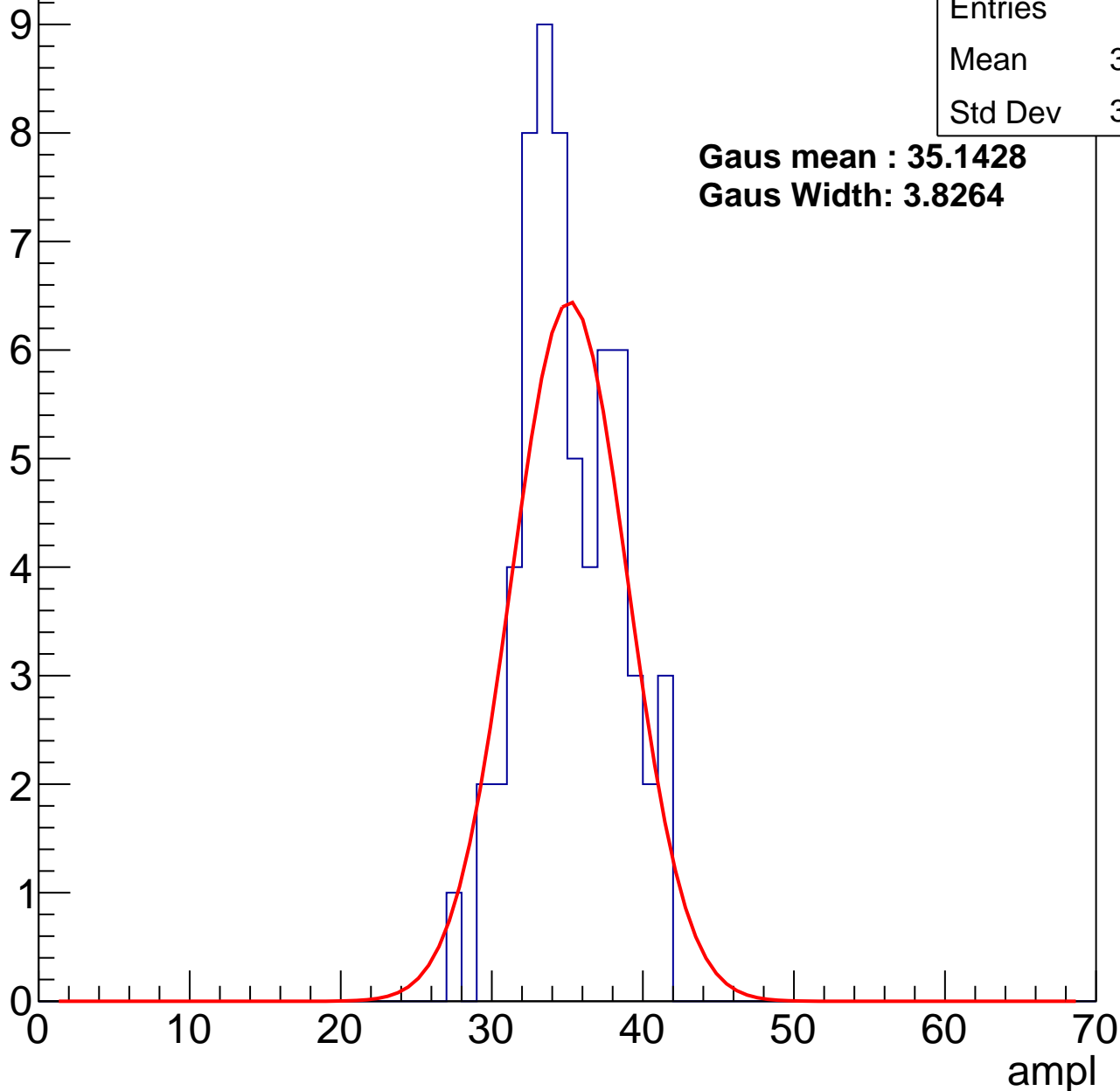
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.65
Std Dev	3.203

**Gaus mean : 35.1428**

**Gaus Width: 3.8264**



# B1L002S, U8-ch67, adc2

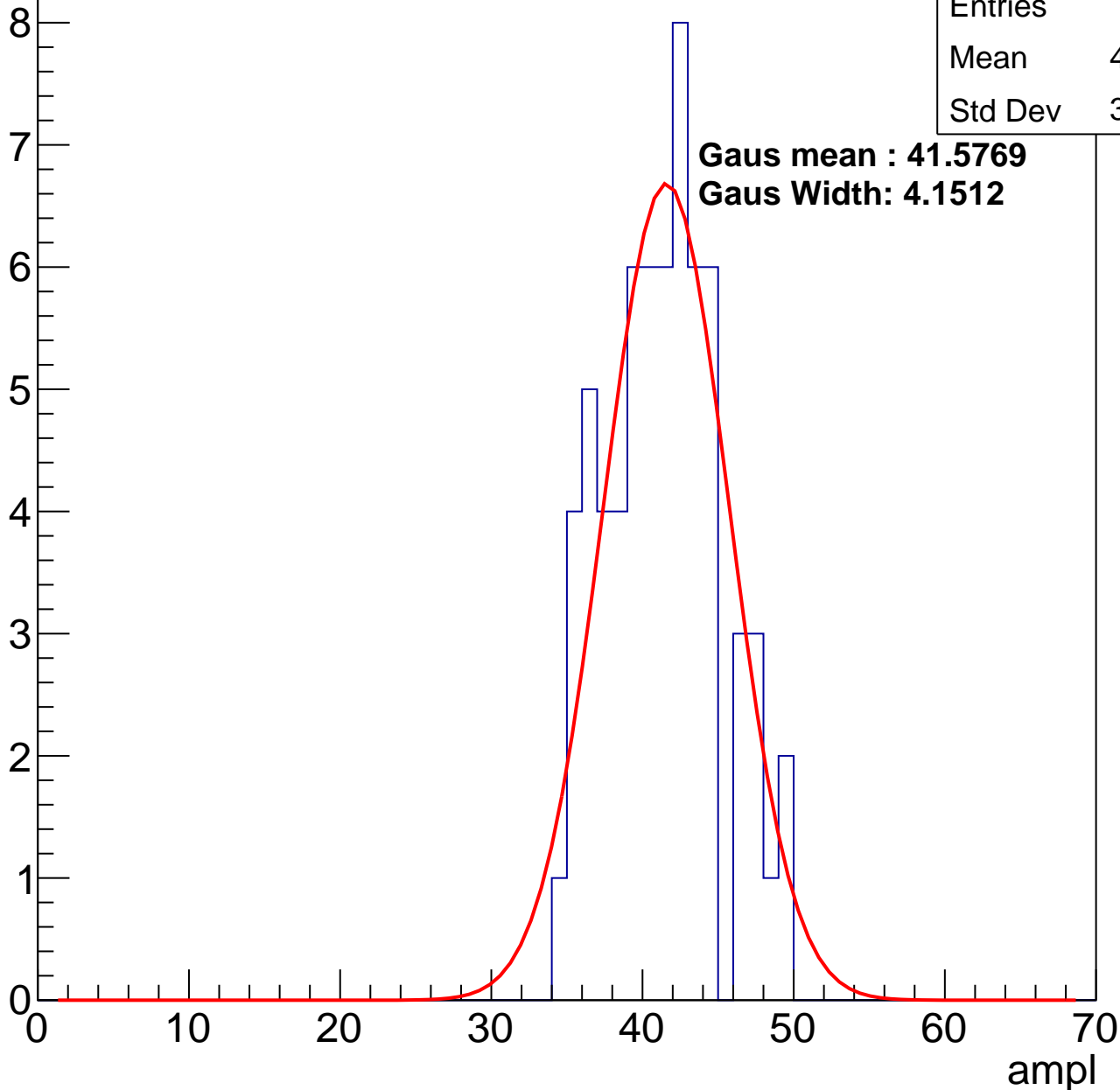
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.88
Std Dev	3.707

**Gaus mean : 41.5769**

**Gaus Width: 4.1512**



# B1L002S, U8-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

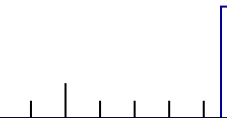
Entries	68
Mean	46.94
Std Dev	3.329

Entry

10  
8  
6  
4  
2  
0

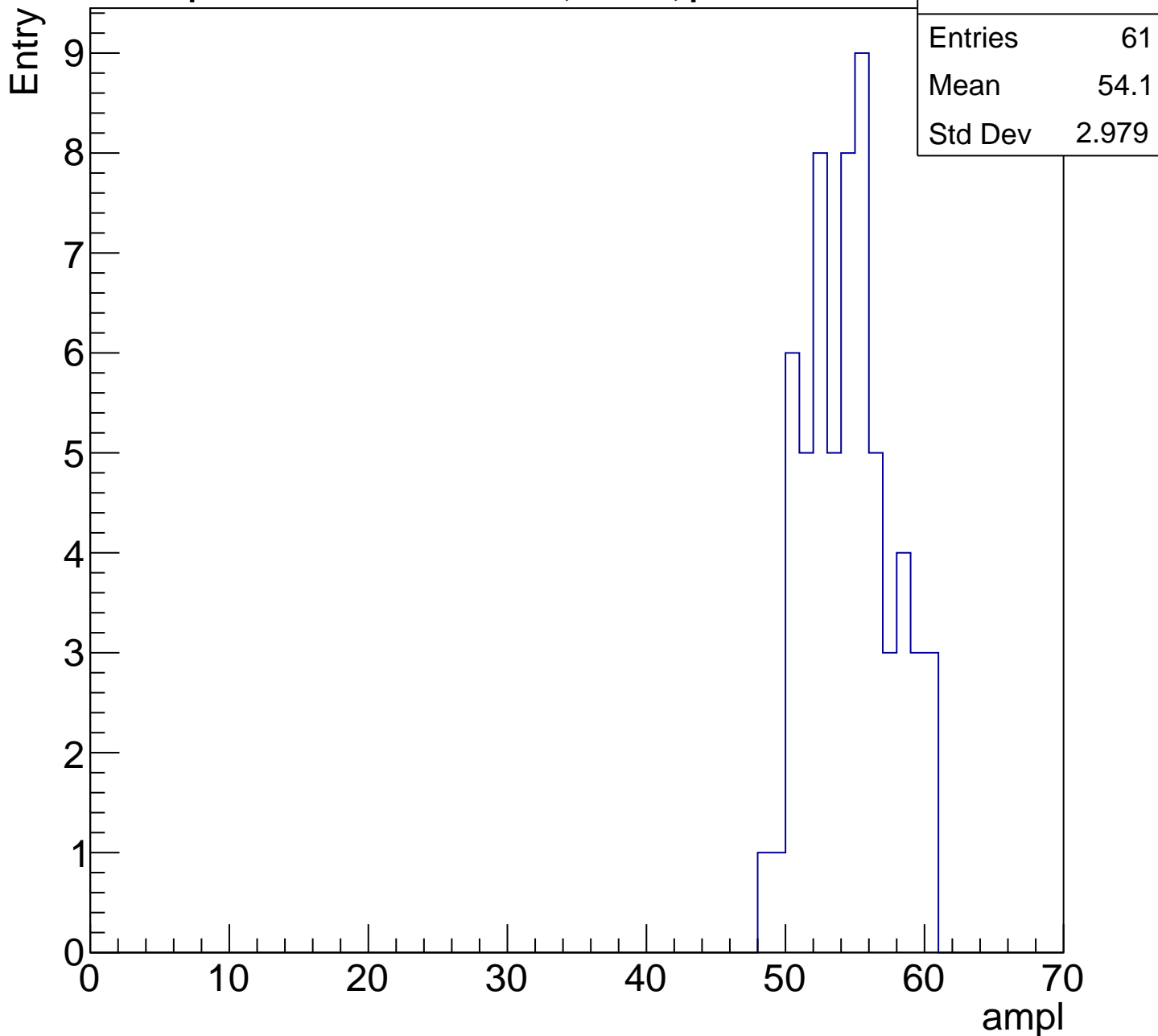
0 10 20 30 40 50 60 70

ampl



# B1L002S, U8-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

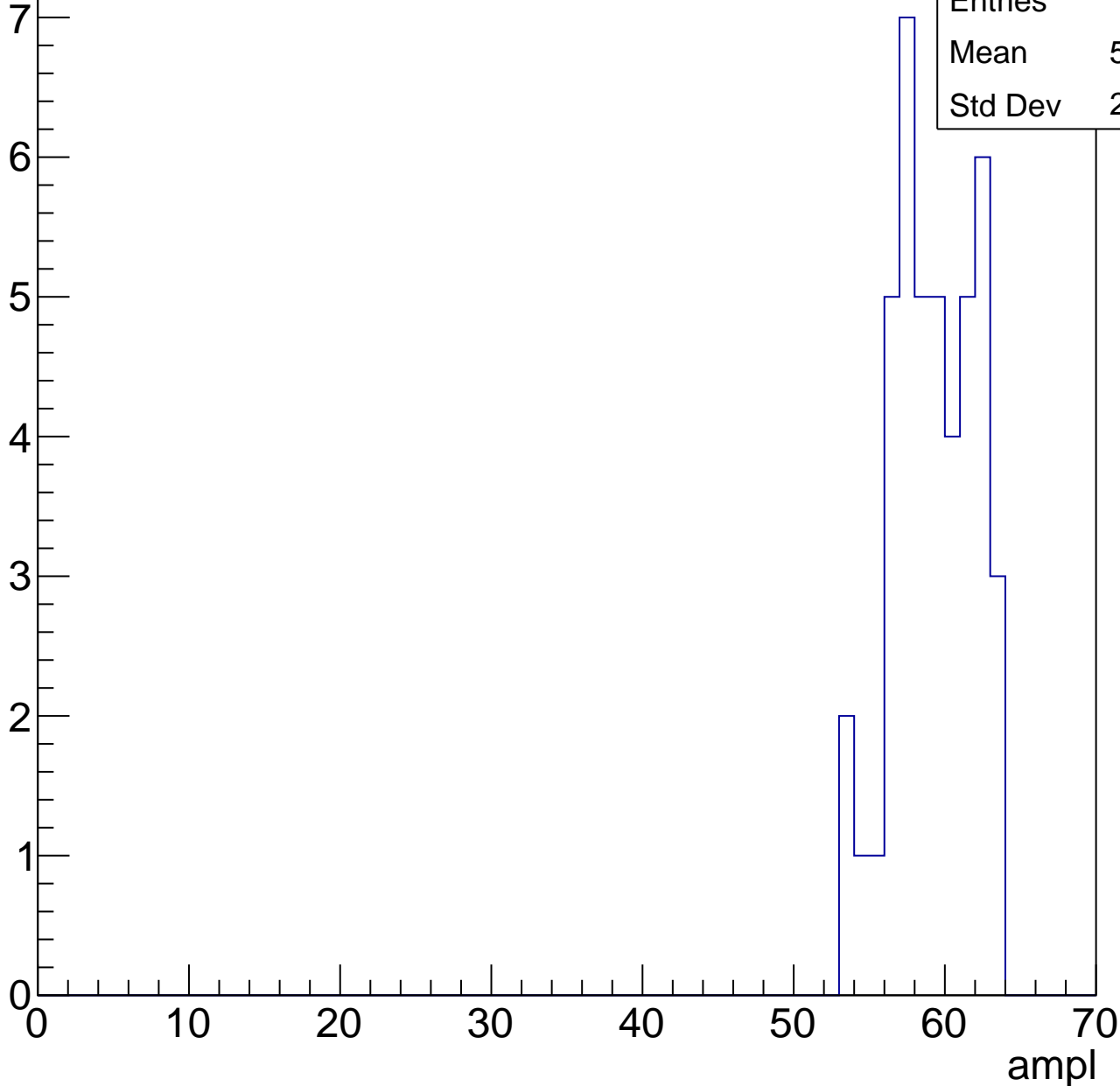


# B1L002S, U8-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.75
Std Dev	2.672

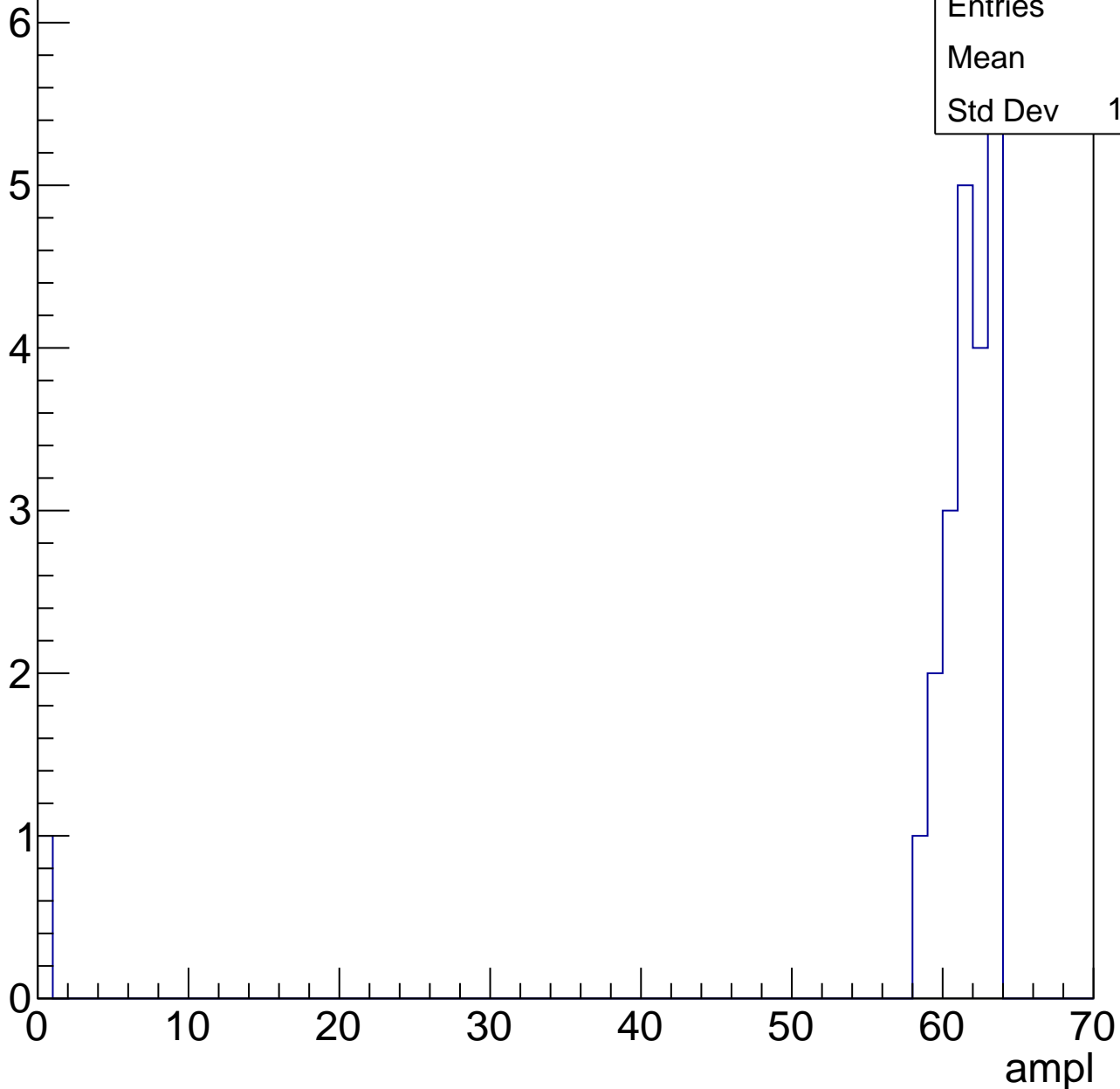


# B1L002S, U8-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.5
Std Dev	12.85

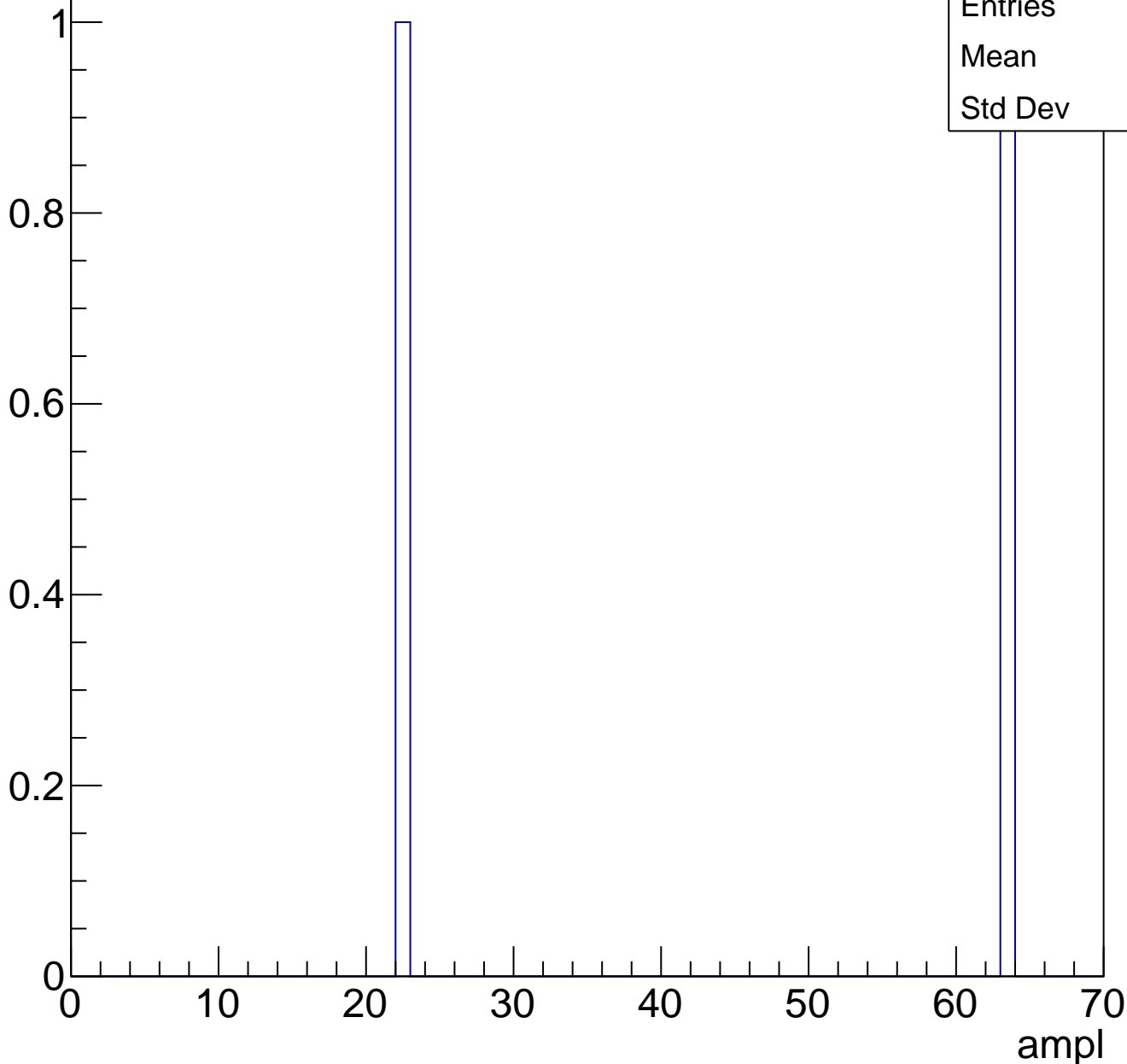




# B1L002S, U8-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch68, adc0

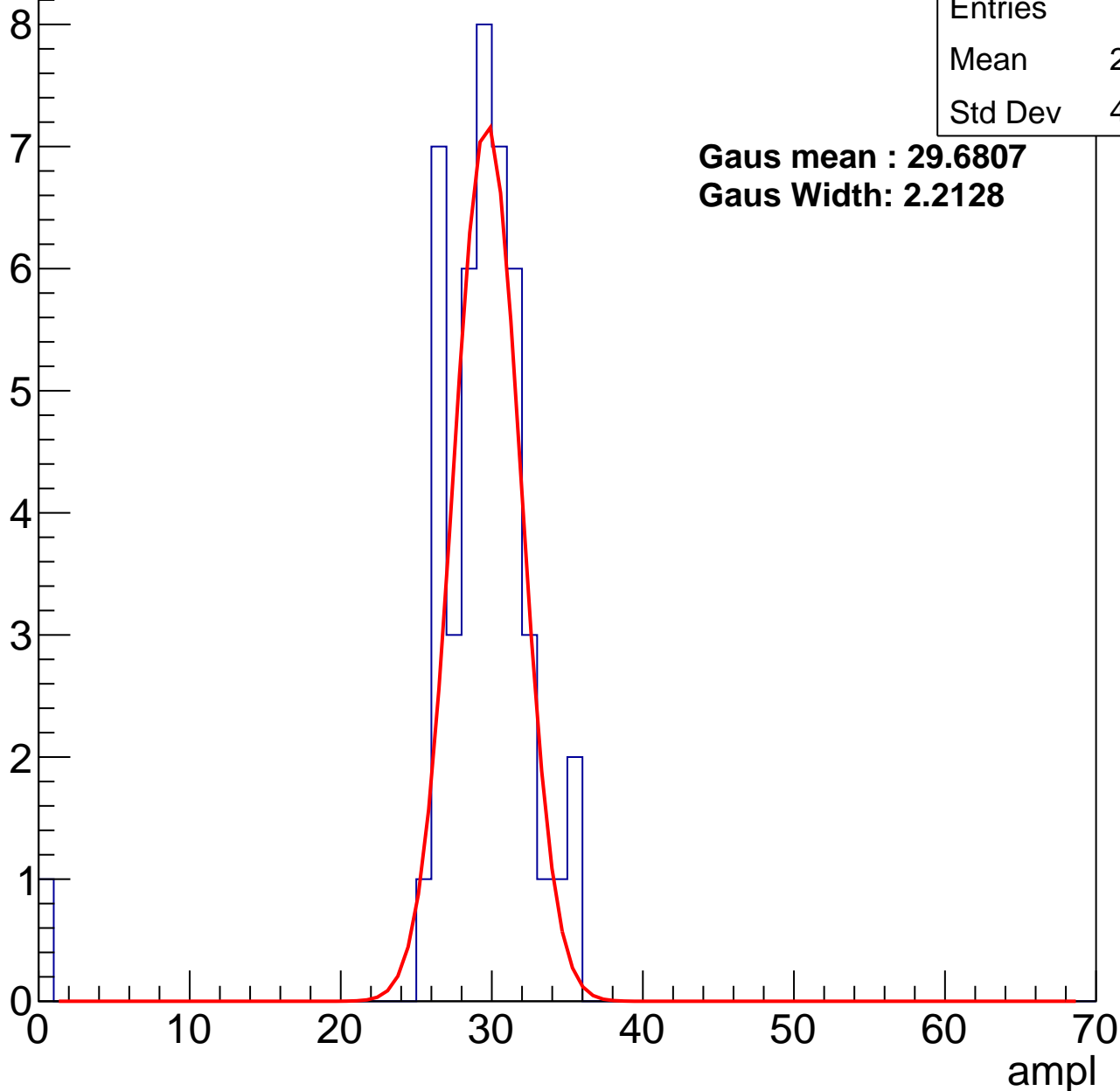
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	28.63
Std Dev	4.896

**Gaus mean : 29.6807**

**Gaus Width: 2.2128**



# B1L002S, U8-ch68, adc1

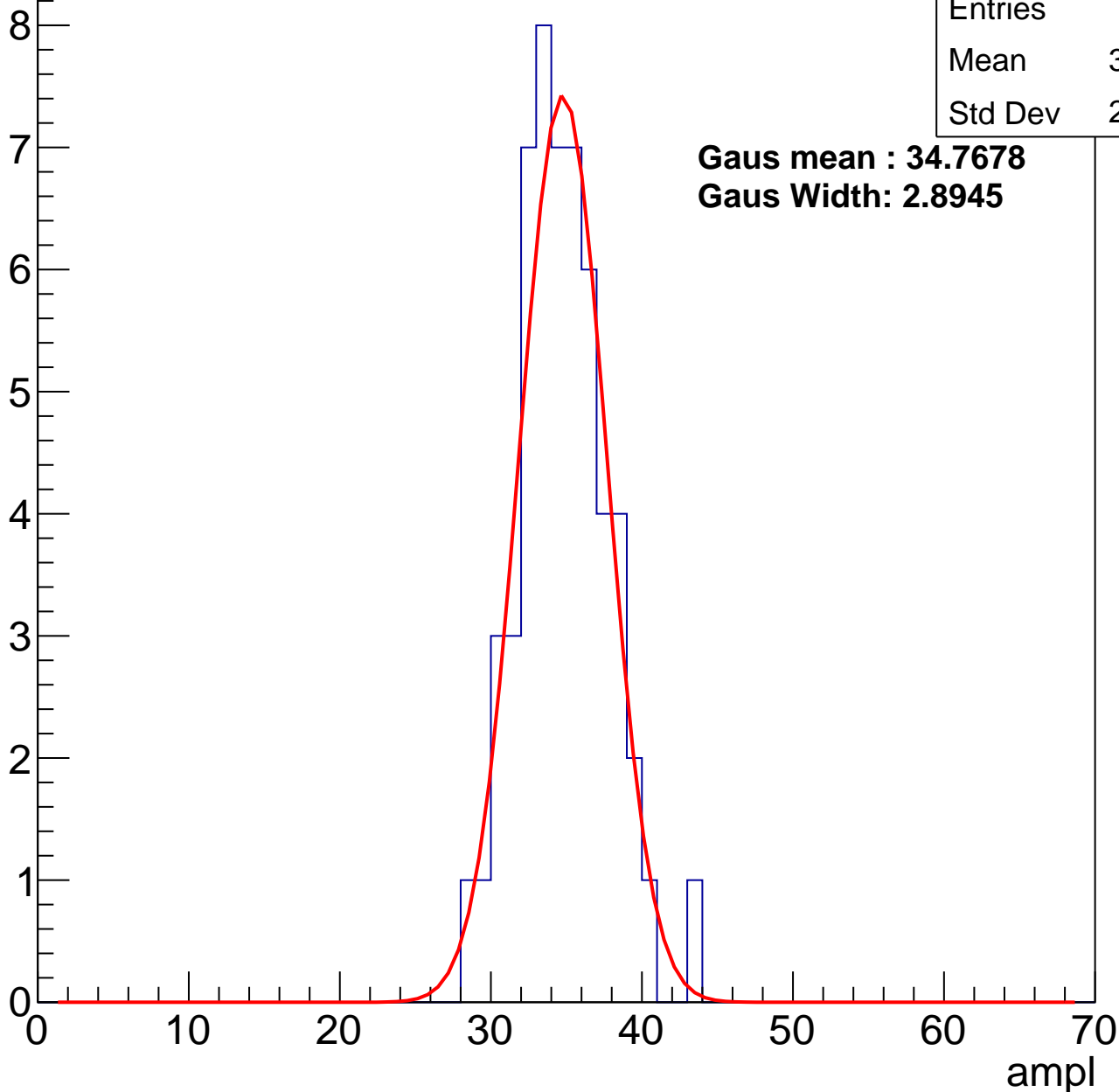
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.33
Std Dev	2.905

**Gaus mean : 34.7678**

**Gaus Width: 2.8945**



# B1L002S, U8-ch68, adc2

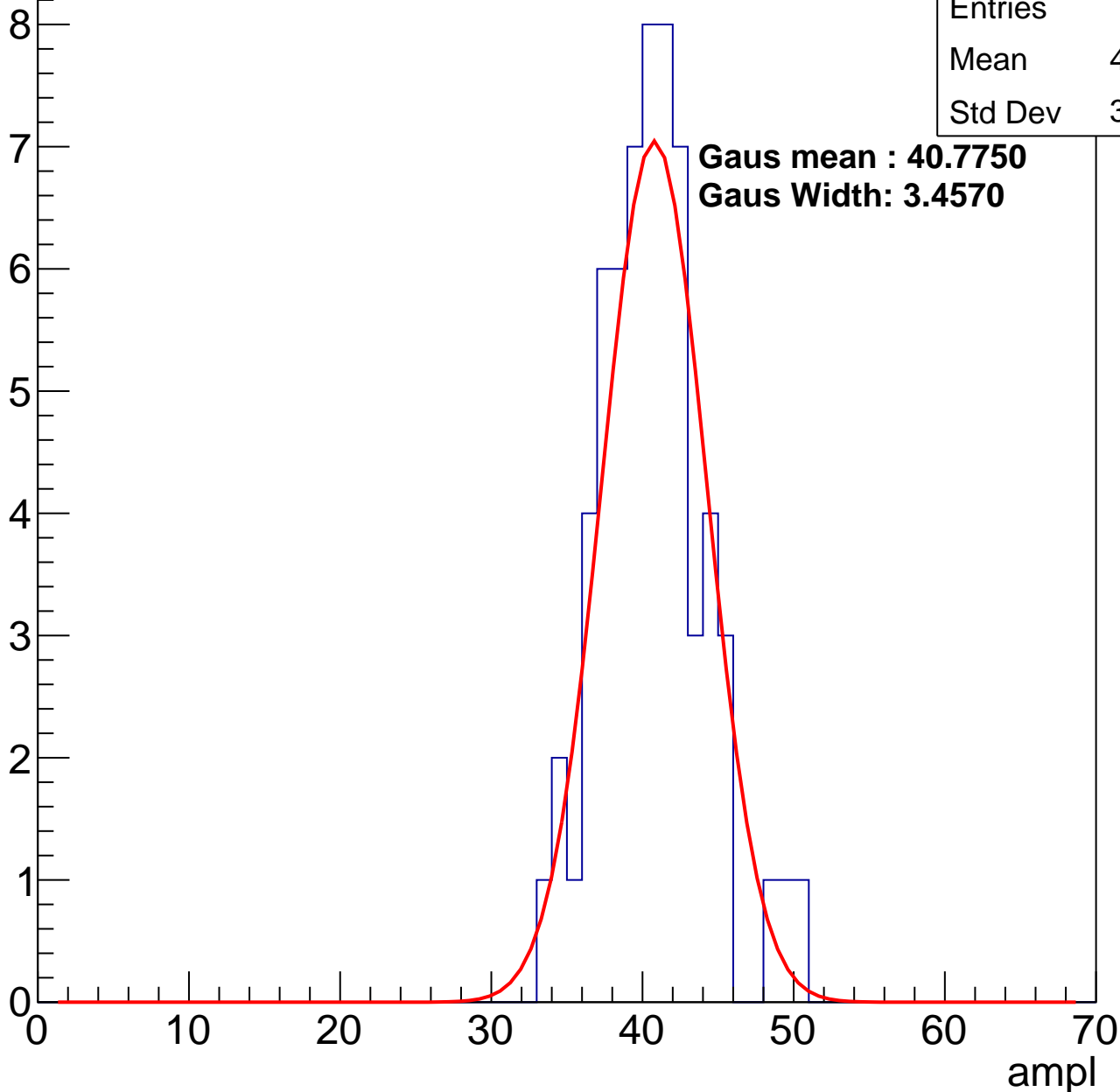
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.19
Std Dev	3.427

**Gaus mean : 40.7750**

**Gaus Width: 3.4570**



# B1L002S, U8-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

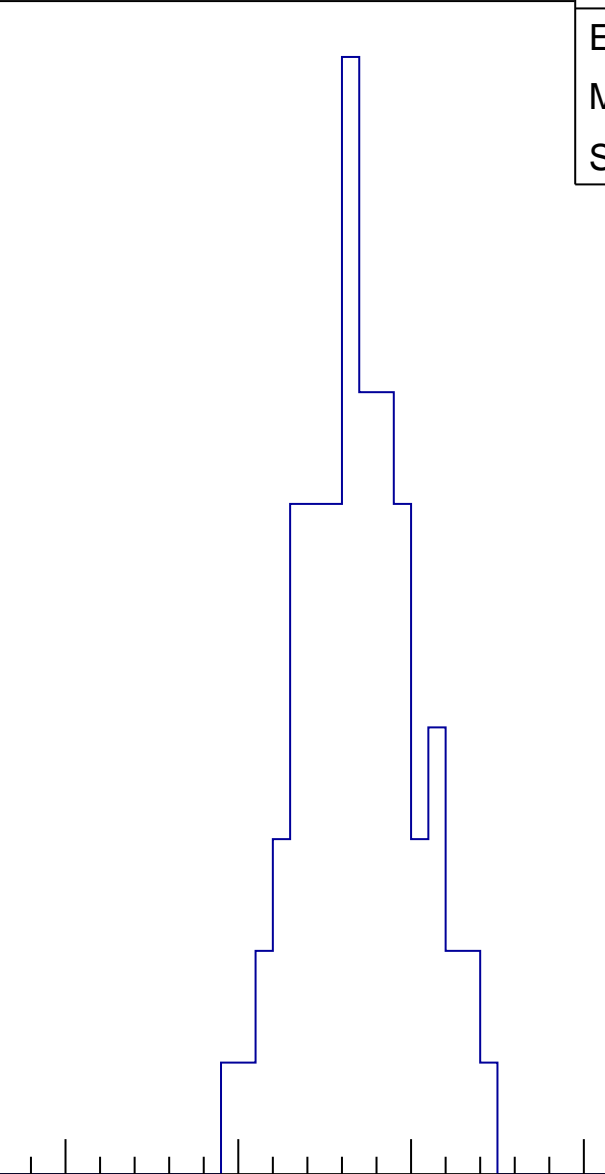
Entries	67
Mean	46.51
Std Dev	3.28

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

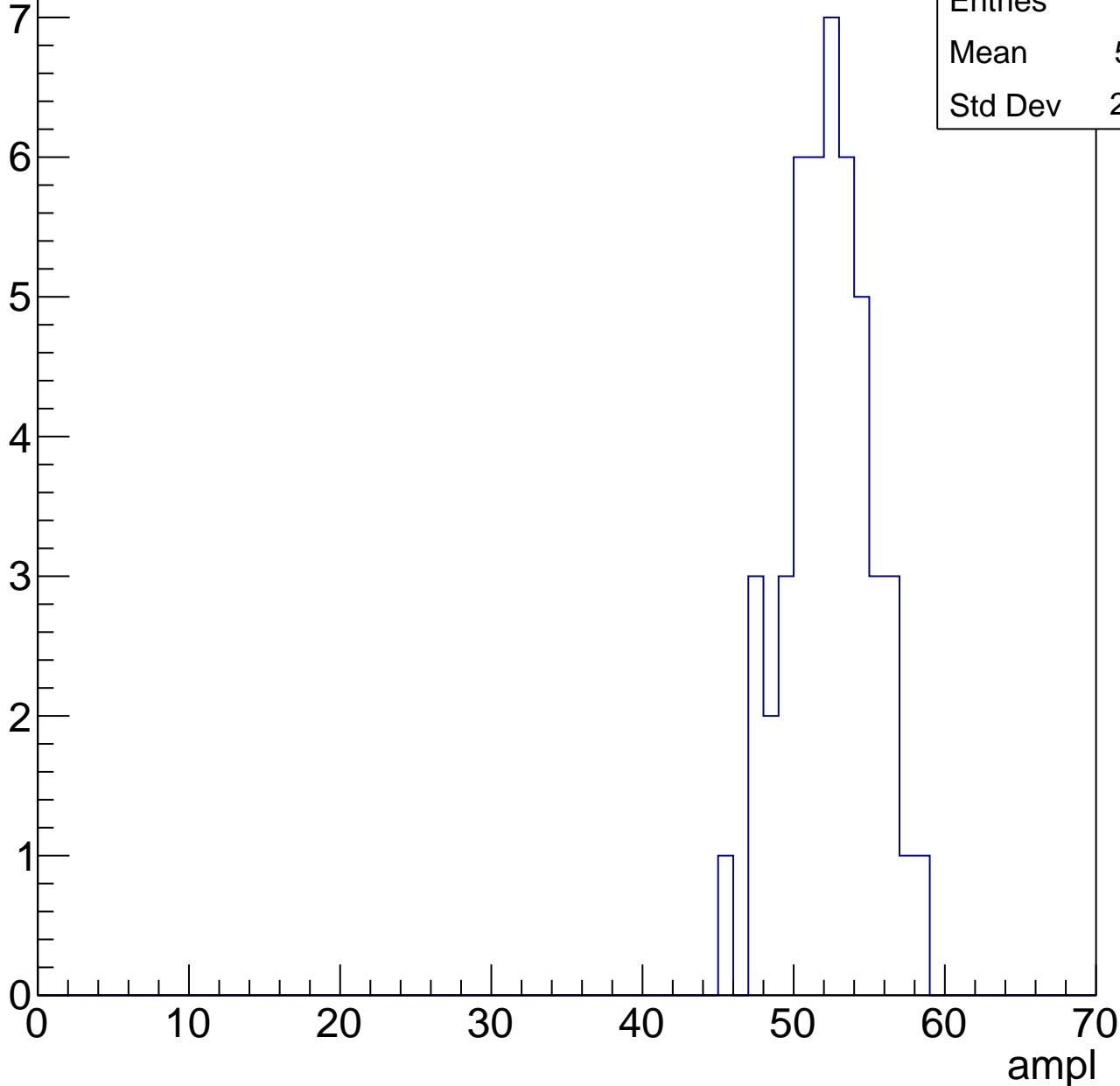


# B1L002S, U8-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	51.81
Std Dev	2.818

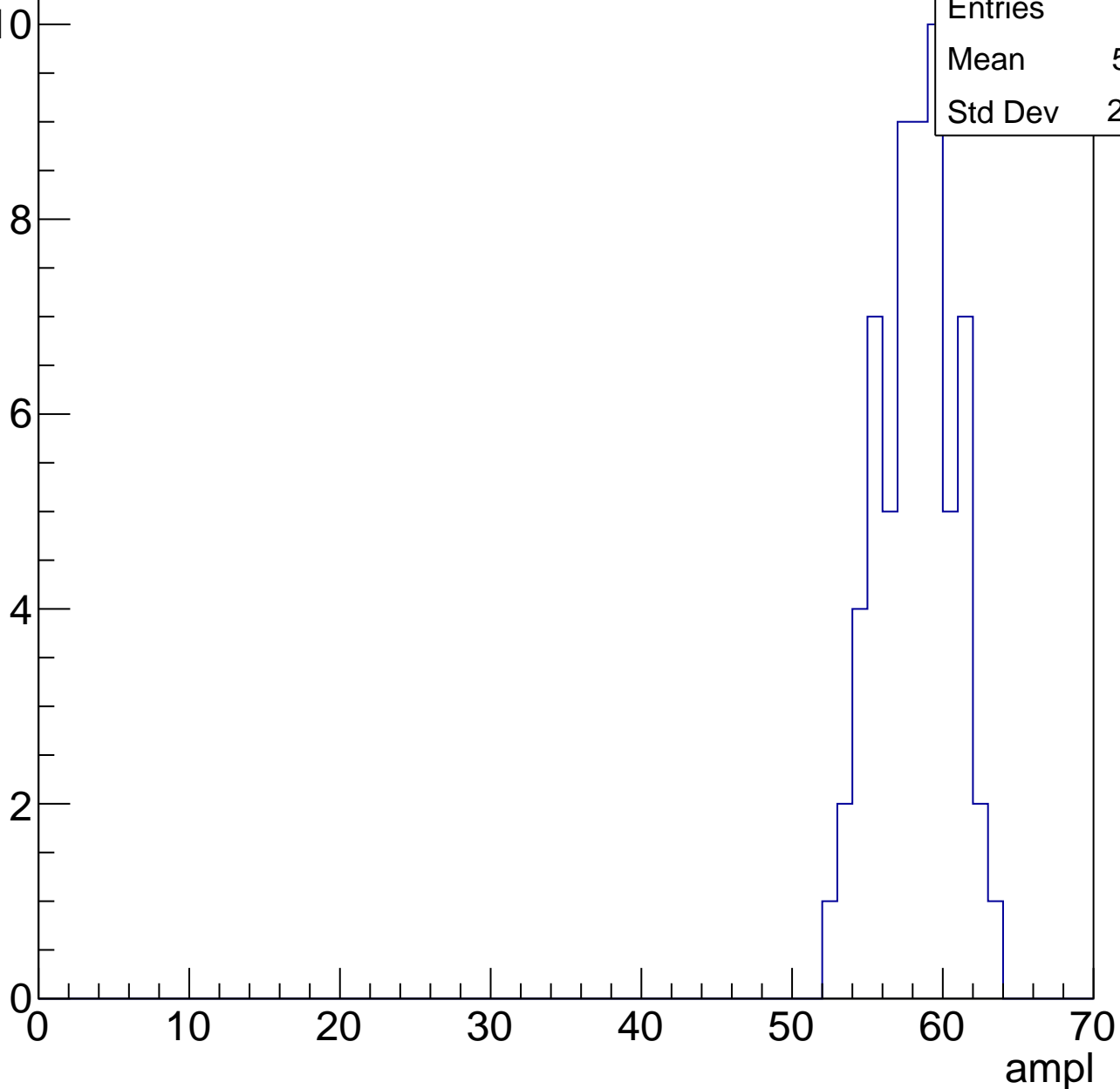


# B1L002S, U8-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	57.71
Std Dev	2.498

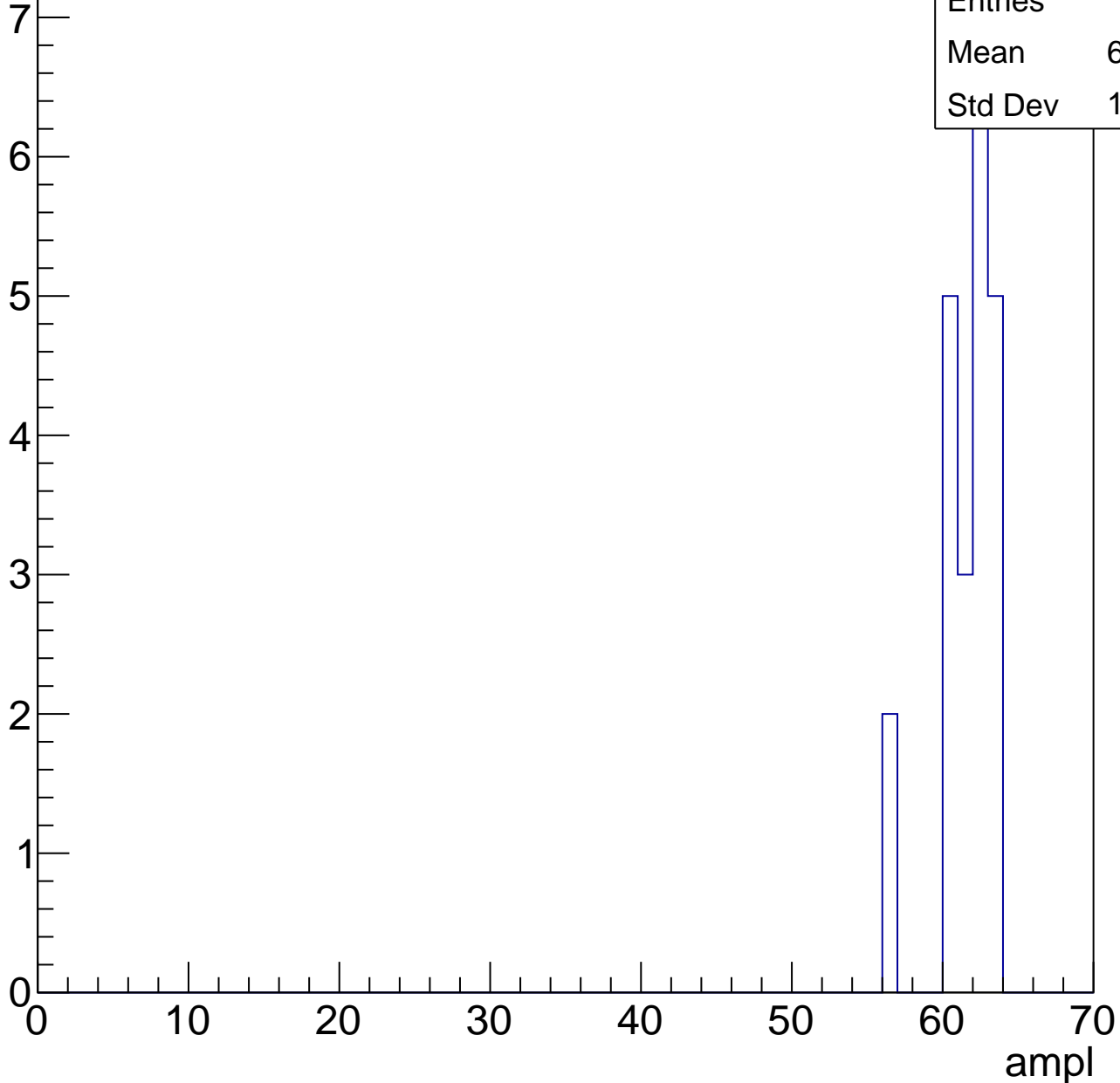


# B1L002S, U8-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	61.09
Std Dev	1.928

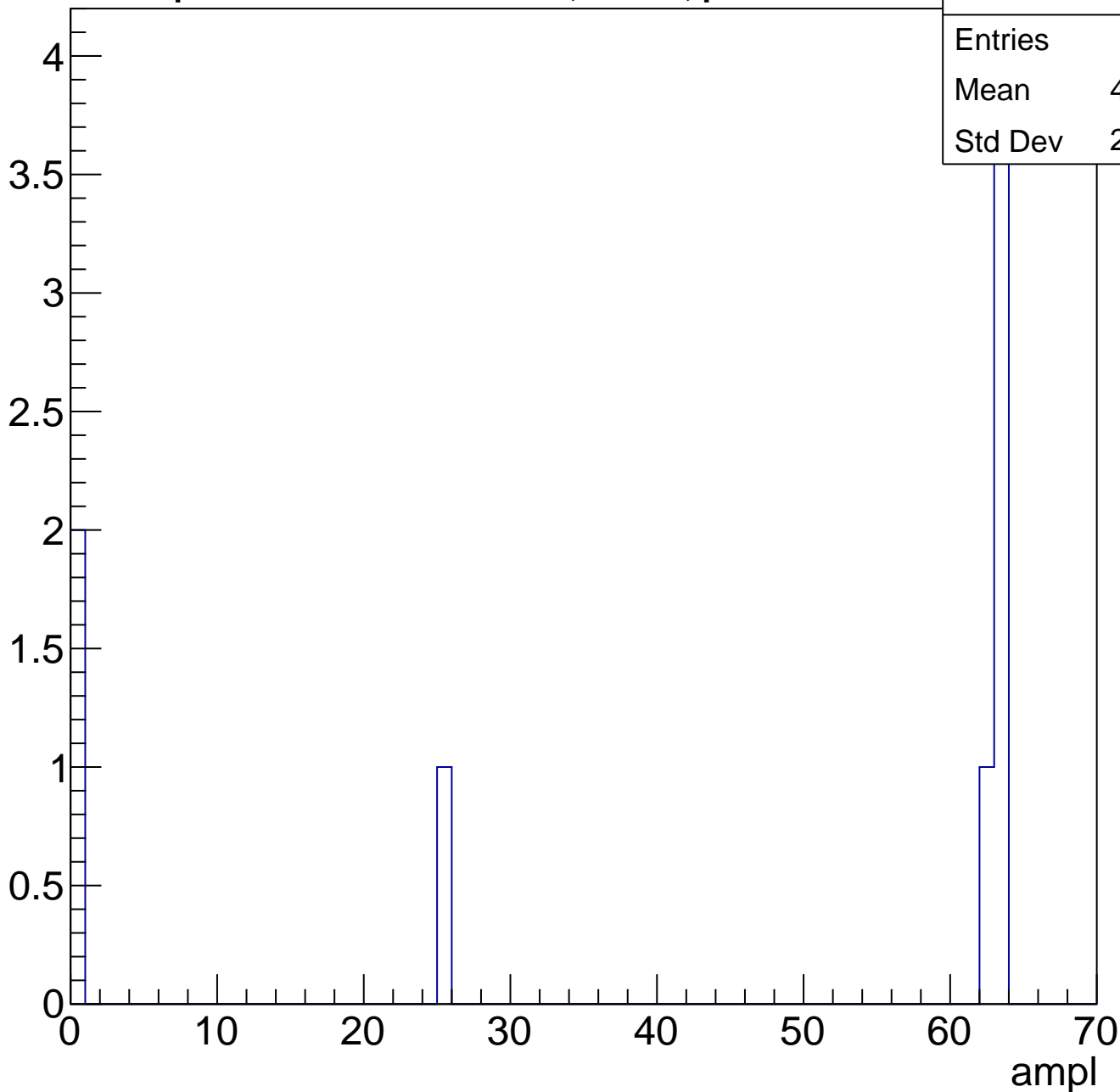




# B1L002S, U8-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



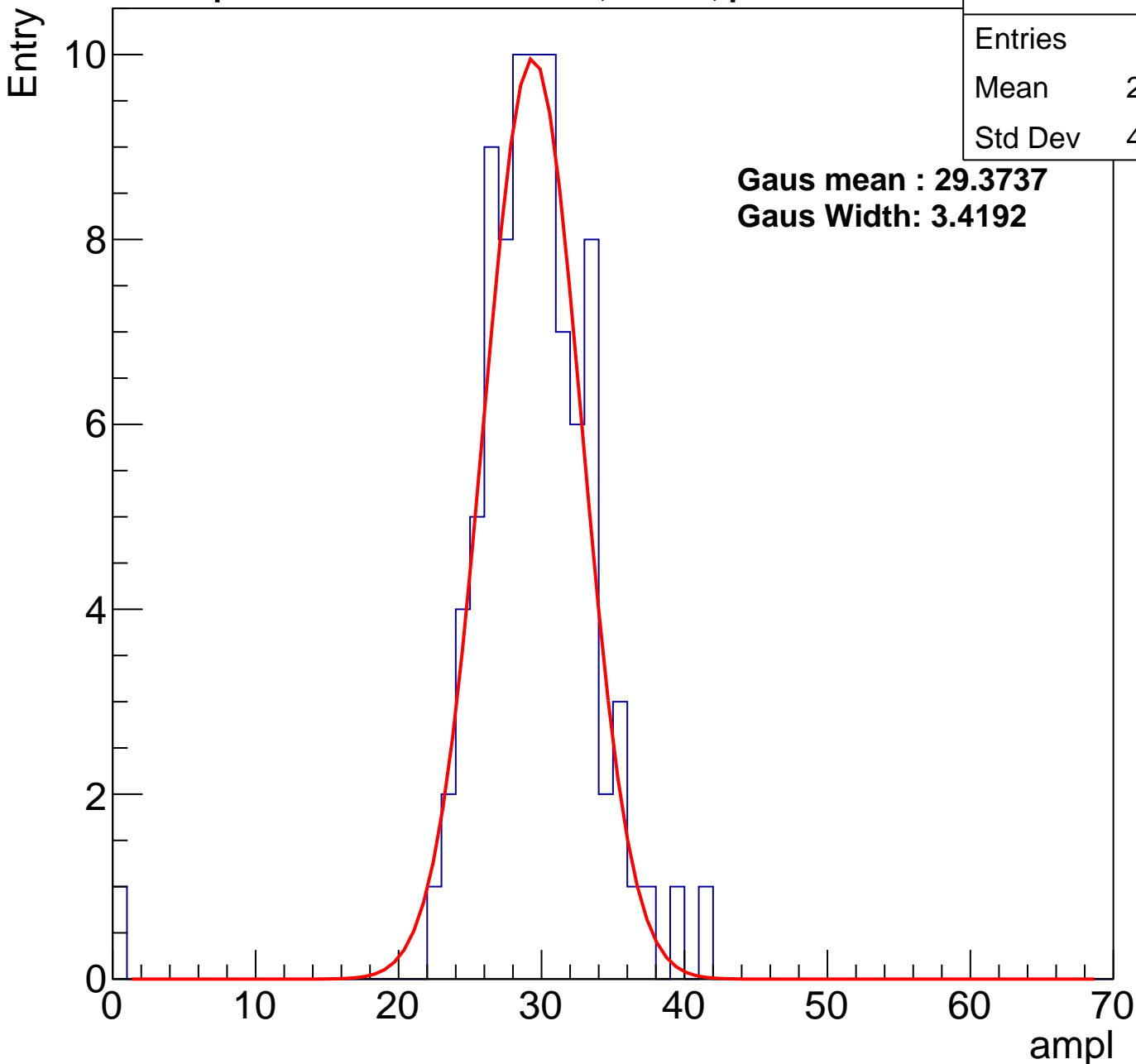
# B1L002S, U8-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	90
Mean	28.98
Std Dev	4.714

**Gaus mean : 29.3737**

**Gaus Width: 3.4192**



# B1L002S, U8-ch69, adc1

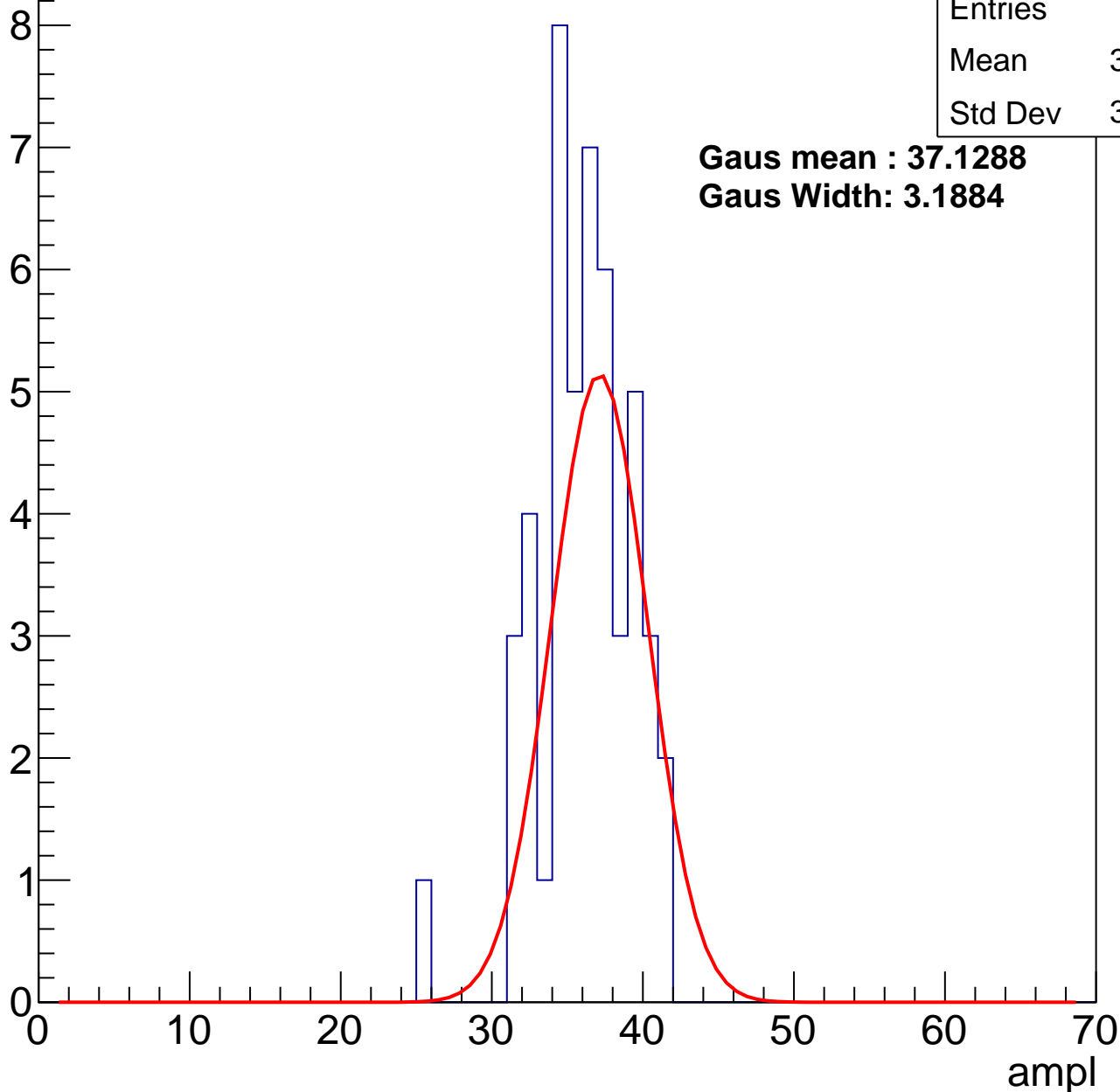
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	35.65
Std Dev	3.099

**Gaus mean : 37.1288**

**Gaus Width: 3.1884**



# B1L002S, U8-ch69, adc2

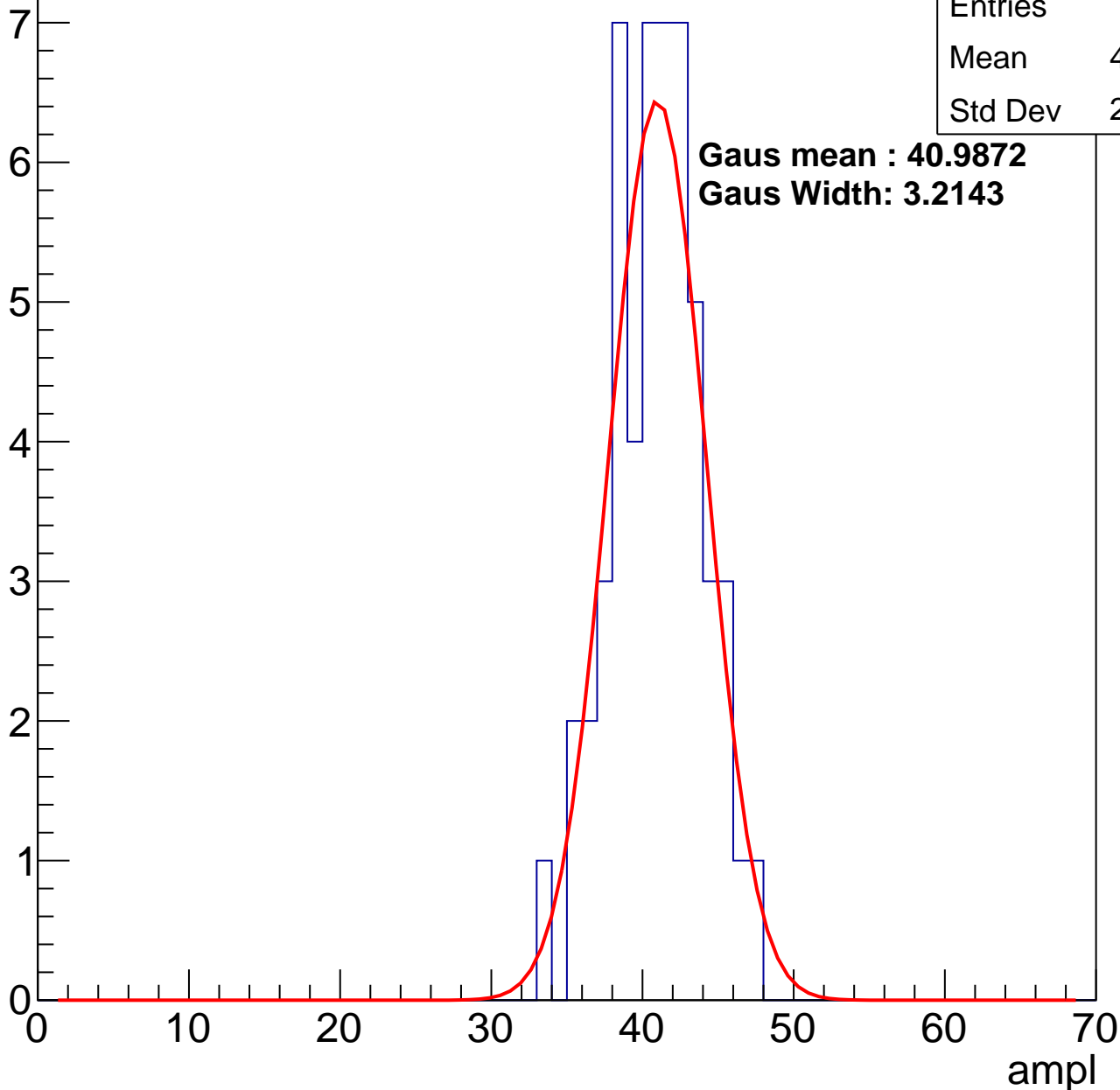
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.45
Std Dev	2.969

**Gaus mean : 40.9872**

**Gaus Width: 3.2143**

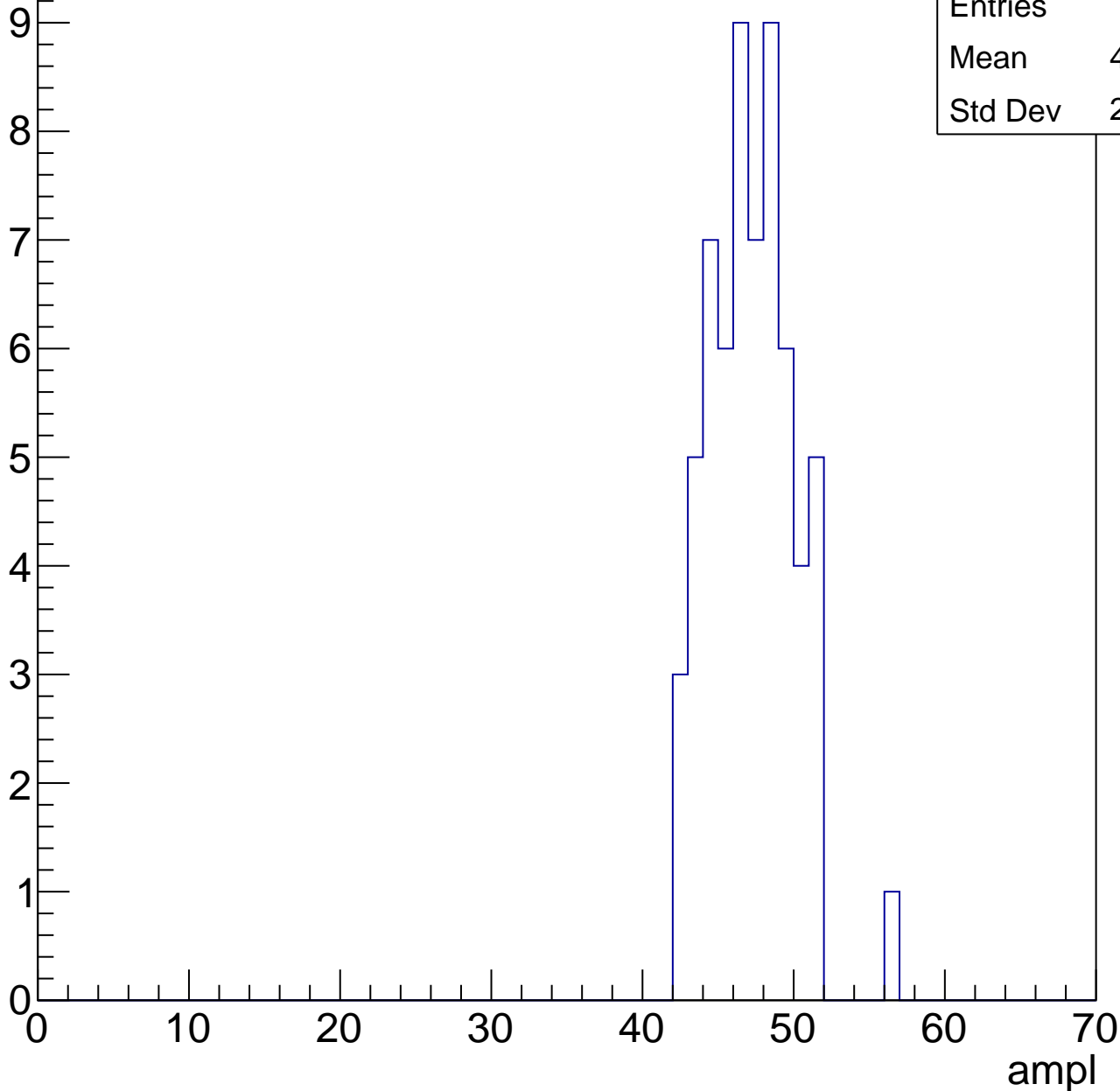


# B1L002S, U8-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.76
Std Dev	2.775

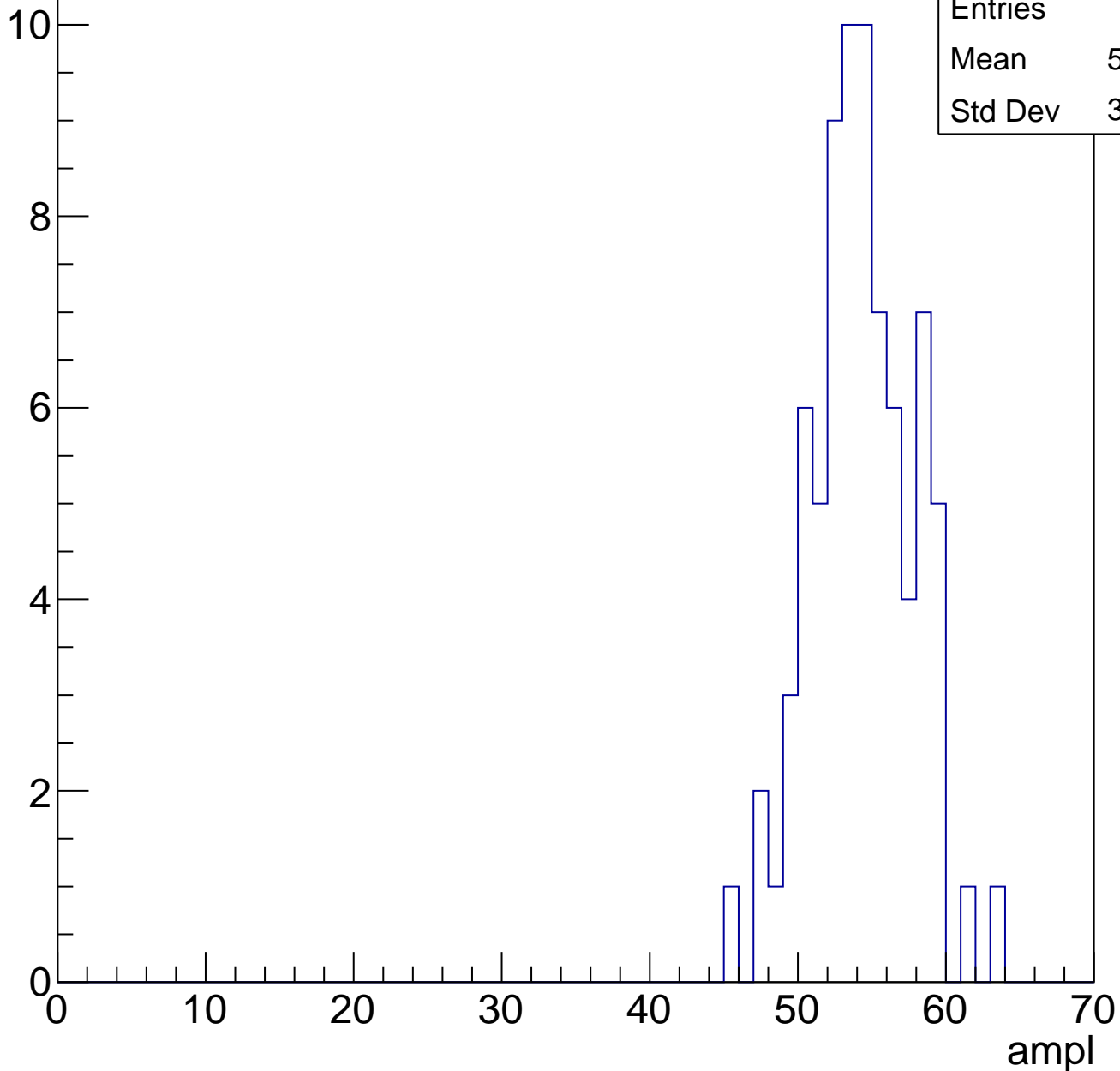


# B1L002S, U8-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	53.86
Std Dev	3.415

Entry

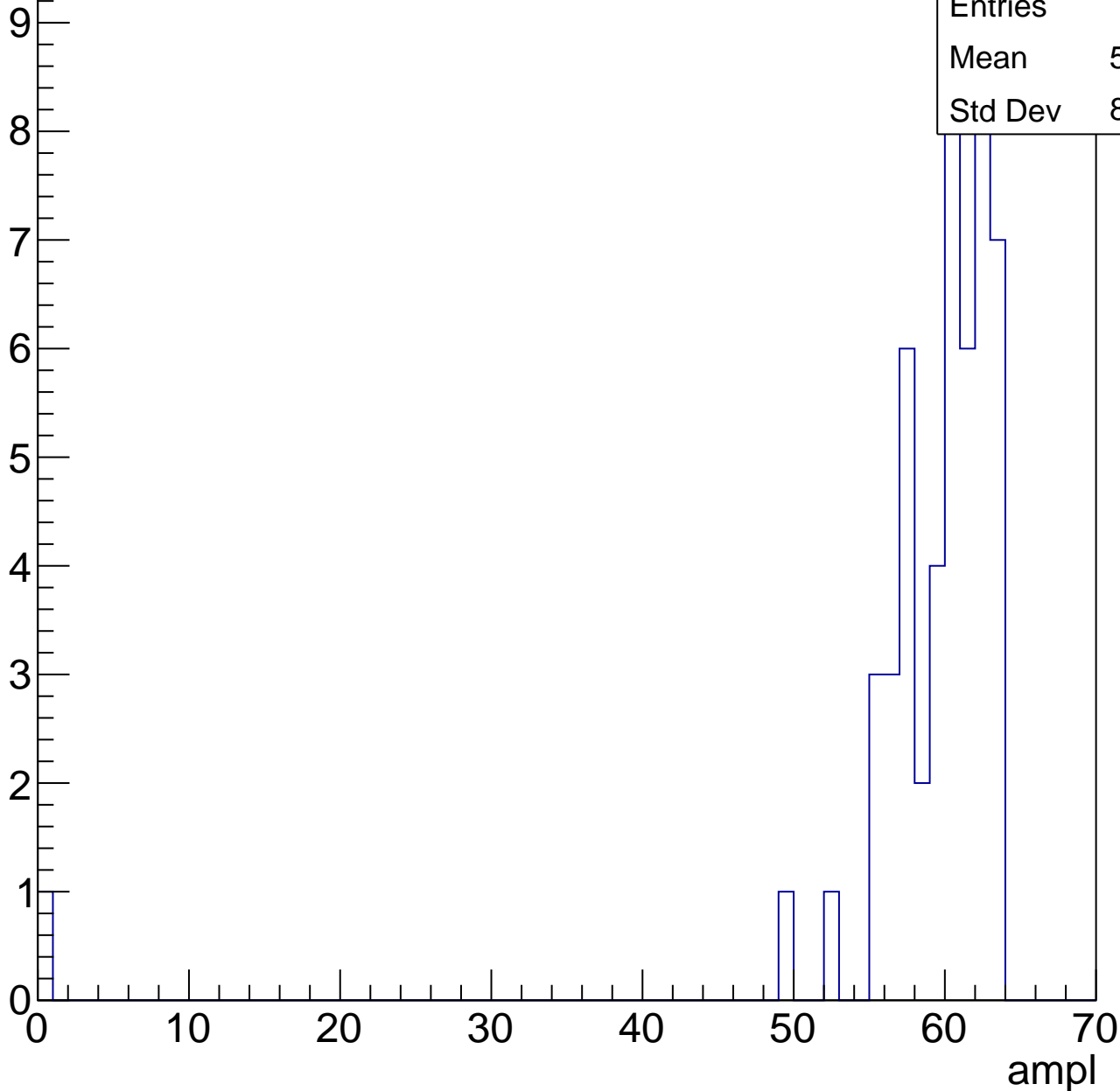


# B1L002S, U8-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.25
Std Dev	8.767



# B1L002S, U8-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

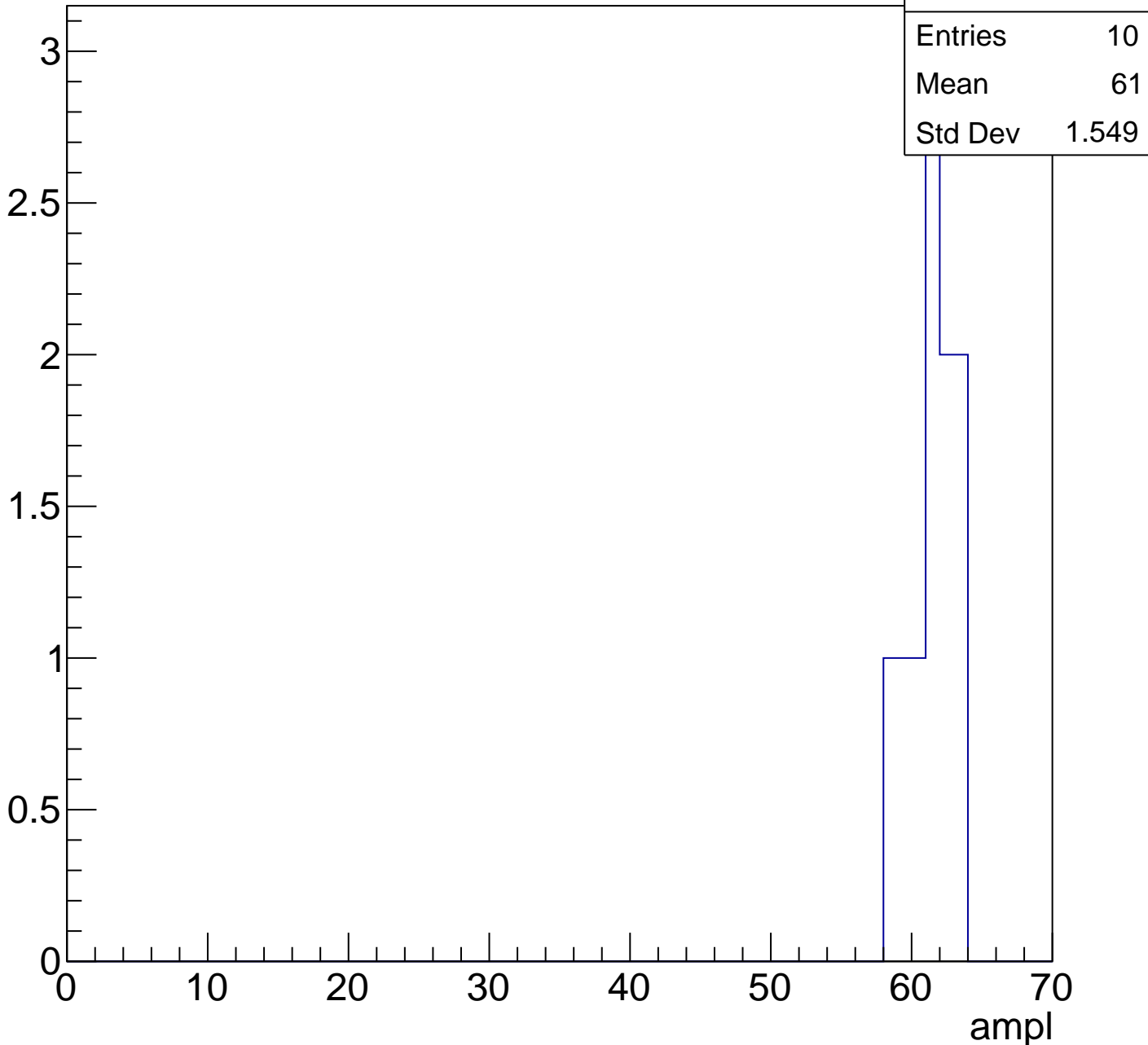
10

Mean

61

Std Dev

1.549





# B1L002S, U8-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch70, adc0

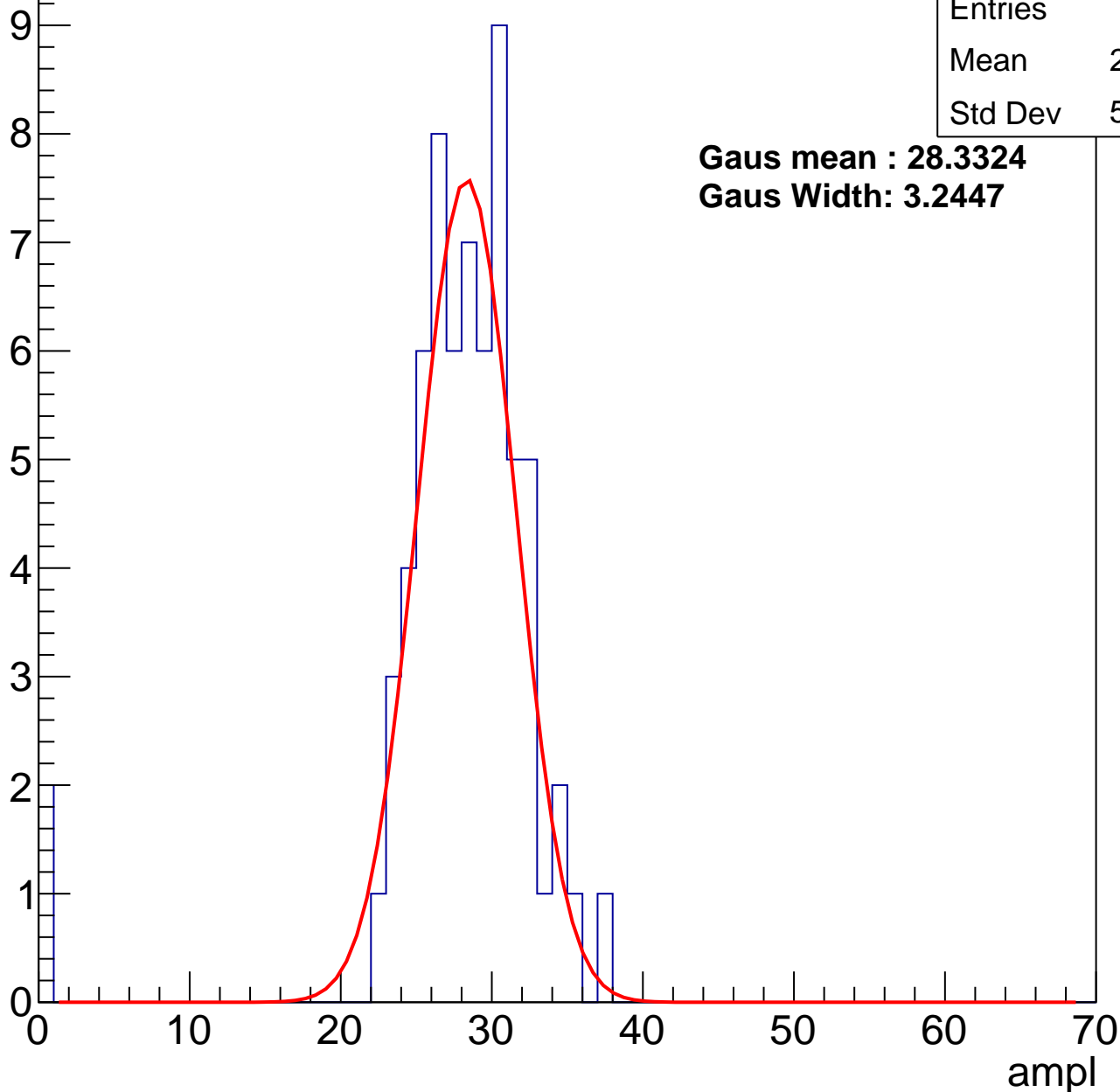
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.39
Std Dev	5.735

**Gaus mean : 28.3324**

**Gaus Width: 3.2447**



# B1L002S, U8-ch70, adc1

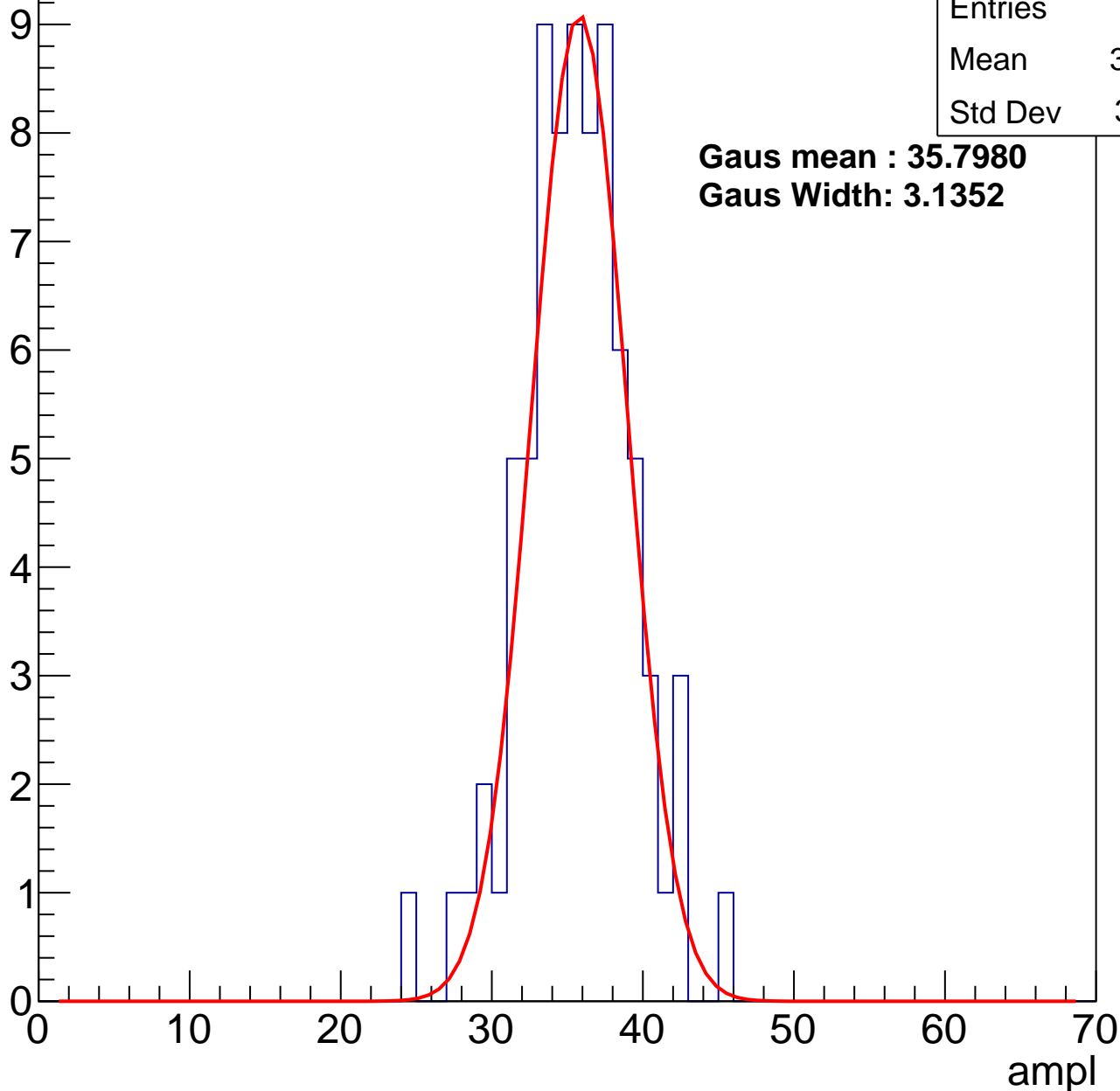
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	35.15
Std Dev	3.641

**Gaus mean : 35.7980**

**Gaus Width: 3.1352**

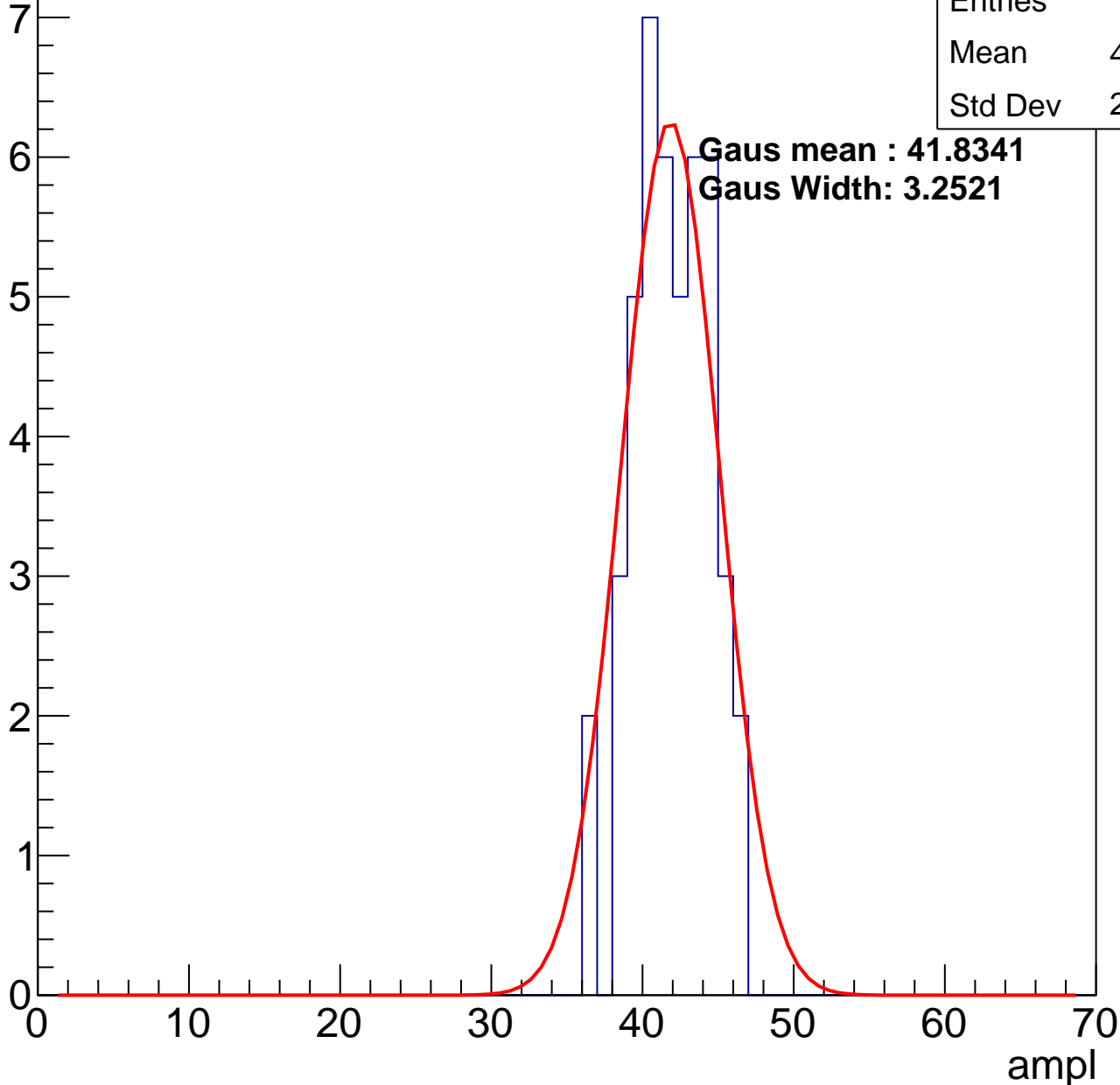


# B1L002S, U8-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	41.47
Std Dev	2.473

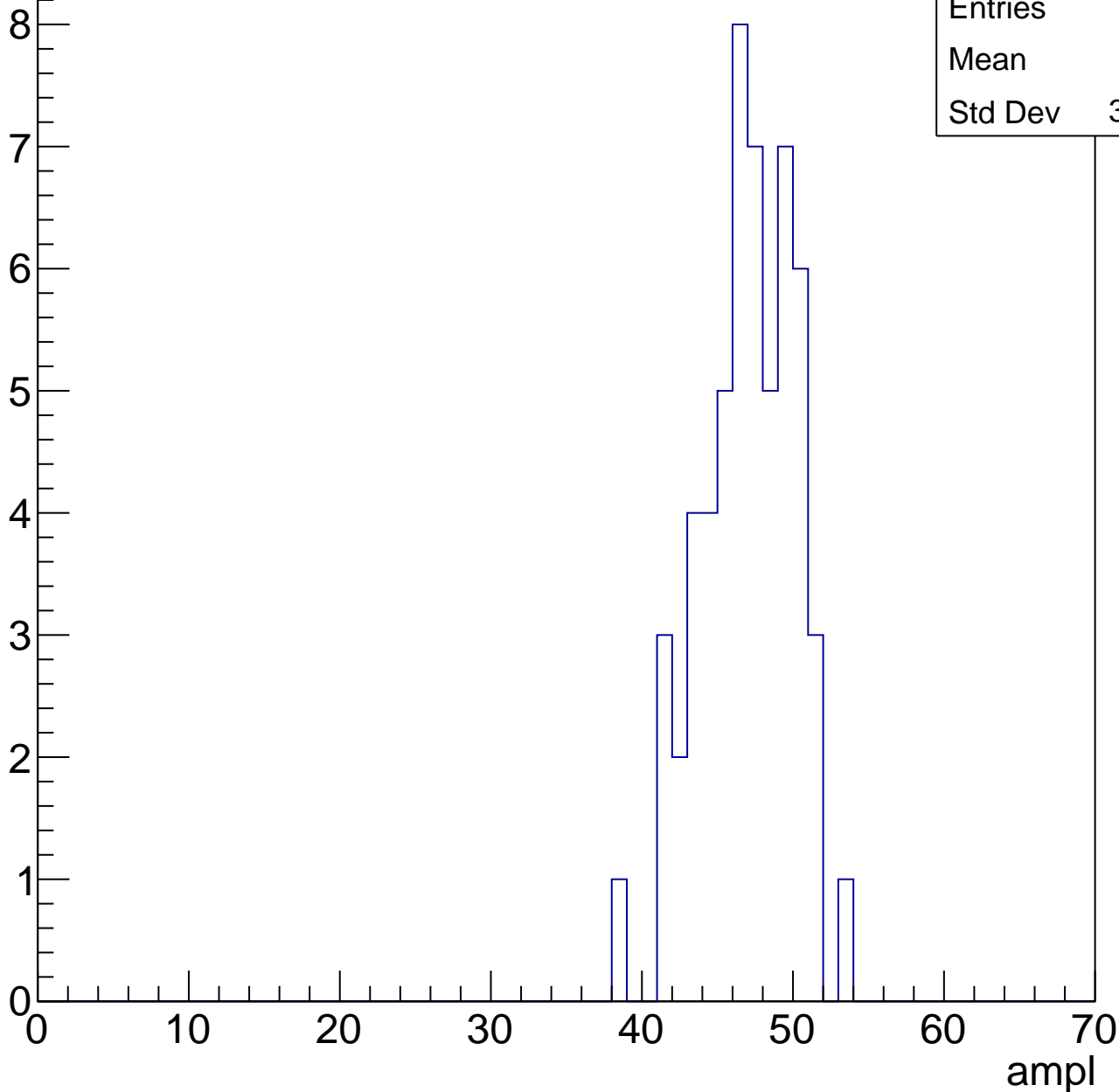


# B1L002S, U8-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	46.5
Std Dev	3.059

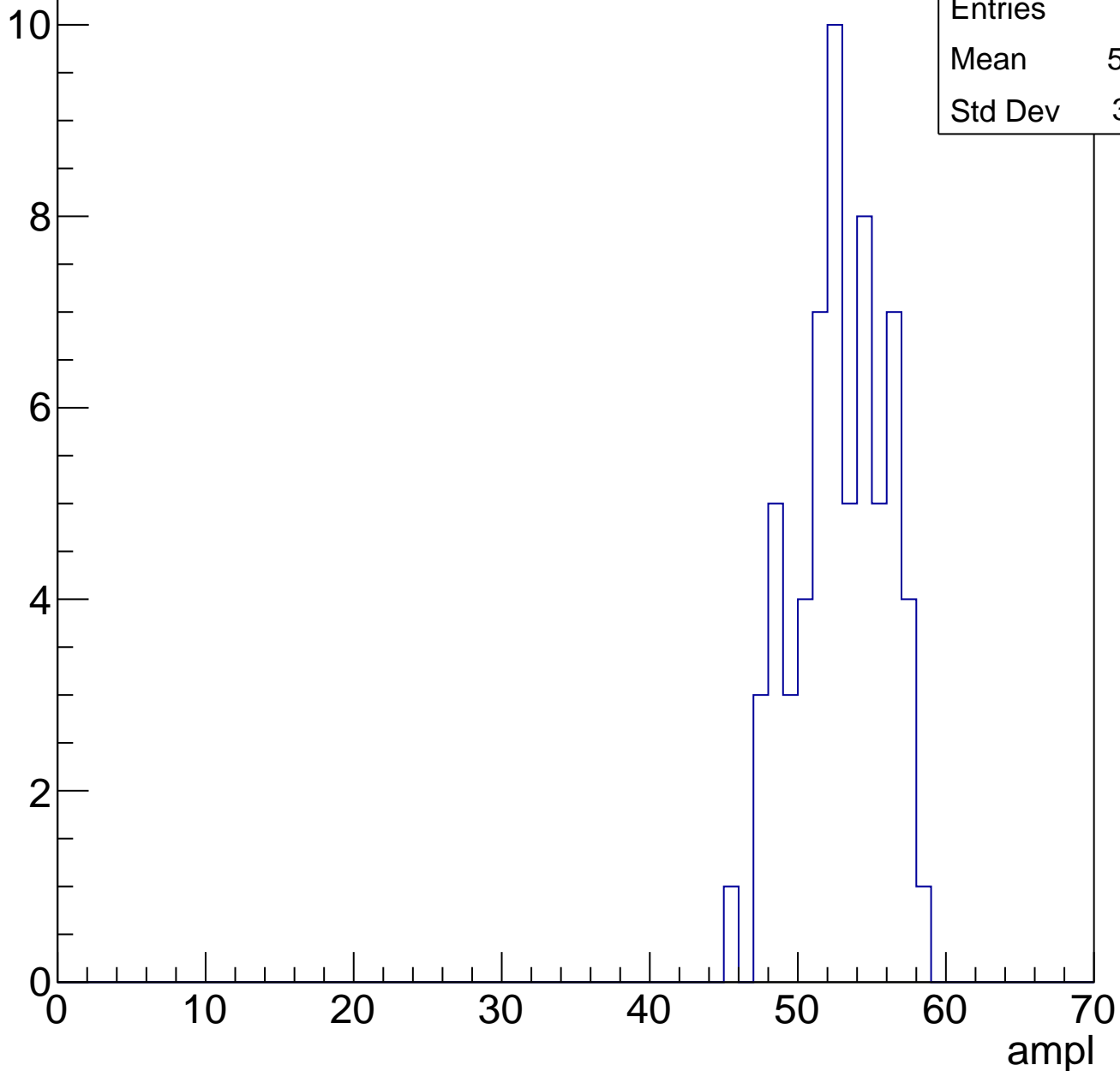


# B1L002S, U8-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

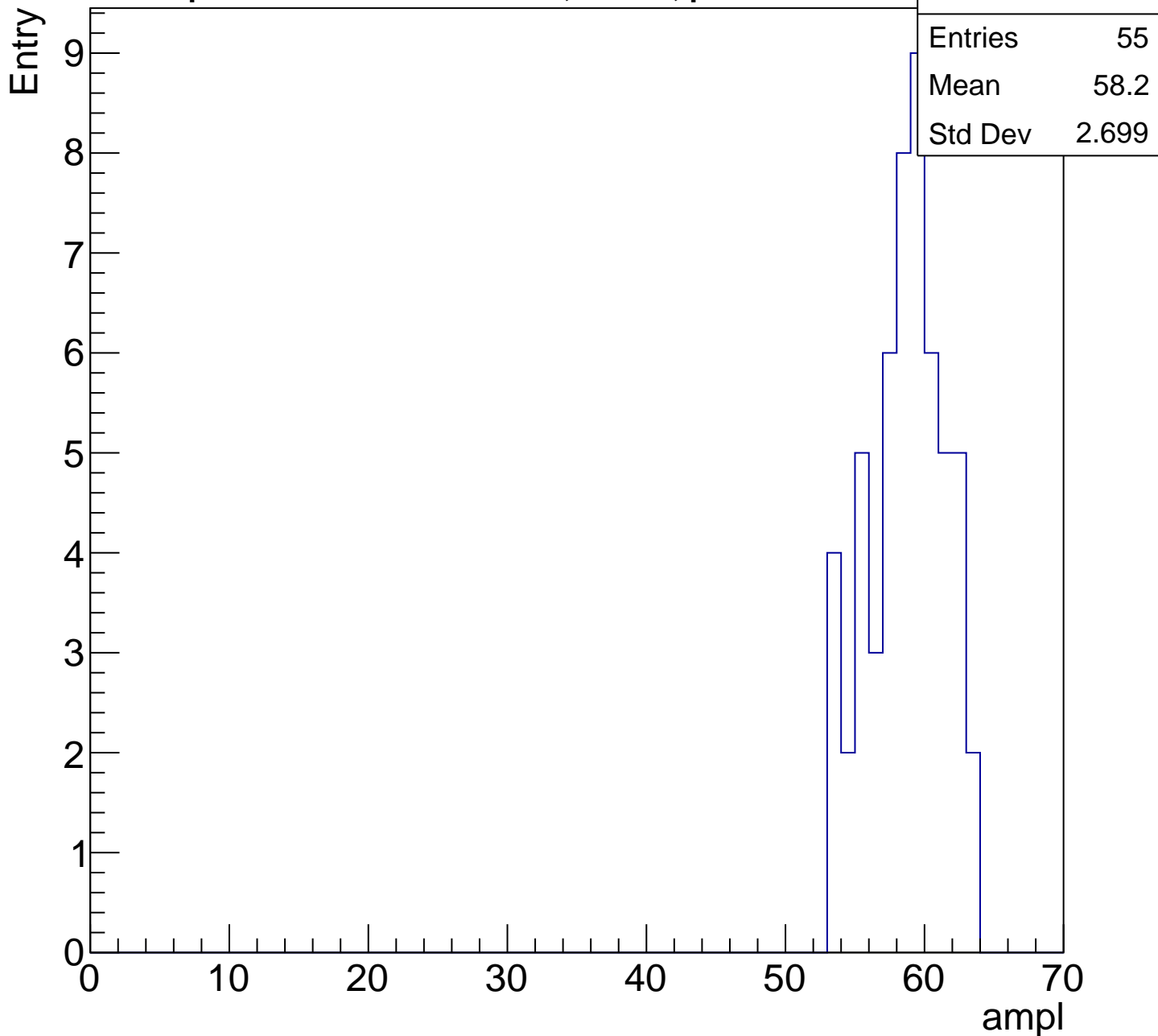
Entries	63
Mean	52.38
Std Dev	3.021

Entry



# B1L002S, U8-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

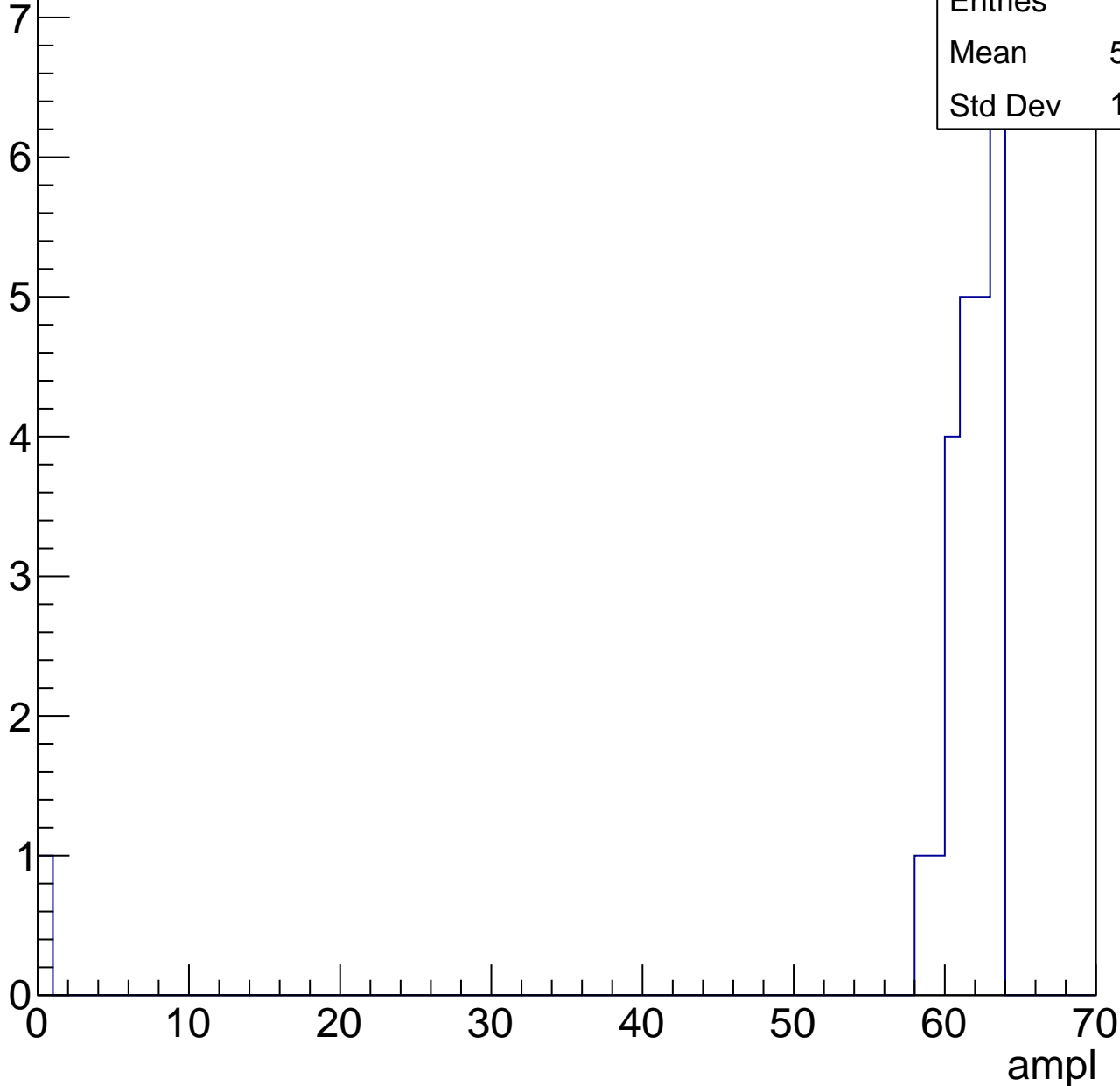


# B1L002S, U8-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.88
Std Dev	12.35





# B1L002S, U8-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch71, adc0

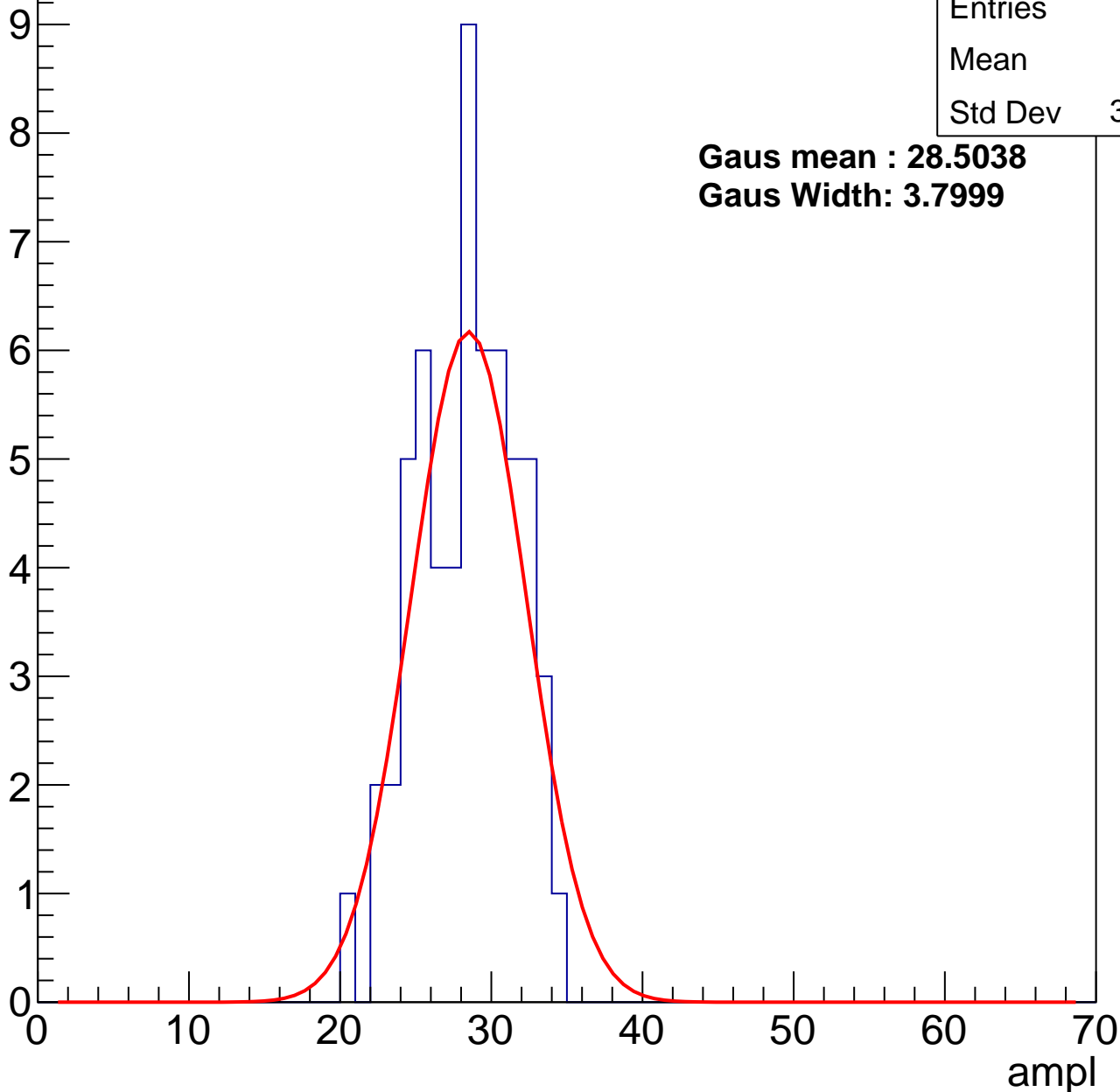
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.9
Std Dev	3.203

**Gaus mean : 28.5038**

**Gaus Width: 3.7999**



# B1L002S, U8-ch71, adc1

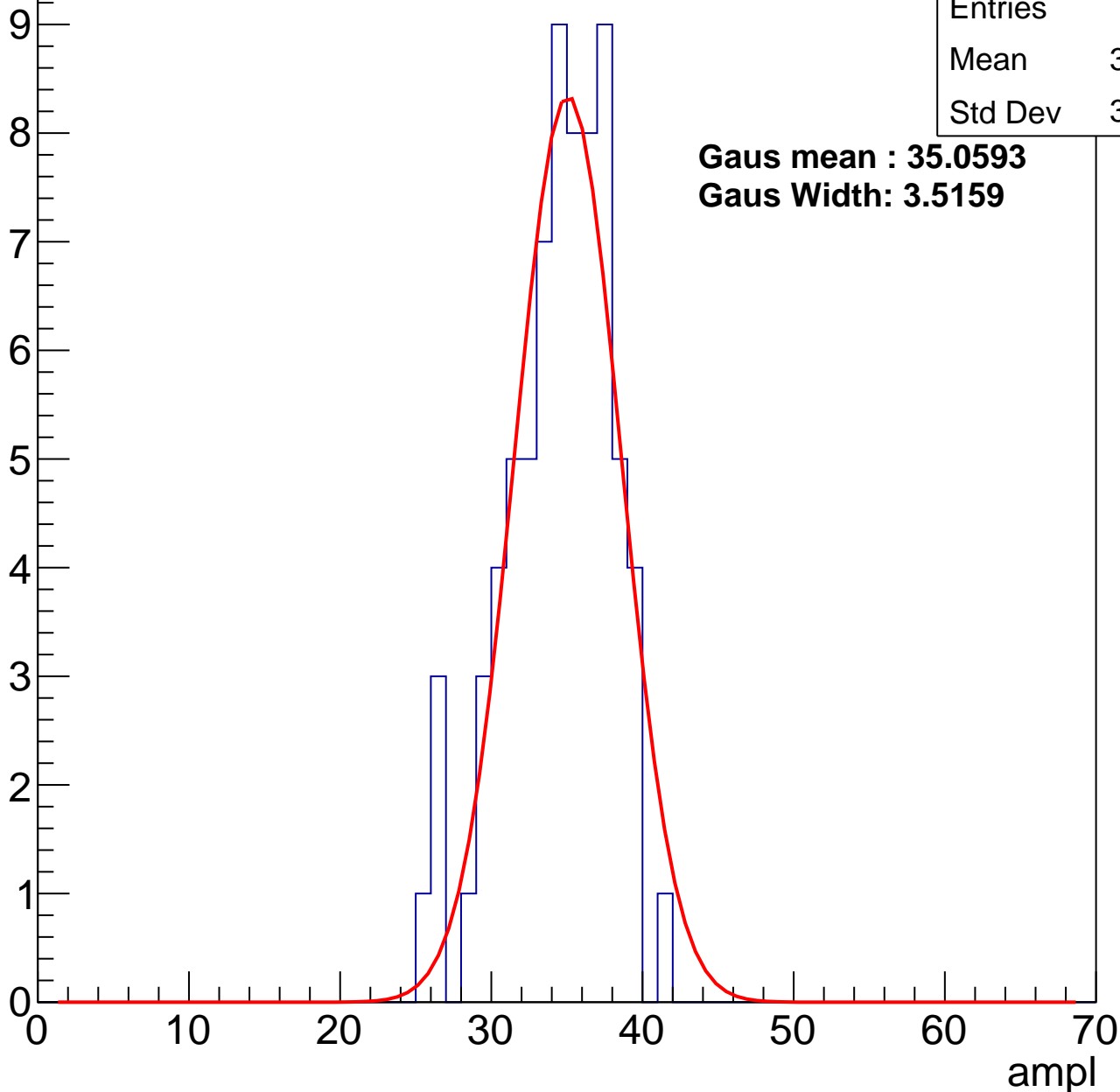
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	33.95
Std Dev	3.452

**Gaus mean : 35.0593**

**Gaus Width: 3.5159**



# B1L002S, U8-ch71, adc2

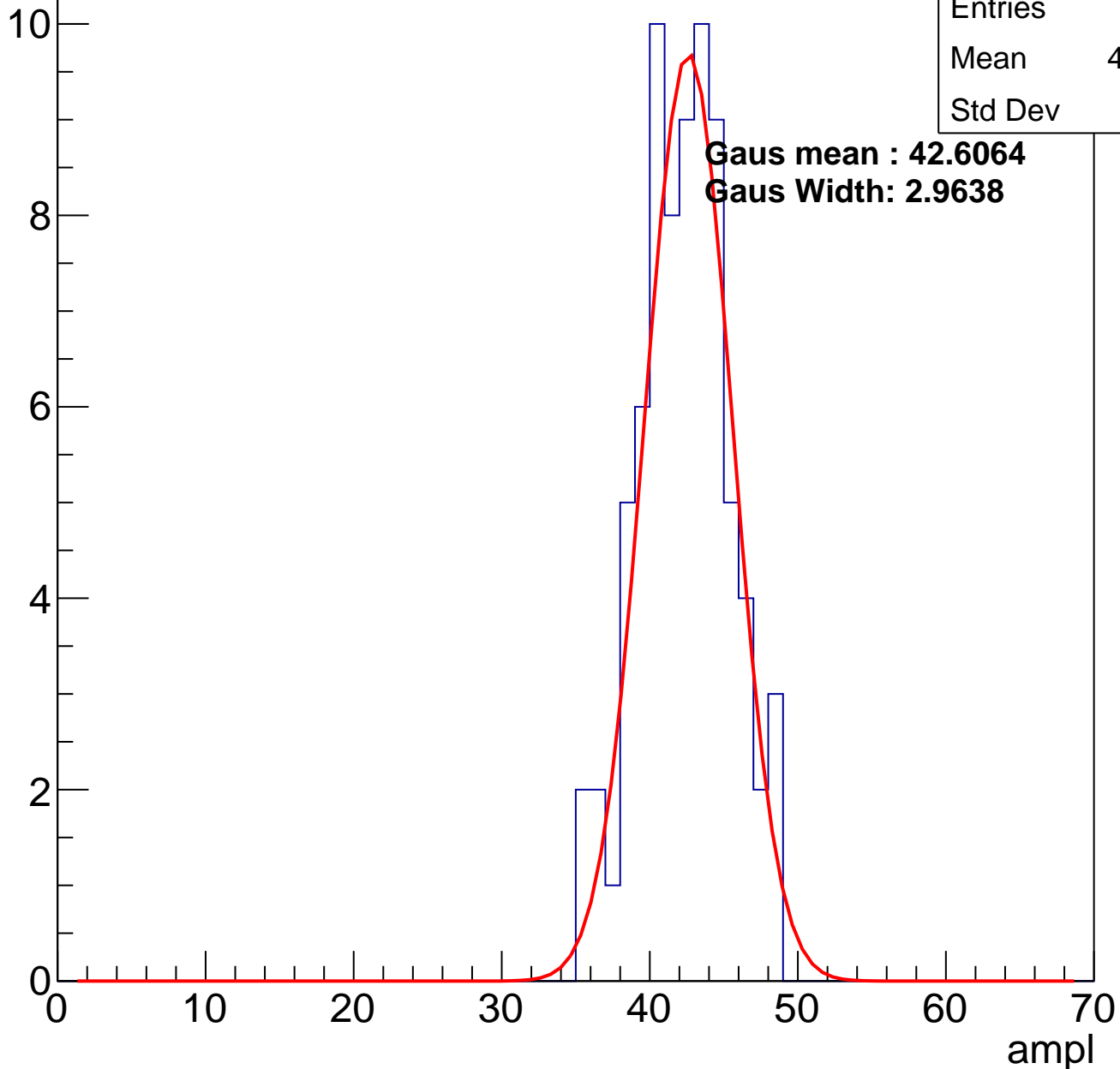
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	41.87
Std Dev	3.01

**Gaus mean : 42.6064**

**Gaus Width: 2.9638**

Entry

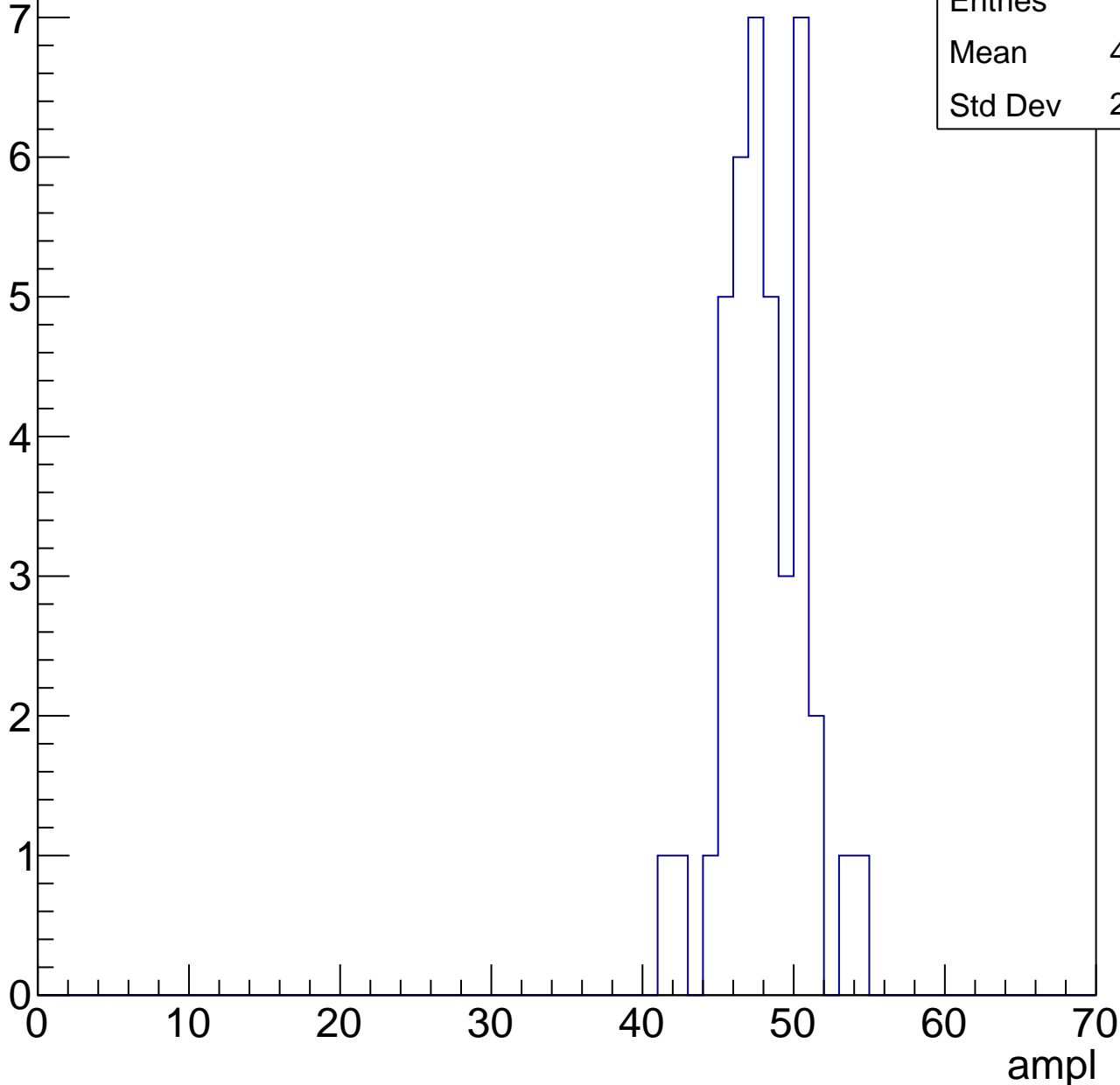


# B1L002S, U8-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	47.58
Std Dev	2.645

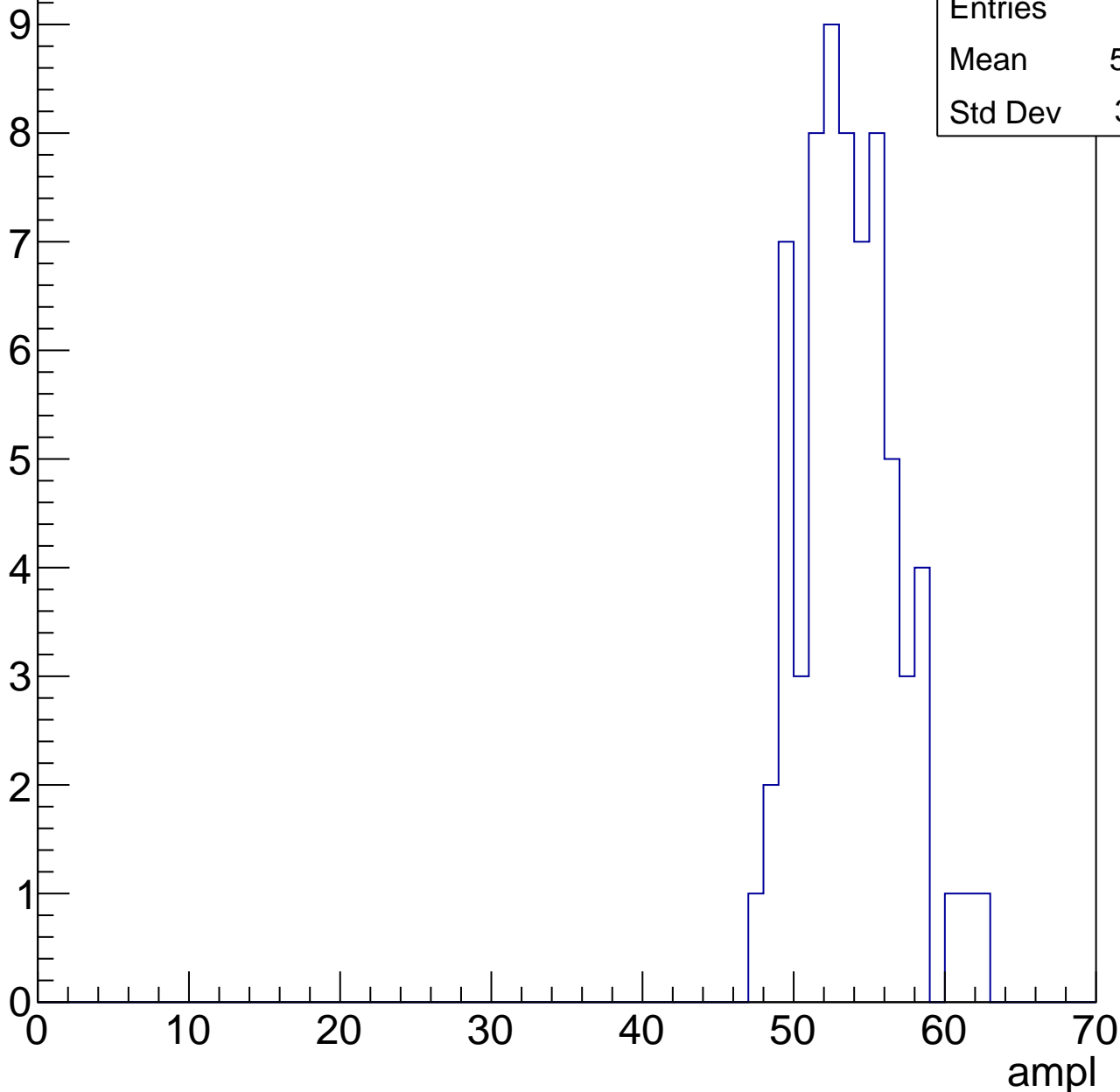


# B1L002S, U8-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	53.24
Std Dev	3.181



# B1L002S, U8-ch71, adc5

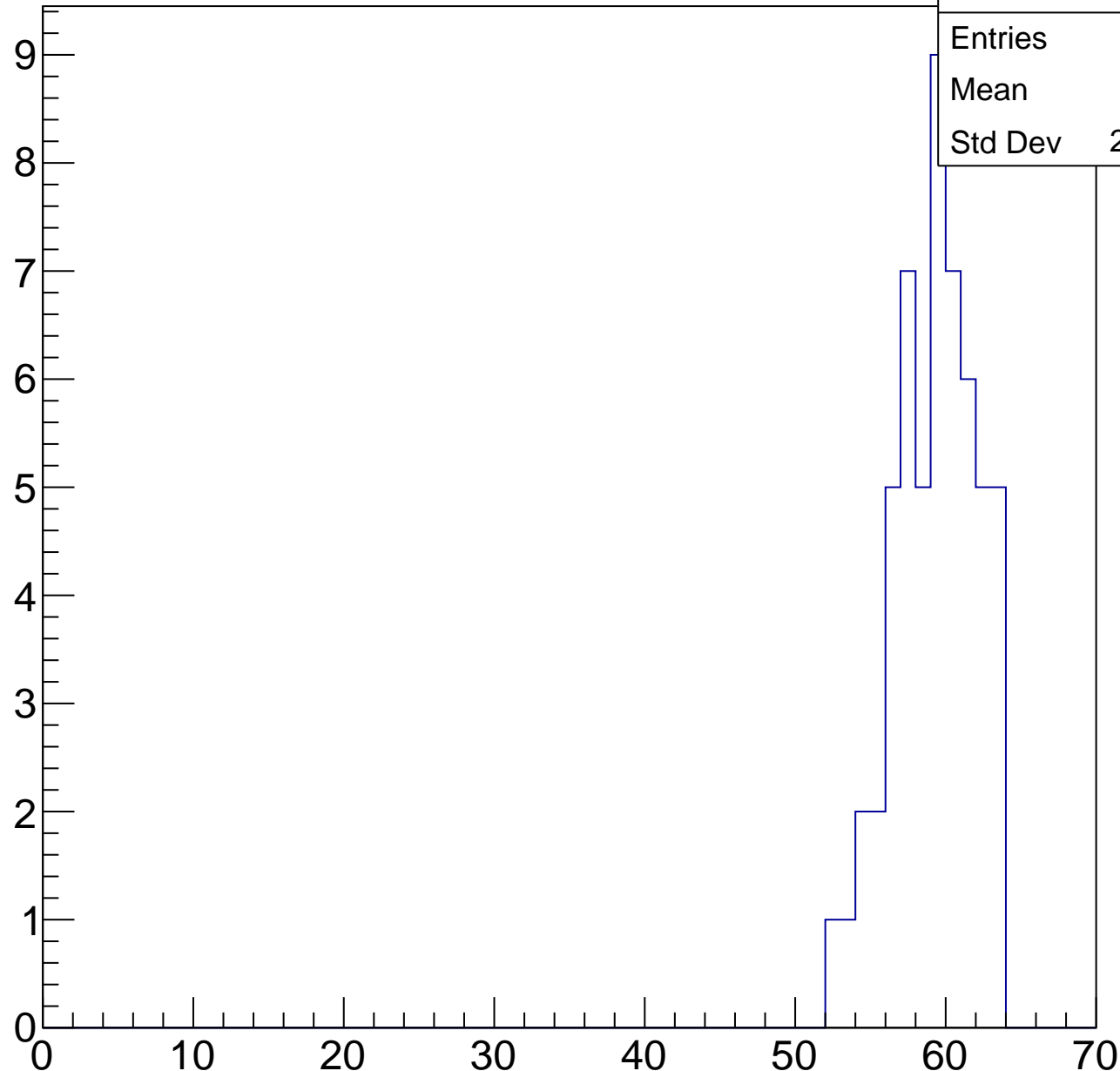
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.8
Std Dev	2.693

ampl

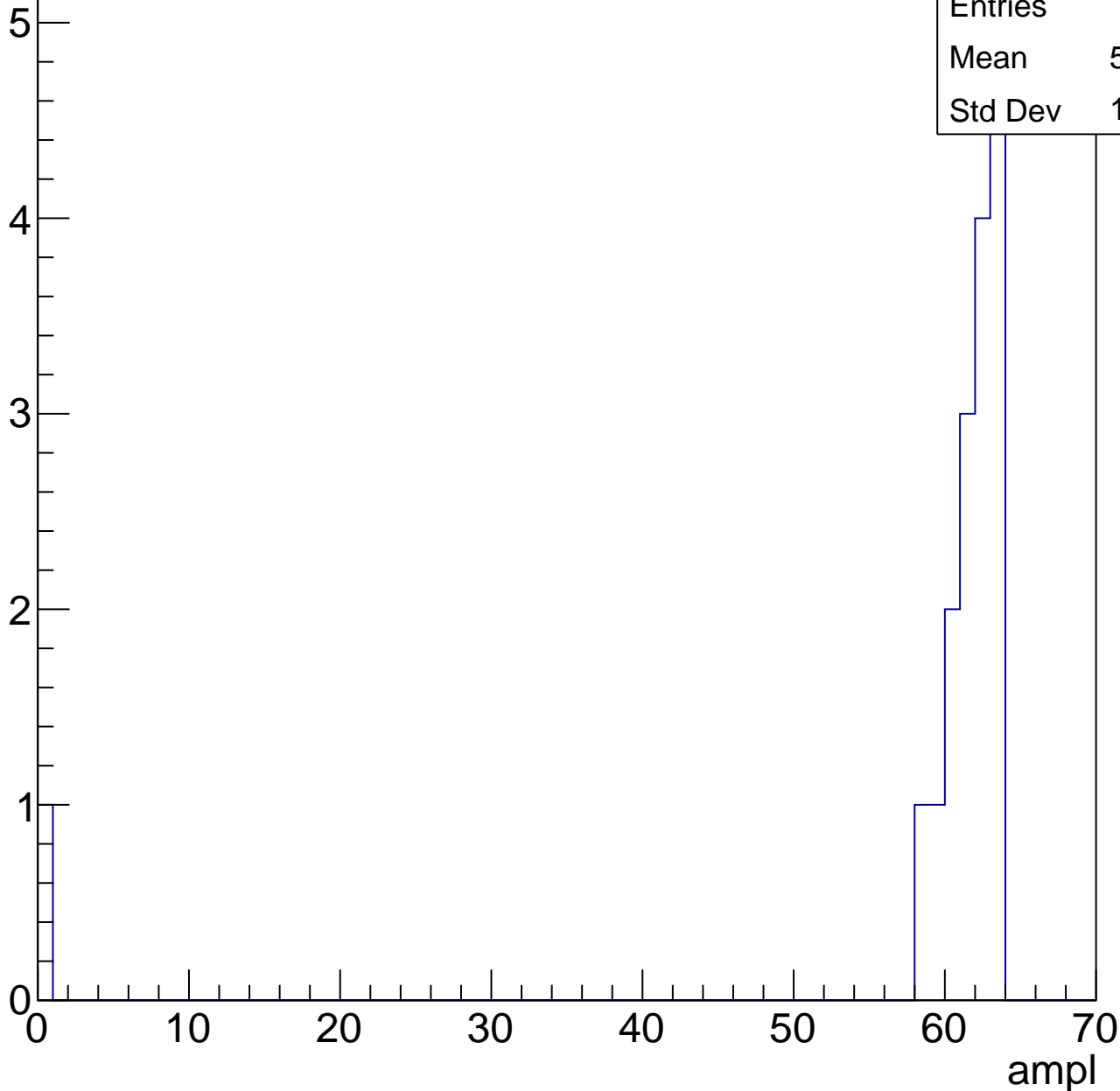


# B1L002S, U8-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	57.82
Std Dev	14.53





# B1L002S, U8-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch72, adc0

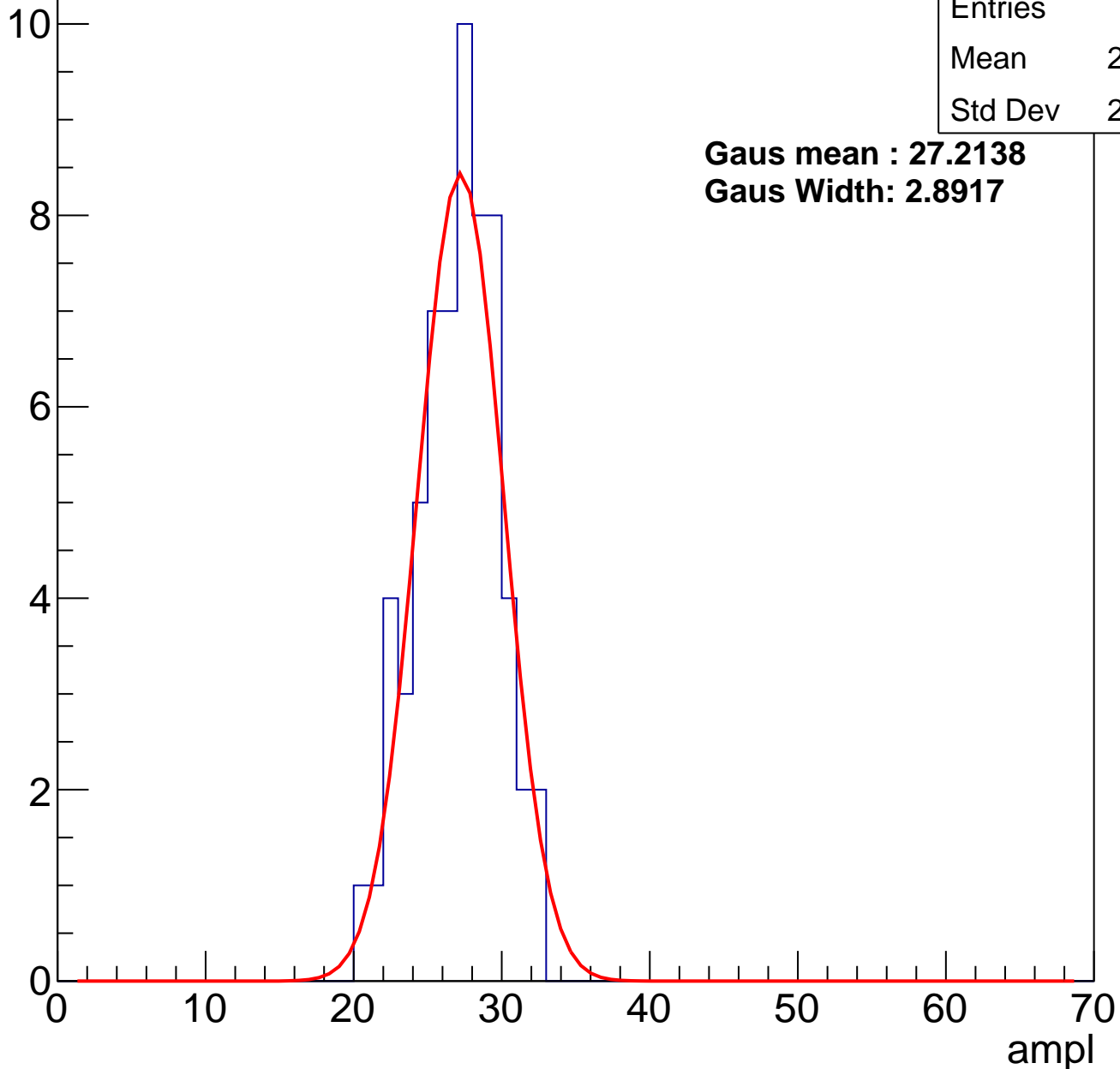
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	26.56
Std Dev	2.722

**Gaus mean : 27.2138**

**Gaus Width: 2.8917**

Entry



# B1L002S, U8-ch72, adc1

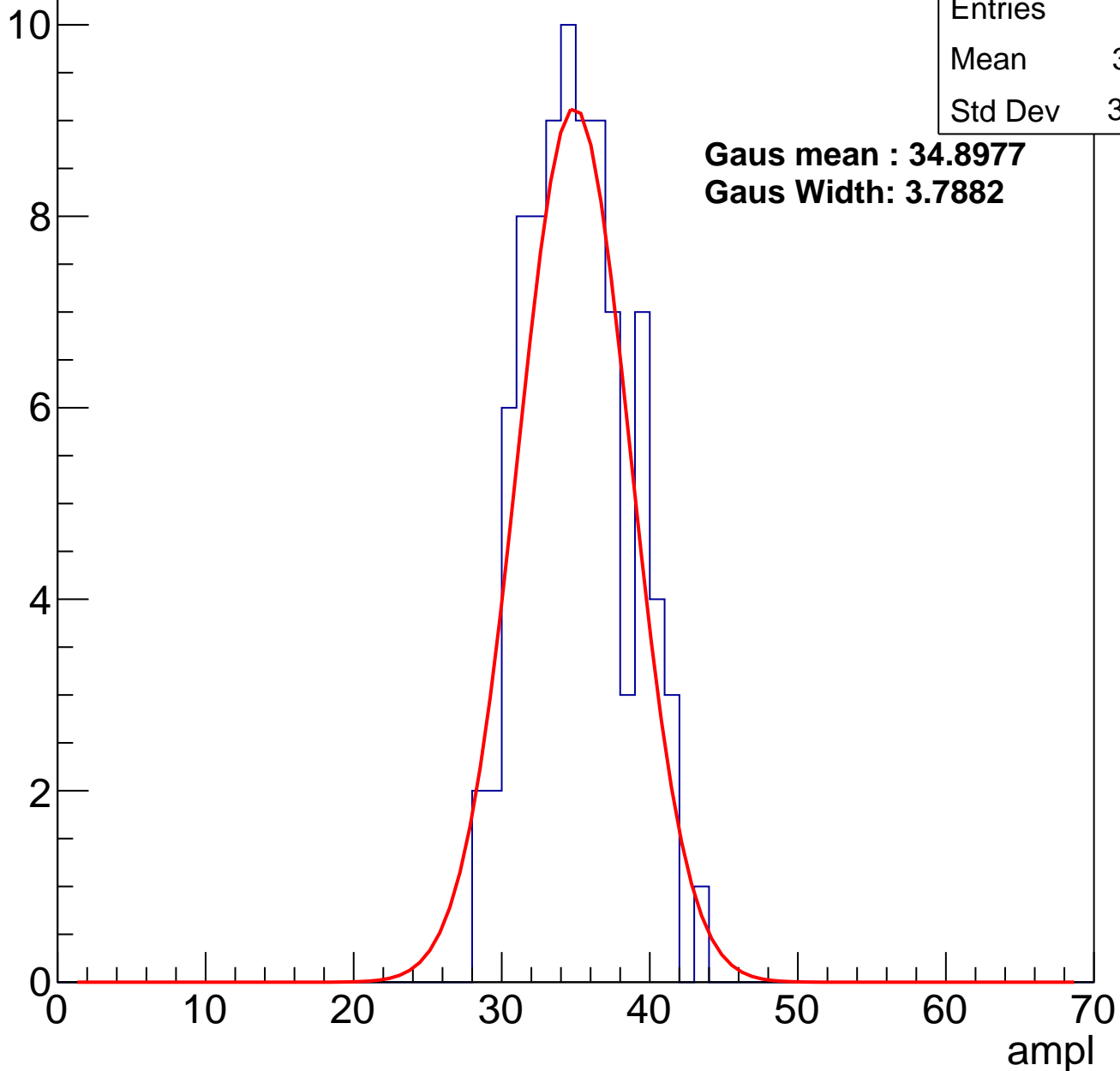
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	88
Mean	34.61
Std Dev	3.372

**Gaus mean : 34.8977**

**Gaus Width: 3.7882**

Entry



# B1L002S, U8-ch72, adc2

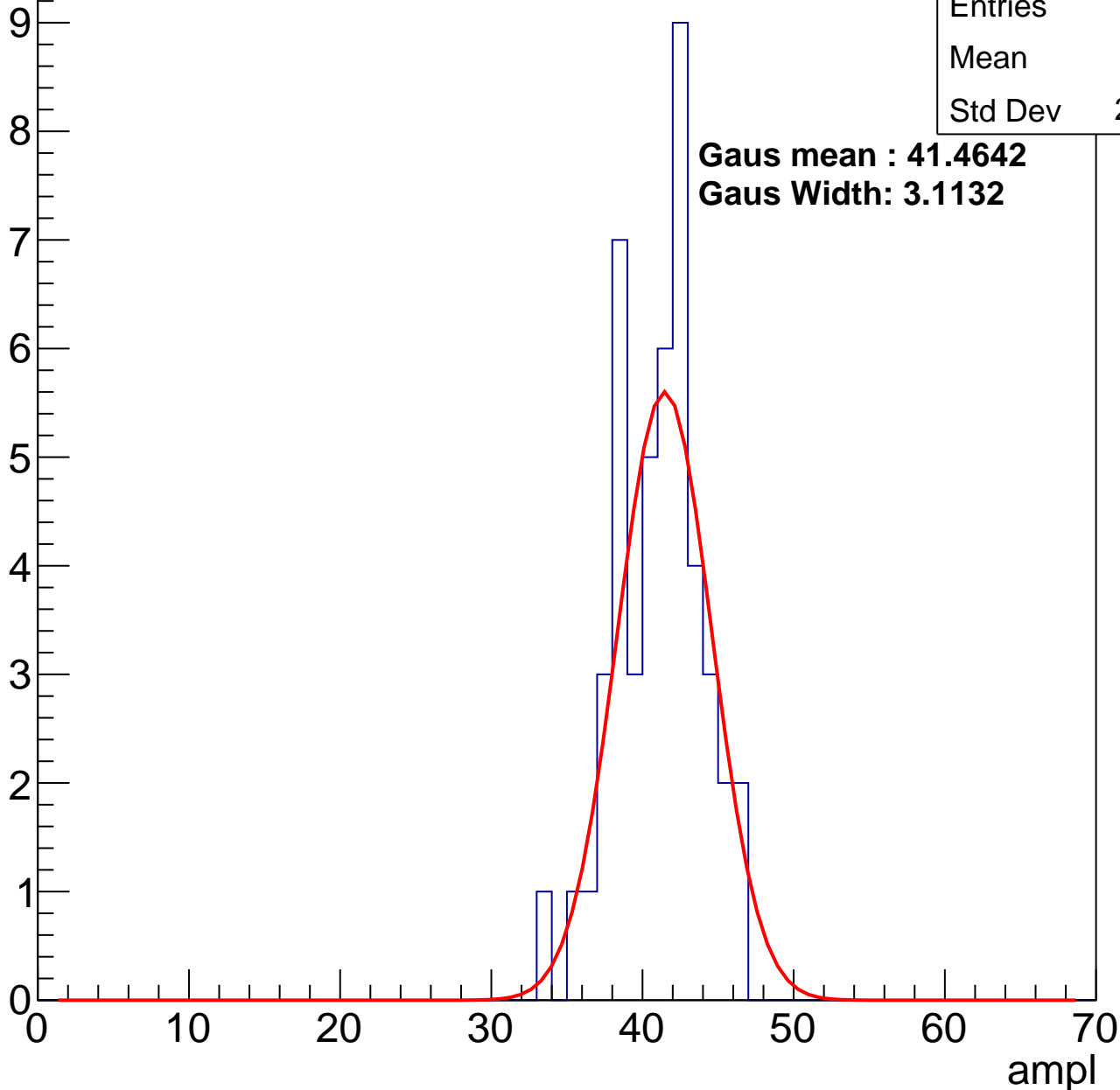
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	40.6
Std Dev	2.841

**Gaus mean : 41.4642**

**Gaus Width: 3.1132**

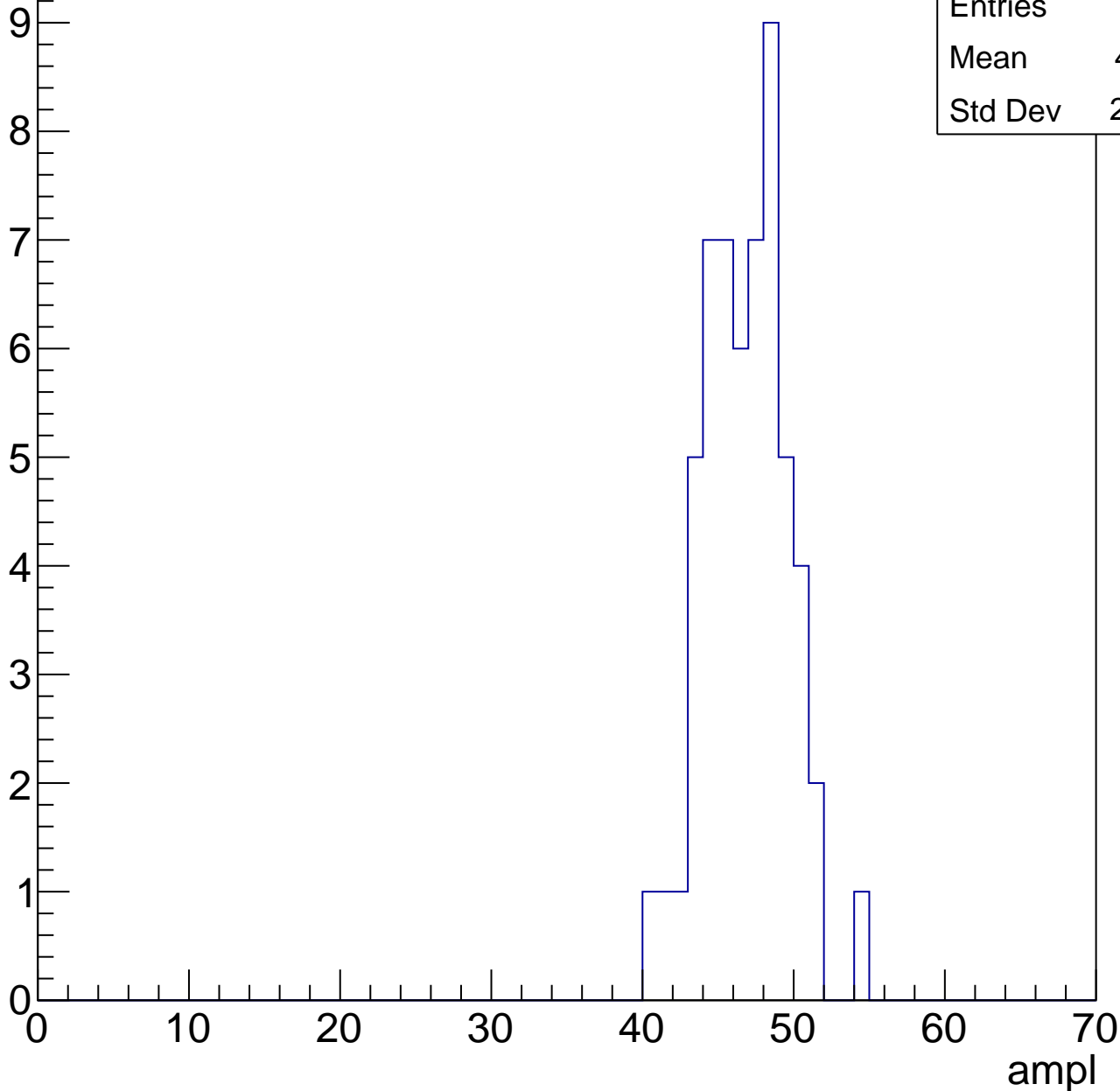


# B1L002S, U8-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	46.41
Std Dev	2.718

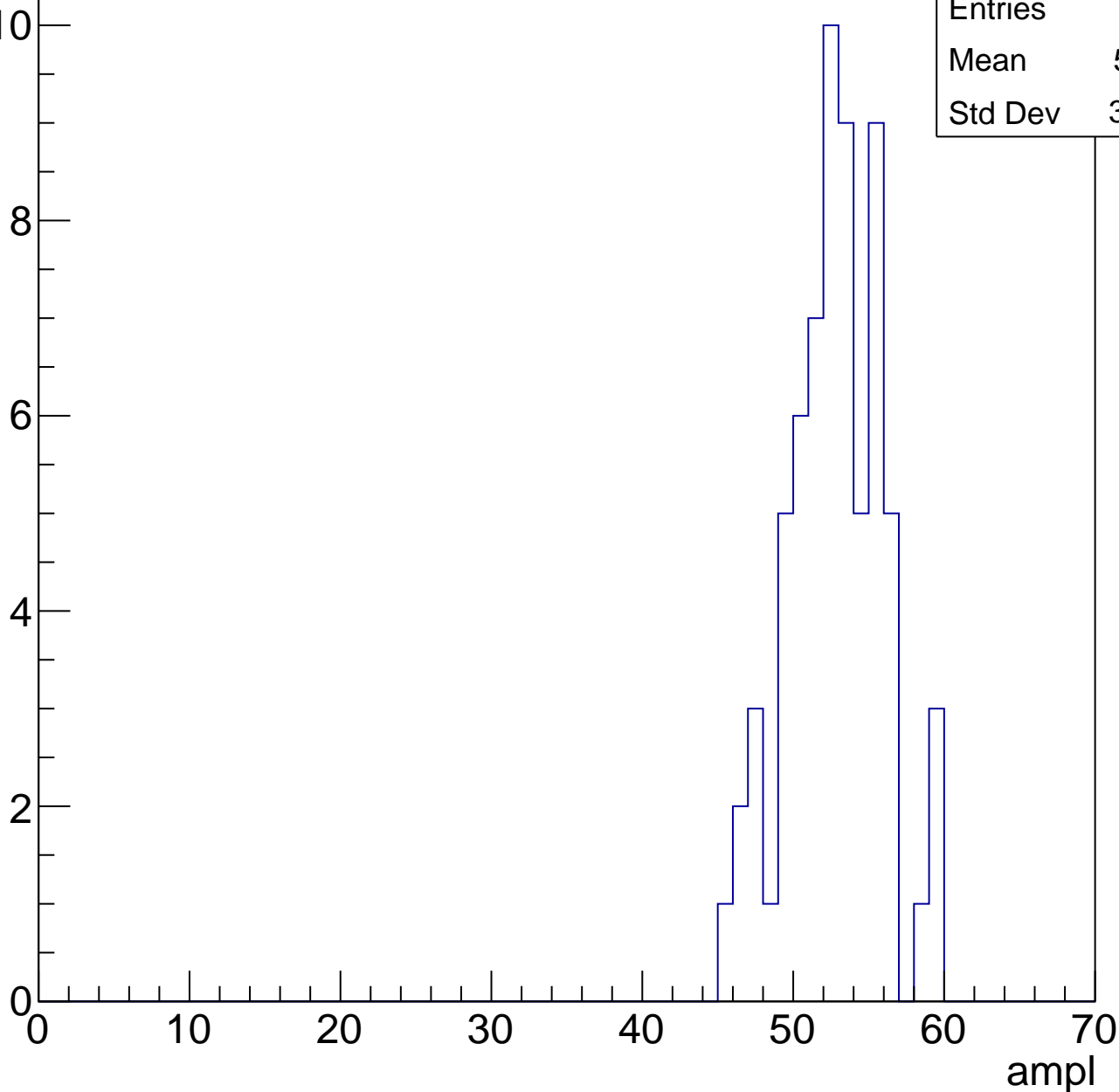


# B1L002S, U8-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

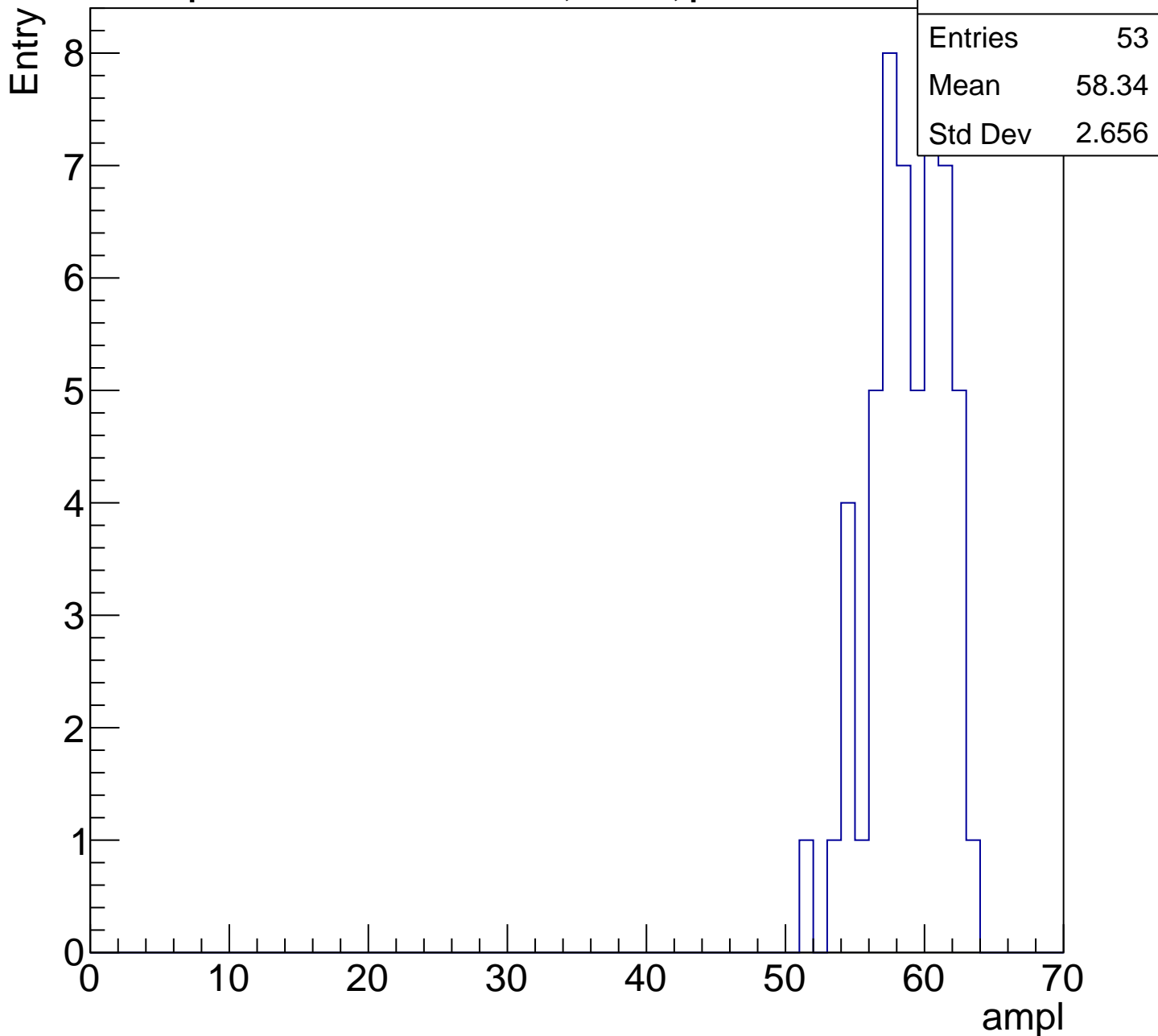
Entry

Entries	67
Mean	52.31
Std Dev	3.125



# B1L002S, U8-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

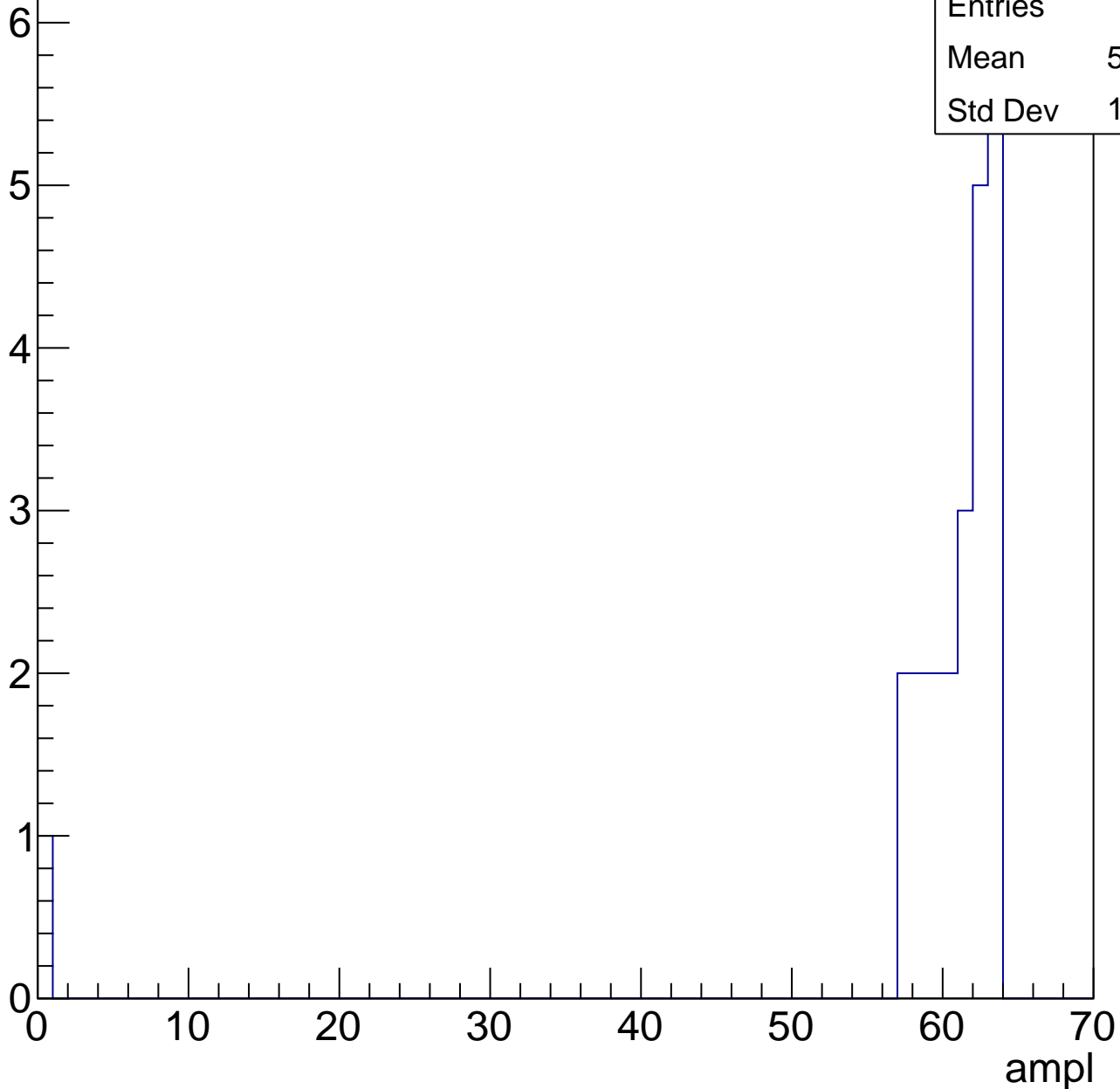


# B1L002S, U8-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.22
Std Dev	12.57

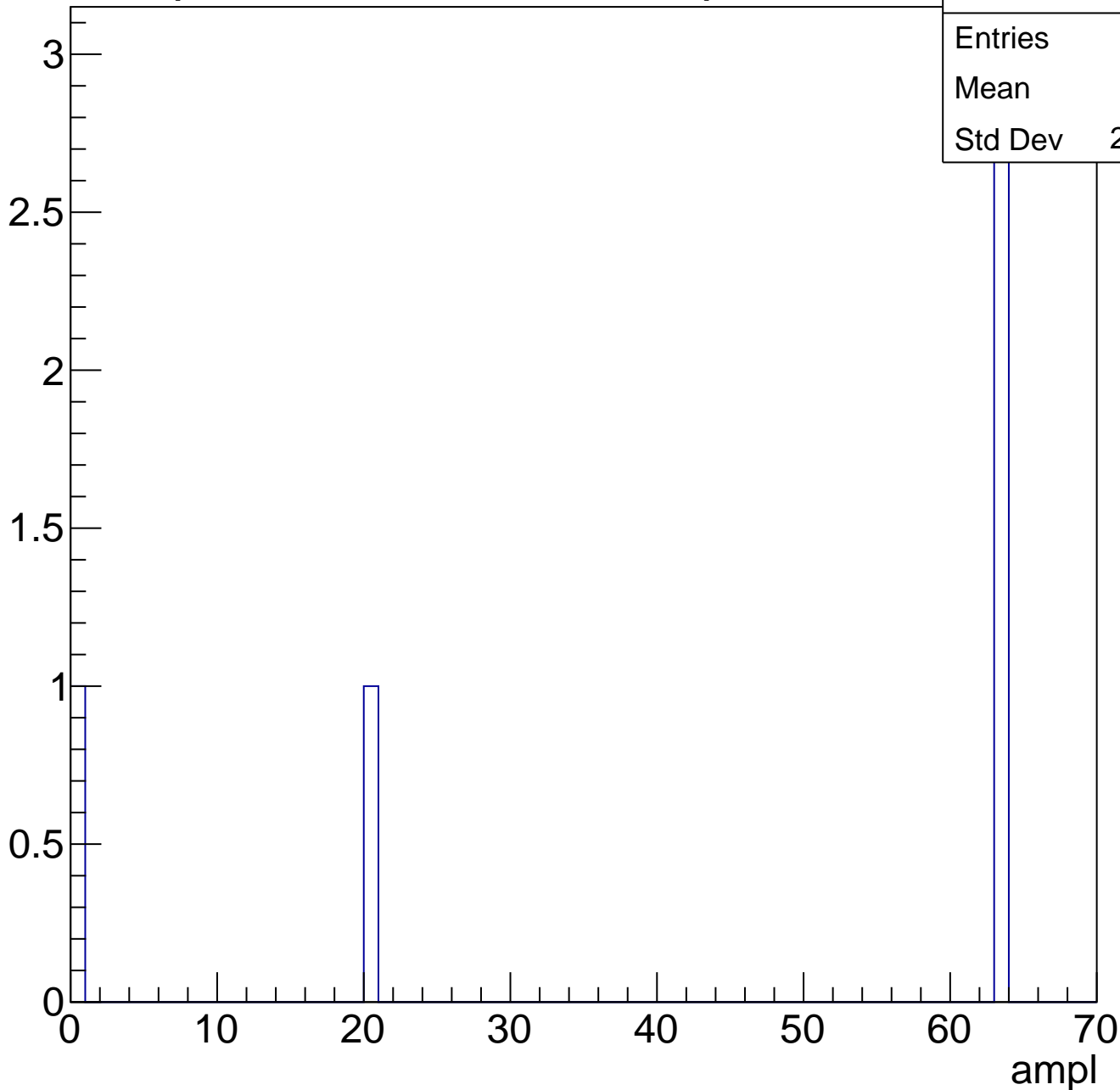




# B1L002S, U8-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	41.8
Std Dev	26.72

# B1L002S, U8-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	29.86
Std Dev	3.01

**Gaus mean : 30.2812**

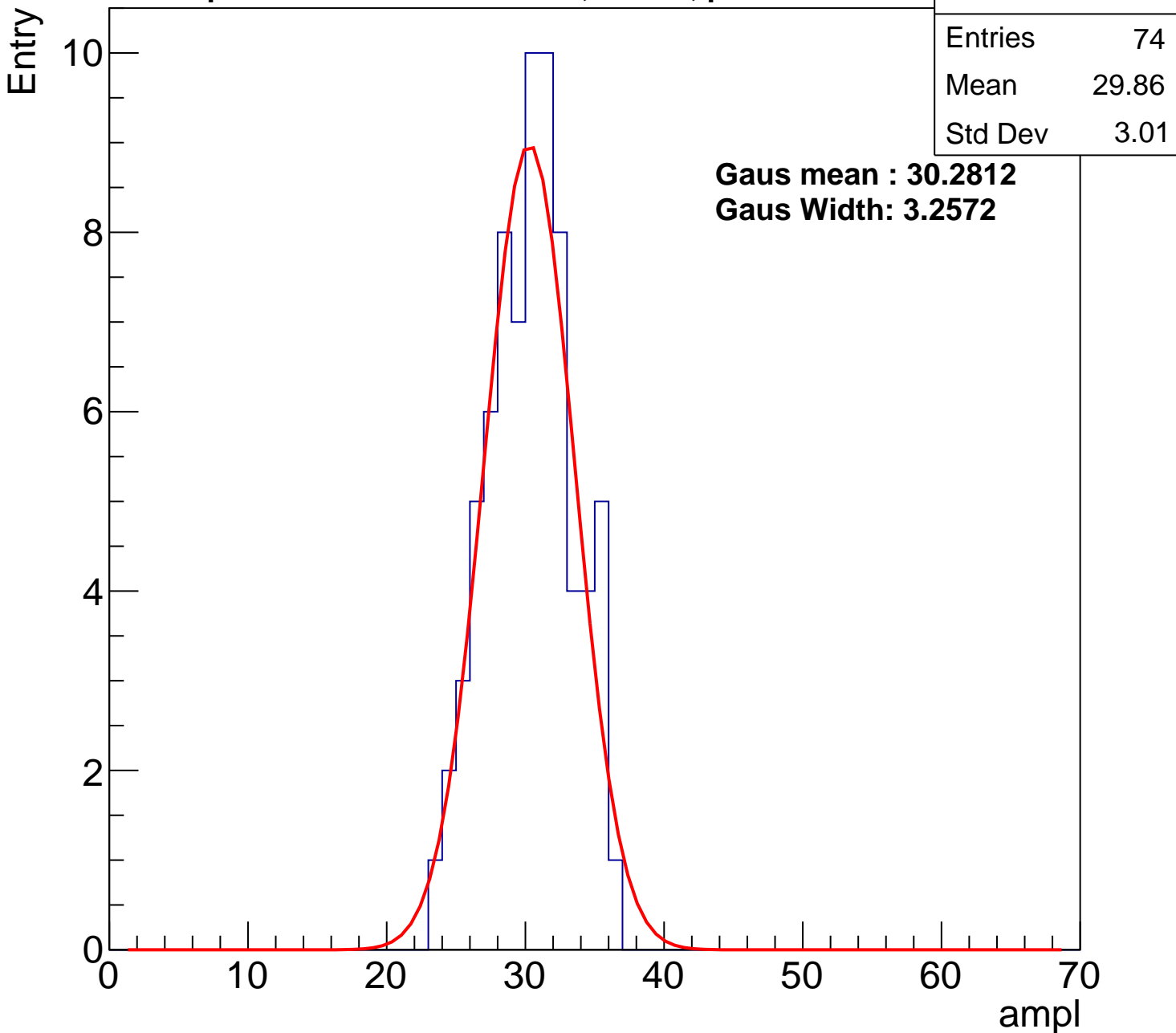
**Gaus Width: 3.2572**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch73, adc1

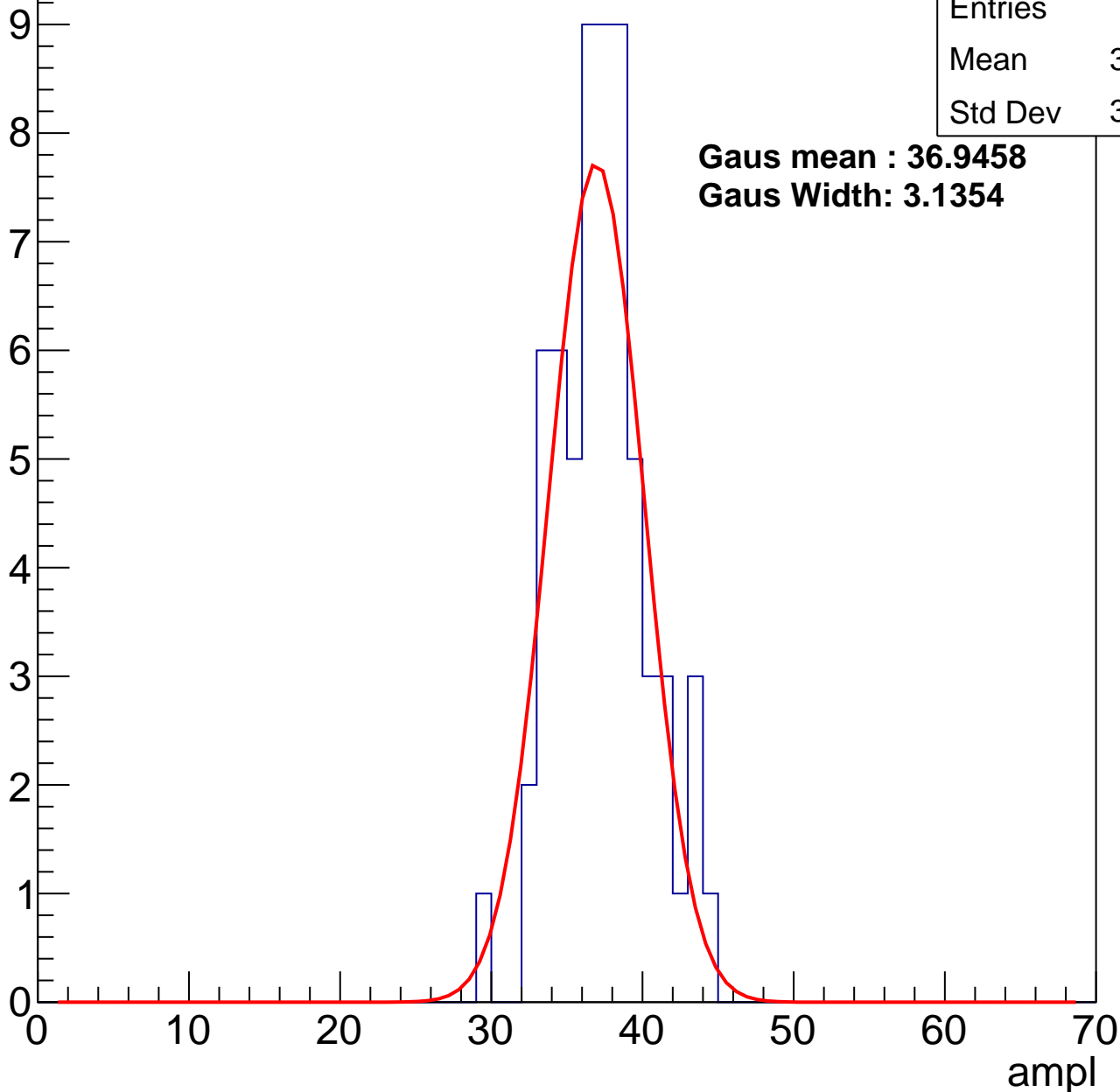
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.86
Std Dev	3.028

**Gaus mean : 36.9458**

**Gaus Width: 3.1354**



# B1L002S, U8-ch73, adc2

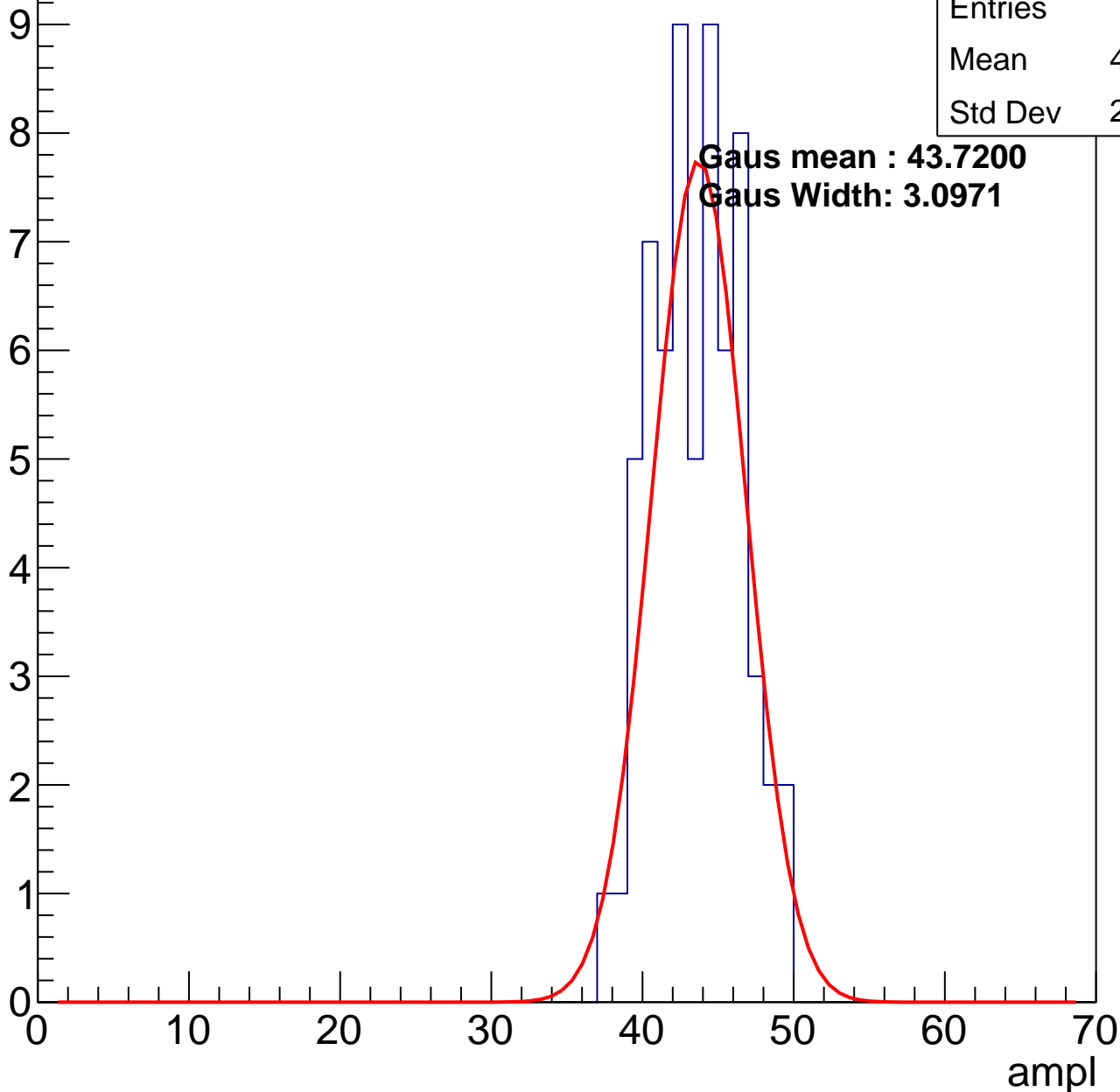
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	43.09
Std Dev	2.827

**Gaus mean : 43.7200**

**Gaus Width: 3.0971**

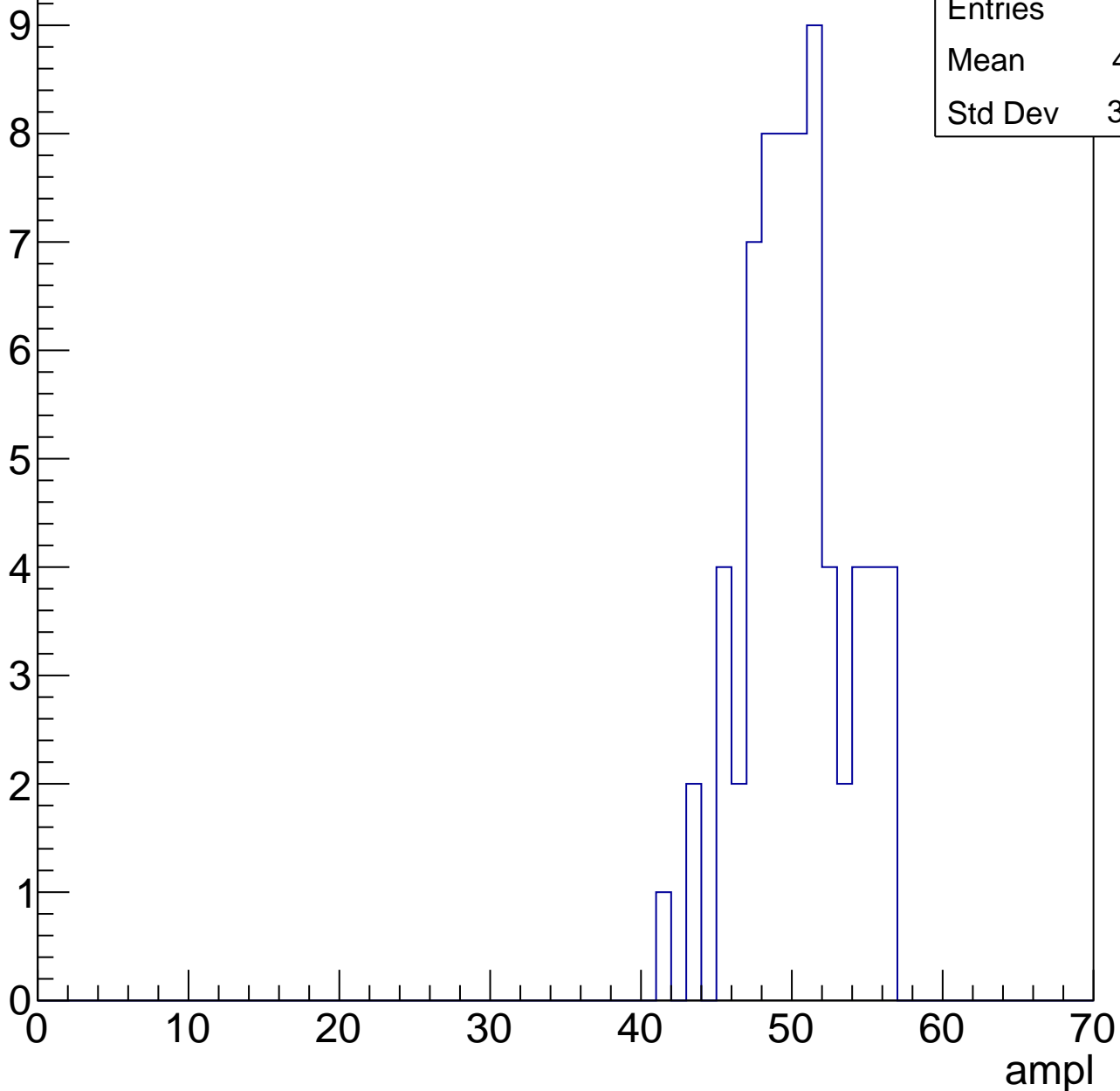


# B1L002S, U8-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

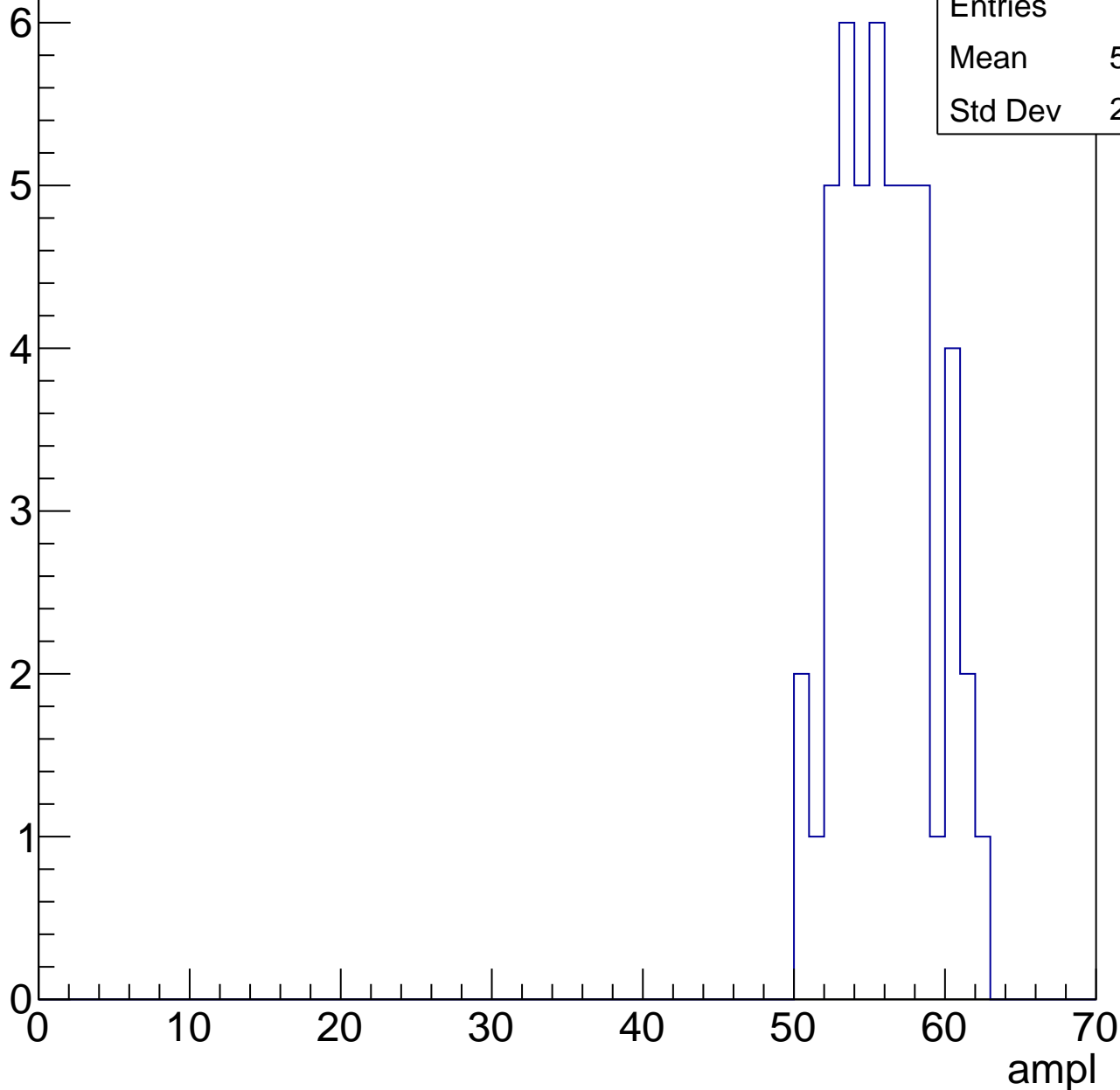
Entries	67
Mean	49.81
Std Dev	3.373



# B1L002S, U8-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

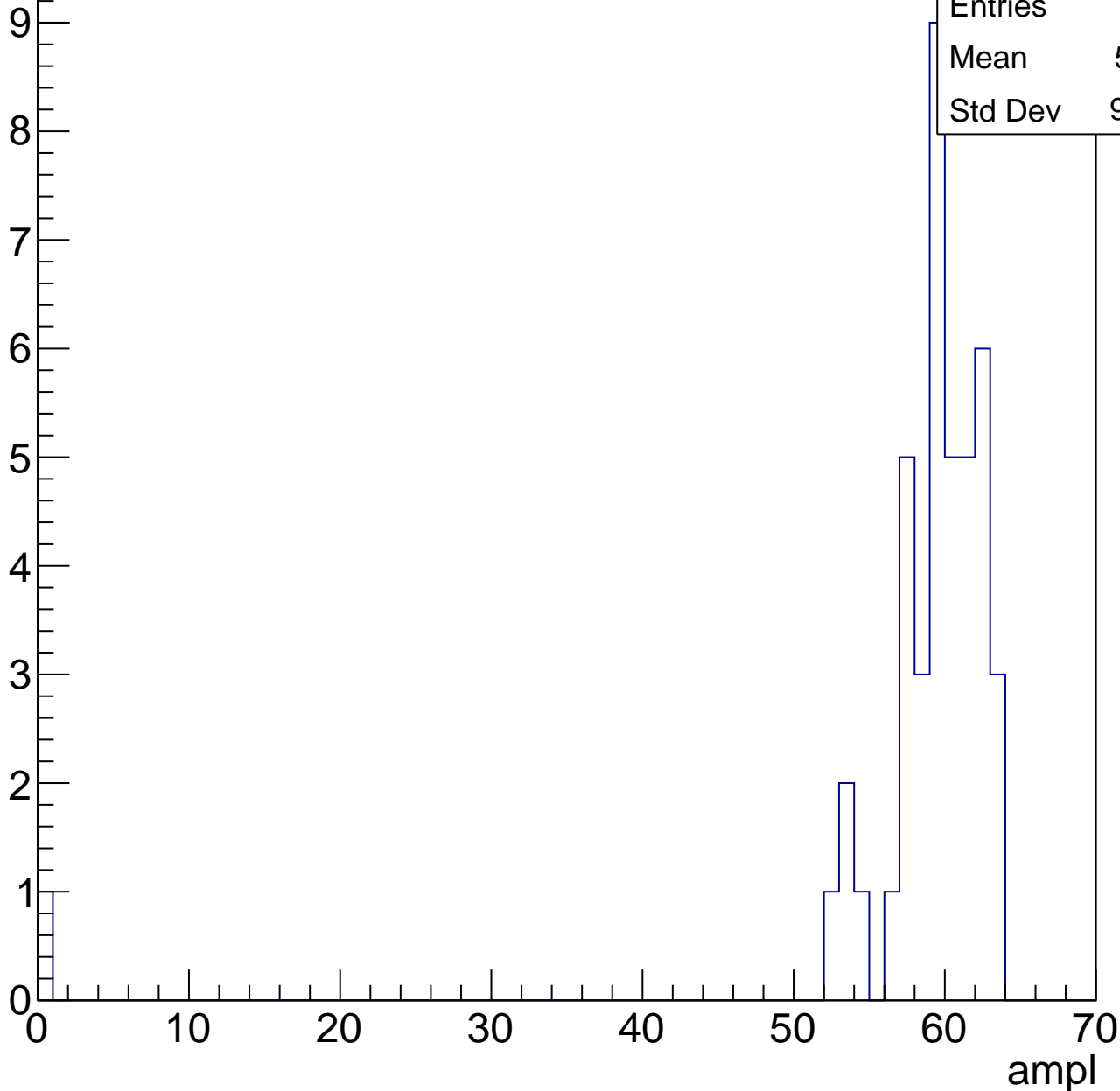


# B1L002S, U8-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	57.71
Std Dev	9.407

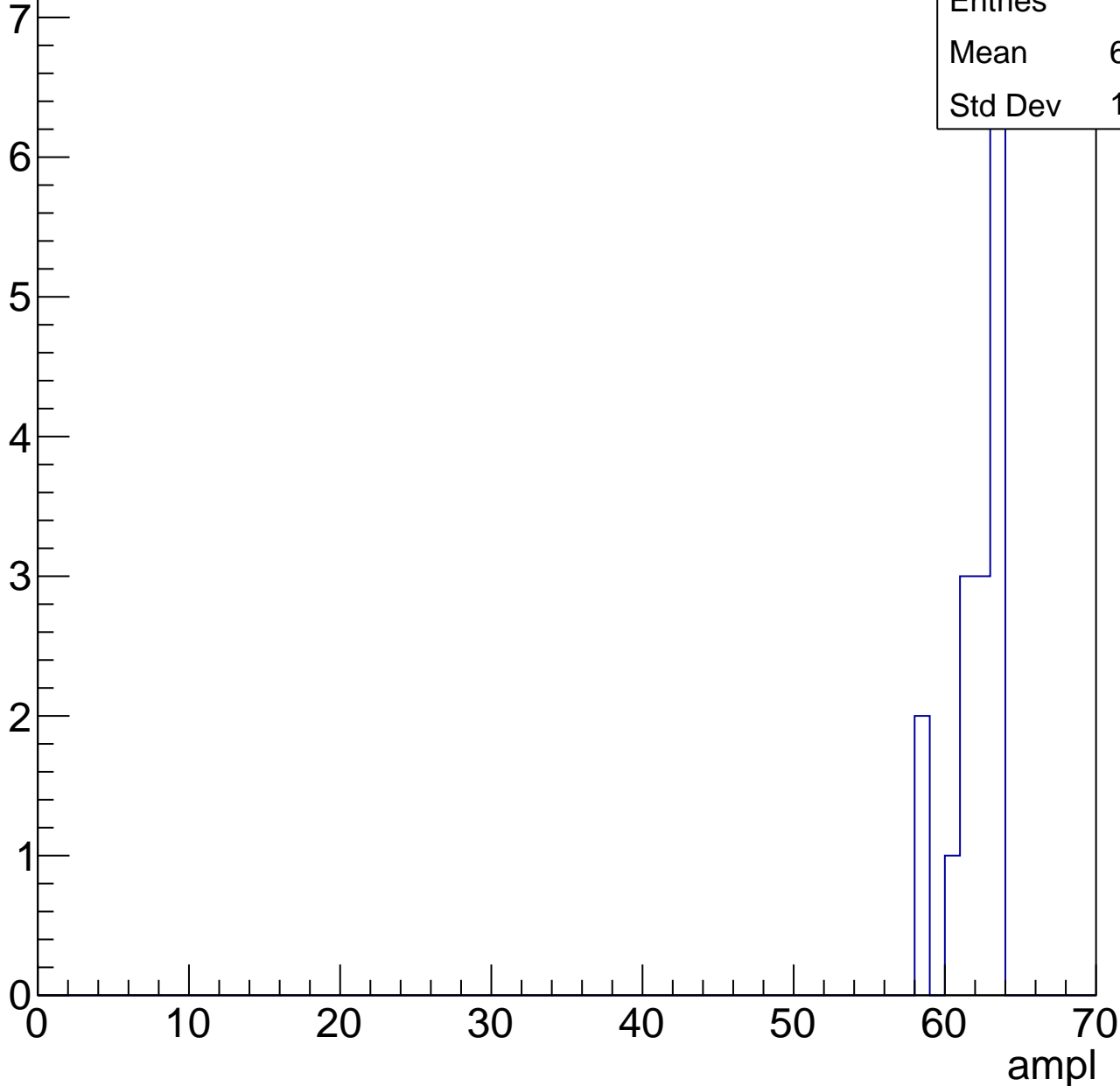


# B1L002S, U8-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	61.62
Std Dev	1.654





# B1L002S, U8-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch74, adc0

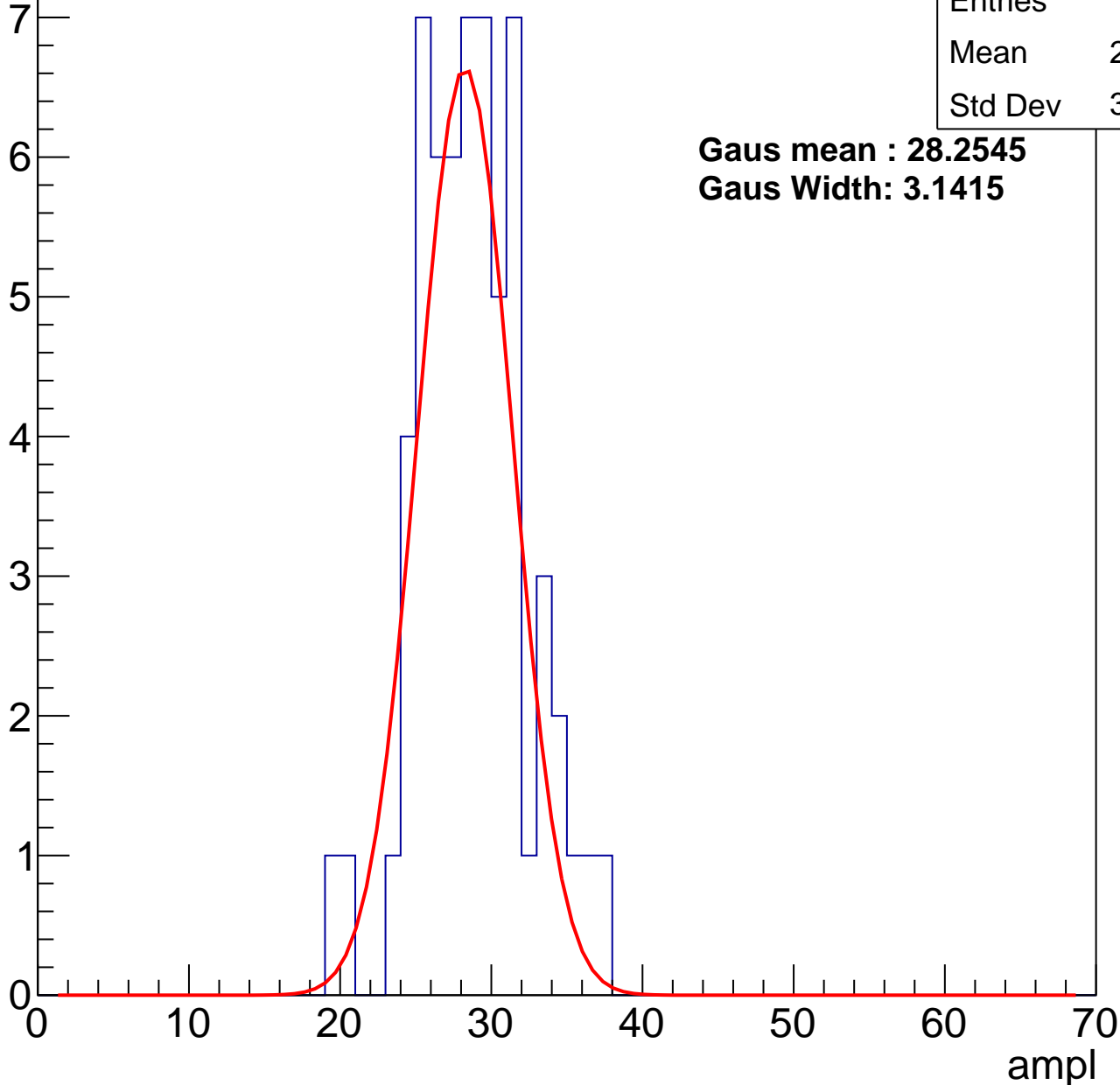
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.26
Std Dev	3.552

**Gaus mean : 28.2545**

**Gaus Width: 3.1415**



# B1L002S, U8-ch74, adc1

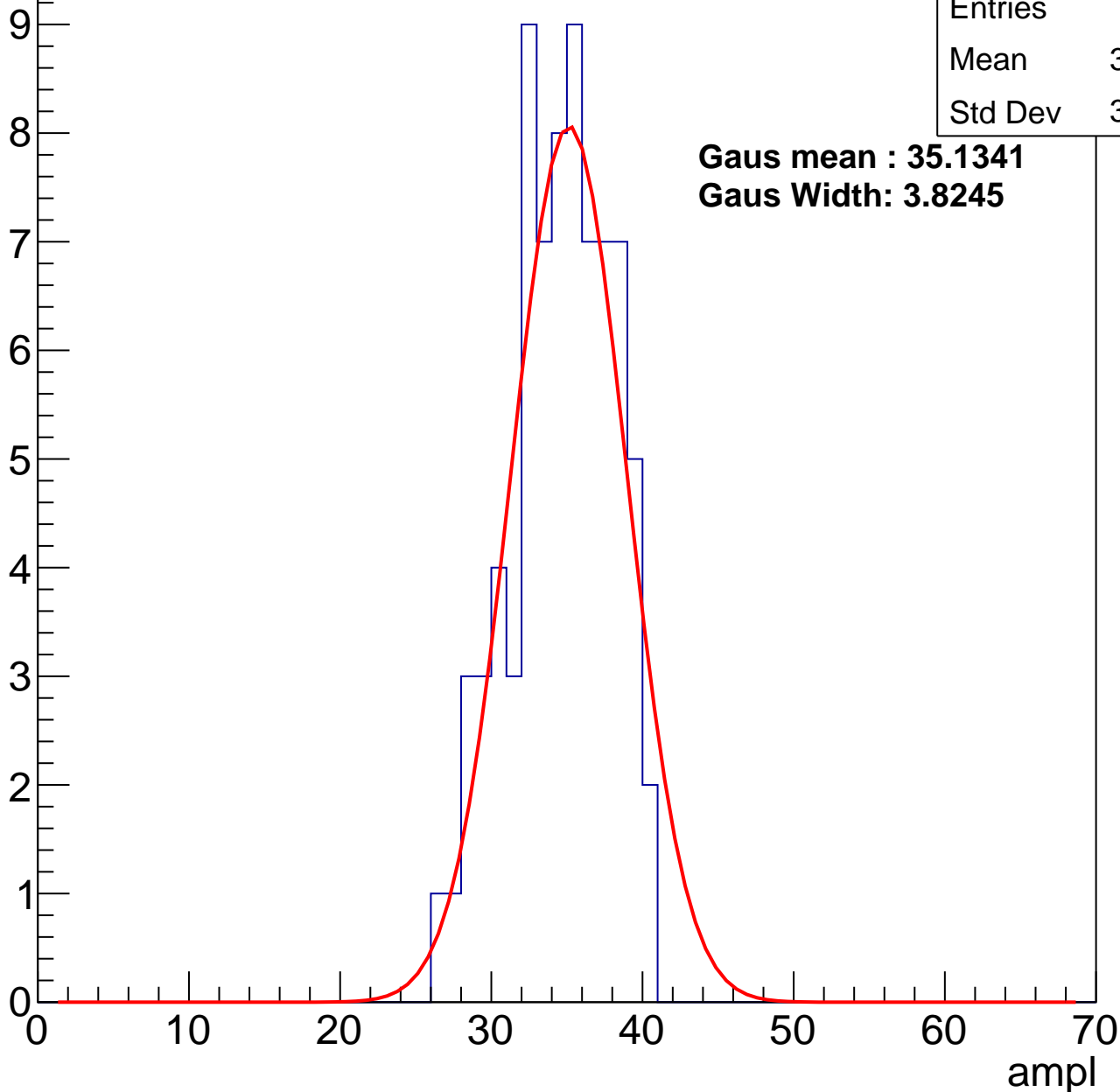
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	34.14
Std Dev	3.319

**Gaus mean : 35.1341**

**Gaus Width: 3.8245**



# B1L002S, U8-ch74, adc2

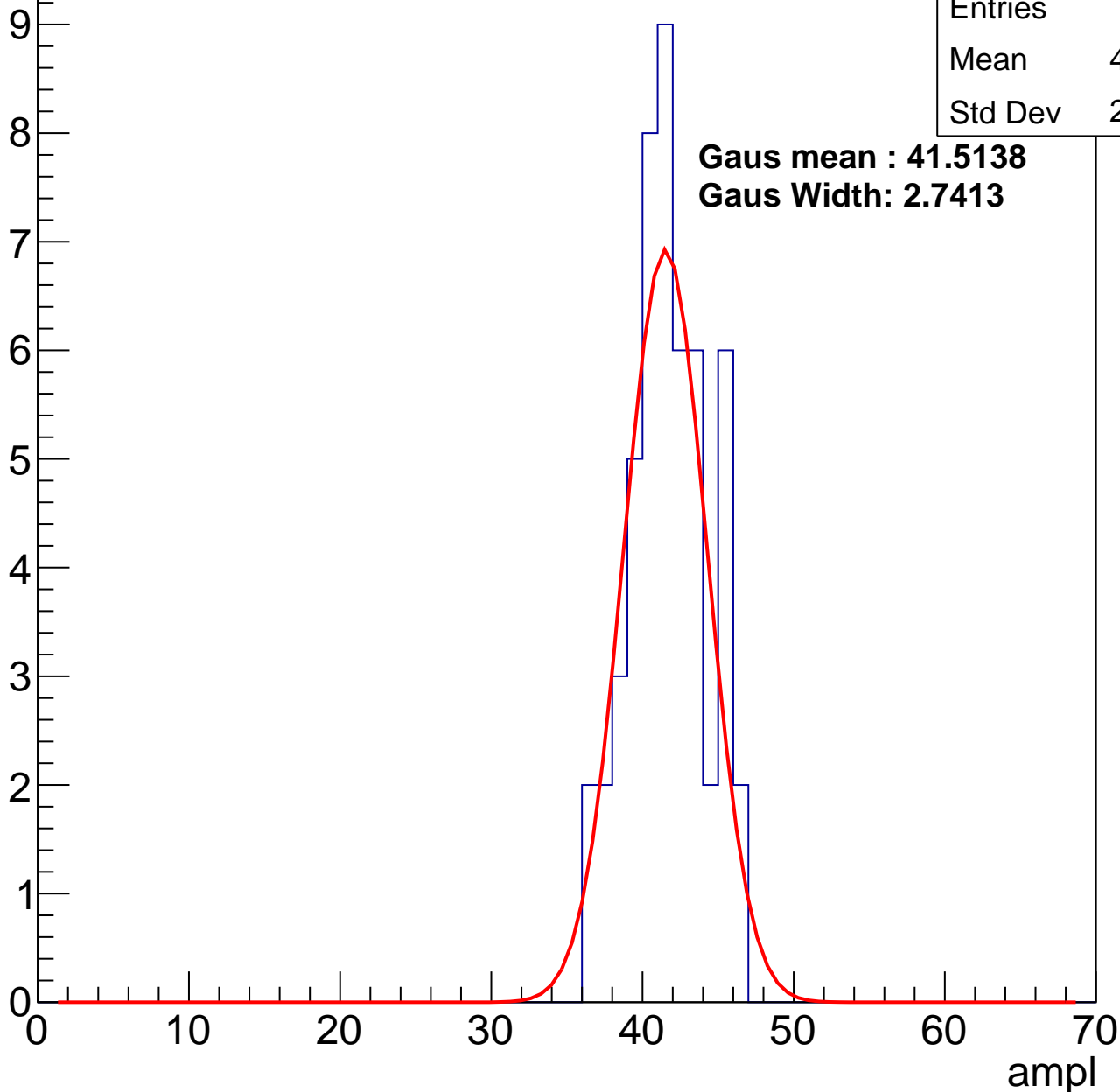
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.25
Std Dev	2.535

**Gaus mean : 41.5138**

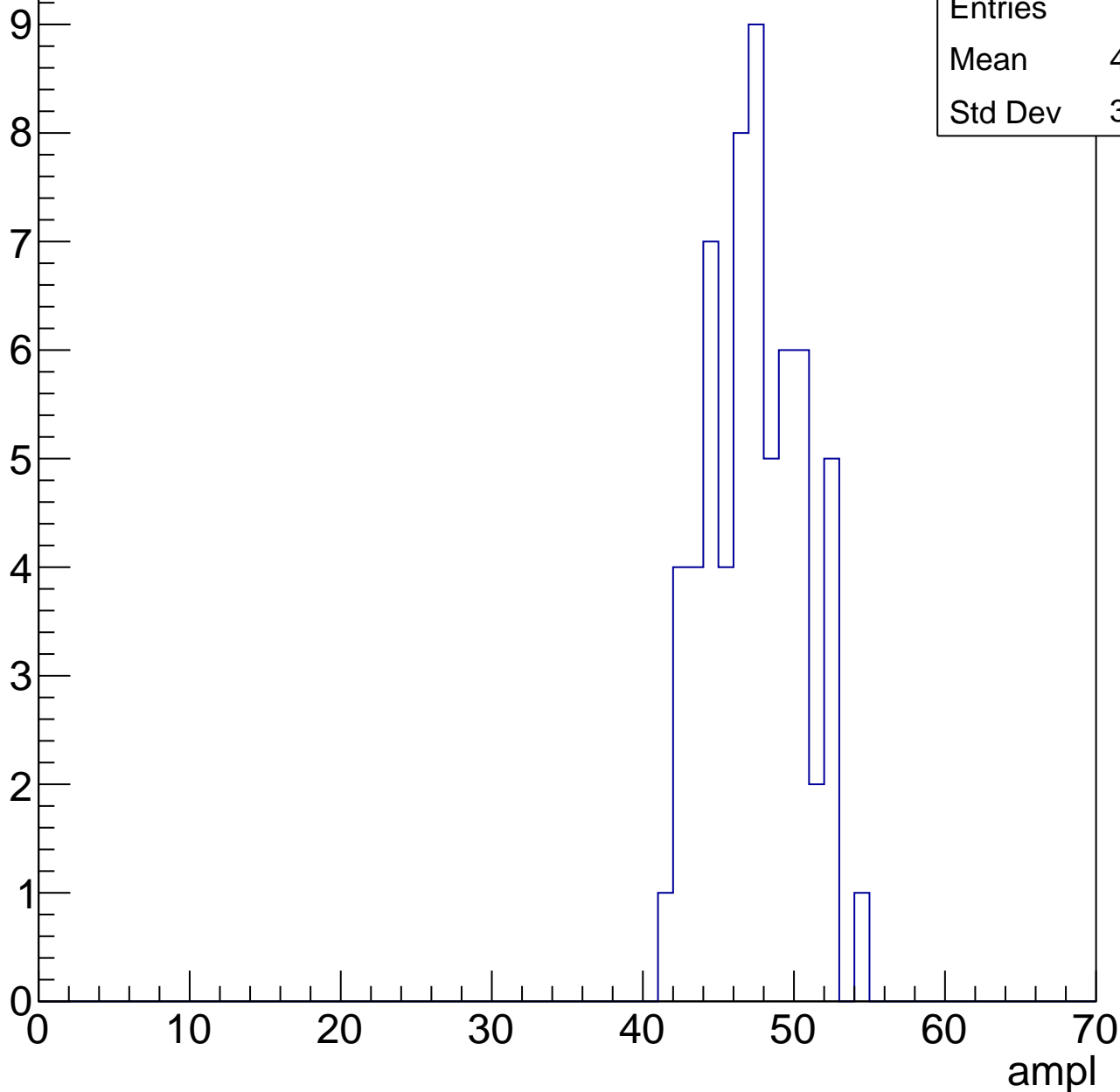
**Gaus Width: 2.7413**



# B1L002S, U8-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



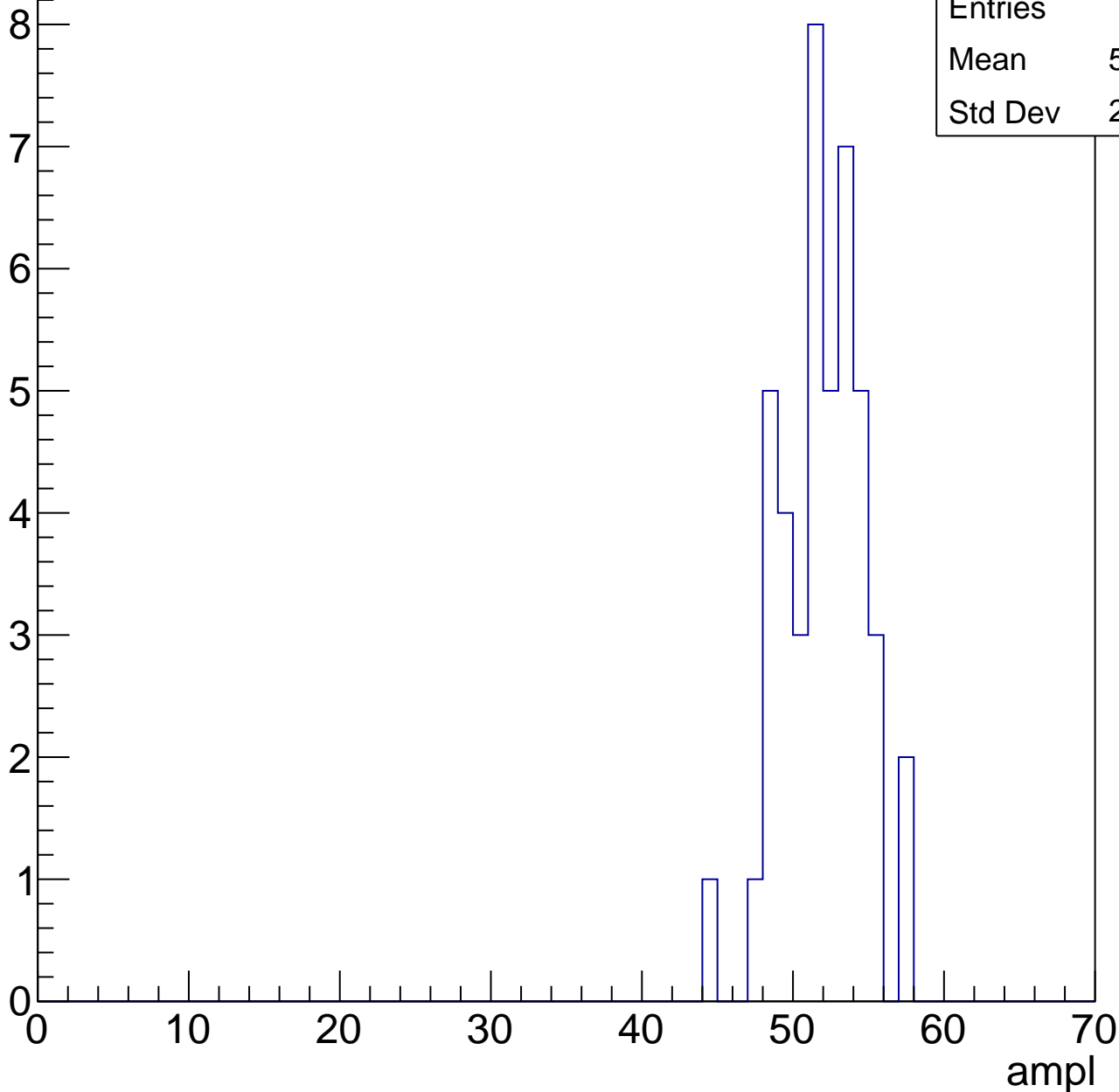
Entries	62
Mean	46.94
Std Dev	3.047

# B1L002S, U8-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.48
Std Dev	2.684



# B1L002S, U8-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

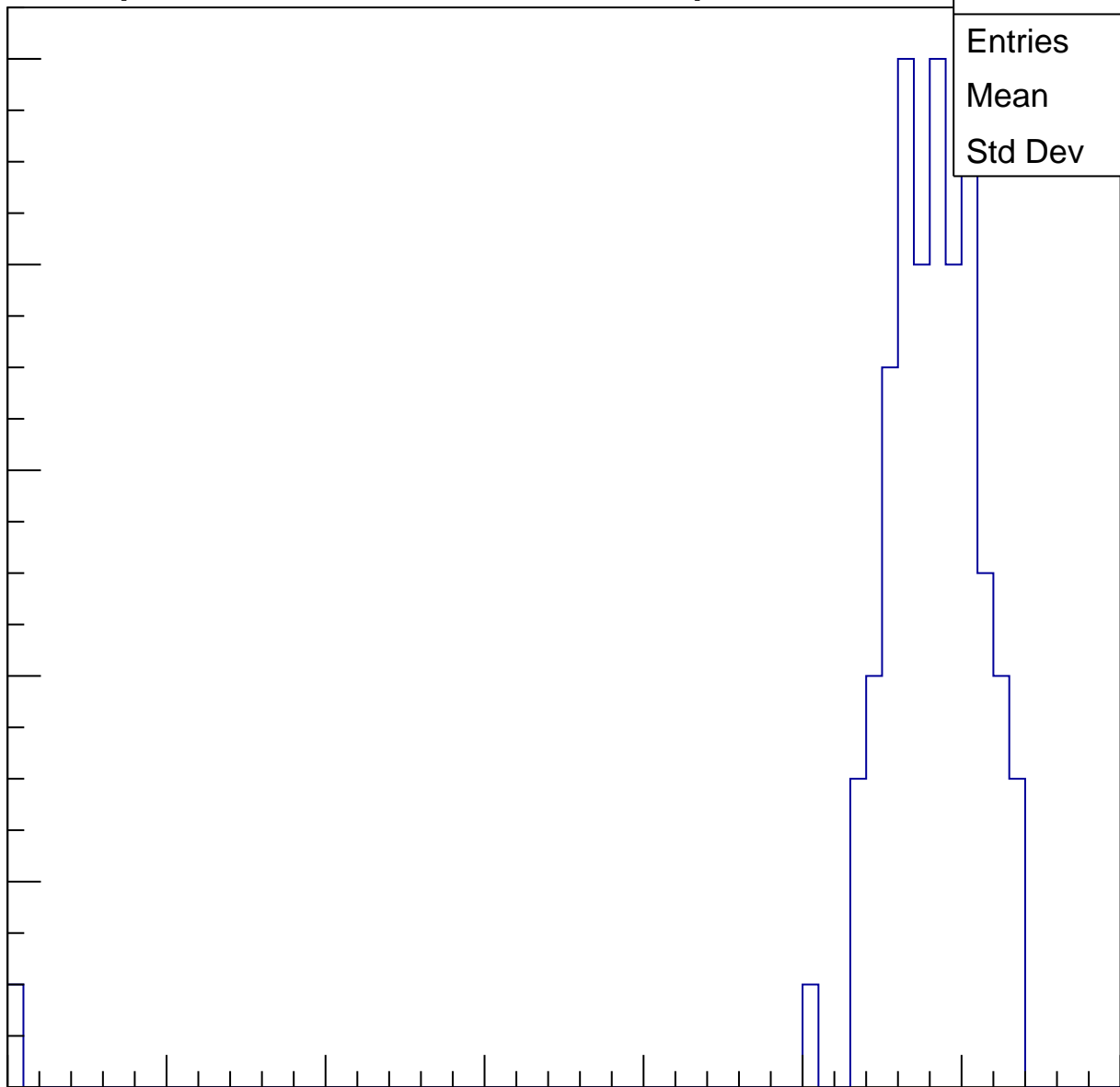
Entry

10  
8  
6  
4  
2  
0

Entries	73
Mean	56.99
Std Dev	7.244

ampl

0 10 20 30 40 50 60 70

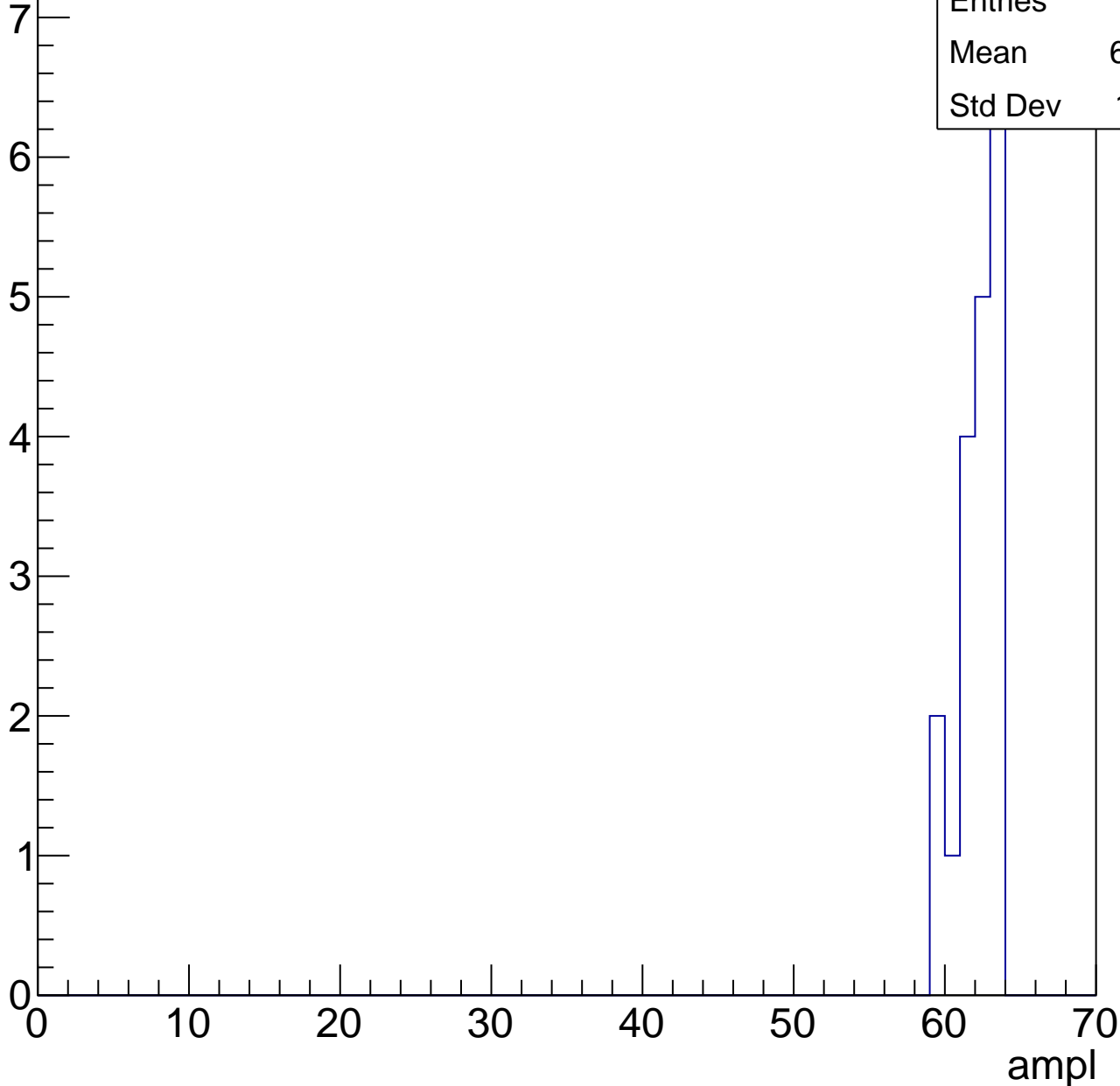


# B1L002S, U8-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.74
Std Dev	1.291

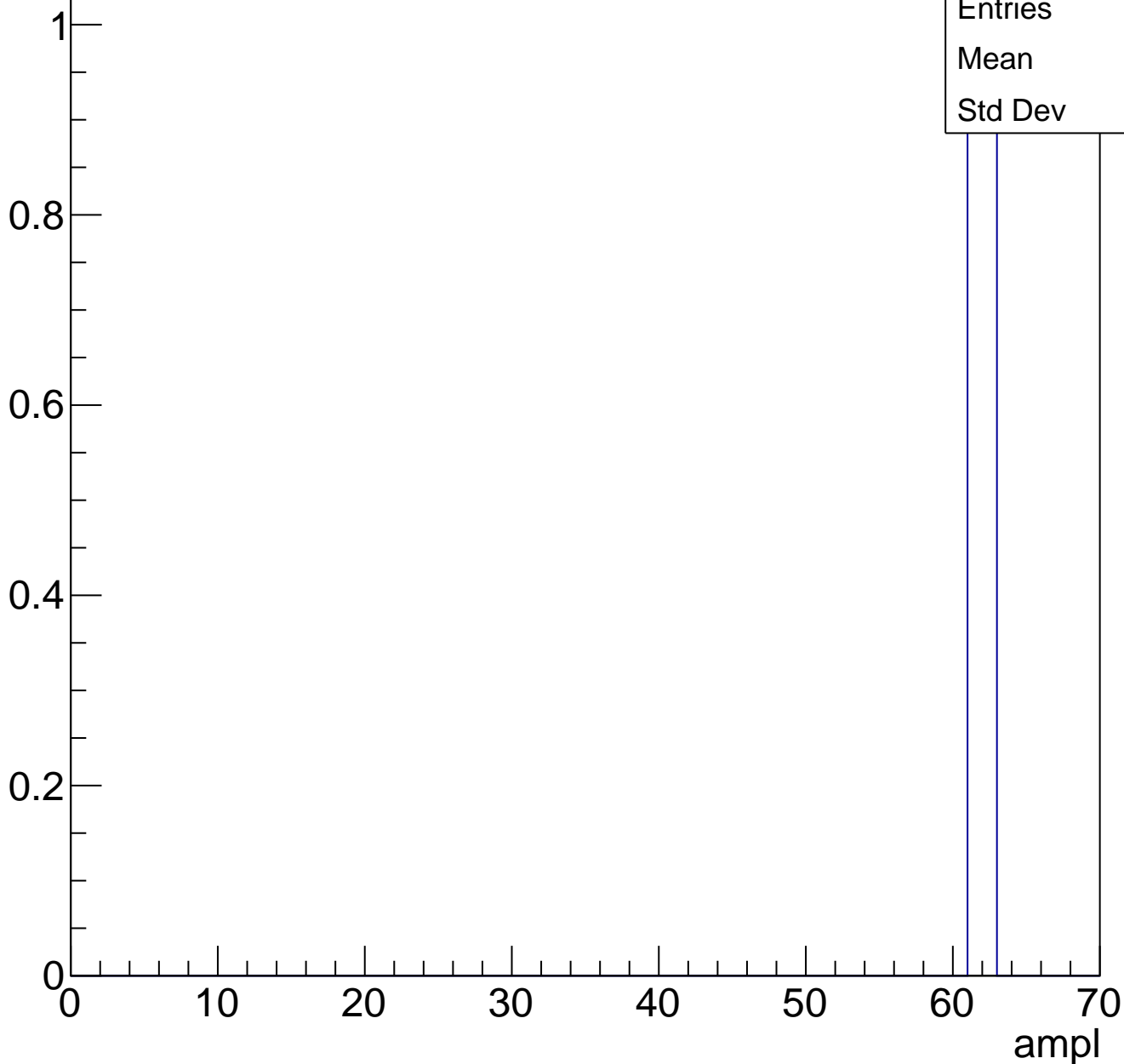




# B1L002S, U8-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch75, adc0

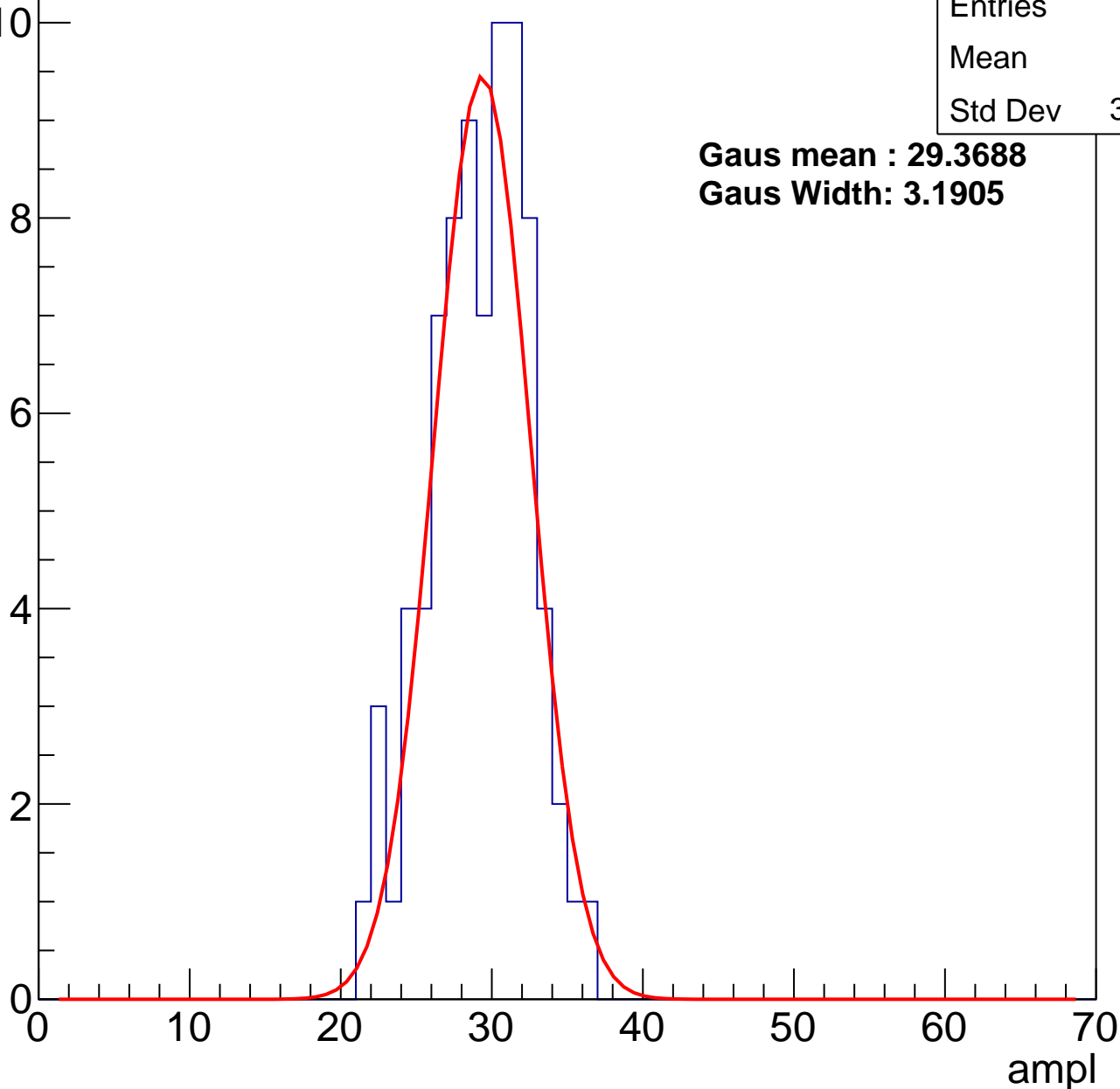
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	28.7
Std Dev	3.219

**Gaus mean : 29.3688**

**Gaus Width: 3.1905**



# B1L002S, U8-ch75, adc1

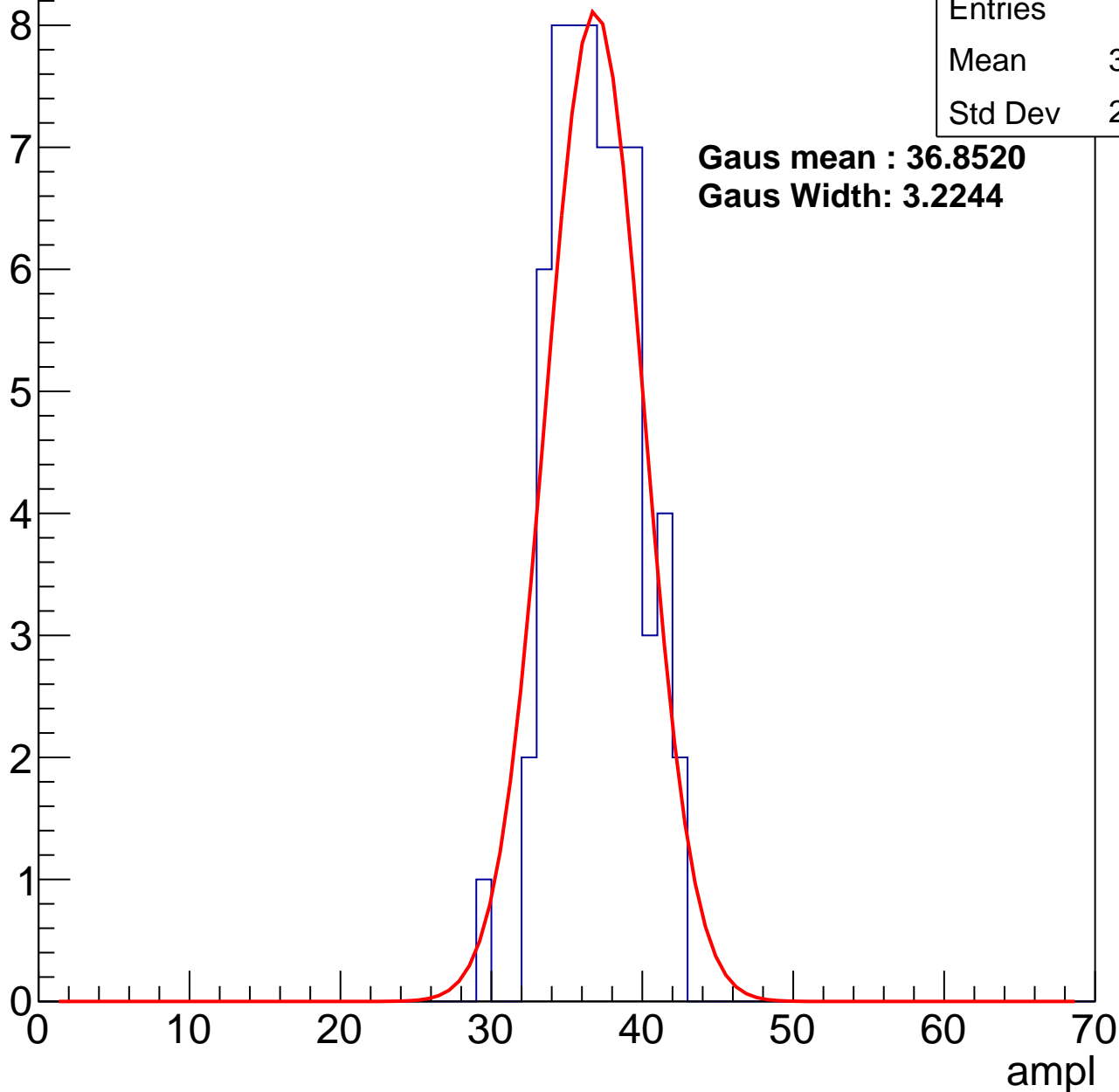
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.46
Std Dev	2.759

**Gaus mean : 36.8520**

**Gaus Width: 3.2244**



# B1L002S, U8-ch75, adc2

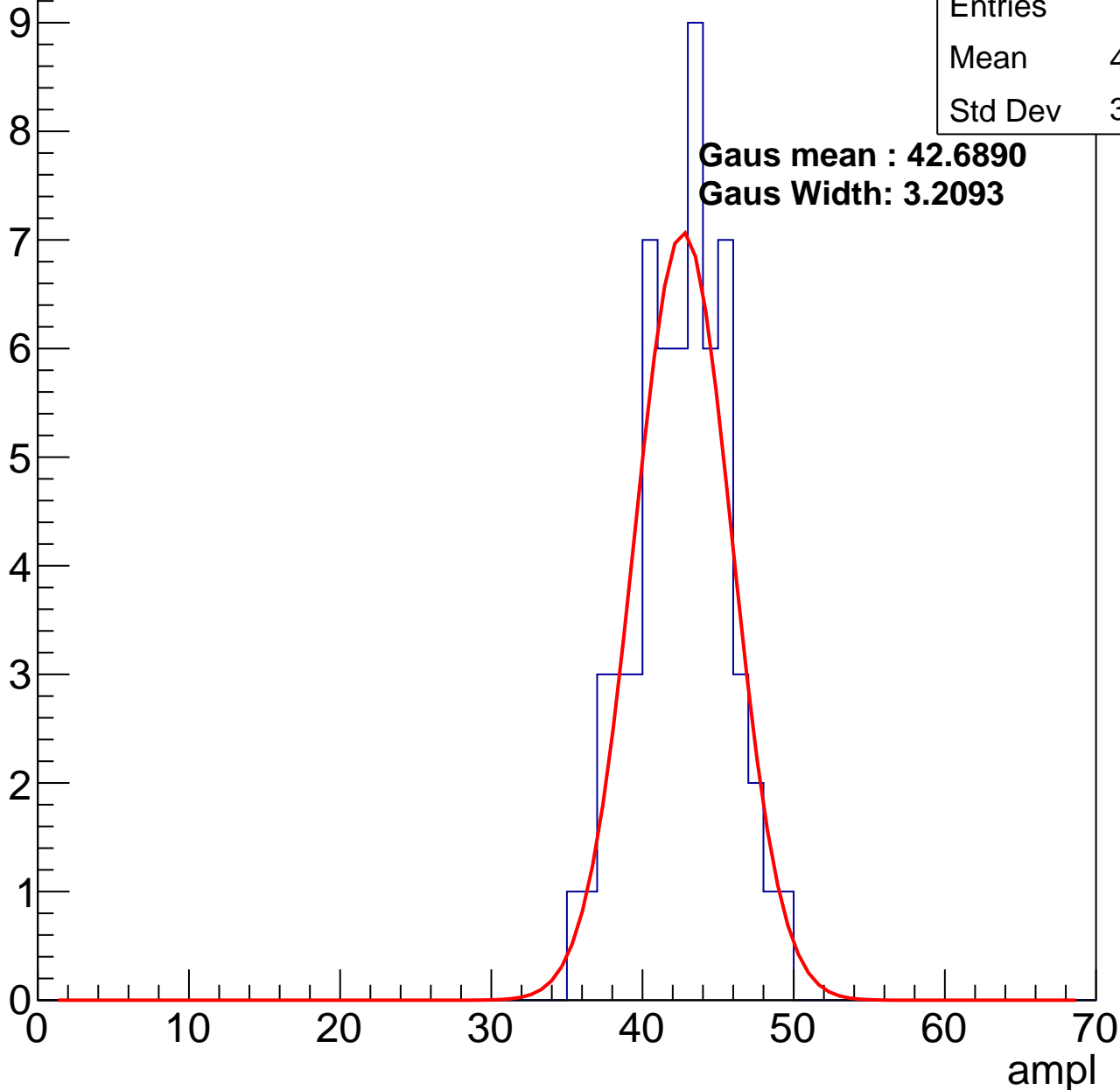
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.14
Std Dev	3.045

**Gaus mean : 42.6890**

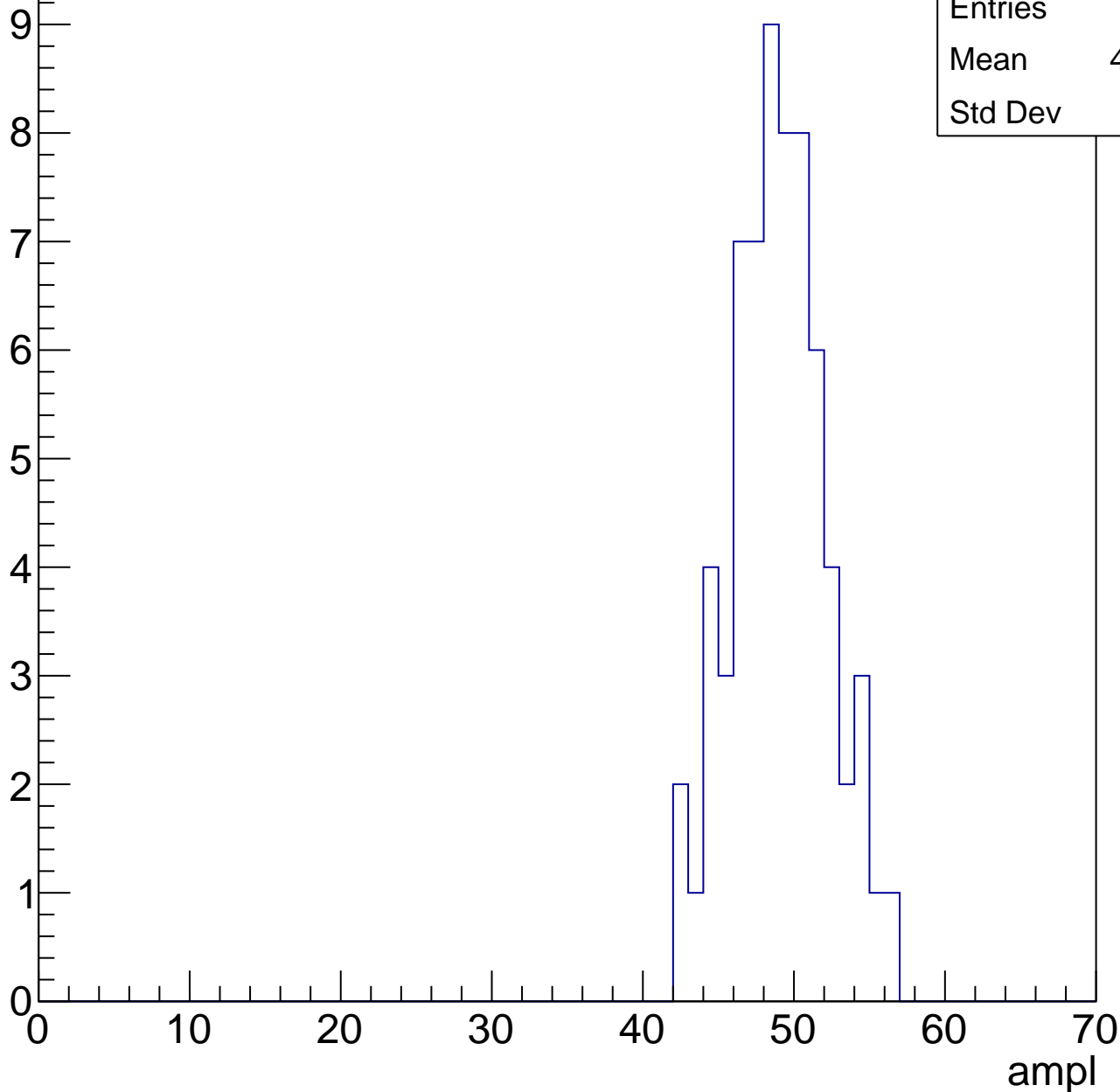
**Gaus Width: 3.2093**



# B1L002S, U8-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



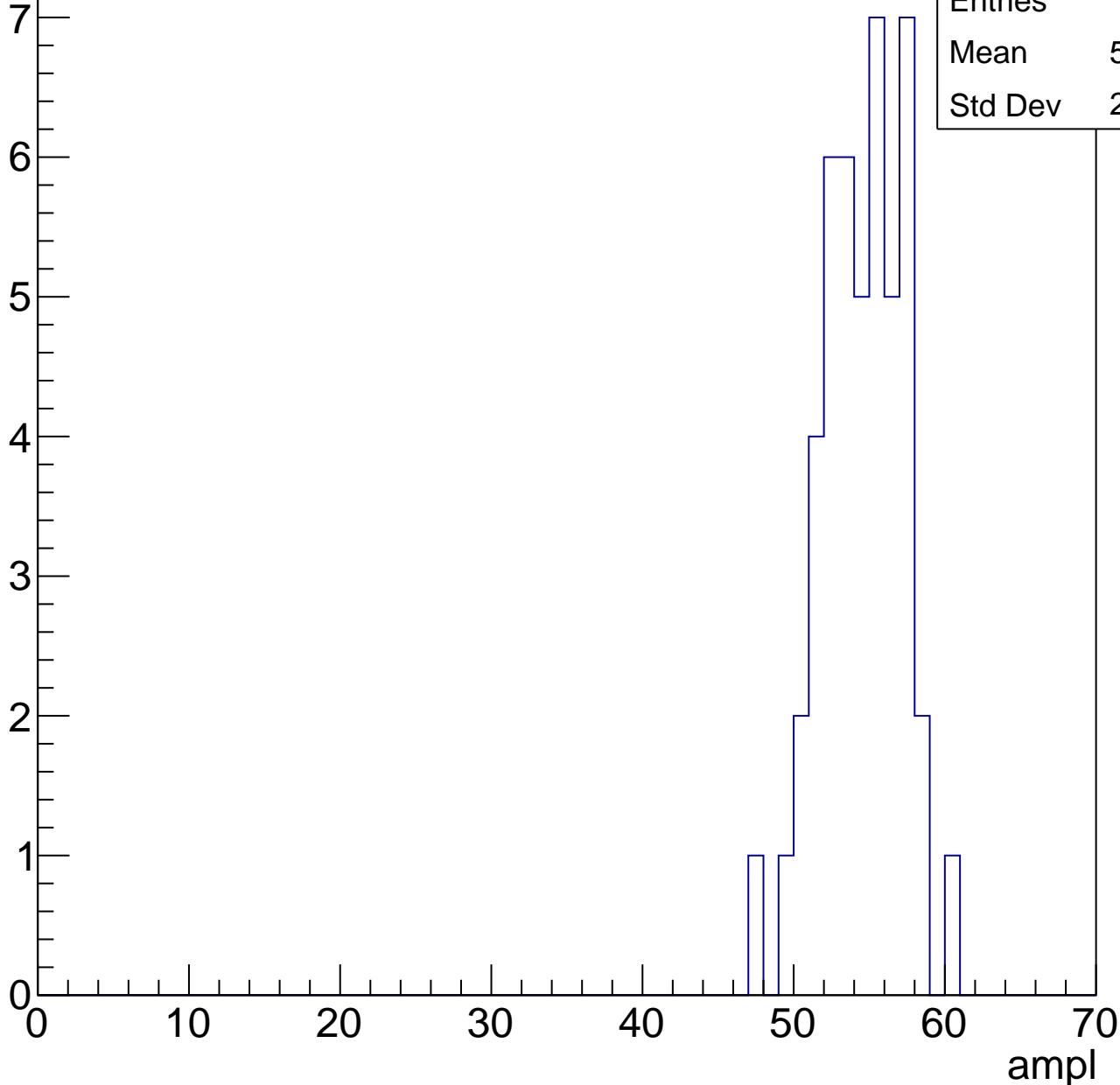
Entries	66
Mean	48.58
Std Dev	3.09

# B1L002S, U8-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	54.04
Std Dev	2.649

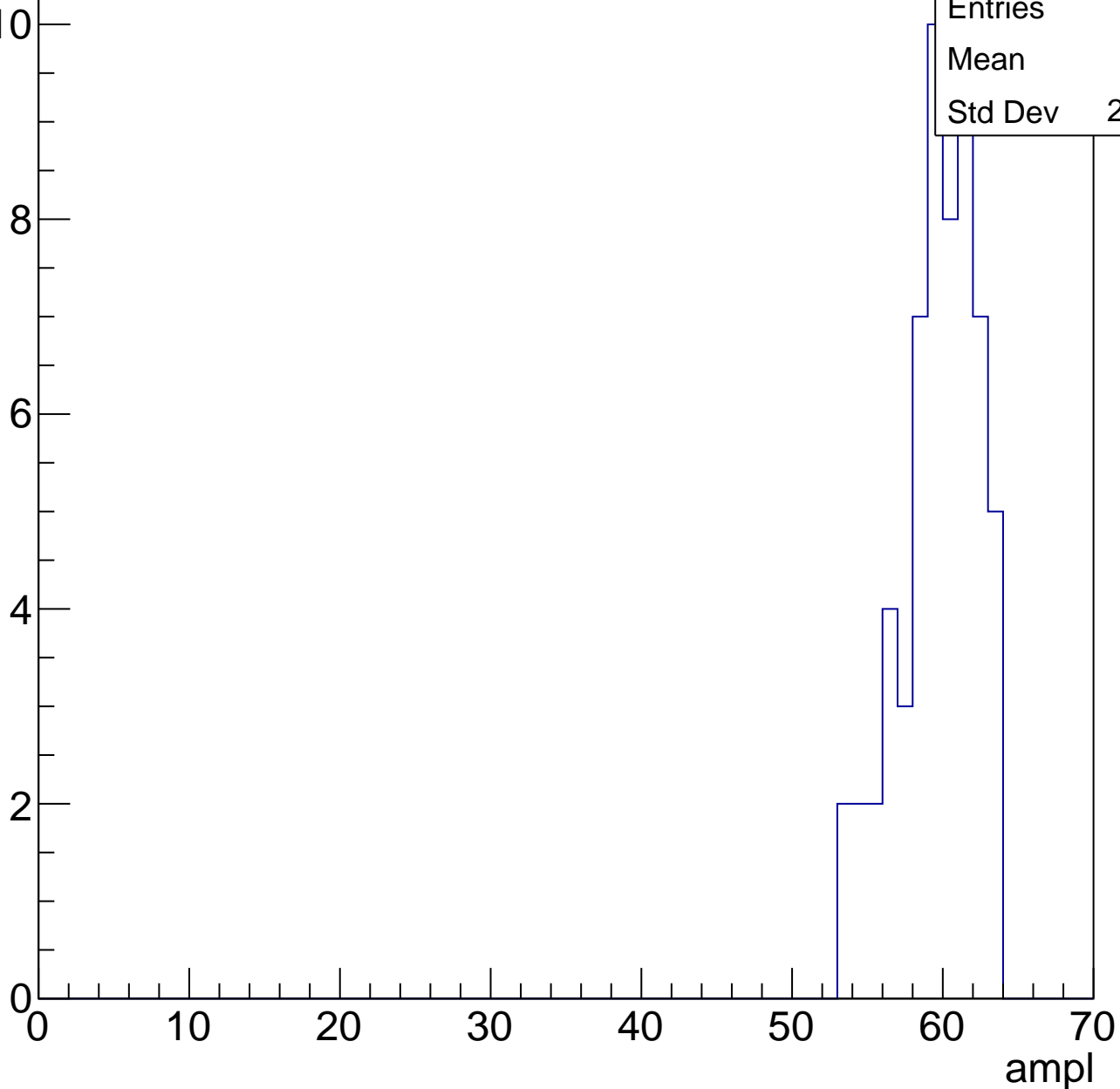


# B1L002S, U8-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	59.2
Std Dev	2.583

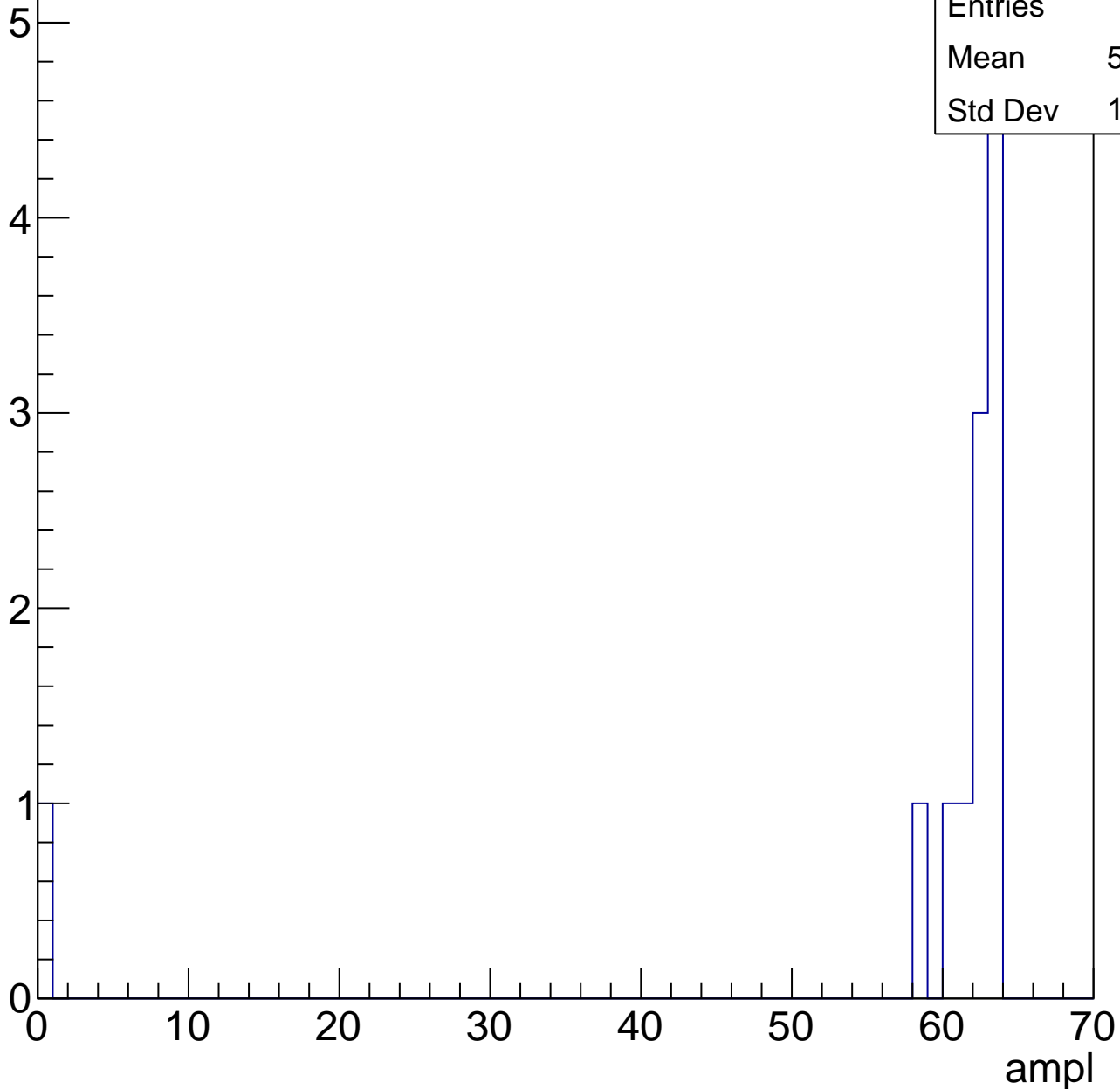


# B1L002S, U8-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	12
Mean	56.67
Std Dev	17.15





# B1L002S, U8-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch76, adc0

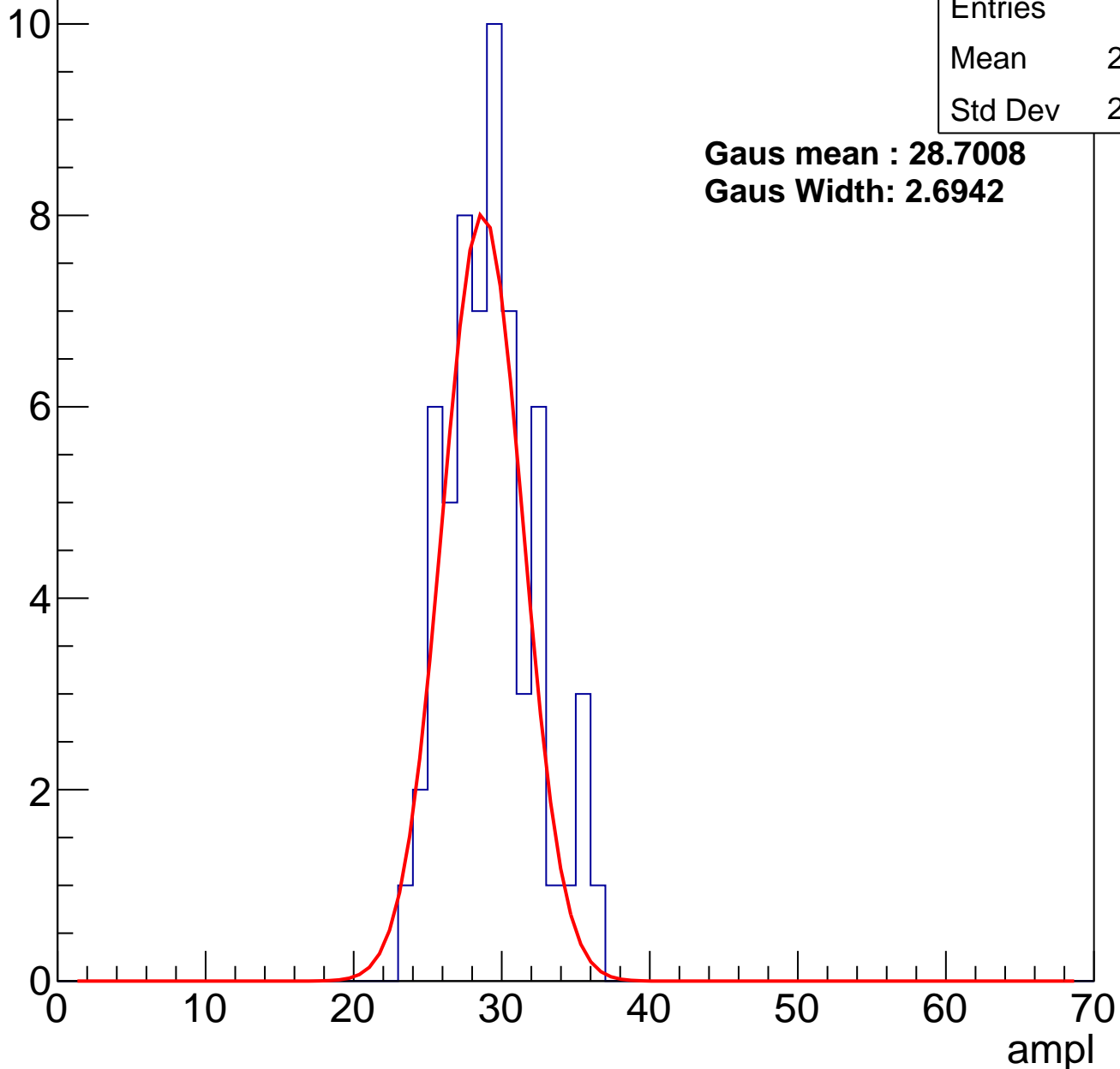
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	28.79
Std Dev	2.959

**Gaus mean : 28.7008**

**Gaus Width: 2.6942**

Entry



# B1L002S, U8-ch76, adc1

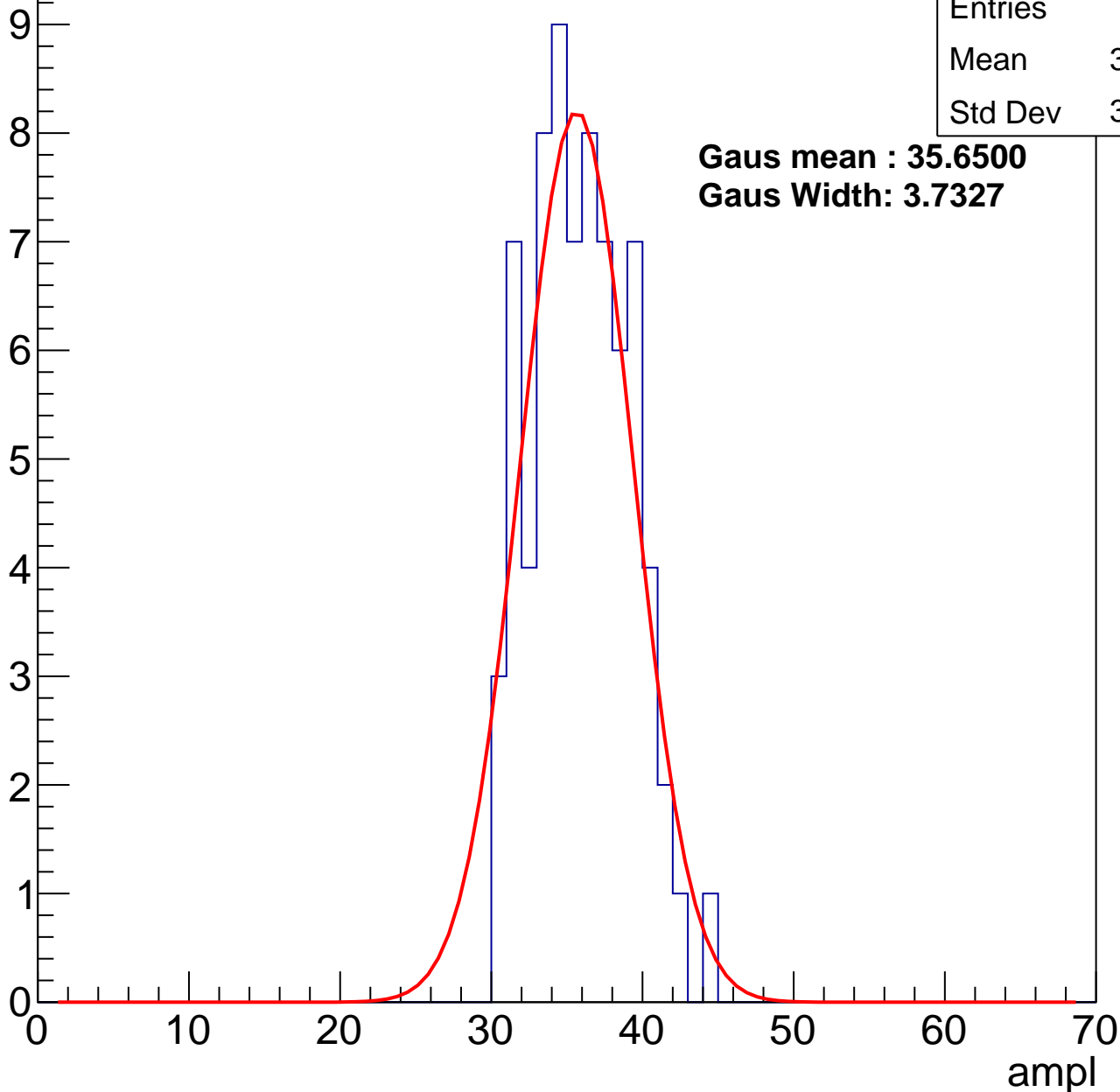
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	35.49
Std Dev	3.185

**Gaus mean : 35.6500**

**Gaus Width: 3.7327**



# B1L002S, U8-ch76, adc2

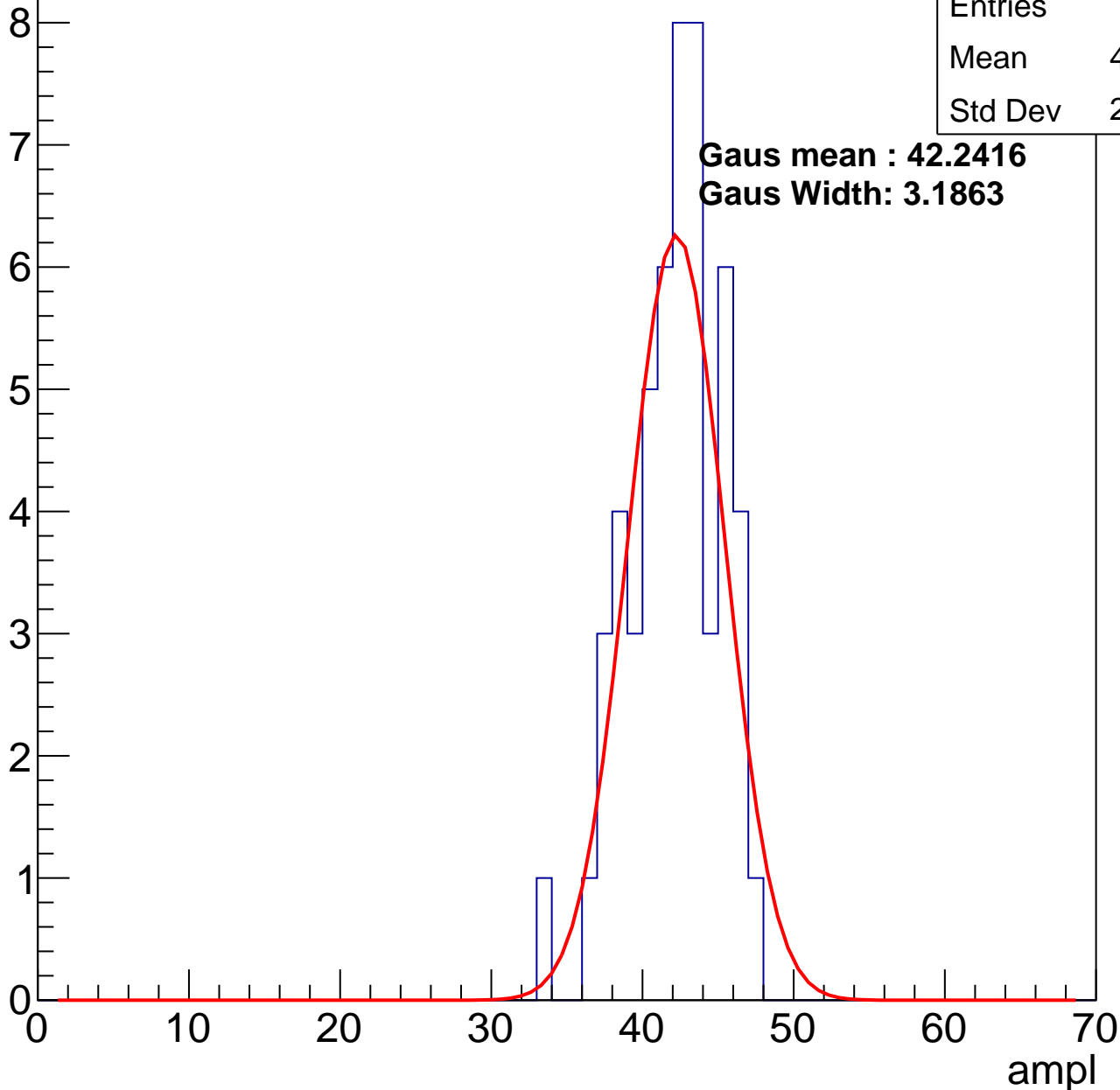
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	41.66
Std Dev	2.965

**Gaus mean : 42.2416**

**Gaus Width: 3.1863**

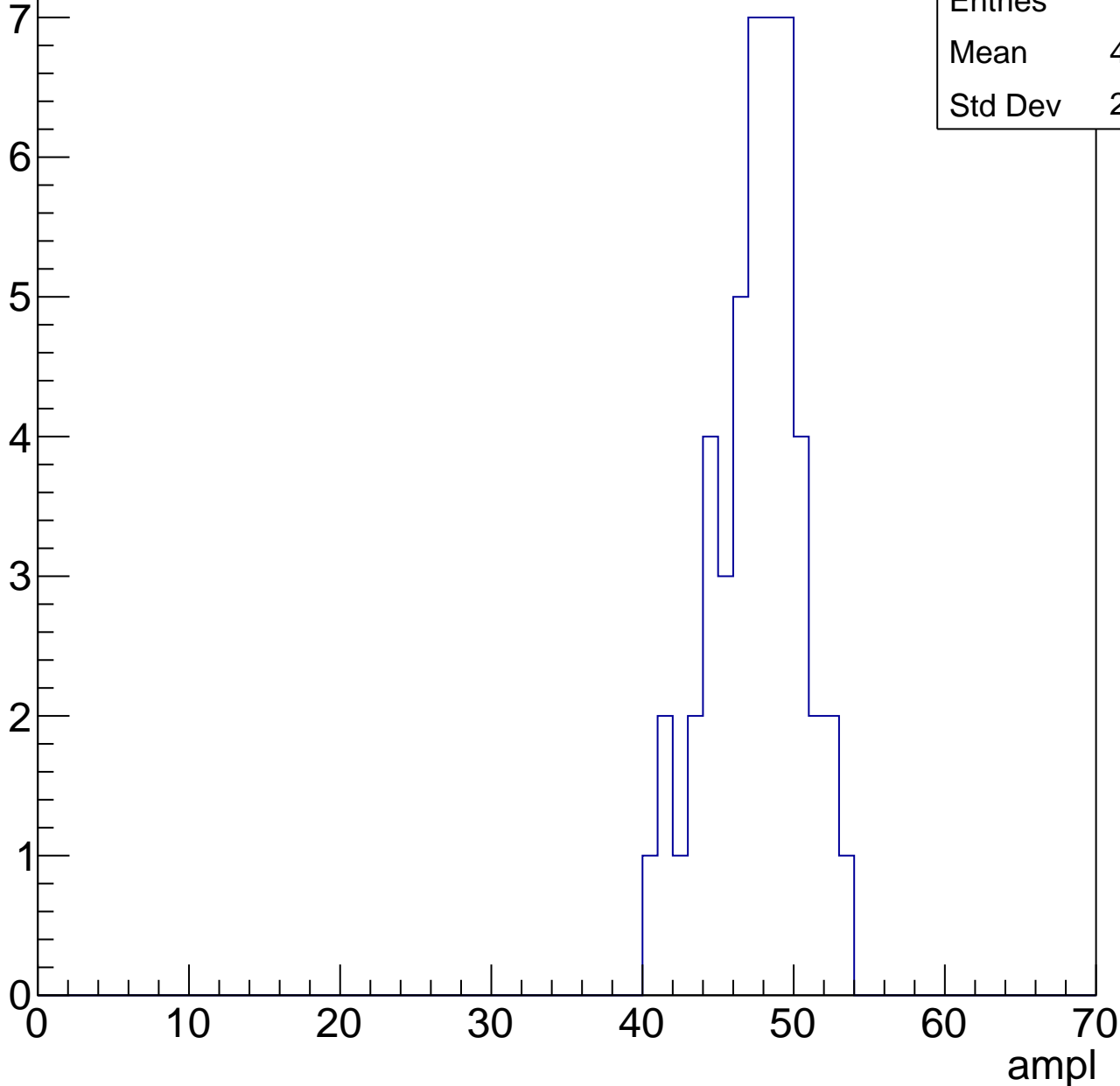


# B1L002S, U8-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	47.04
Std Dev	2.958



# B1L002S, U8-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

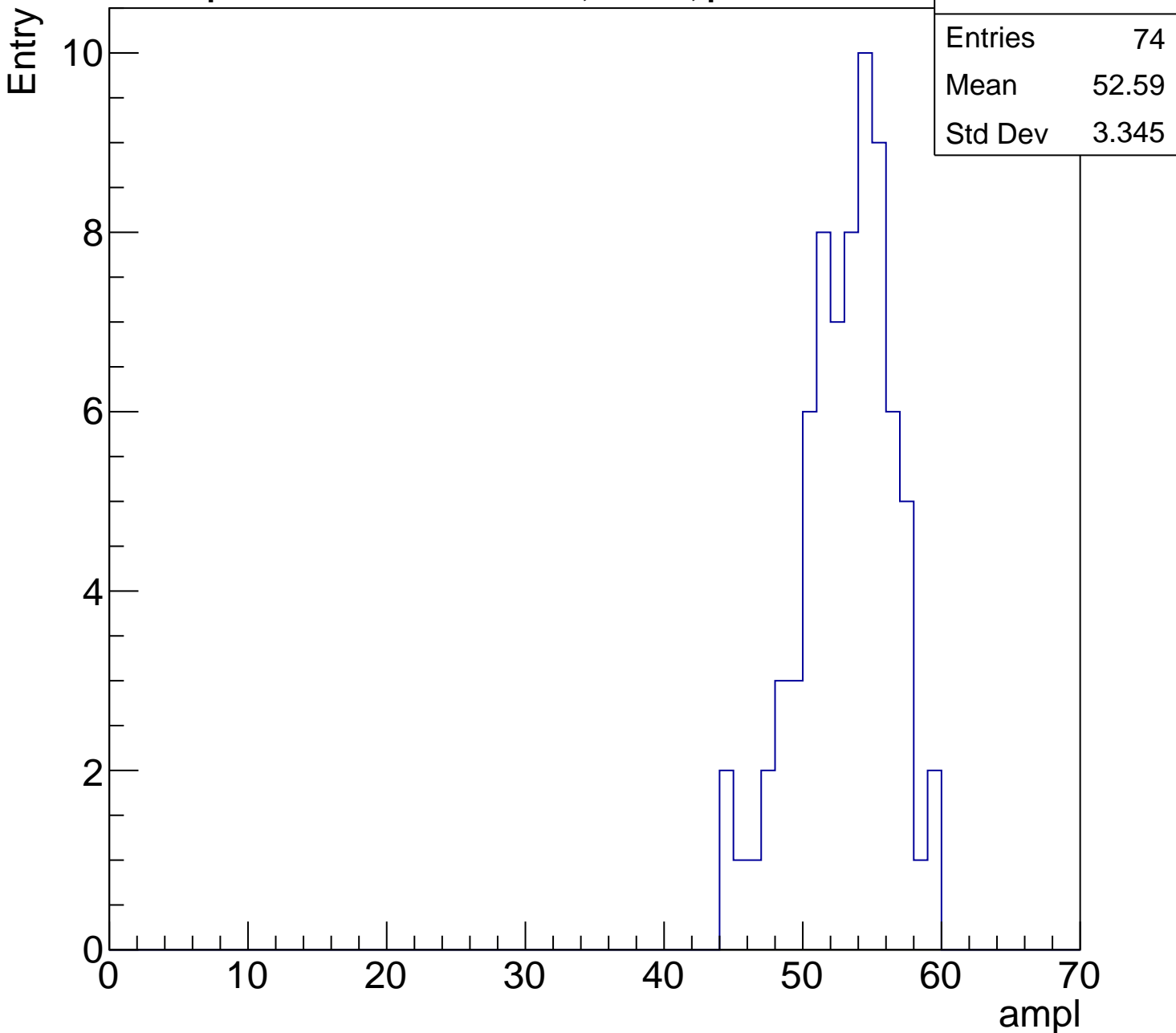
Entries	74
Mean	52.59
Std Dev	3.345

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

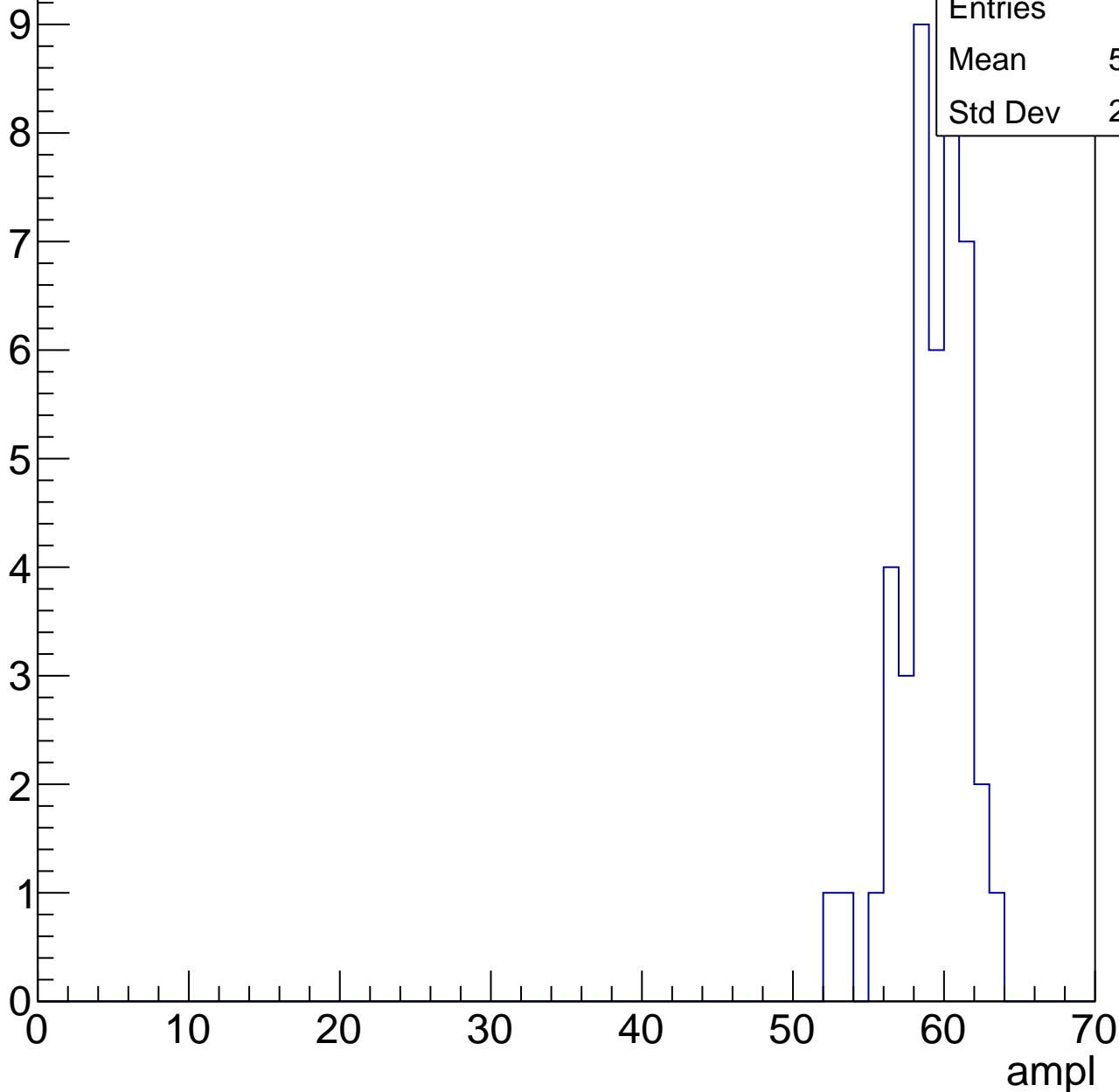


# B1L002S, U8-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.75
Std Dev	2.268

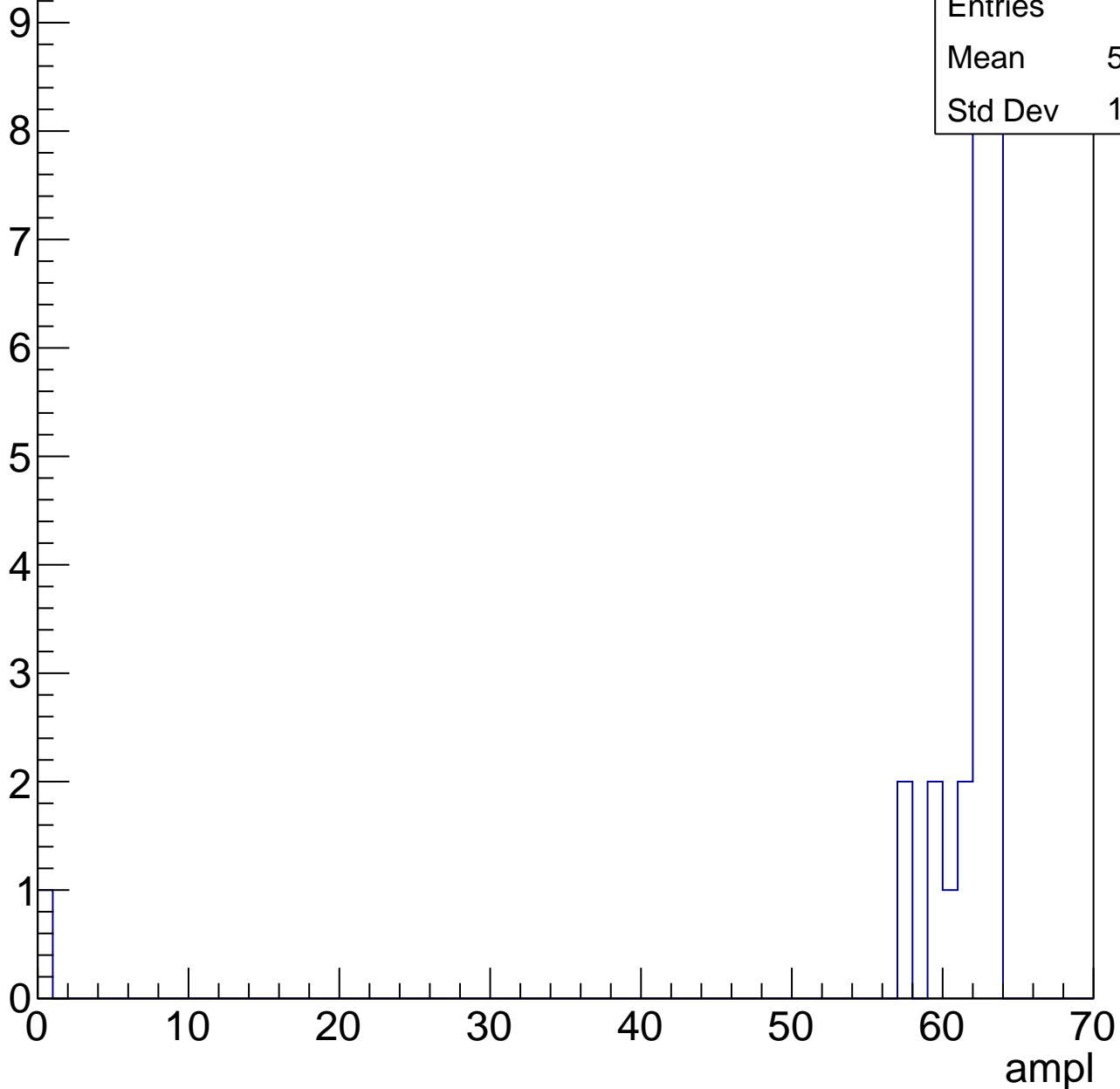


# B1L002S, U8-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	59.08
Std Dev	12.19

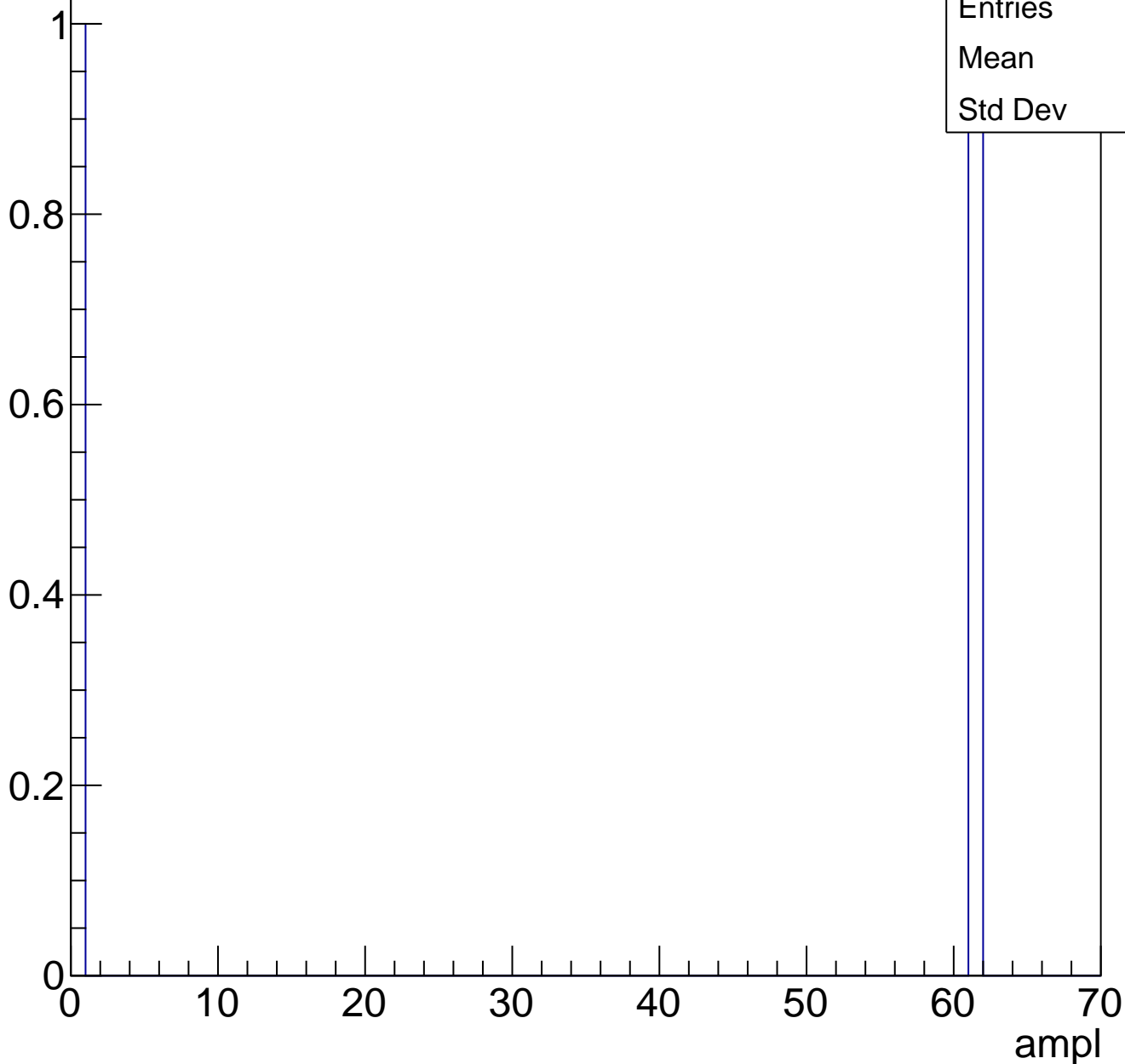




# B1L002S, U8-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch77, adc0

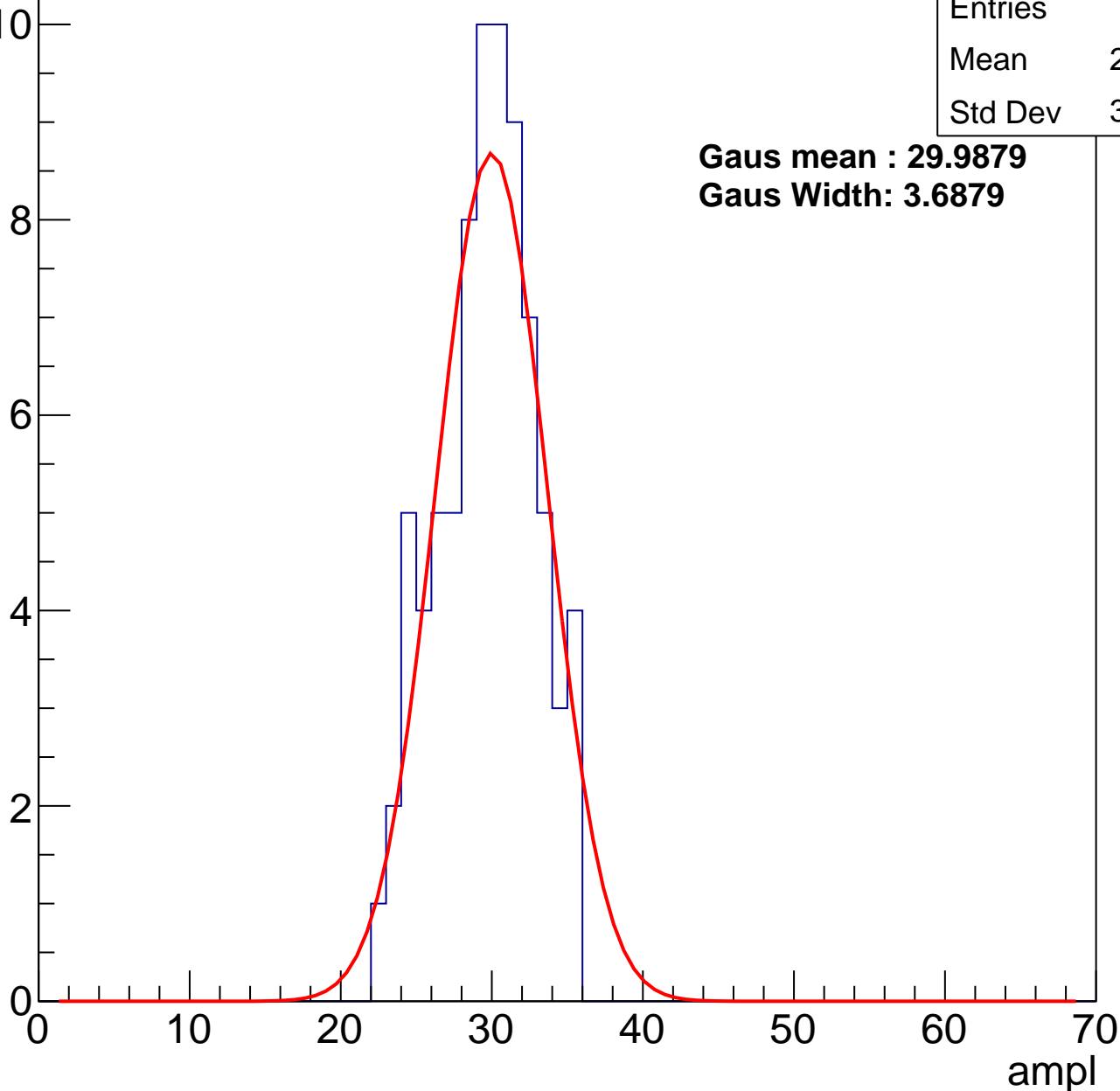
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	29.19
Std Dev	3.179

**Gaus mean : 29.9879**

**Gaus Width: 3.6879**



# B1L002S, U8-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	36.28
Std Dev	2.491

**Gaus mean : 36.7403**

**Gaus Width: 2.6362**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch77, adc2

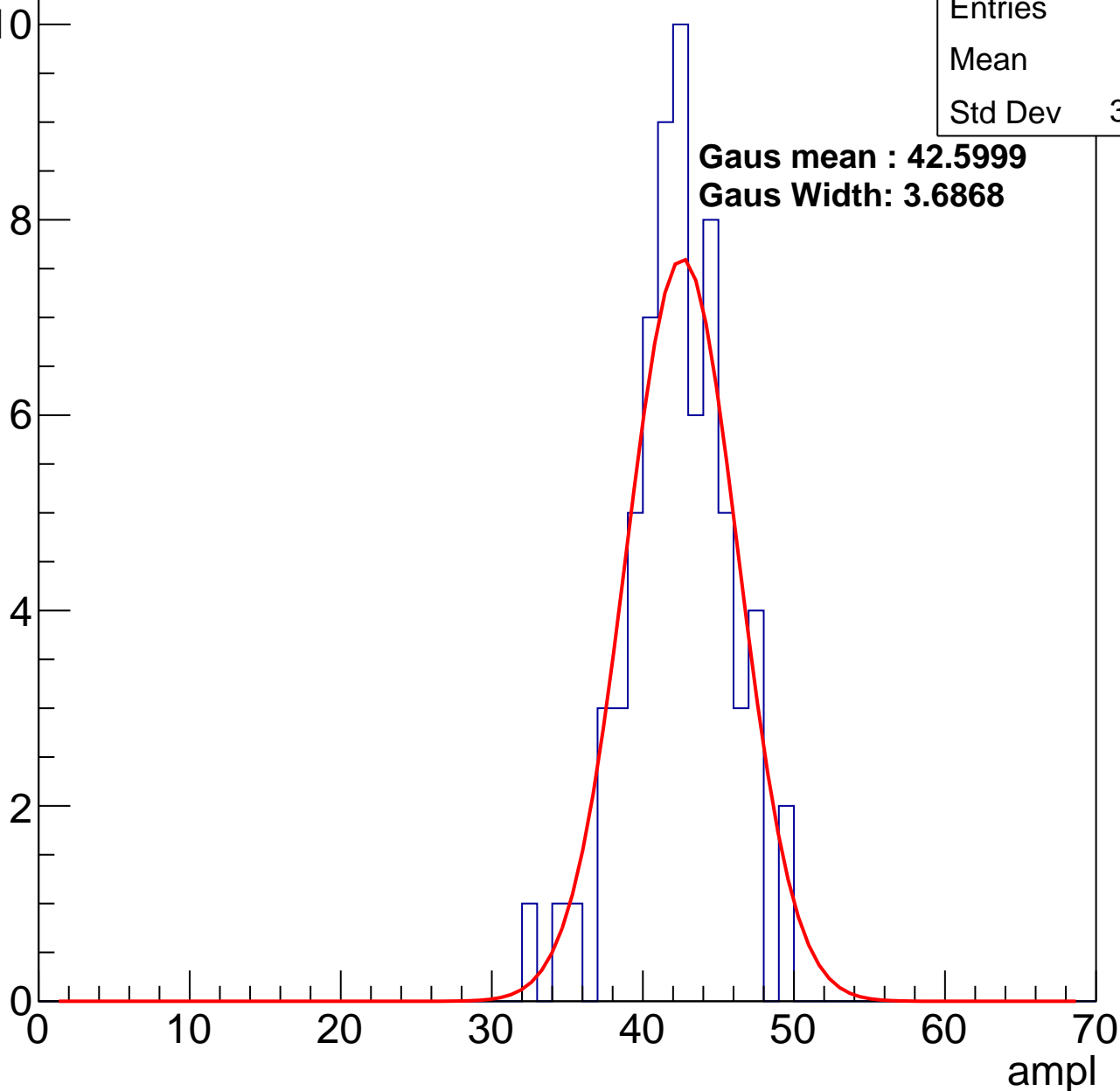
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.9
Std Dev	3.313

**Gaus mean : 42.5999**

**Gaus Width: 3.6868**



# B1L002S, U8-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

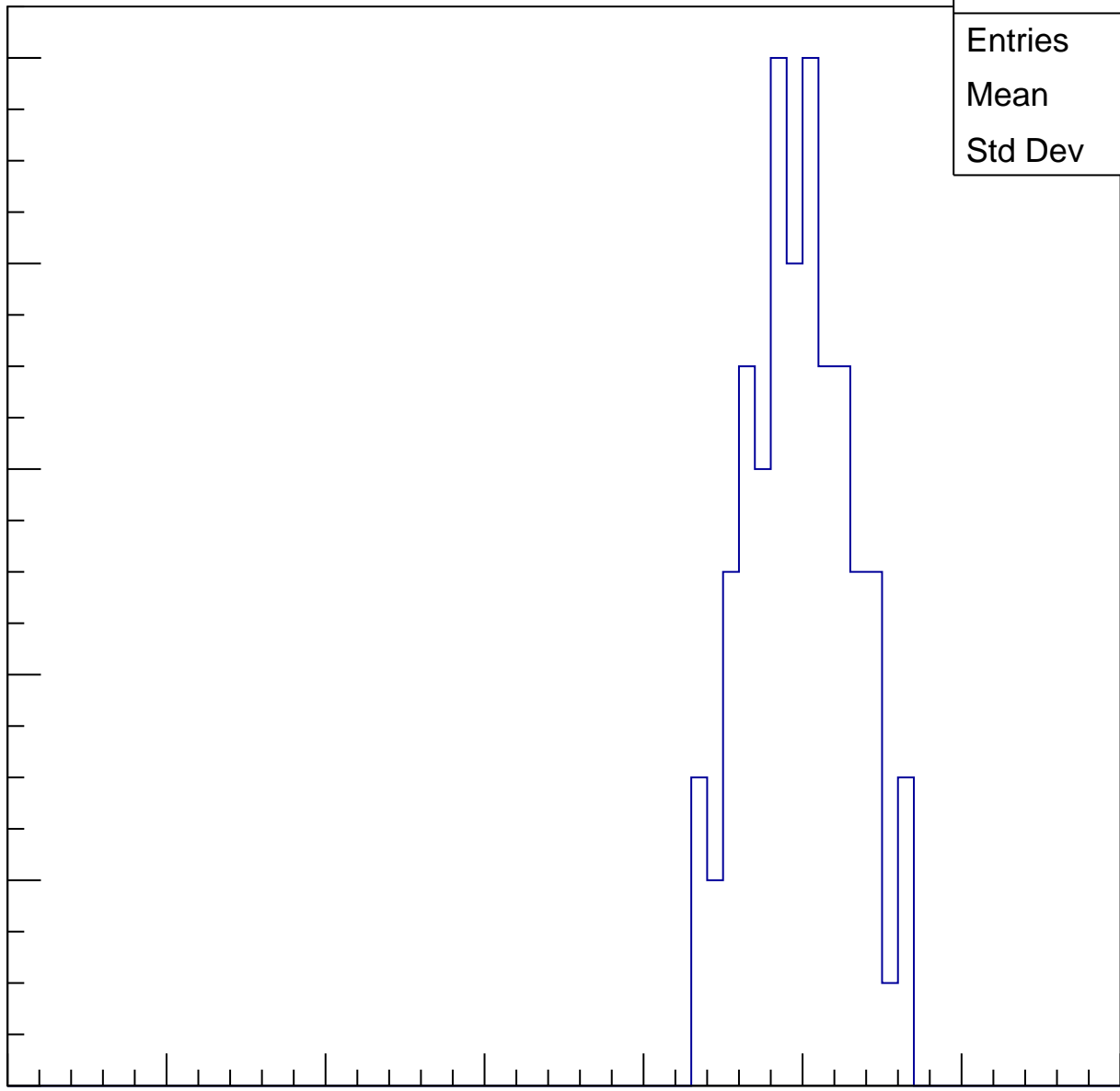
Entries	79
Mean	49.33
Std Dev	3.213

Entry

10  
8  
6  
4  
2  
0

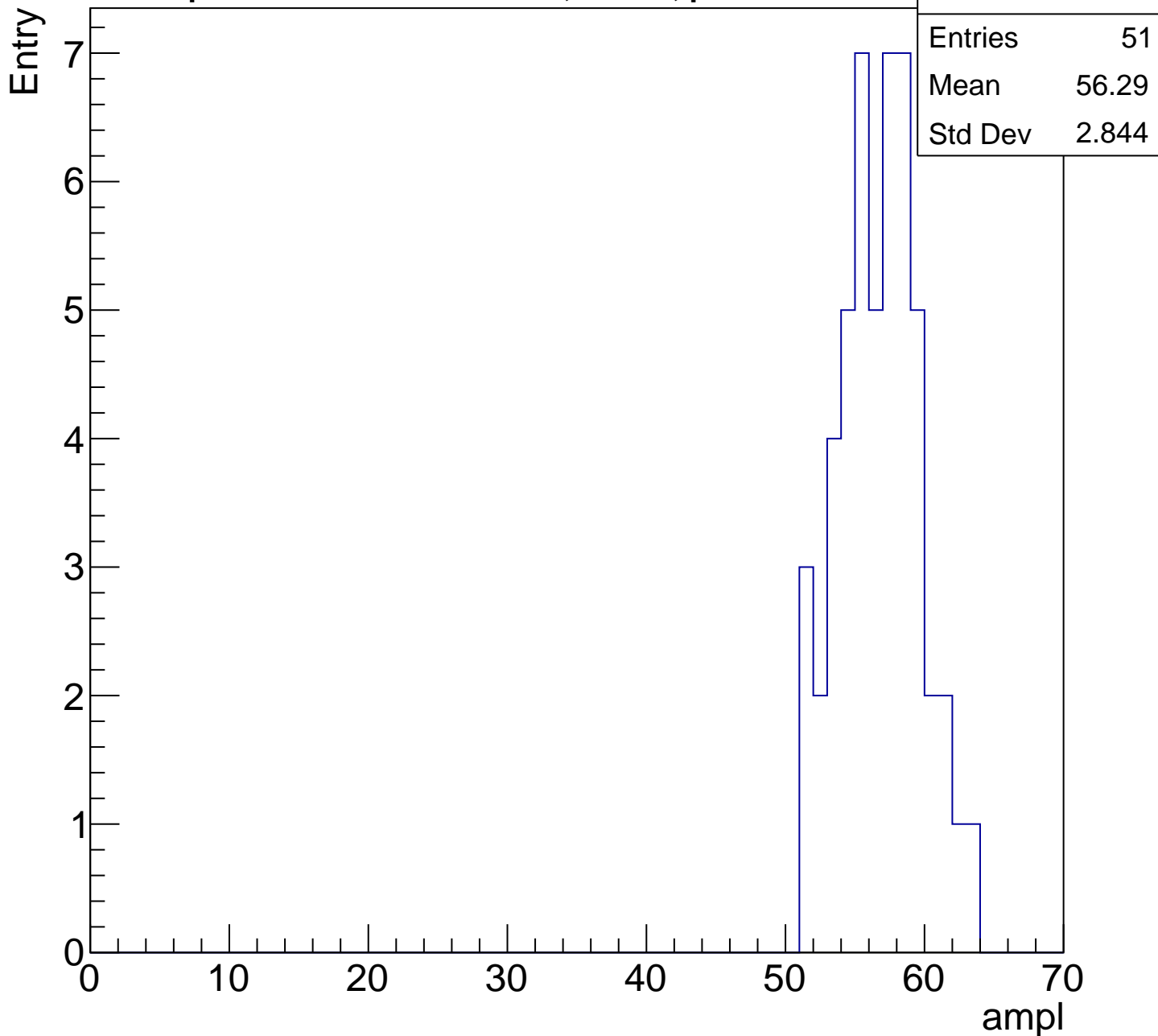
0 10 20 30 40 50 60 70

ampl



# B1L002S, U8-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

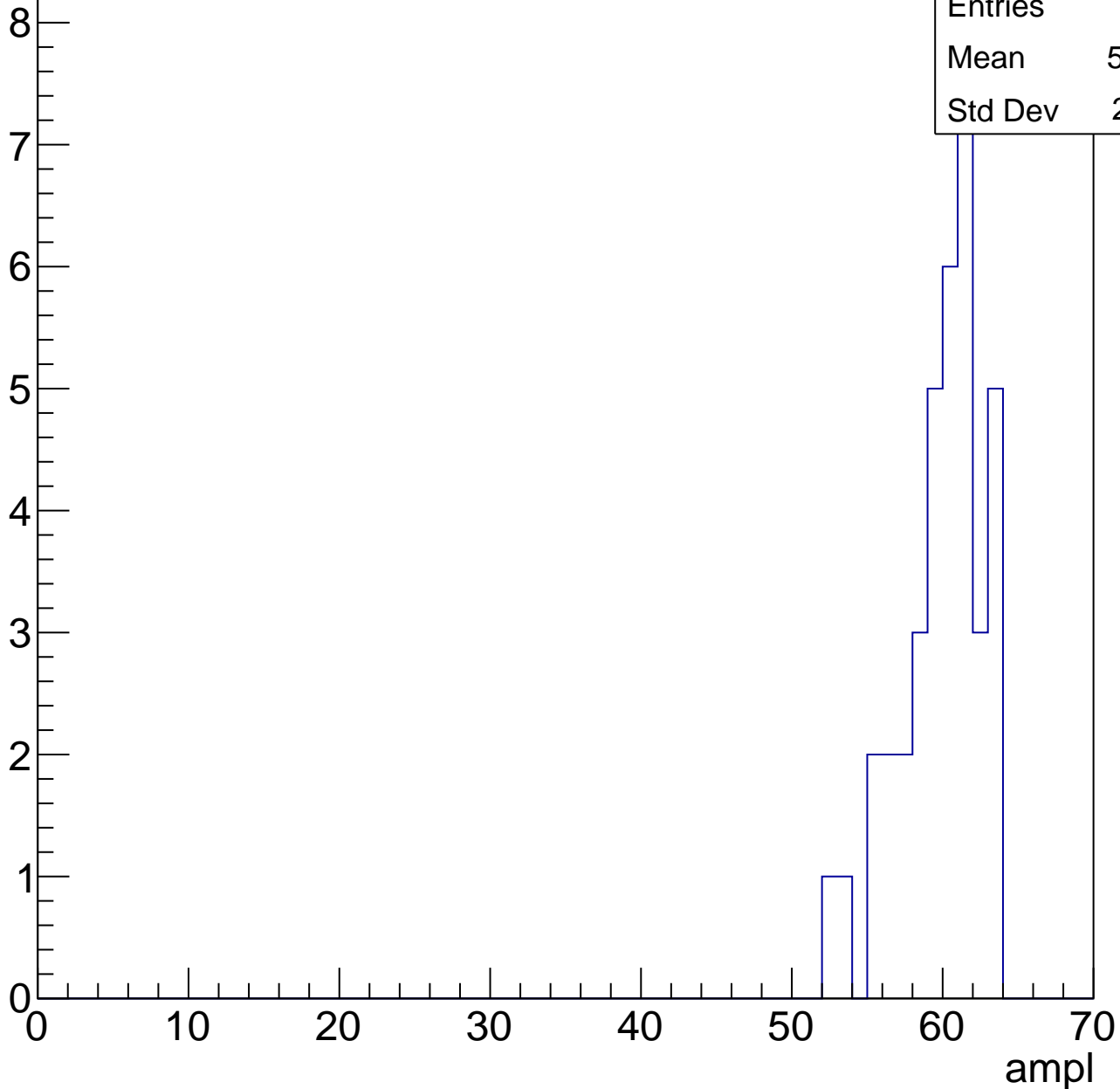


# B1L002S, U8-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	59.45
Std Dev	2.731

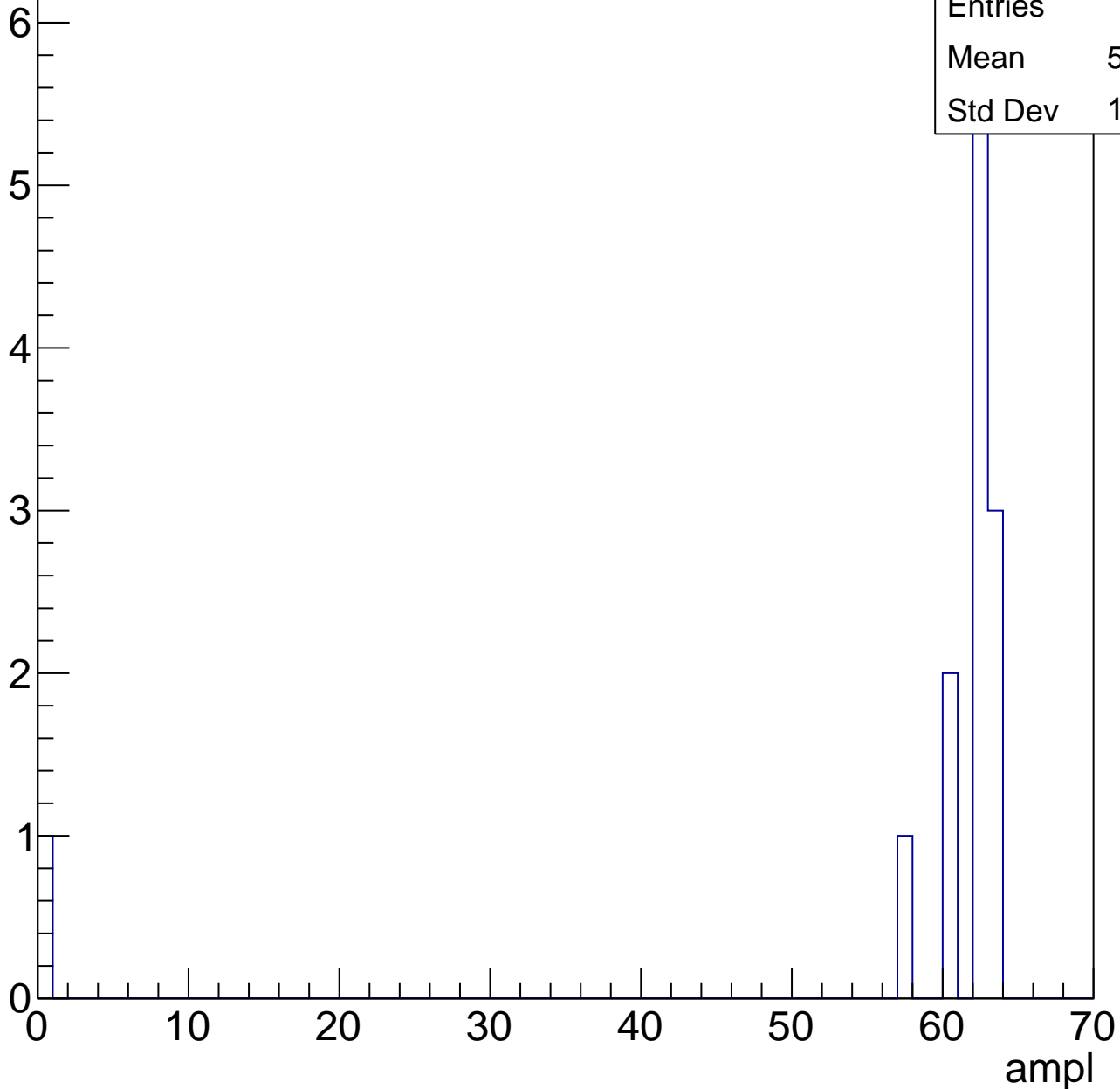


# B1L002S, U8-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	56.77
Std Dev	16.47





# B1L002S, U8-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch78, adc0

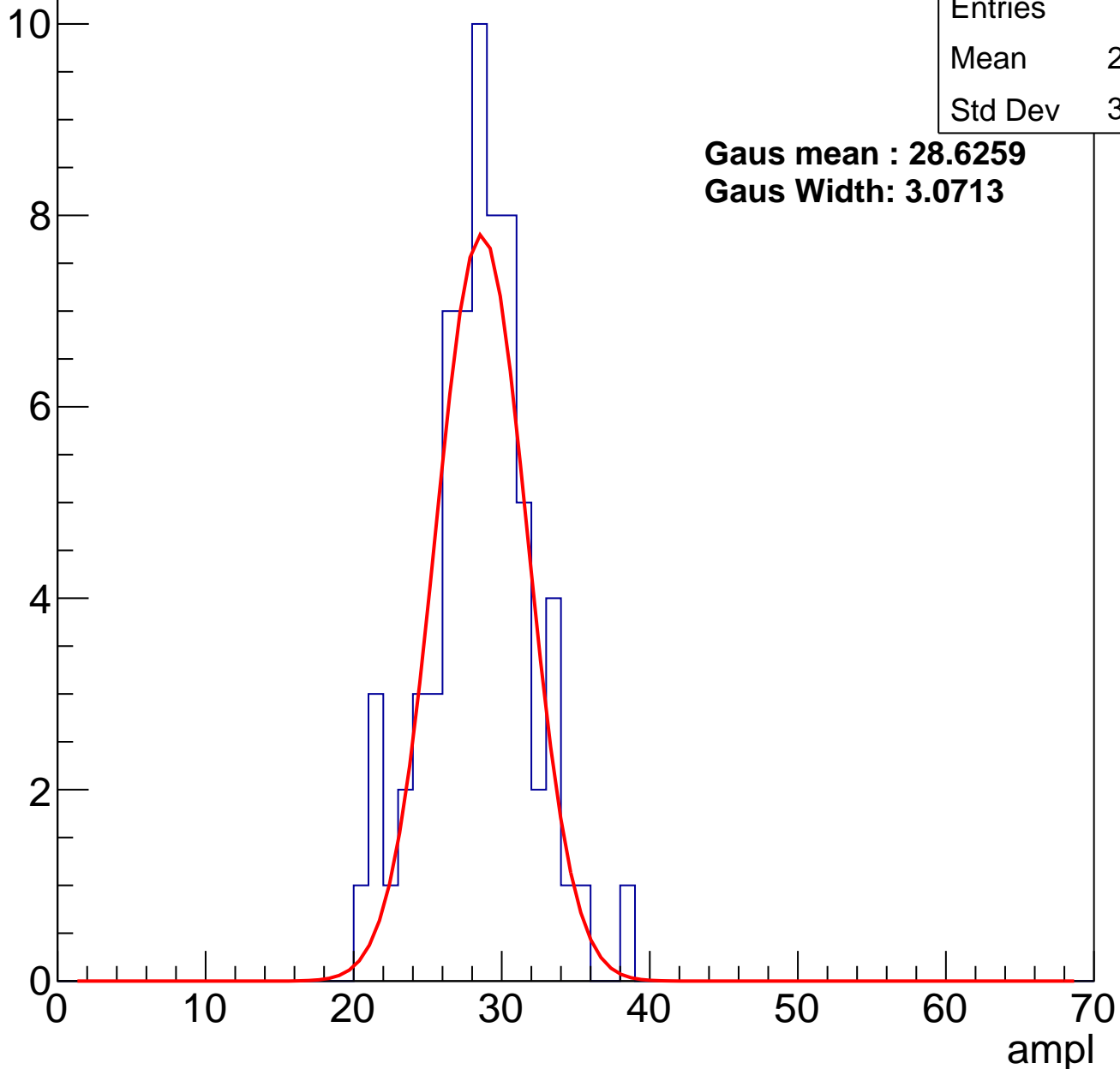
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	28.04
Std Dev	3.475

**Gaus mean : 28.6259**

**Gaus Width: 3.0713**

Entry



# B1L002S, U8-ch78, adc1

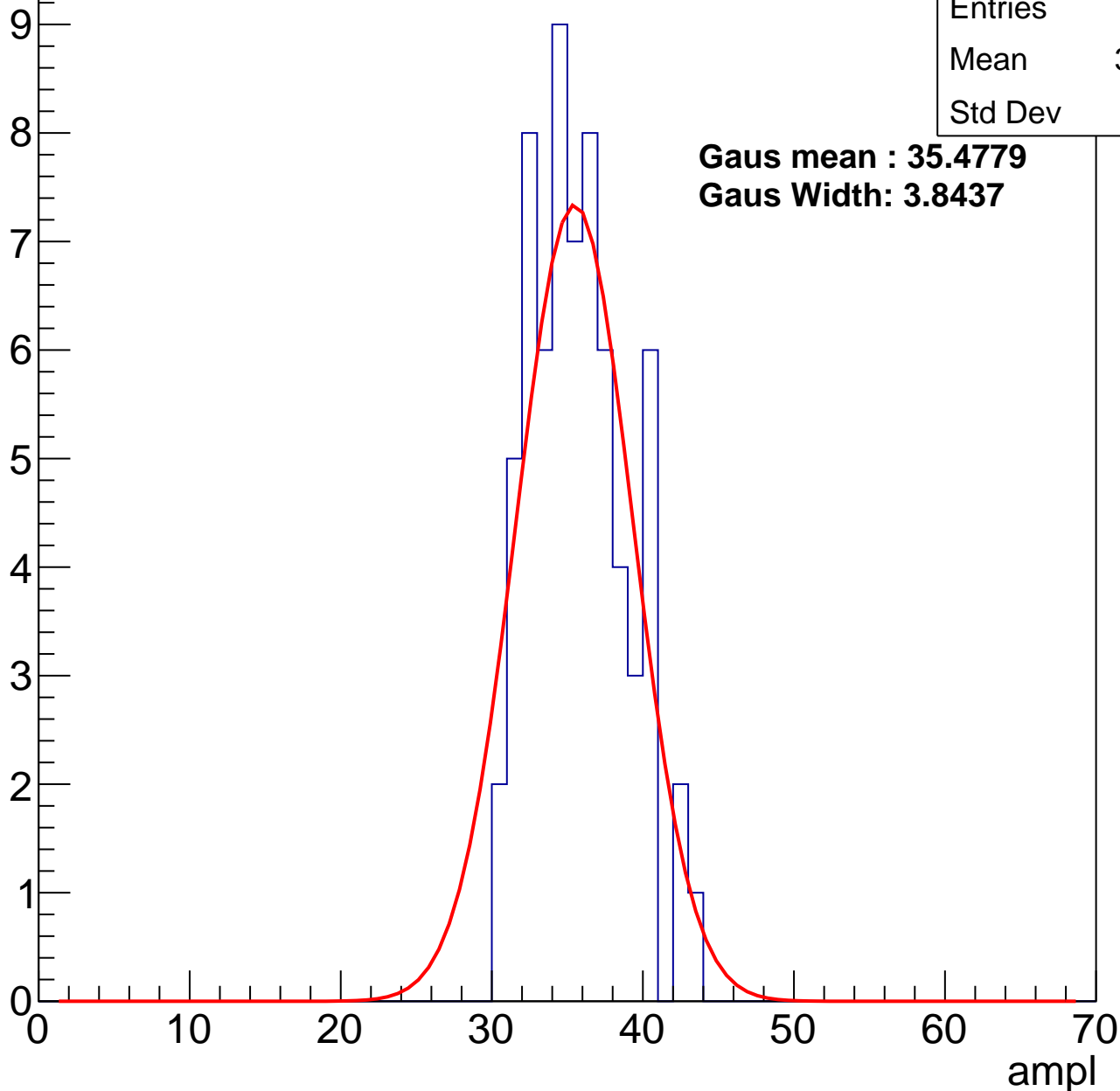
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	35.31
Std Dev	3.13

**Gaus mean : 35.4779**

**Gaus Width: 3.8437**



# B1L002S, U8-ch78, adc2

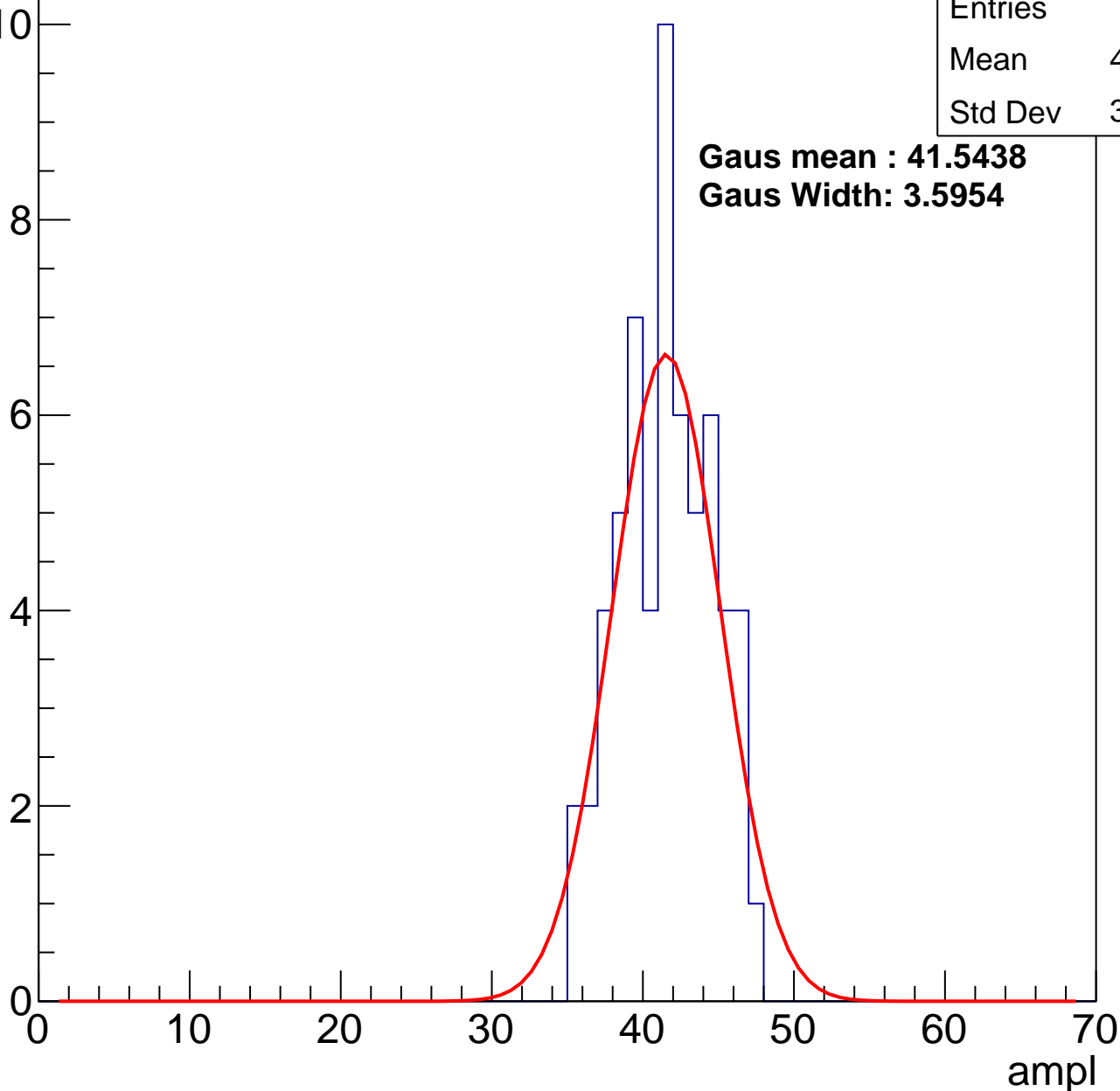
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.08
Std Dev	3.007

**Gaus mean : 41.5438**

**Gaus Width: 3.5954**

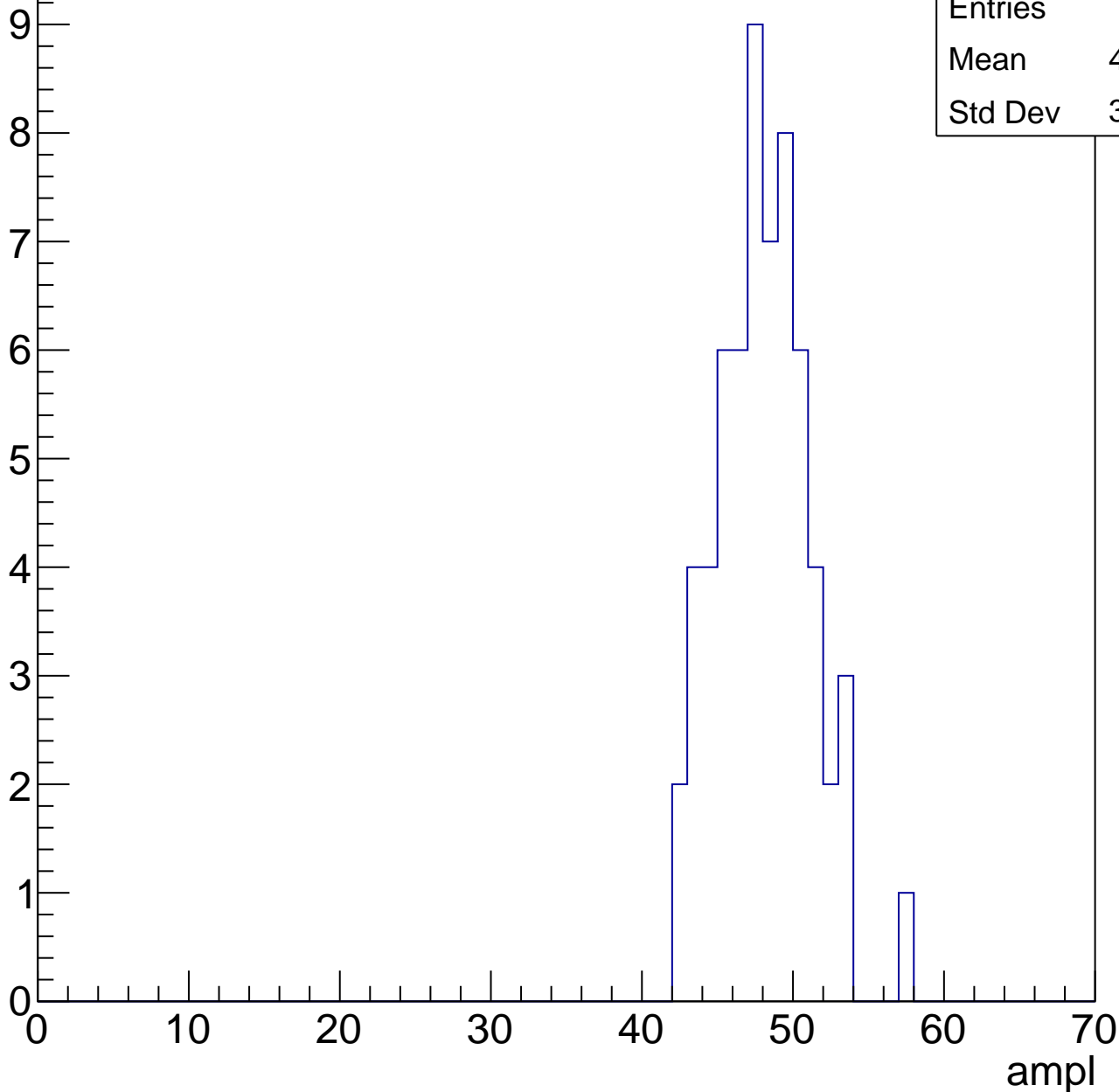


# B1L002S, U8-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

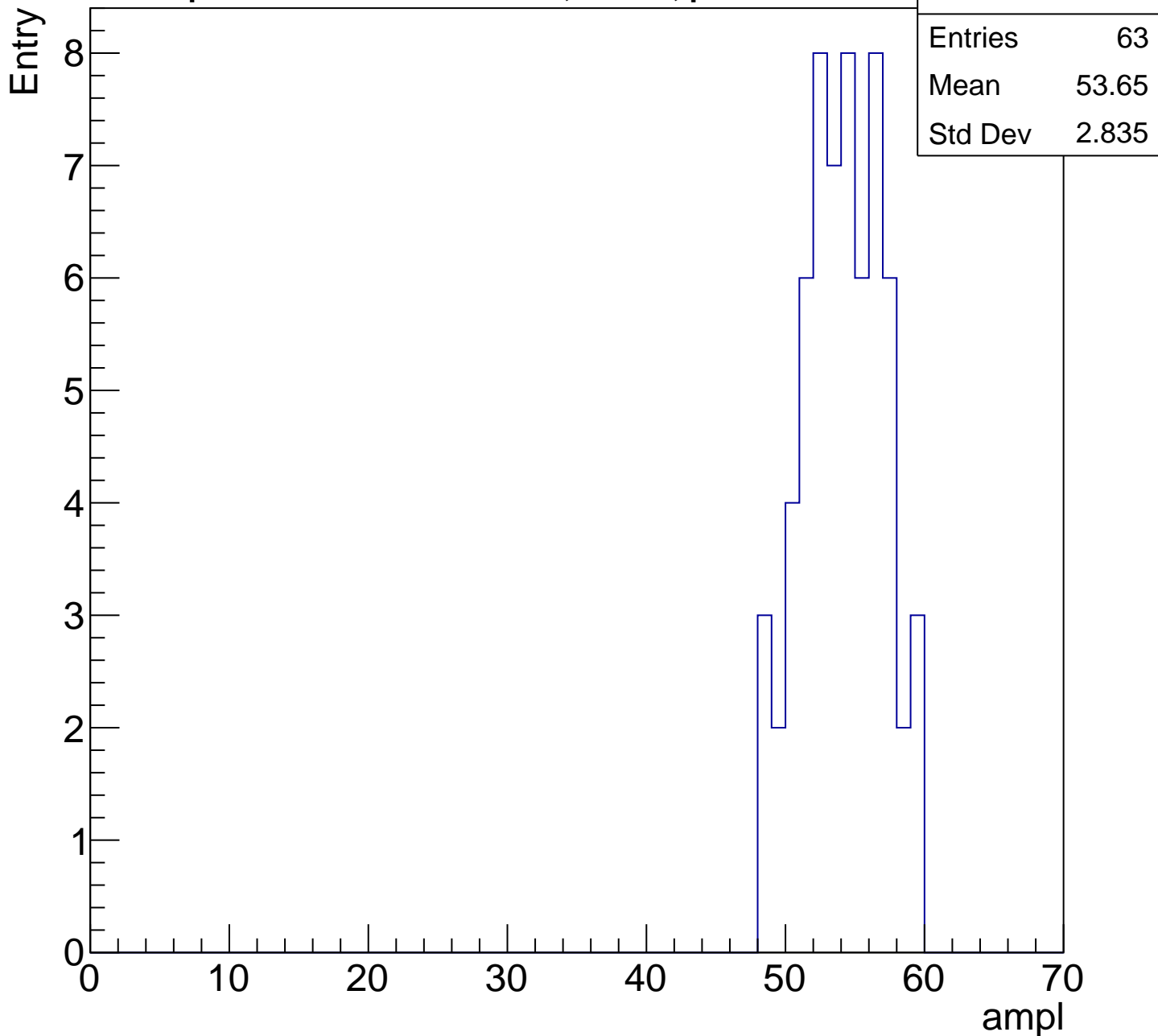
Entry

Entries	62
Mean	47.63
Std Dev	3.033



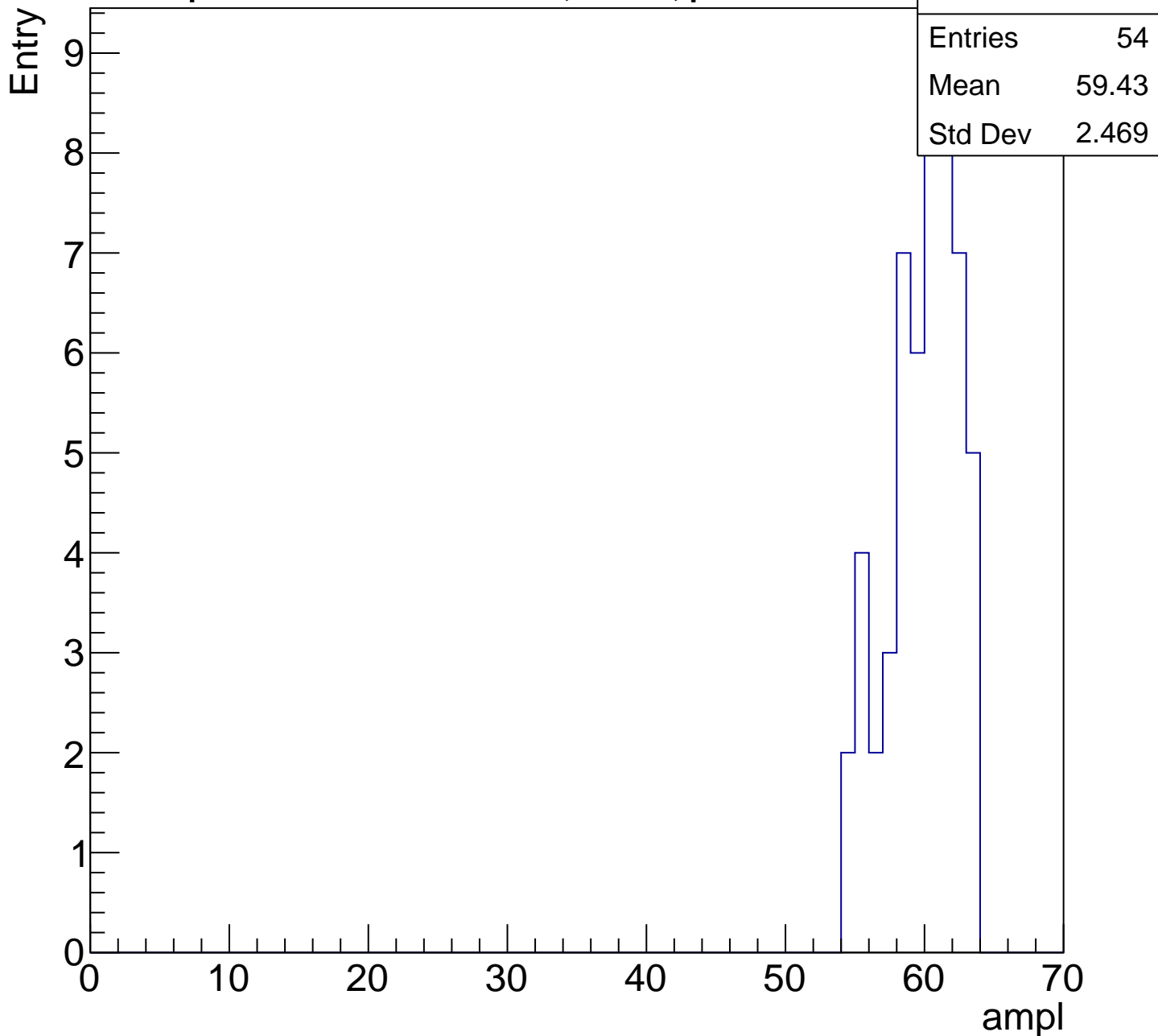
# B1L002S, U8-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

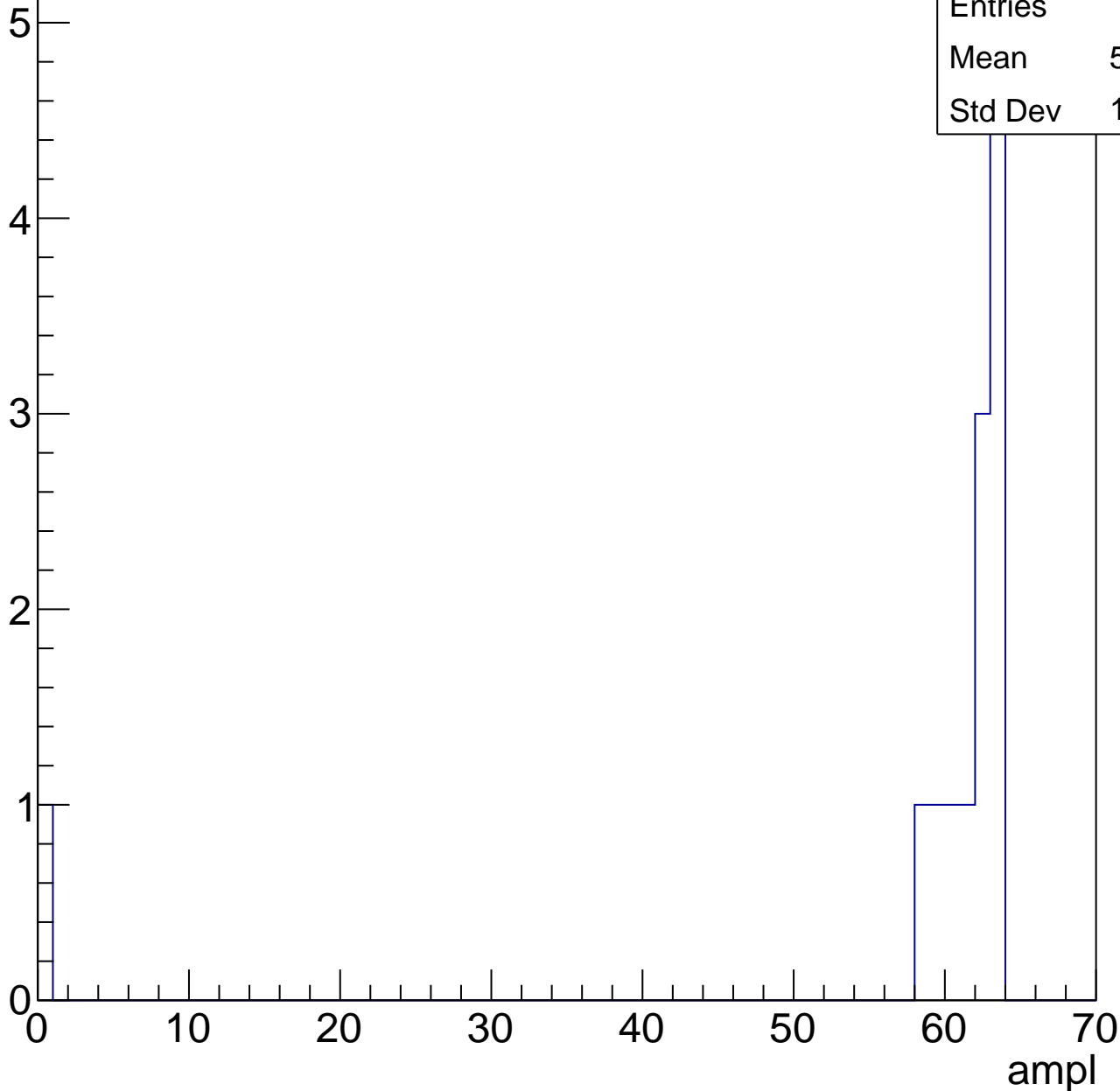


# B1L002S, U8-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	56.85
Std Dev	16.49





# B1L002S, U8-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch79, adc0

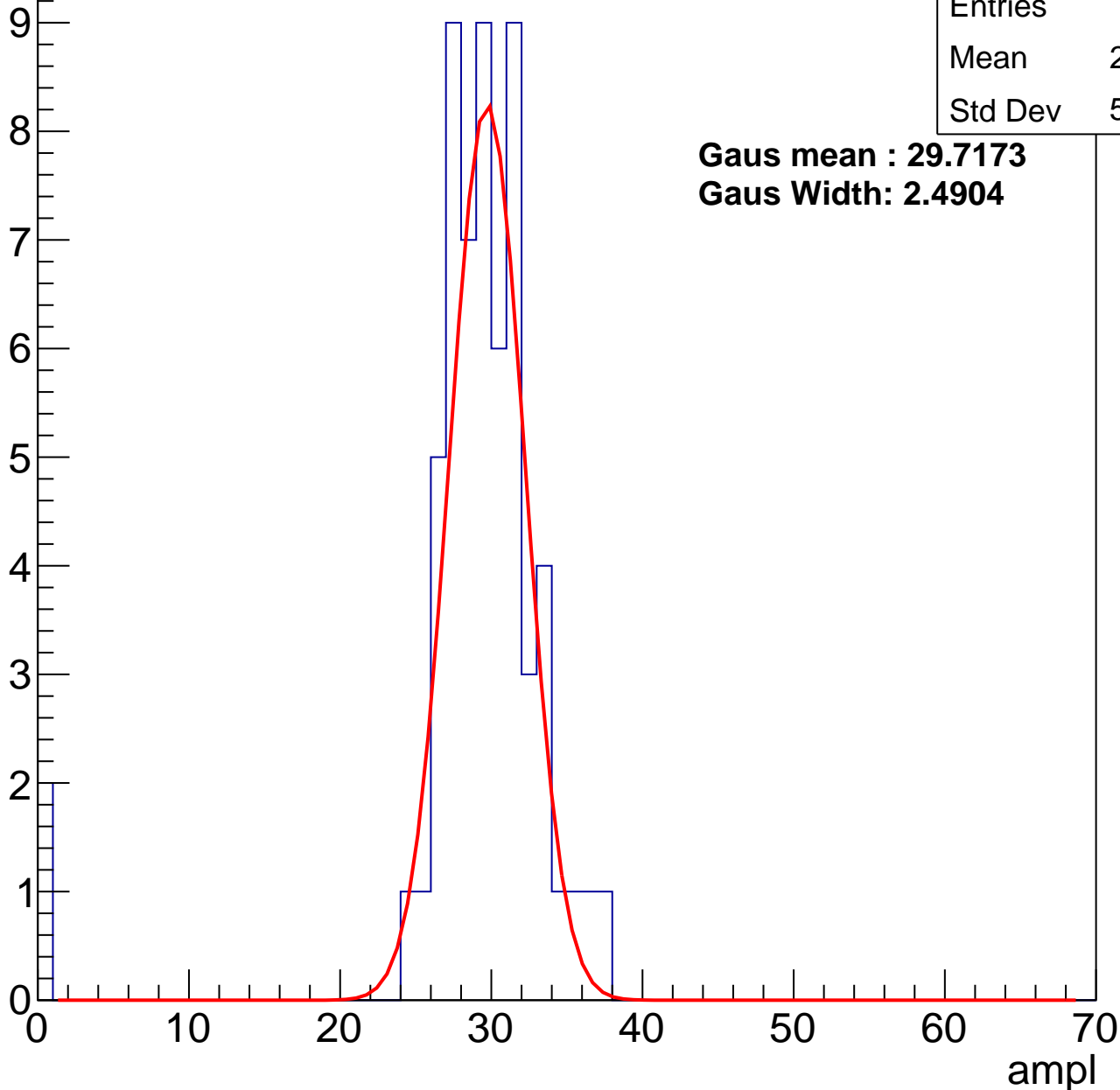
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.47
Std Dev	5.917

**Gaus mean : 29.7173**

**Gaus Width: 2.4904**



# B1L002S, U8-ch79, adc1

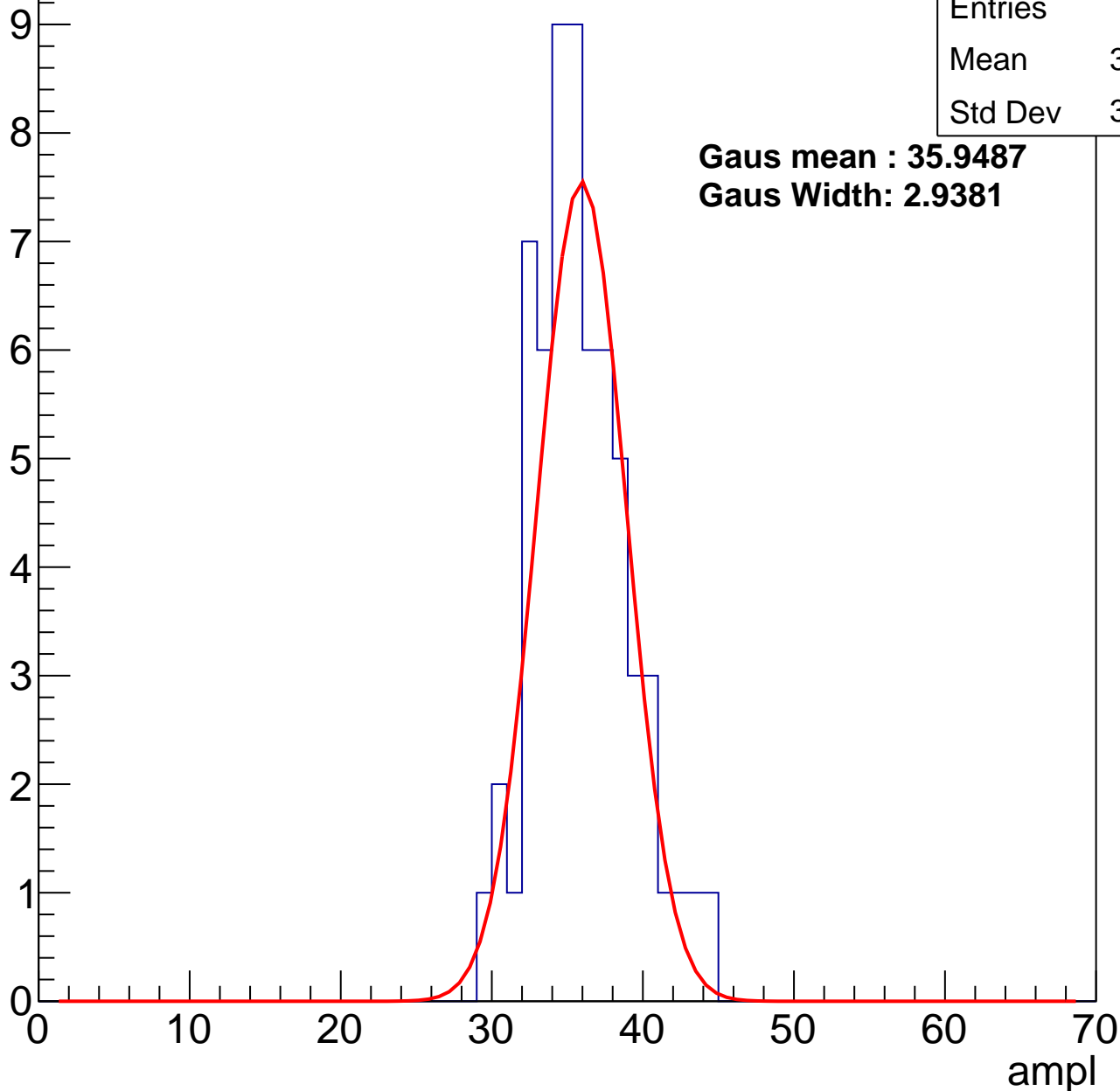
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	35.45
Std Dev	3.145

**Gaus mean : 35.9487**

**Gaus Width: 2.9381**



# B1L002S, U8-ch79, adc2

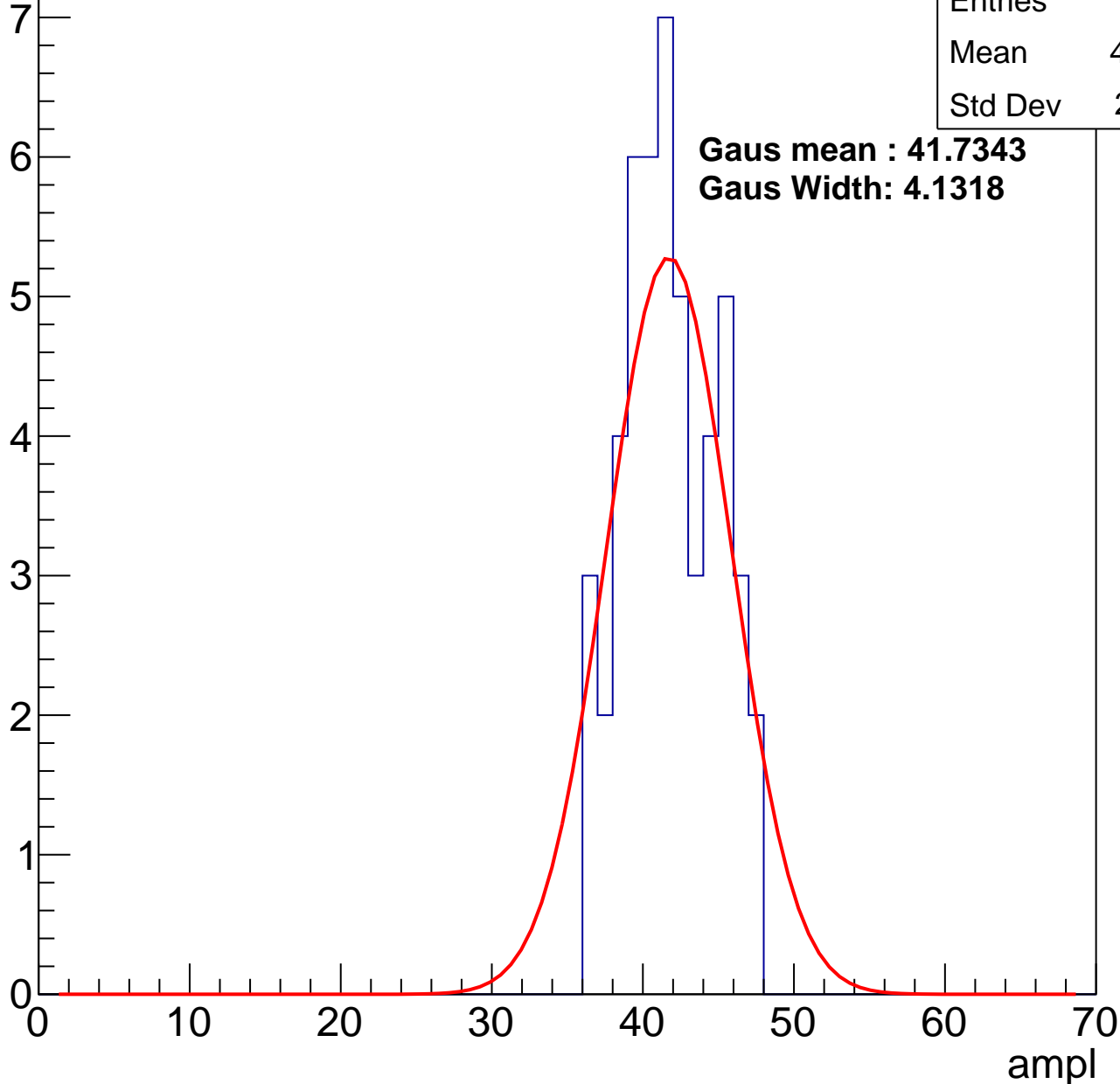
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	41.34
Std Dev	2.991

**Gaus mean : 41.7343**

**Gaus Width: 4.1318**



# B1L002S, U8-ch79, adc3

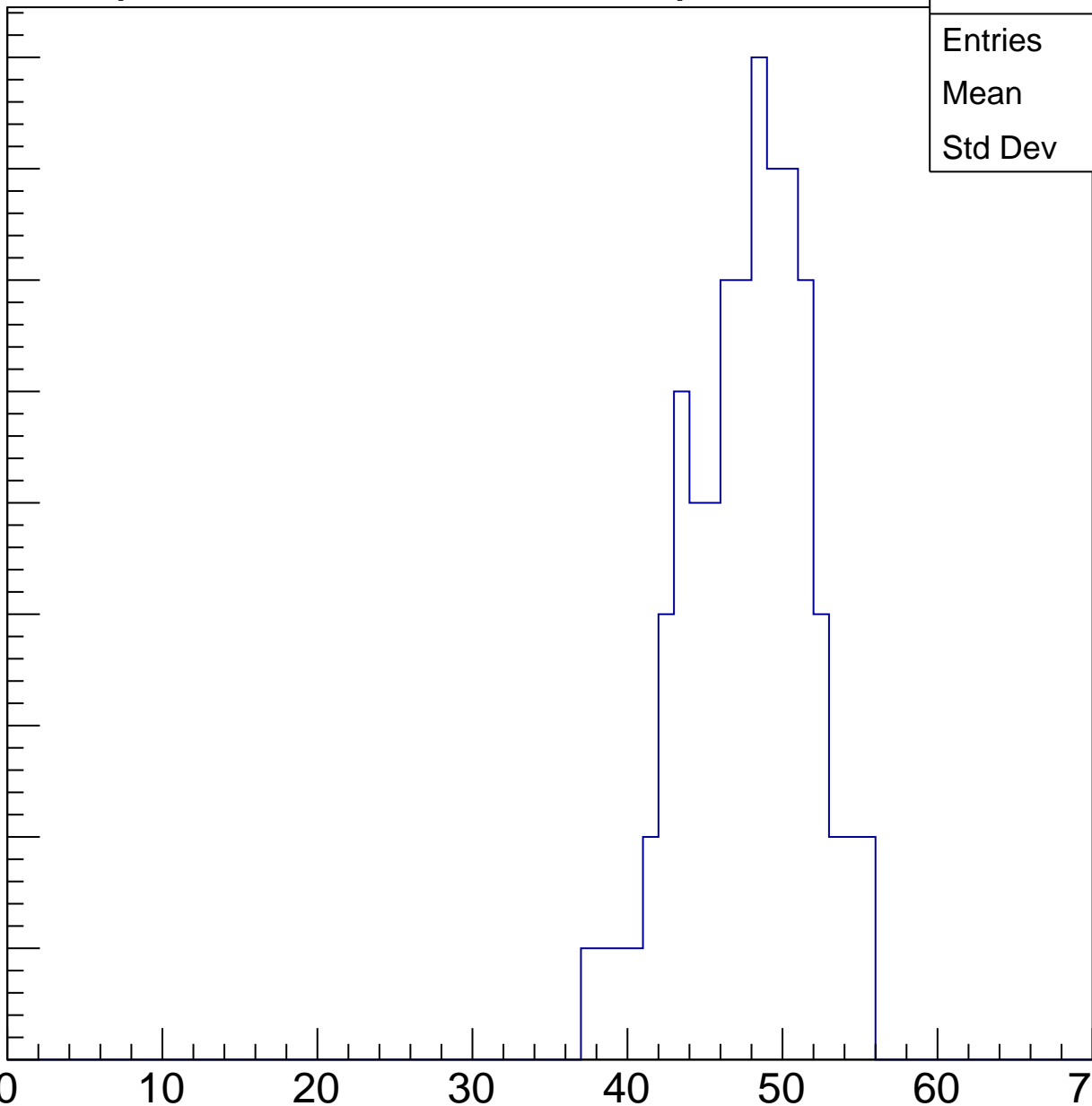
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

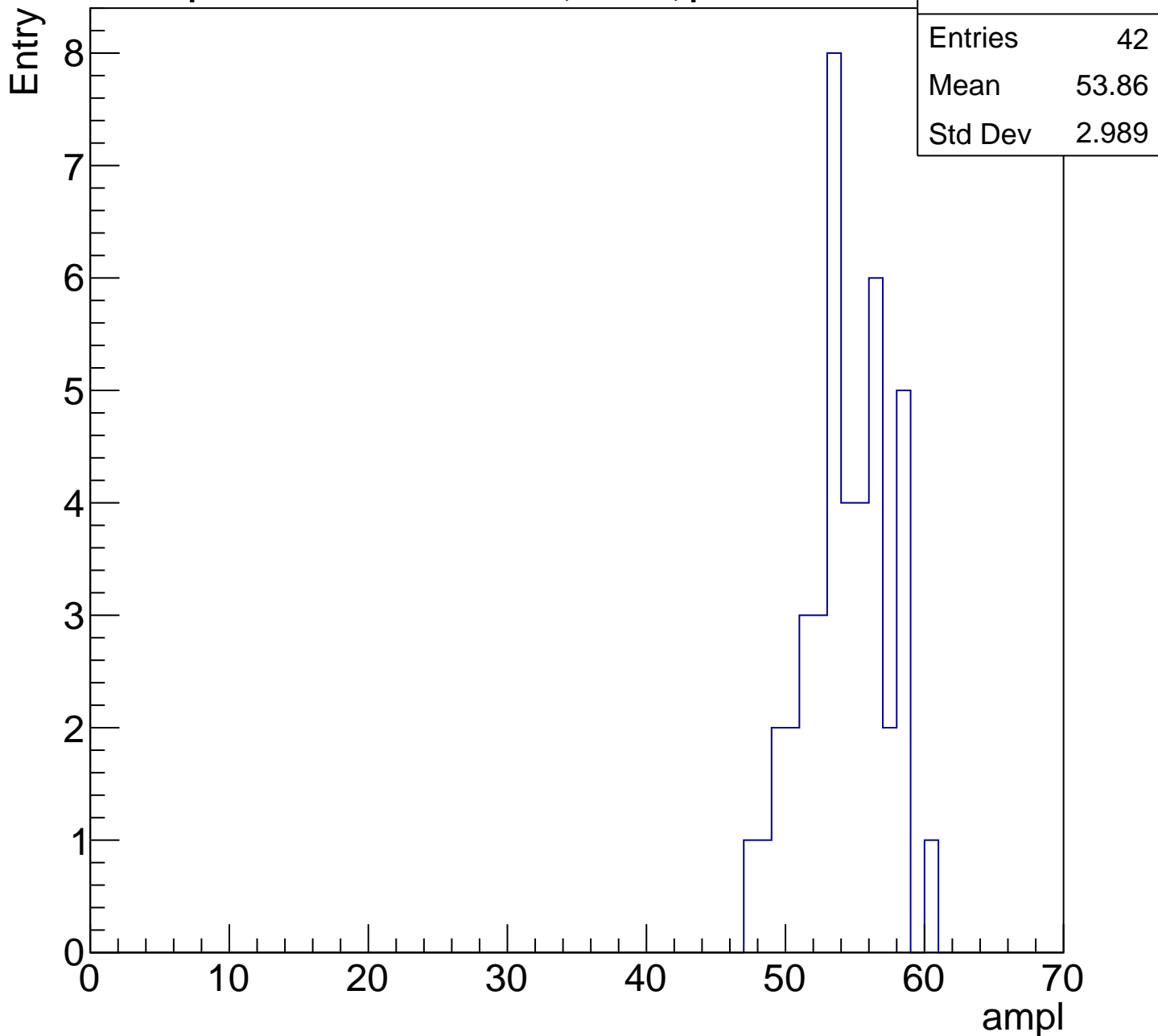
Entries	82
Mean	47.21
Std Dev	3.91

ampl



# B1L002S, U8-ch79, adc4

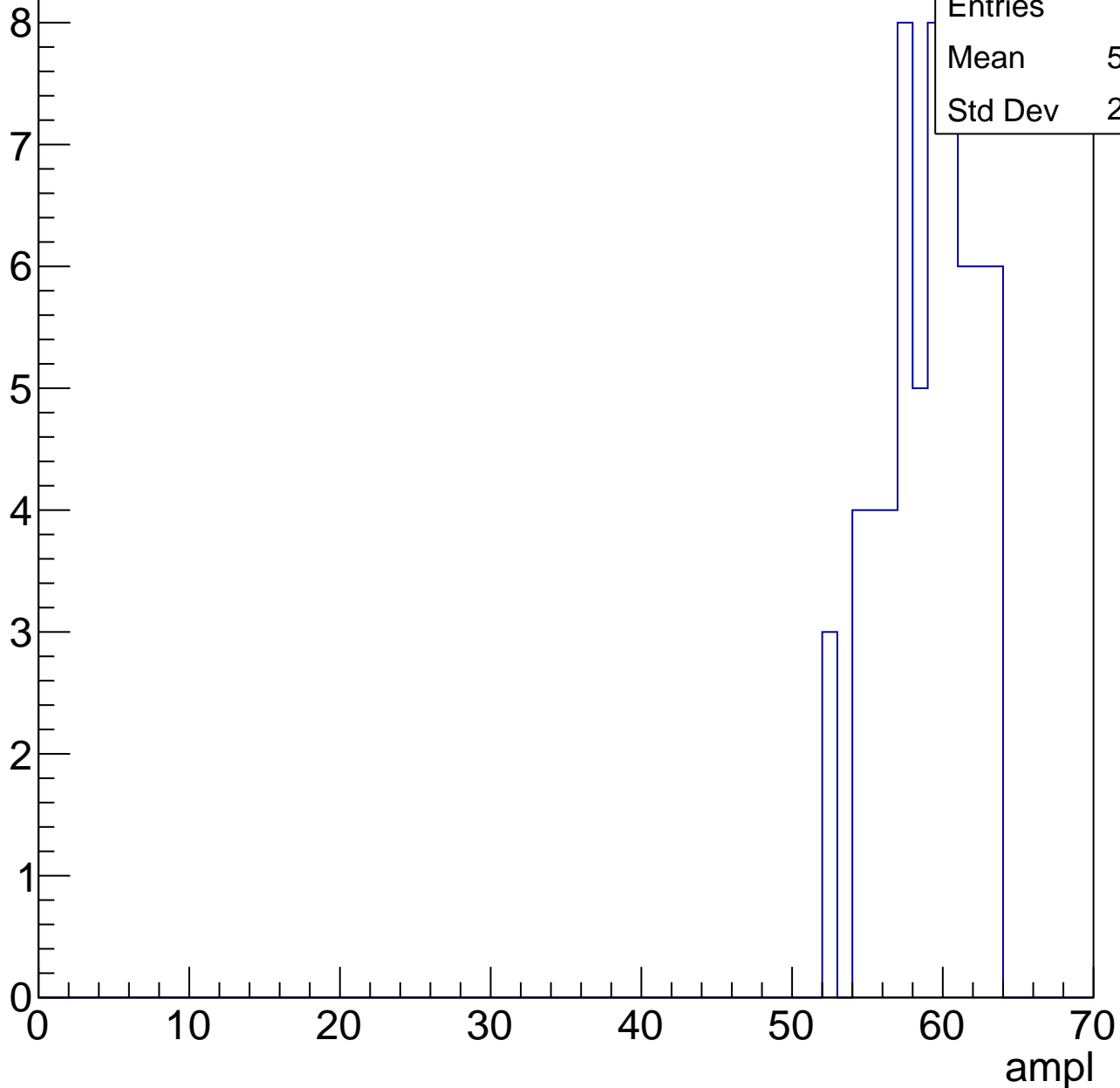
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

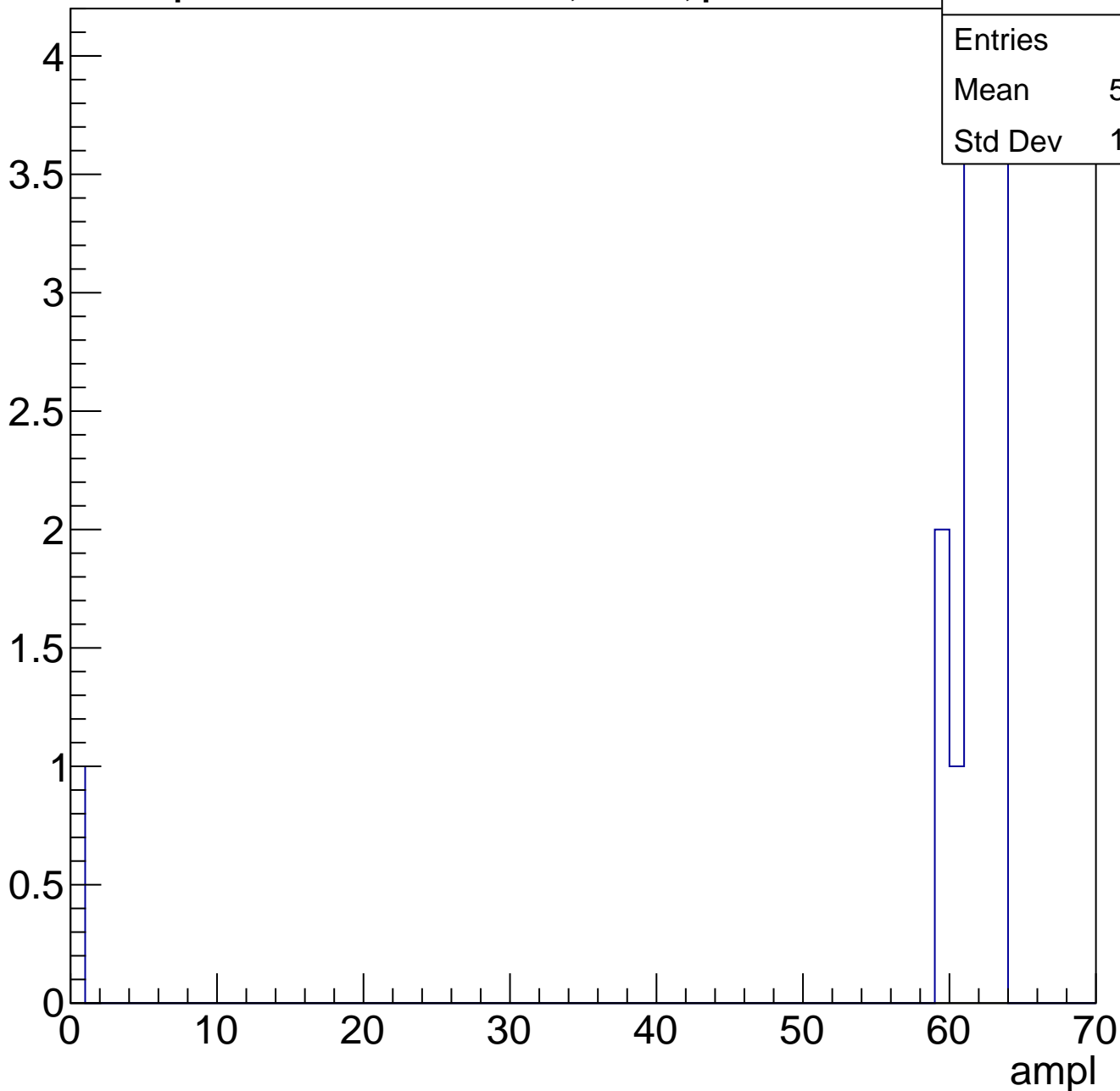


Entries	62
Mean	58.55
Std Dev	2.987

# B1L002S, U8-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch80, adc0

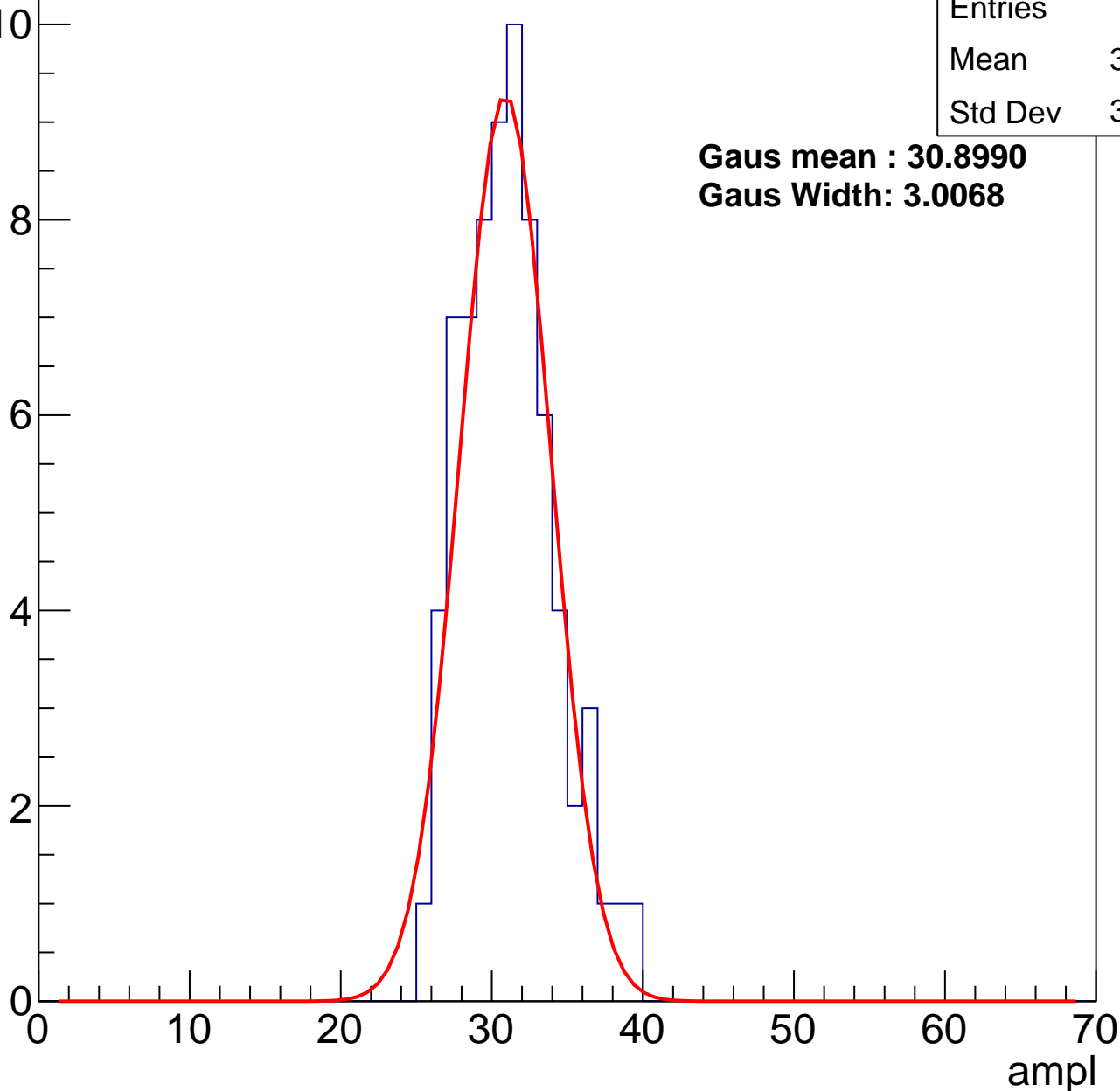
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	30.67
Std Dev	3.046

**Gaus mean : 30.8990**

**Gaus Width: 3.0068**



# B1L002S, U8-ch80, adc1

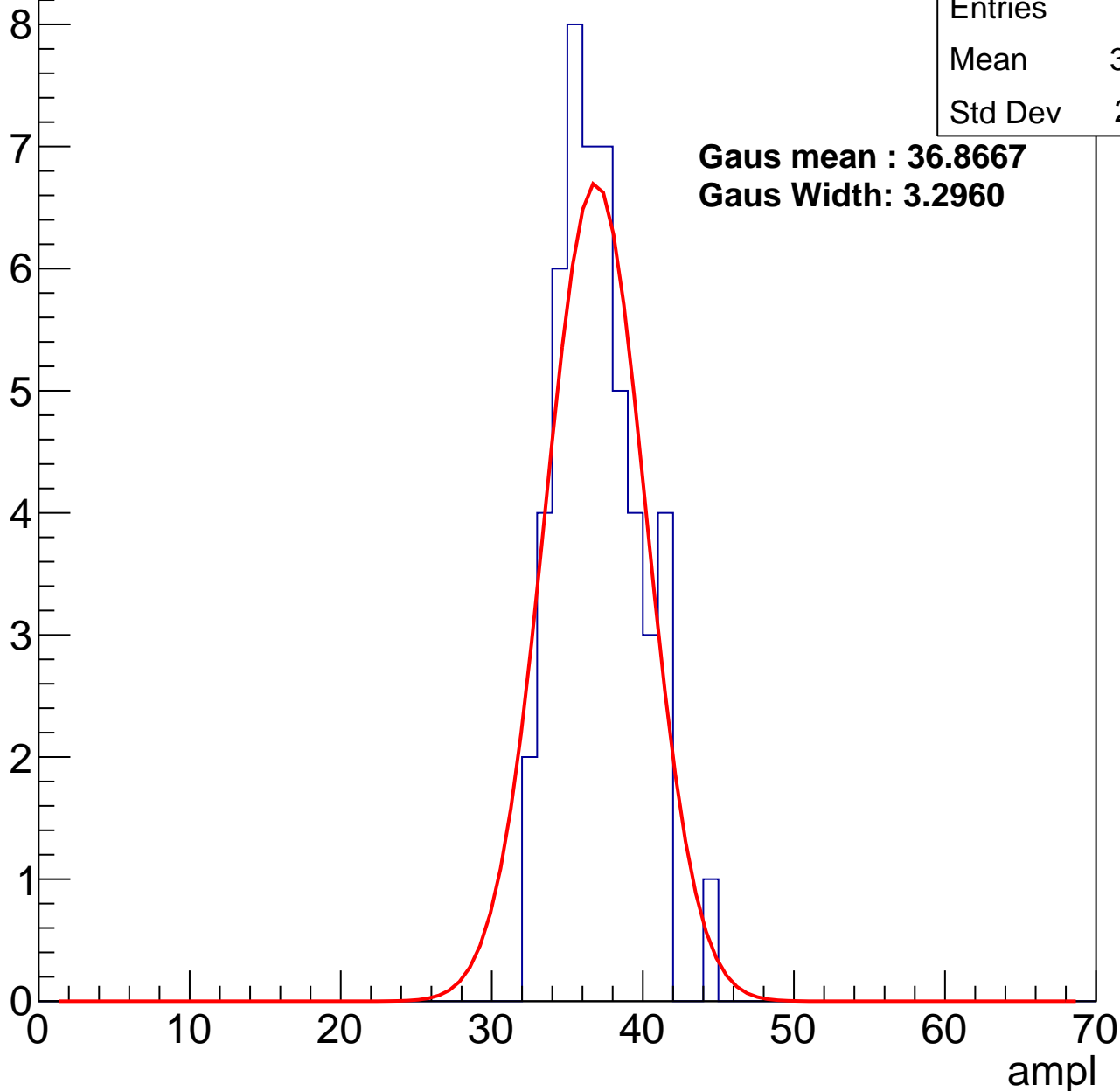
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	36.57
Std Dev	2.651

**Gaus mean : 36.8667**

**Gaus Width: 3.2960**



# B1L002S, U8-ch80, adc2

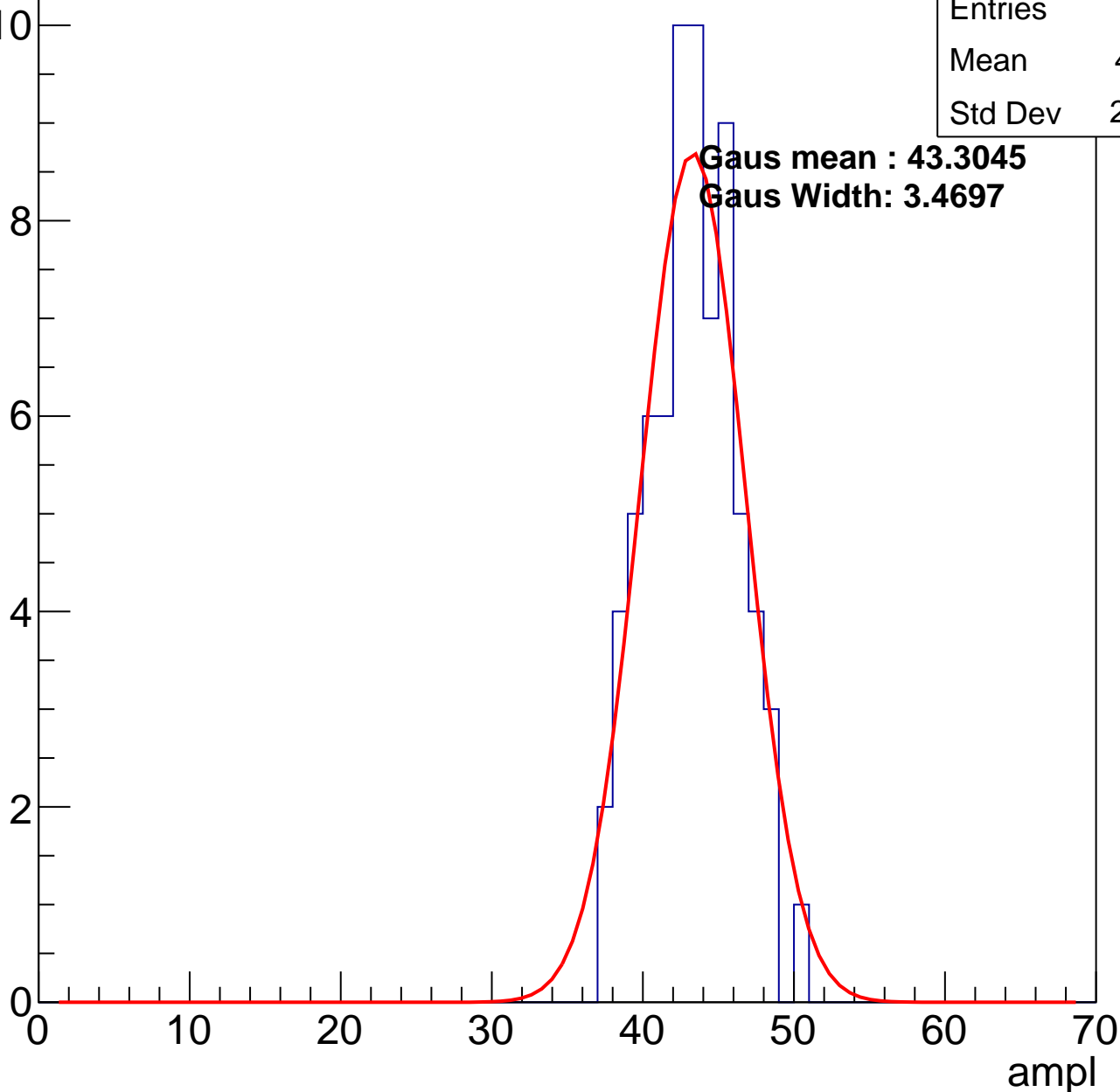
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	42.81
Std Dev	2.919

**Gaus mean : 43.3045**

**Gaus Width: 3.4697**

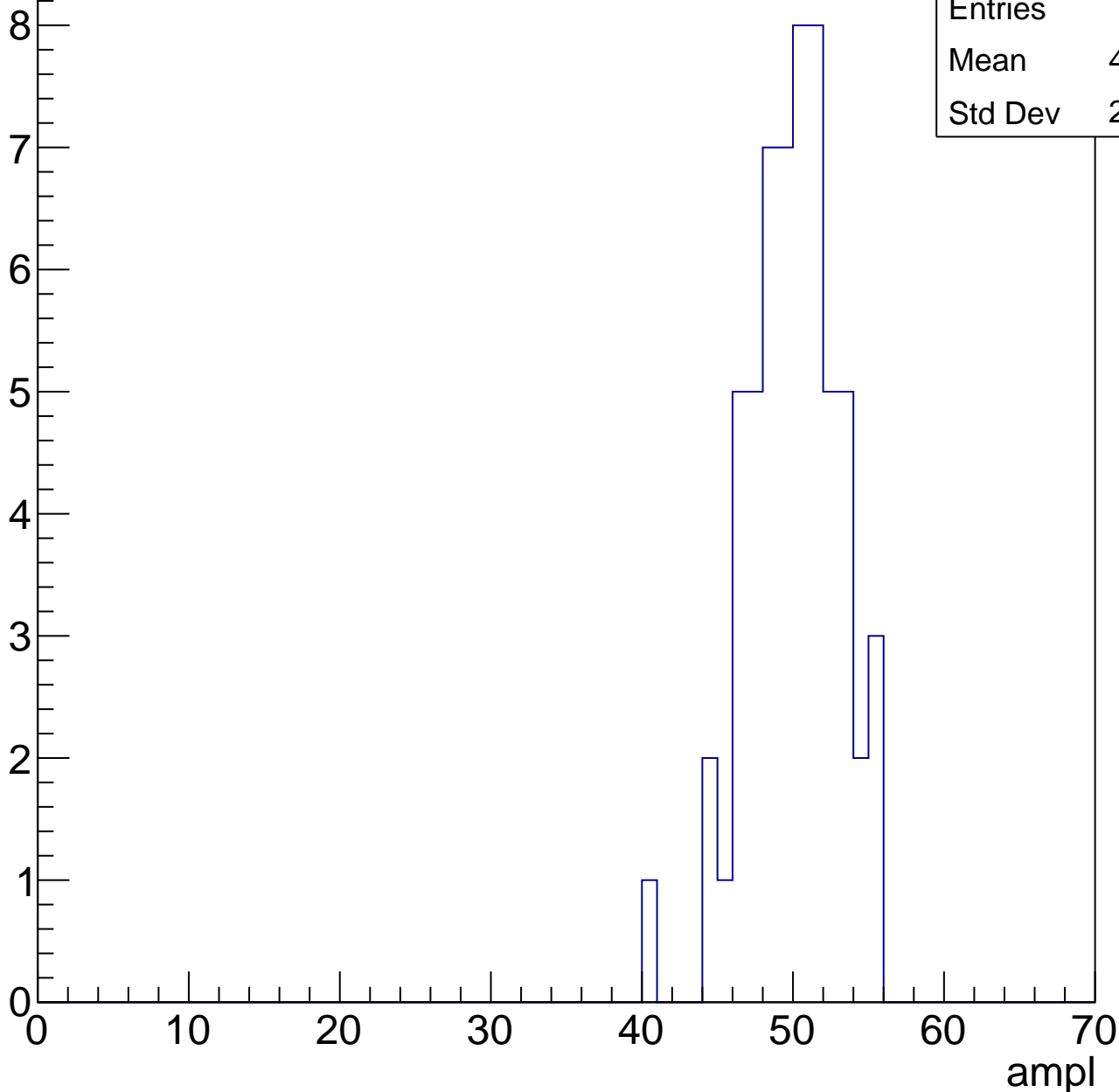


# B1L002S, U8-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	49.54
Std Dev	2.982

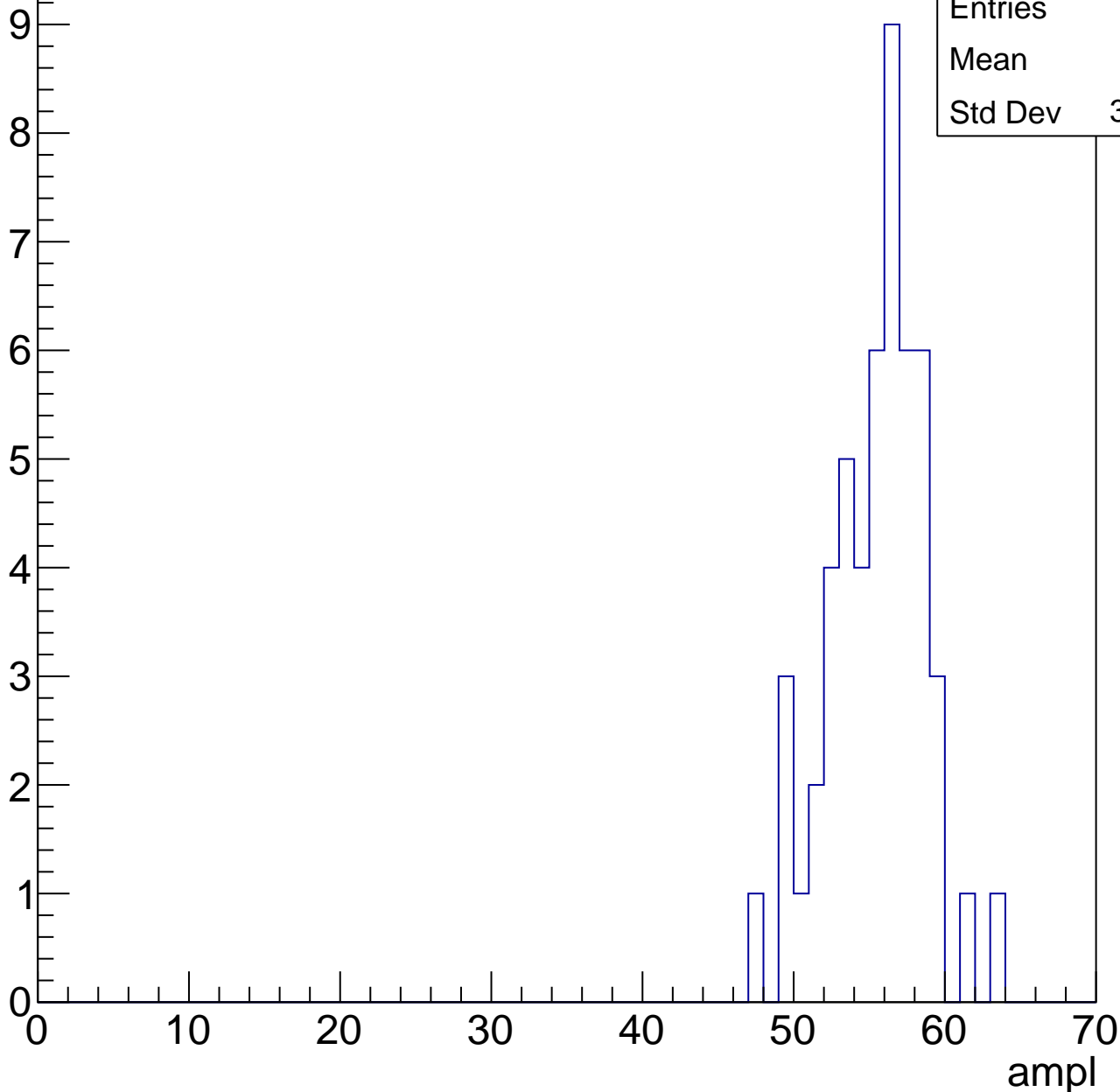


# B1L002S, U8-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	55
Std Dev	3.174

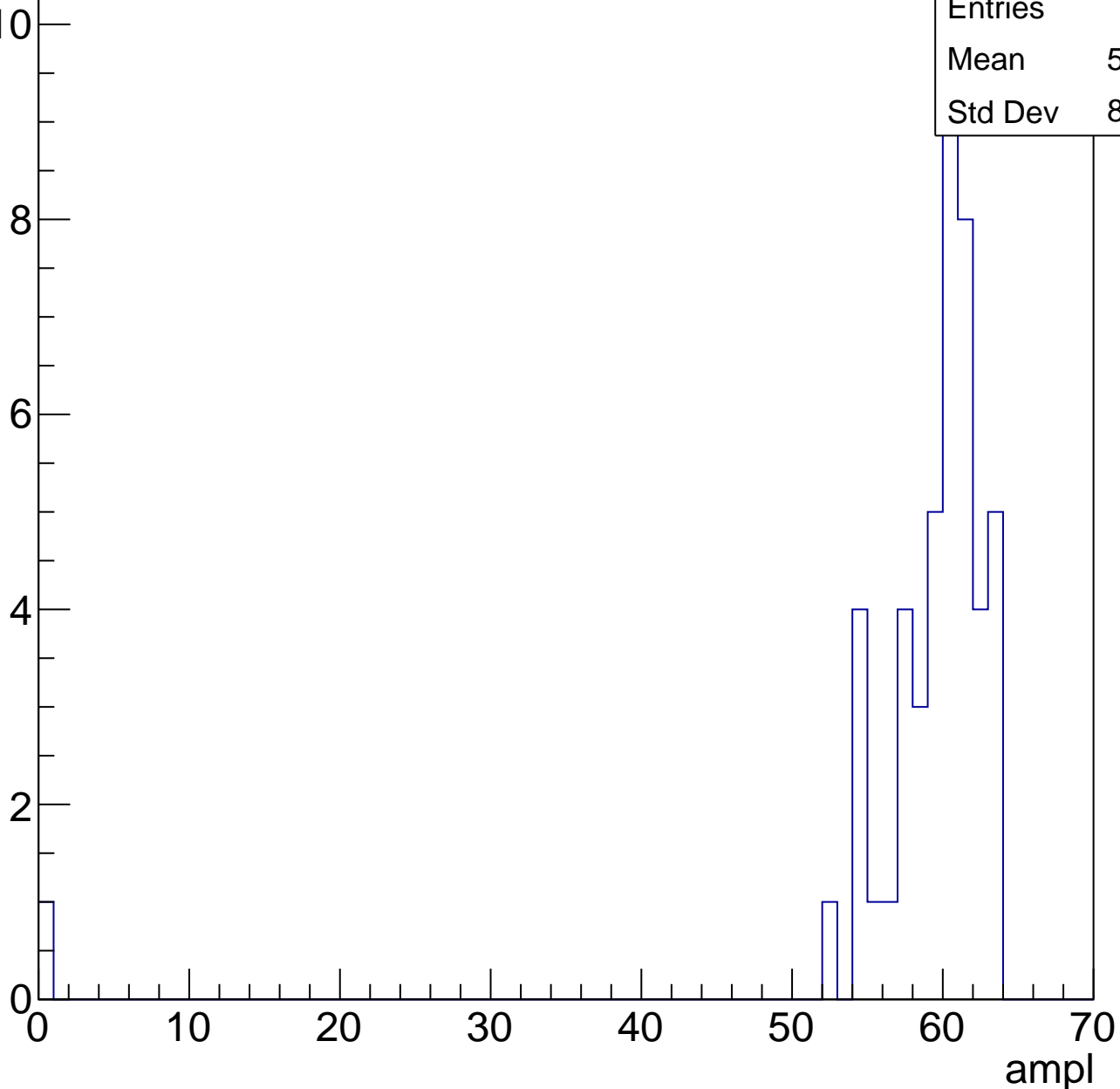


# B1L002S, U8-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	58.02
Std Dev	8.976

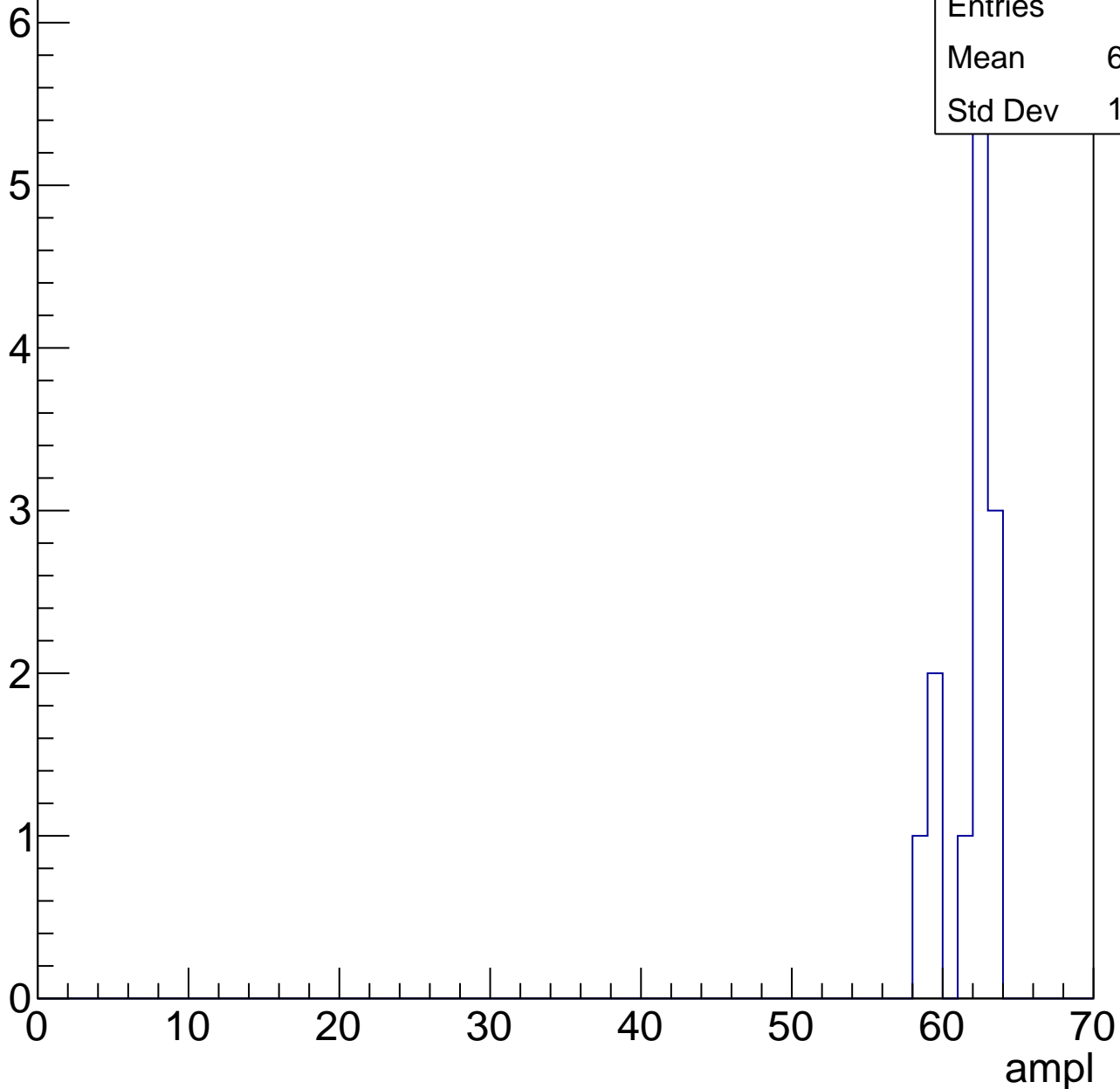


# B1L002S, U8-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.38
Std Dev	1.595





# B1L002S, U8-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	44
Std Dev	19

ampl

# B1L002S, U8-ch81, adc0

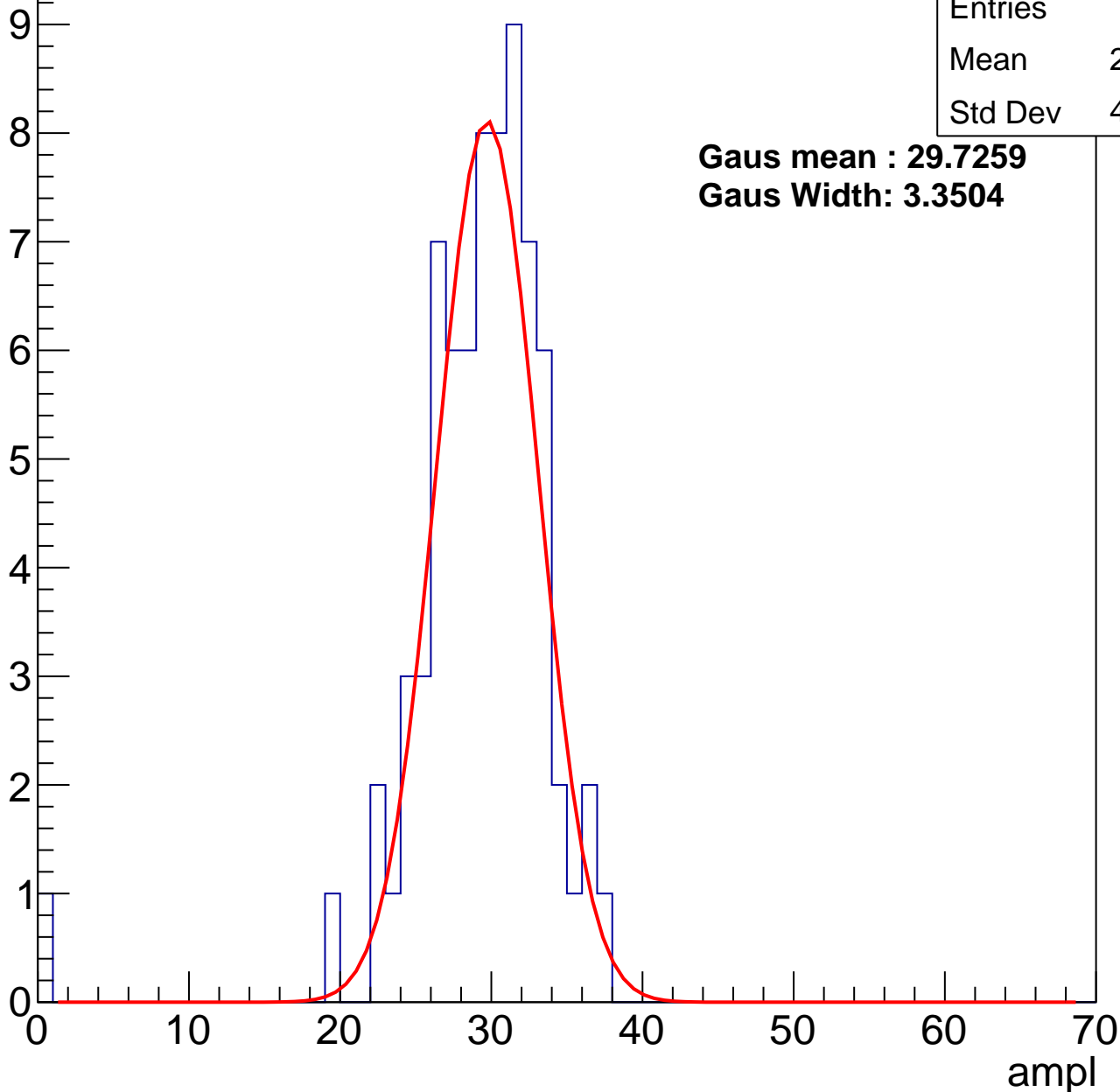
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.78
Std Dev	4.847

**Gaus mean : 29.7259**

**Gaus Width: 3.3504**



# B1L002S, U8-ch81, adc1

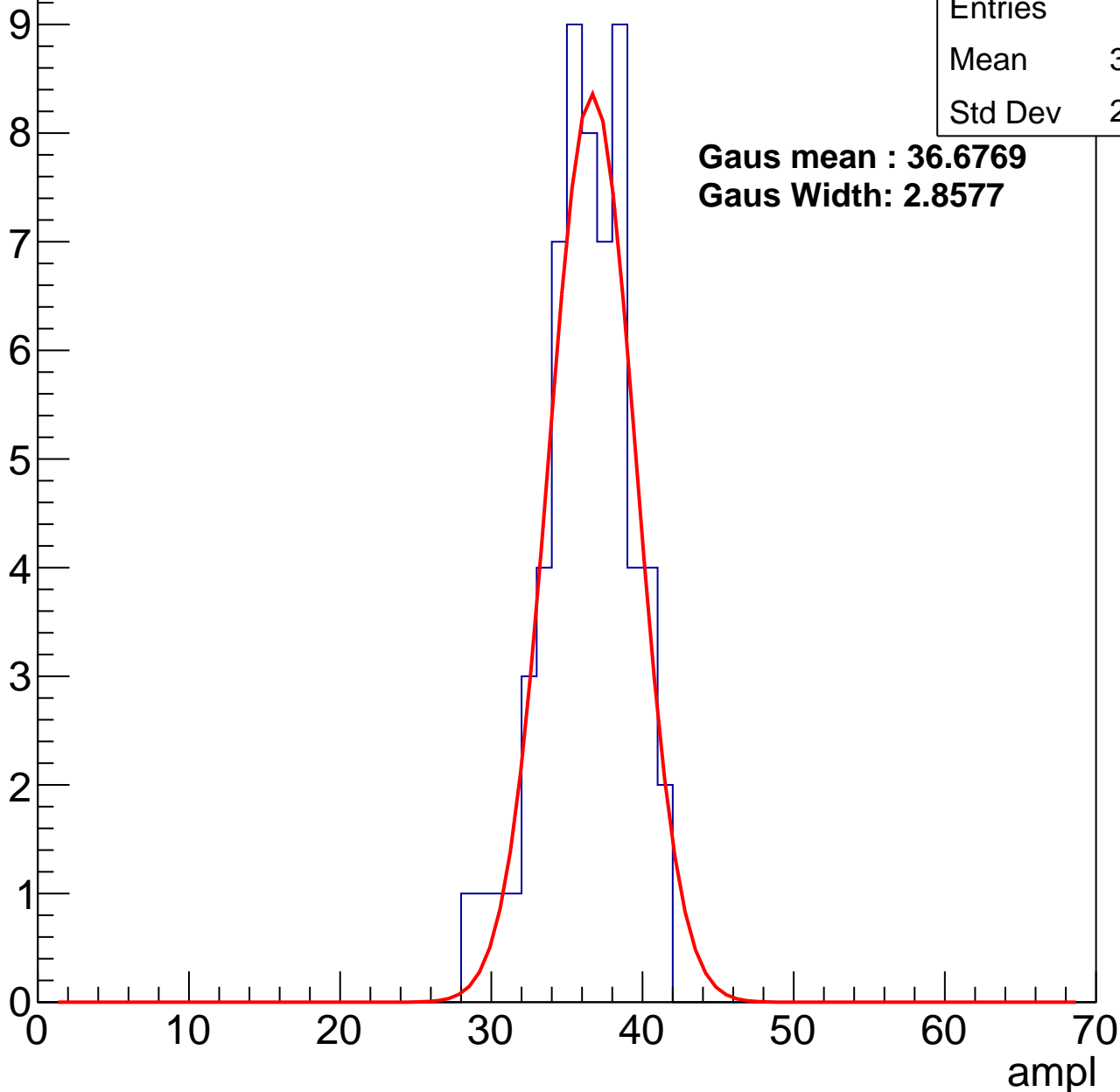
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.84
Std Dev	2.824

**Gaus mean : 36.6769**

**Gaus Width: 2.8577**



# B1L002S, U8-ch81, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	57
Mean	42.16
Std Dev	2.84

**Gaus mean : 42.8831**

**Gaus Width: 2.8323**

Entry

10

8

6

4

2

0

0

10

20

30

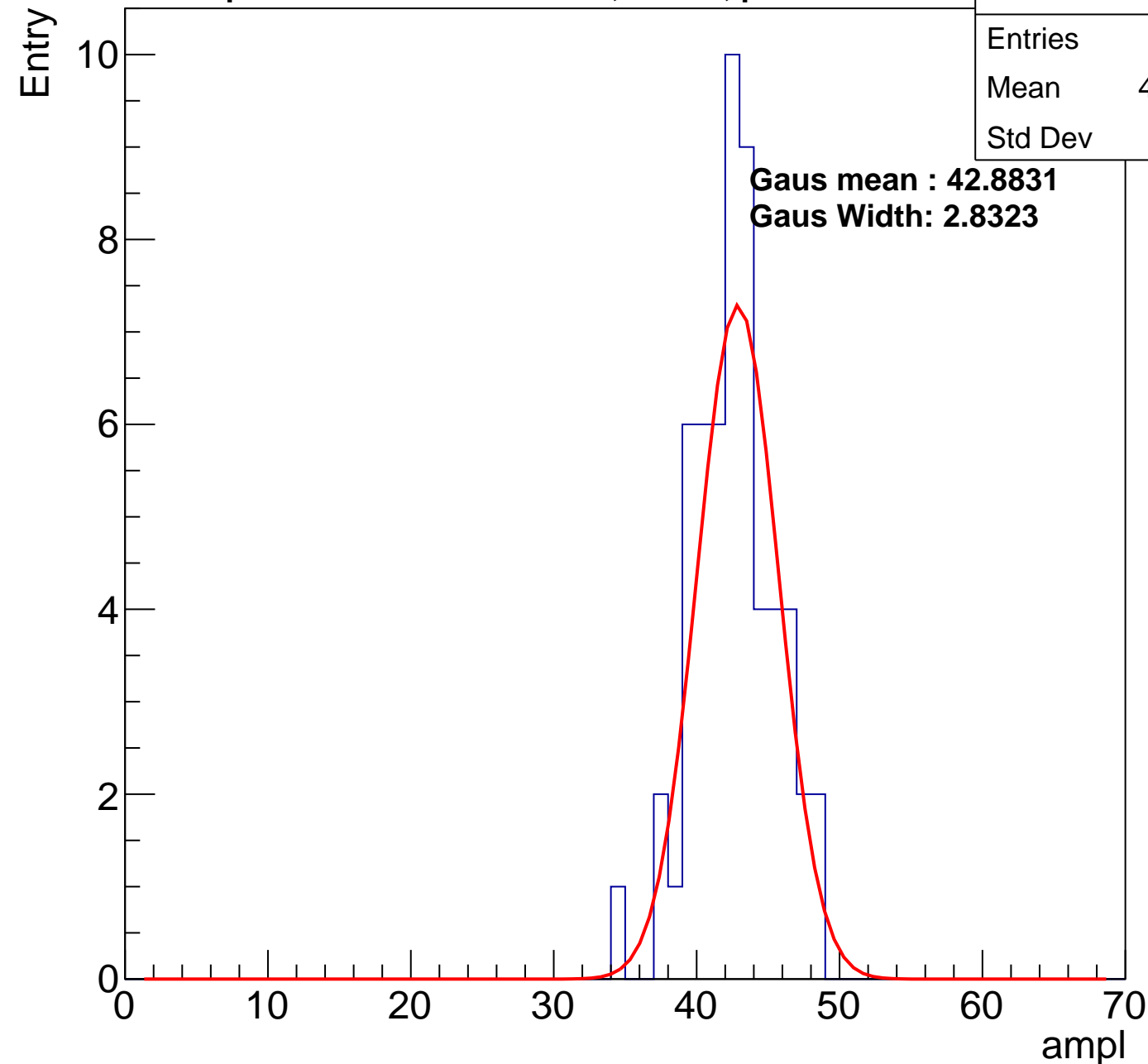
40

50

60

70

ampl

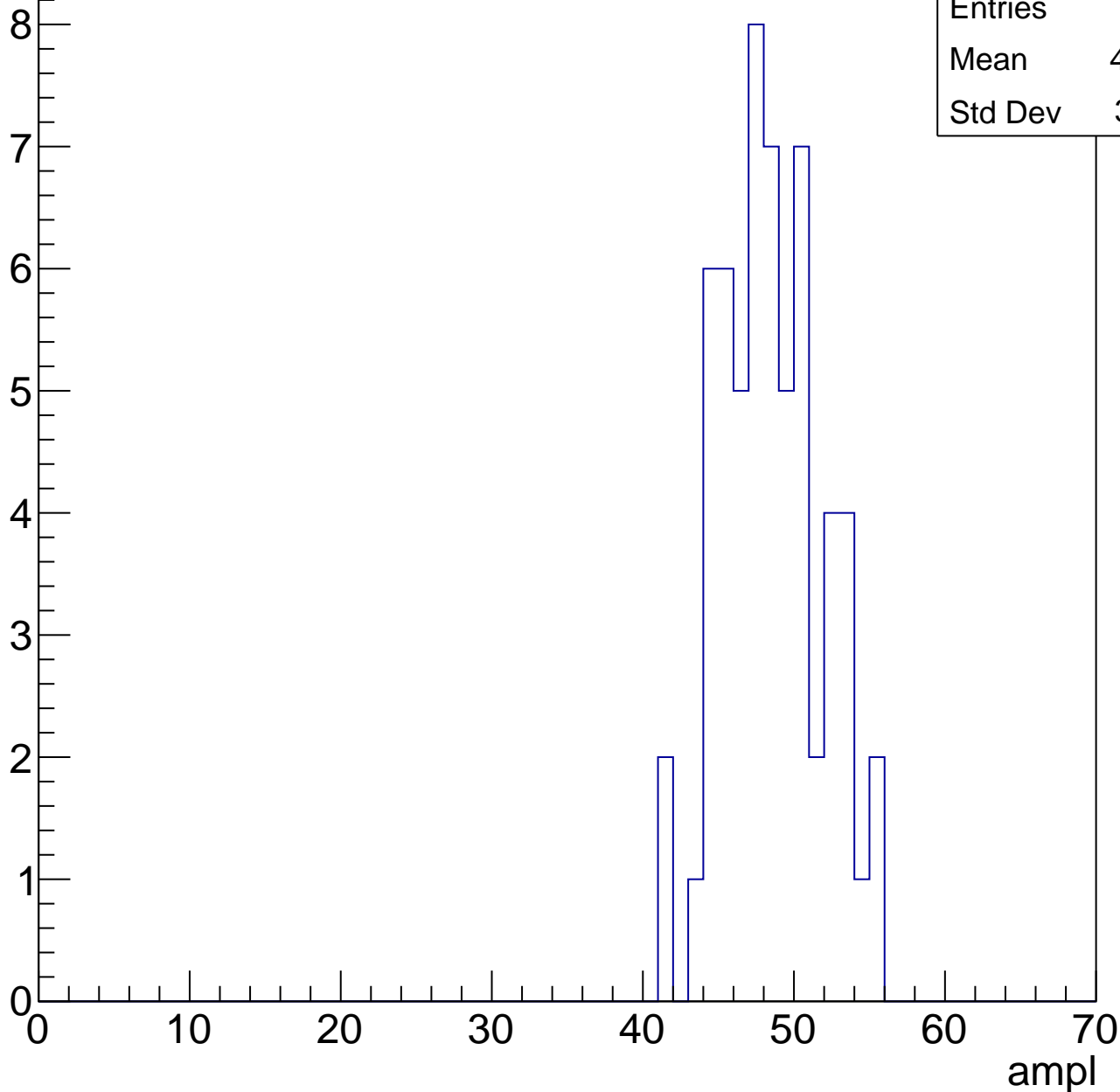


# B1L002S, U8-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.03
Std Dev	3.291

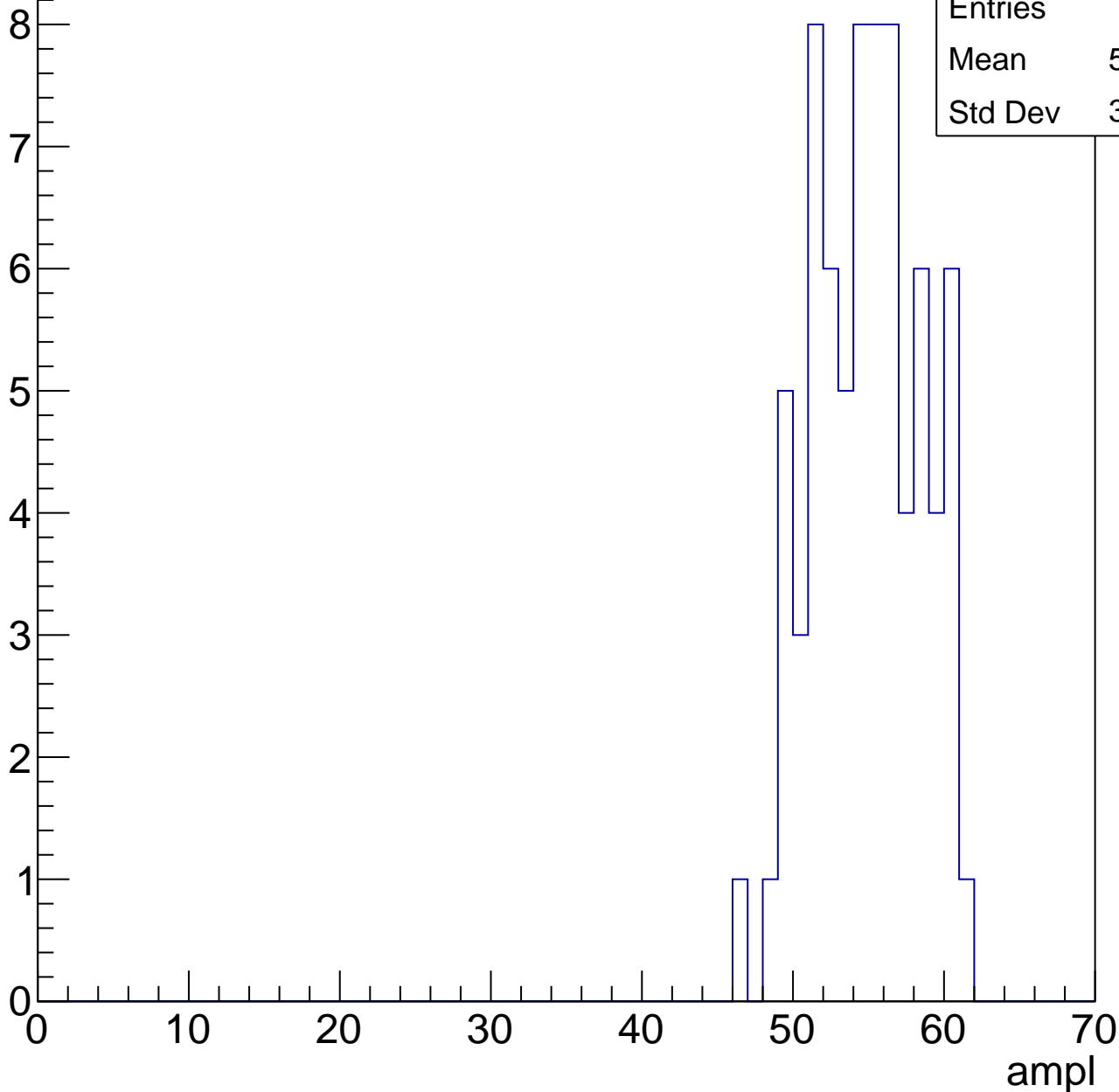


# B1L002S, U8-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	54.42
Std Dev	3.484

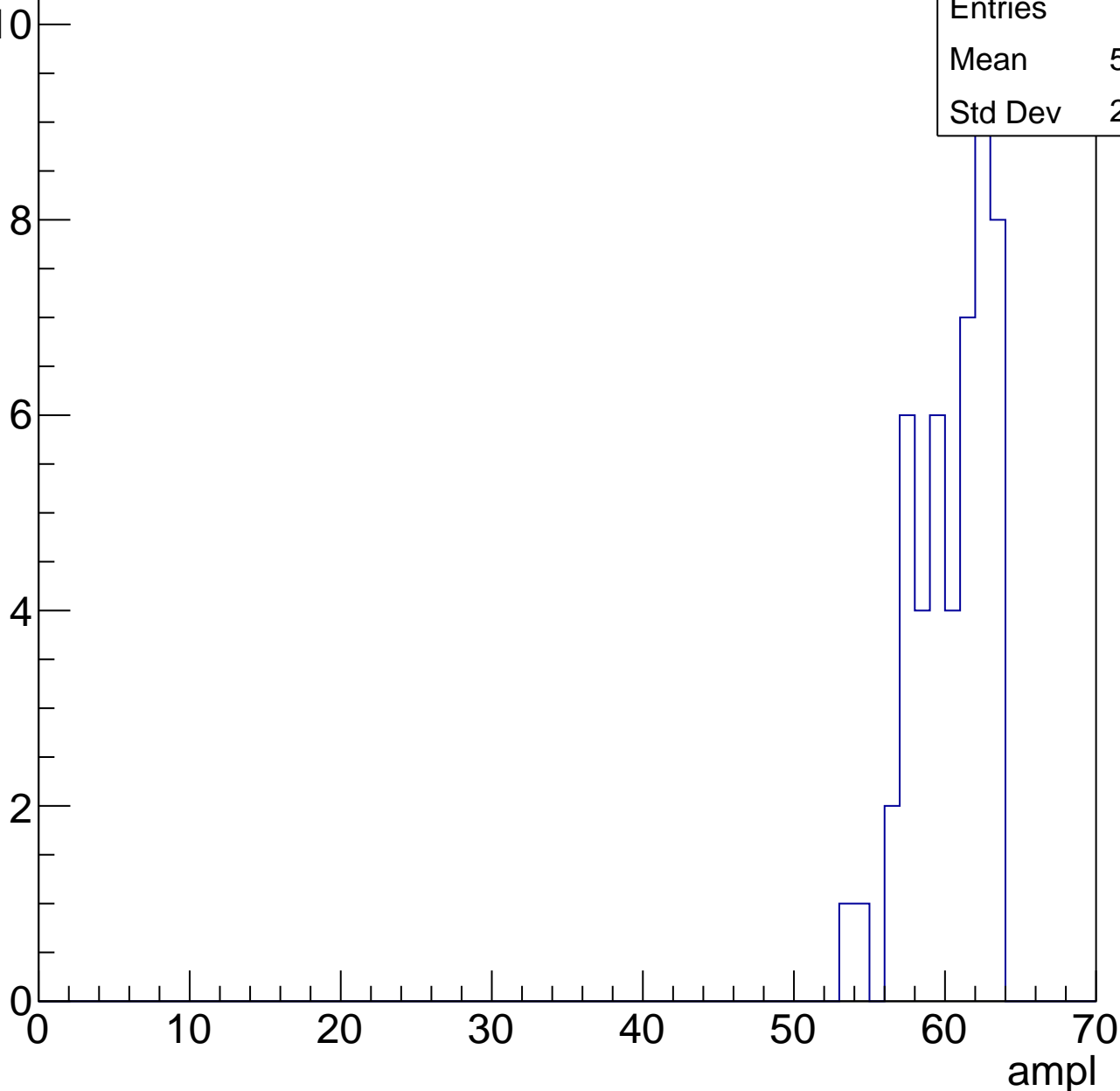


# B1L002S, U8-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

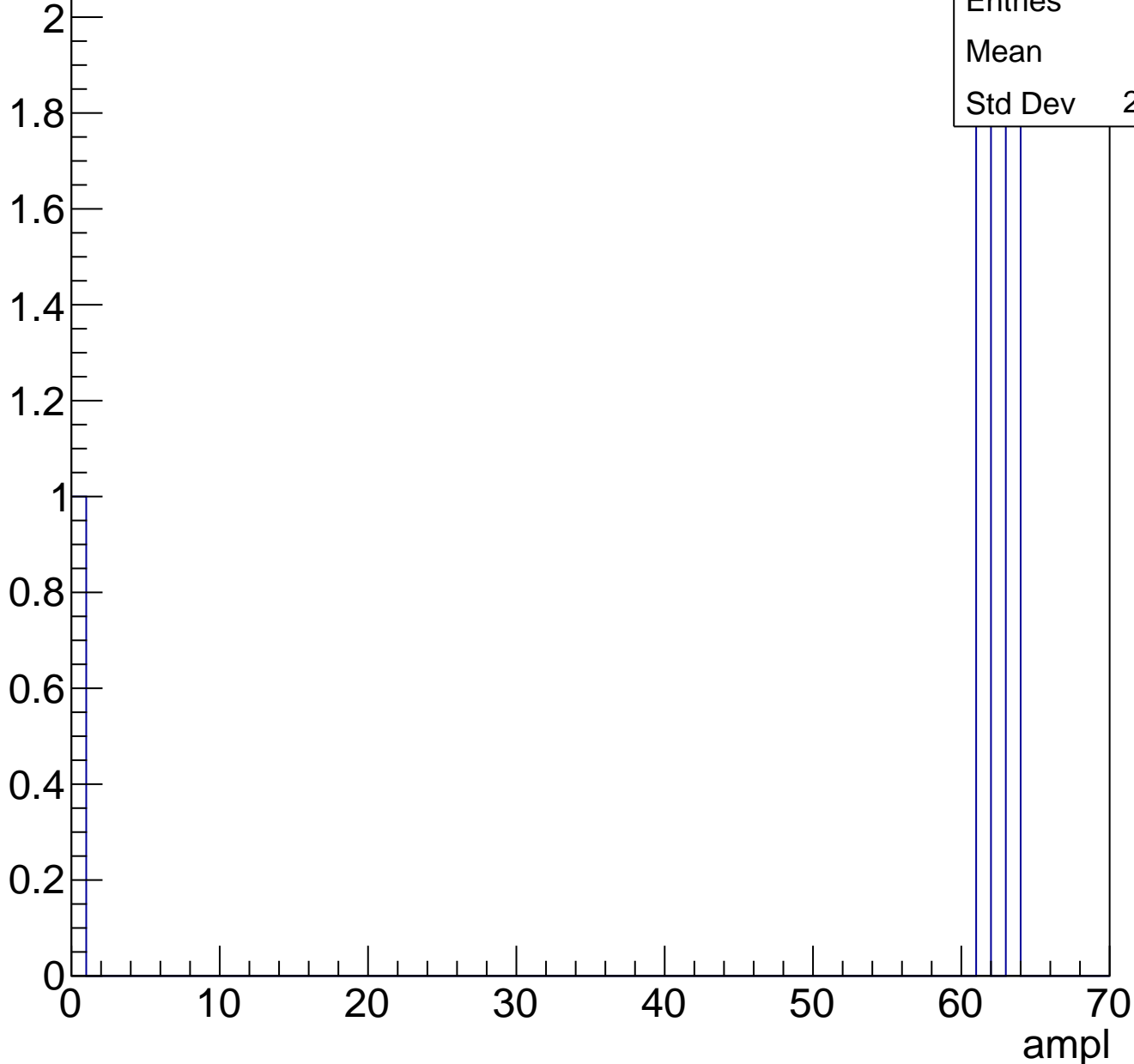
Entries	49
Mean	59.96
Std Dev	2.523



# B1L002S, U8-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

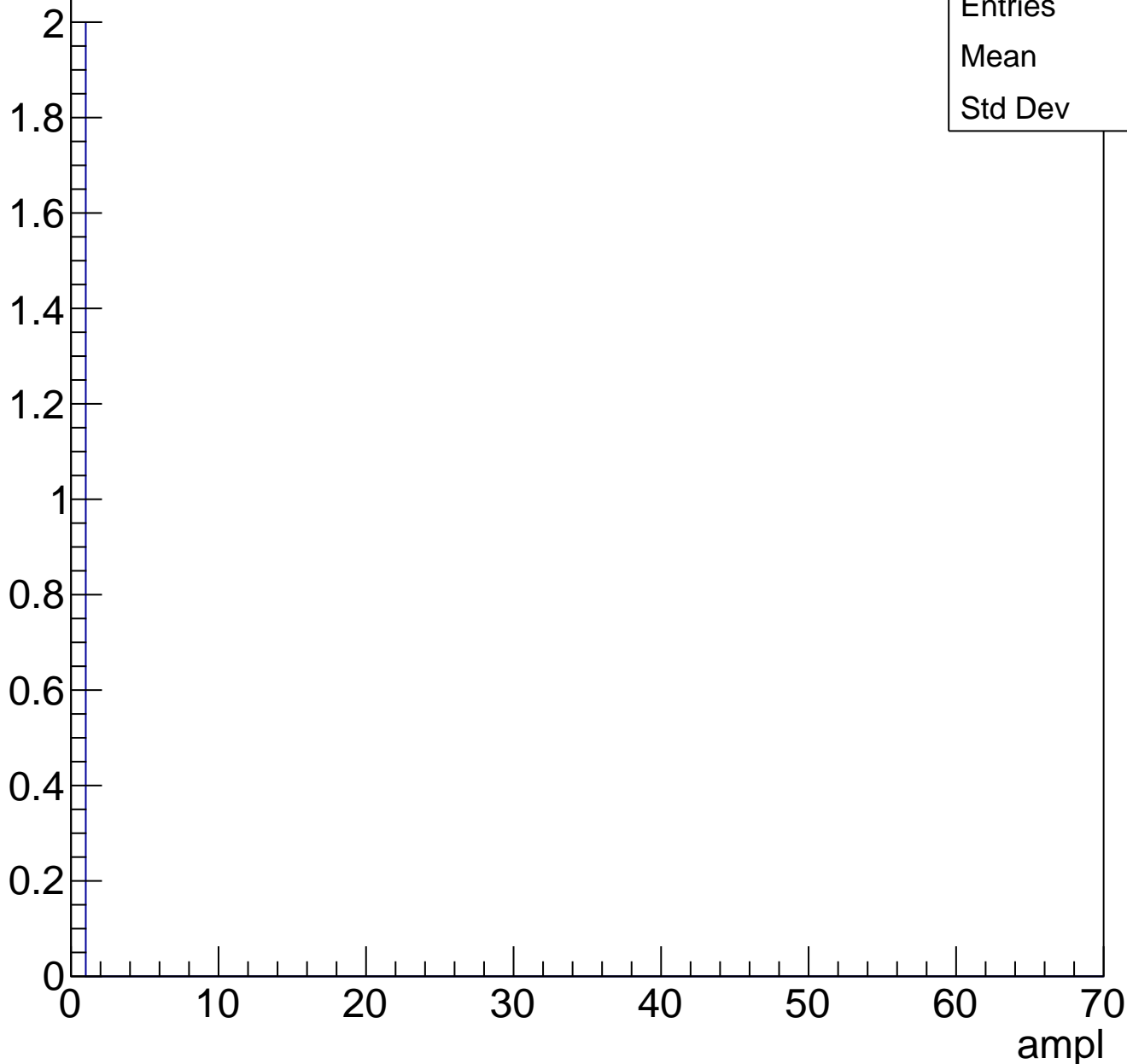




# B1L002S, U8-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch82, adc0

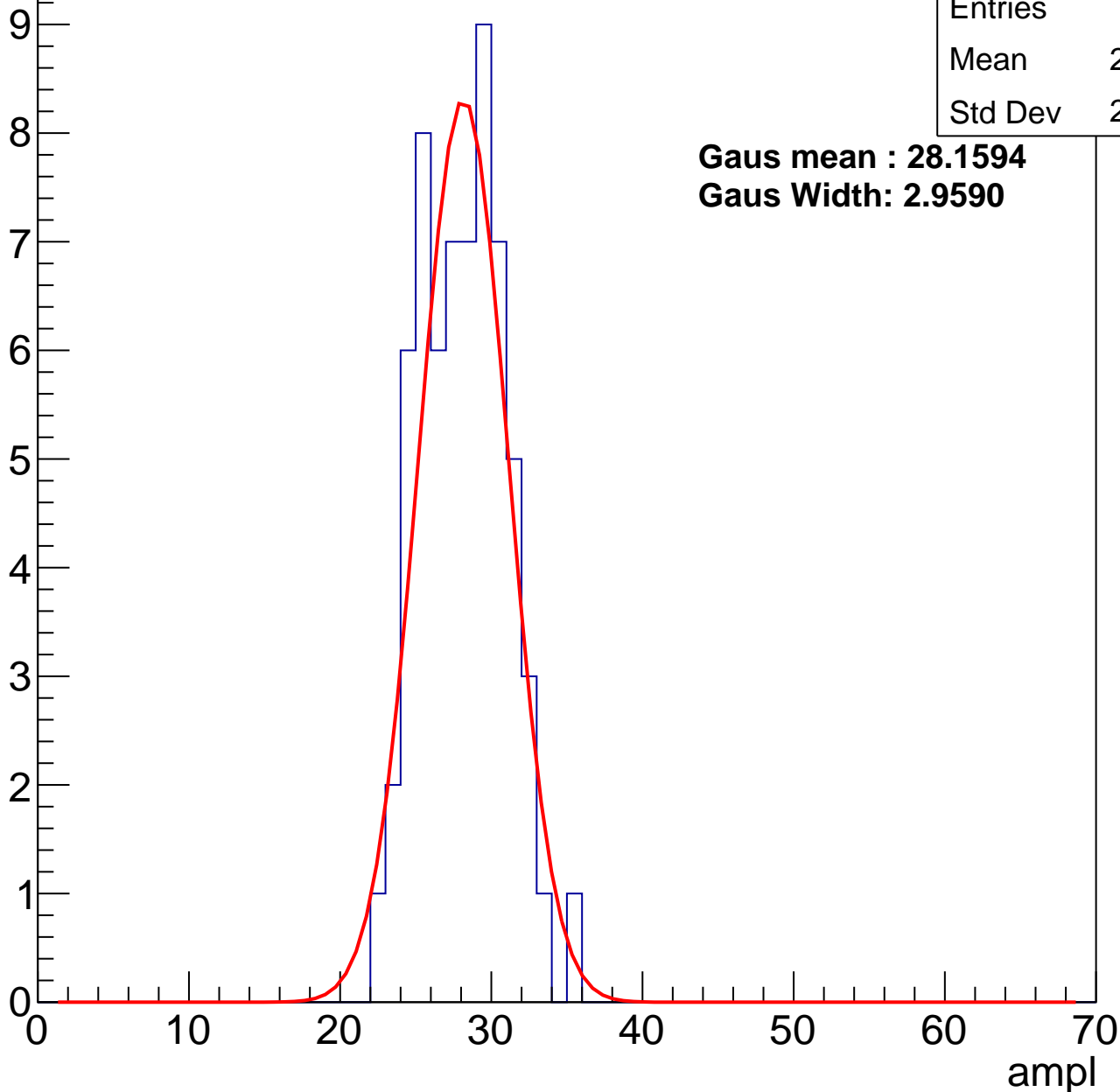
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.67
Std Dev	2.766

**Gaus mean : 28.1594**

**Gaus Width: 2.9590**



# B1L002S, U8-ch82, adc1

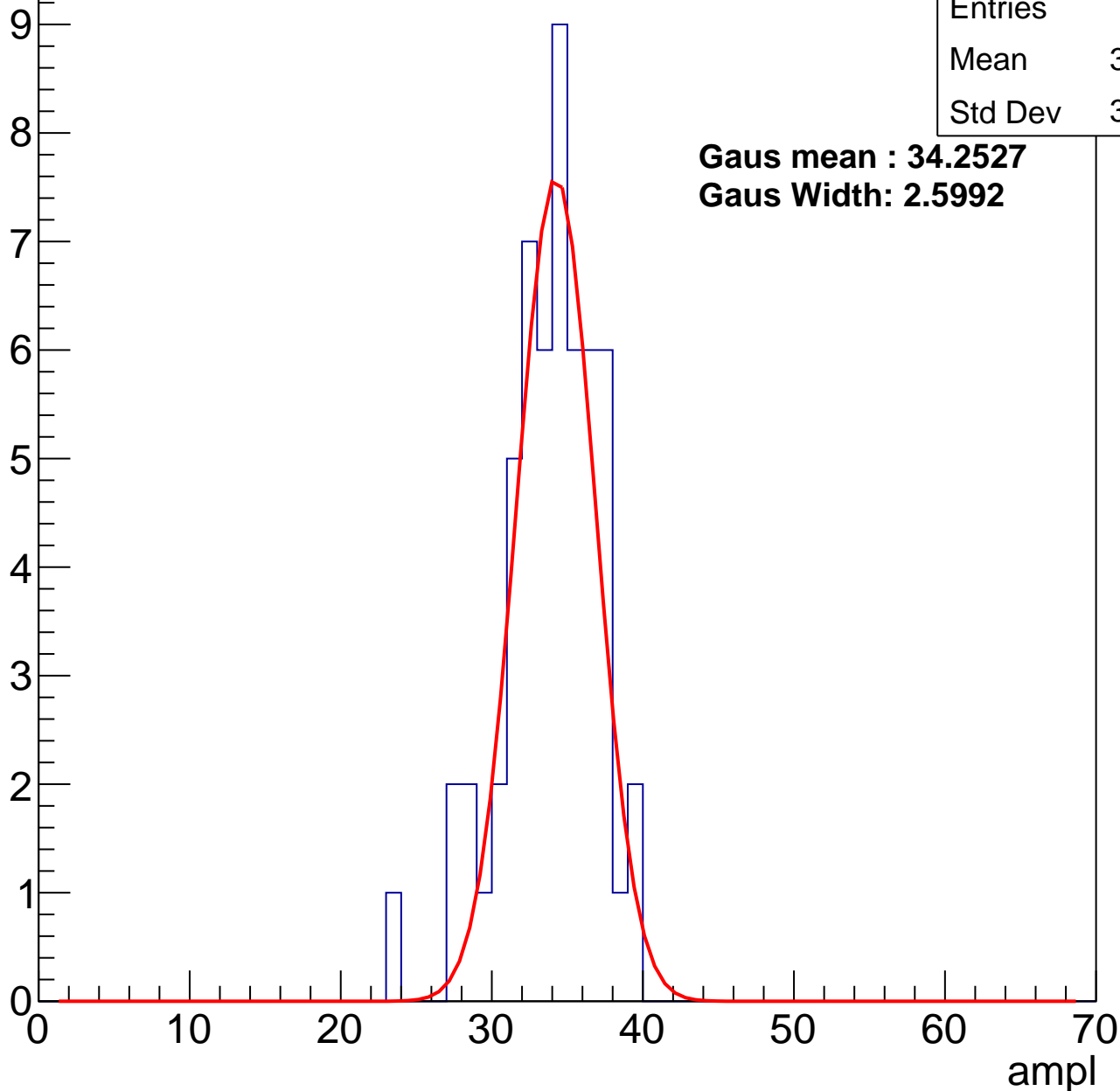
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.38
Std Dev	3.148

**Gaus mean : 34.2527**

**Gaus Width: 2.5992**



# B1L002S, U8-ch82, adc2

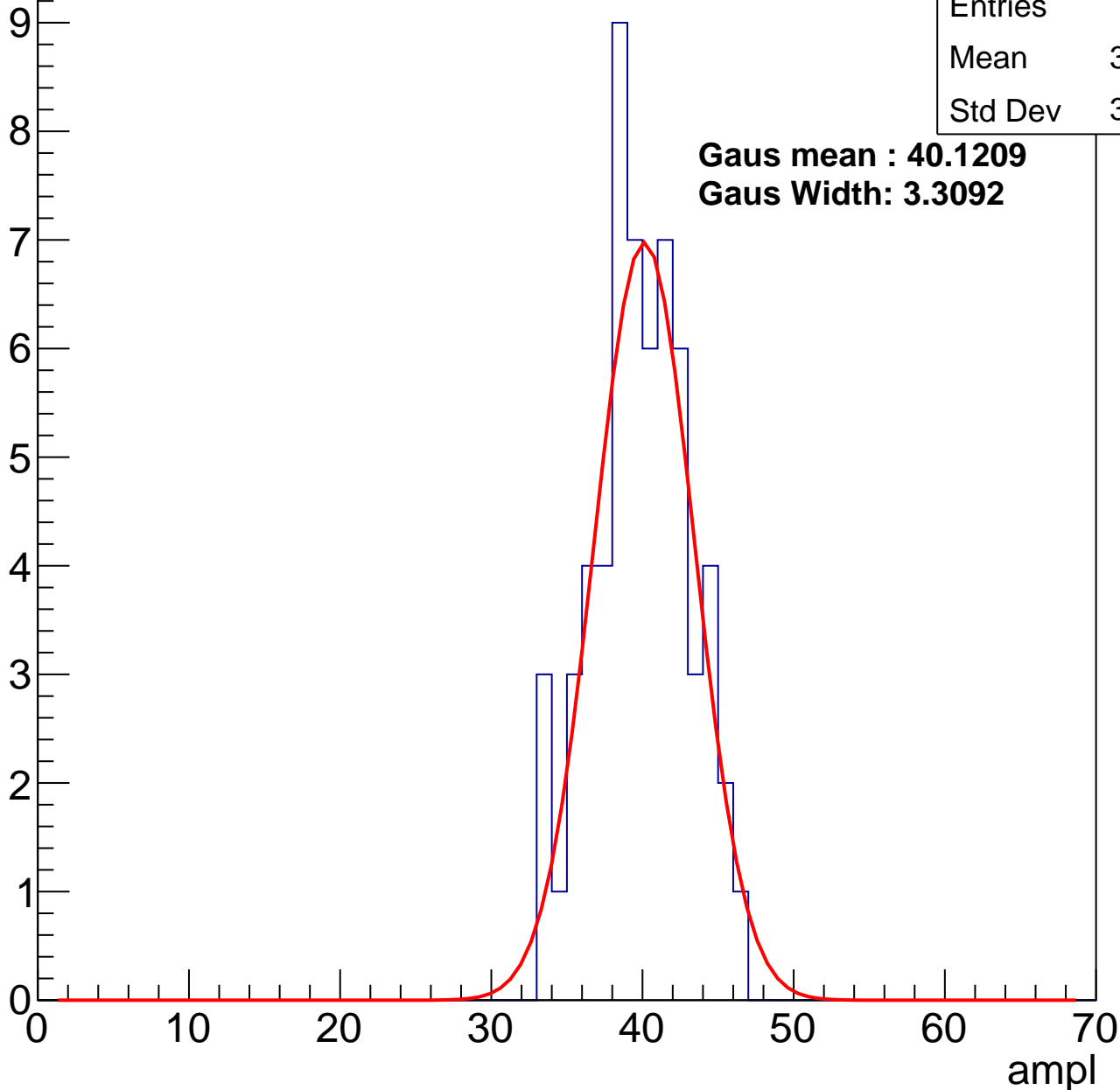
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	39.42
Std Dev	3.132

**Gaus mean : 40.1209**

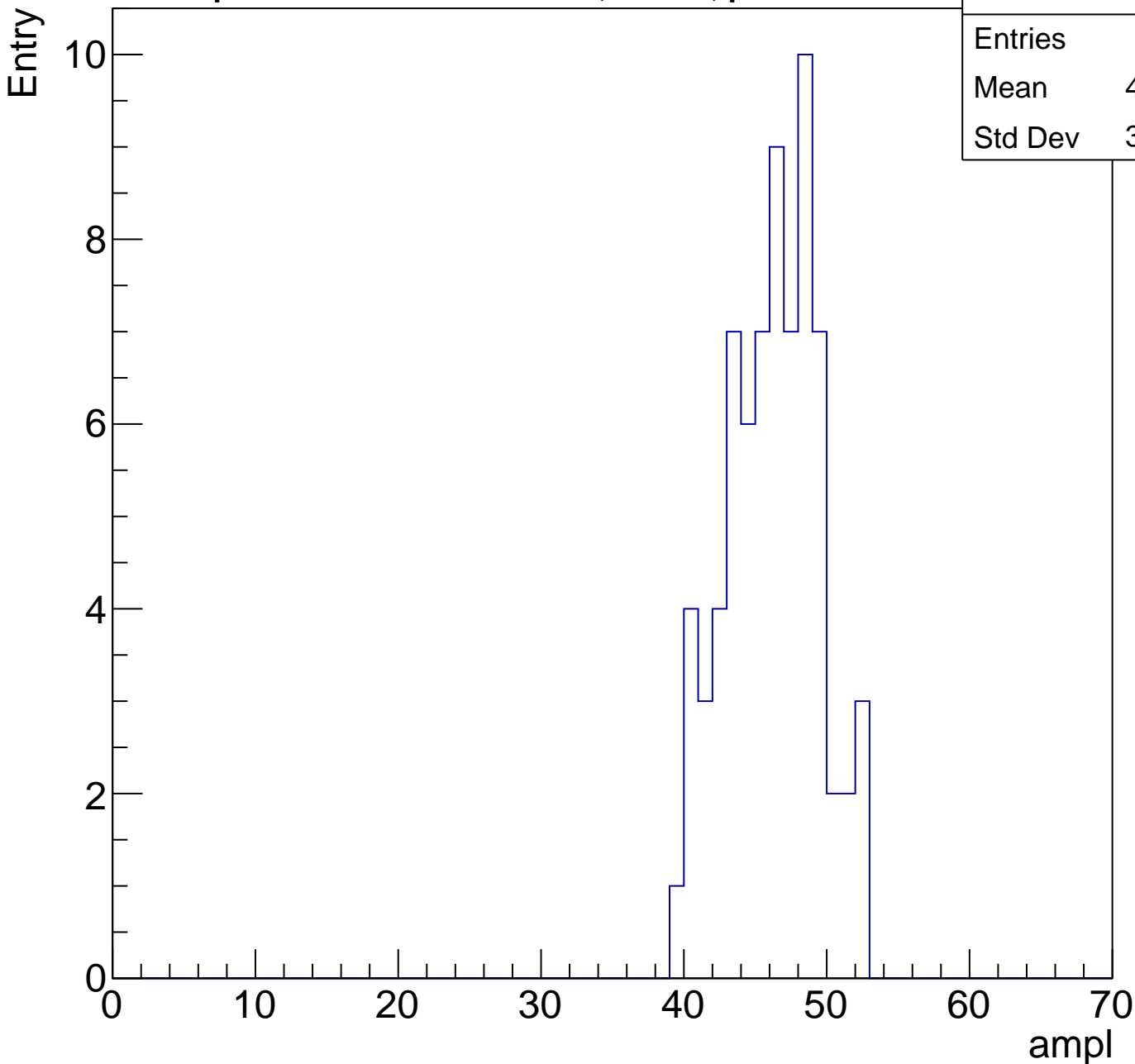
**Gaus Width: 3.3092**



# B1L002S, U8-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	45.75
Std Dev	3.166

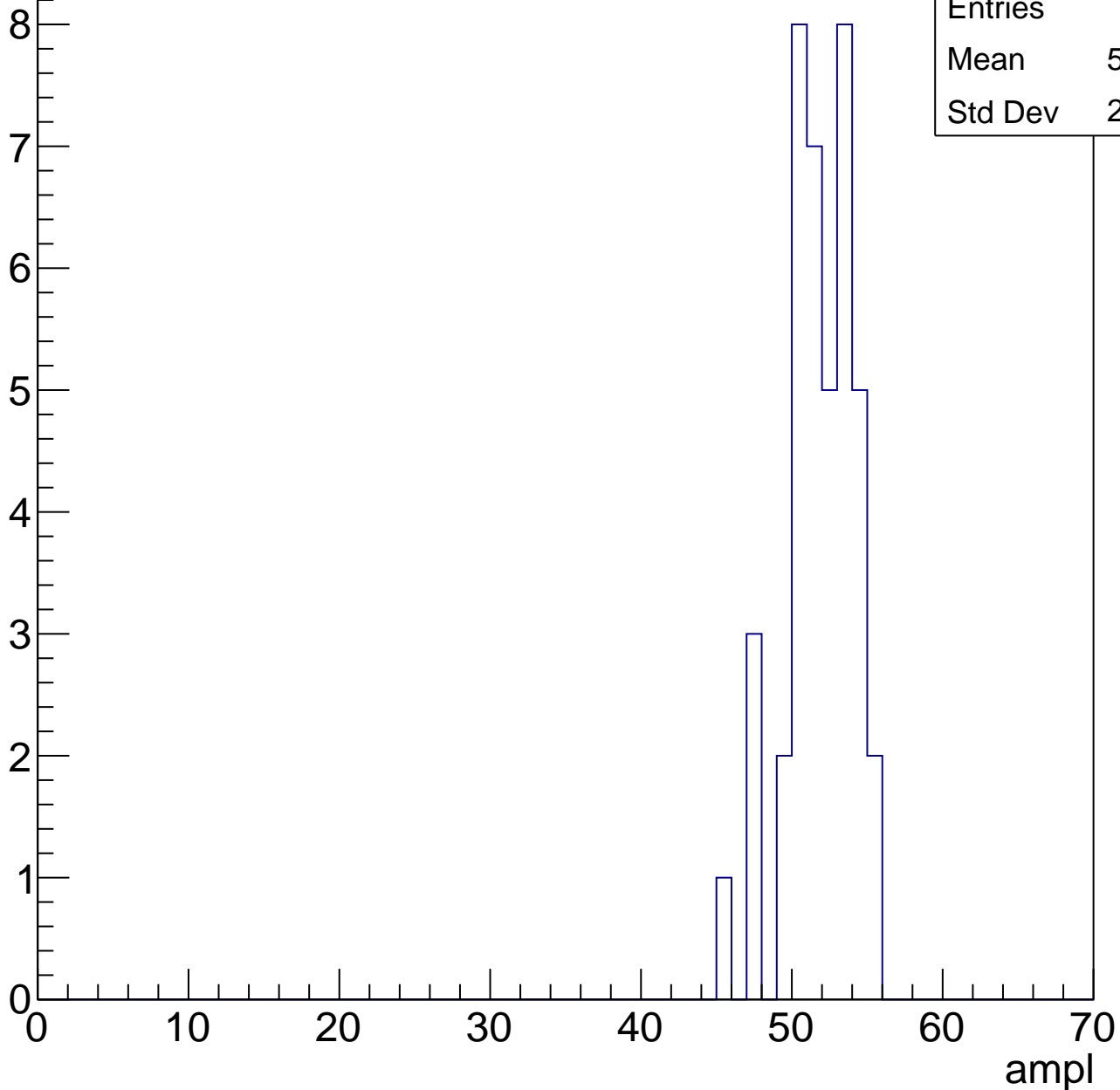


# B1L002S, U8-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	51.34
Std Dev	2.259

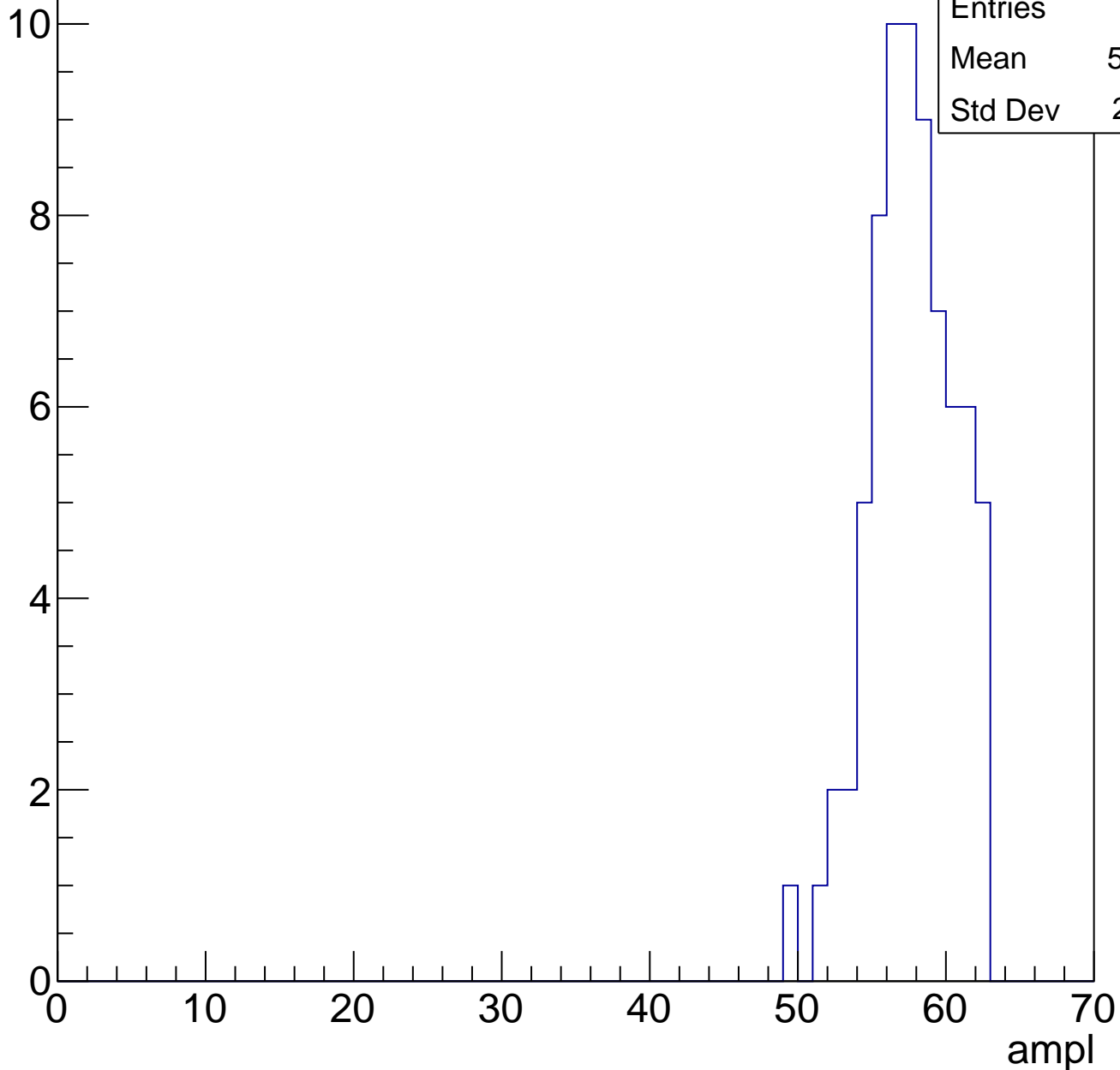


# B1L002S, U8-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	57.24
Std Dev	2.831

Entry

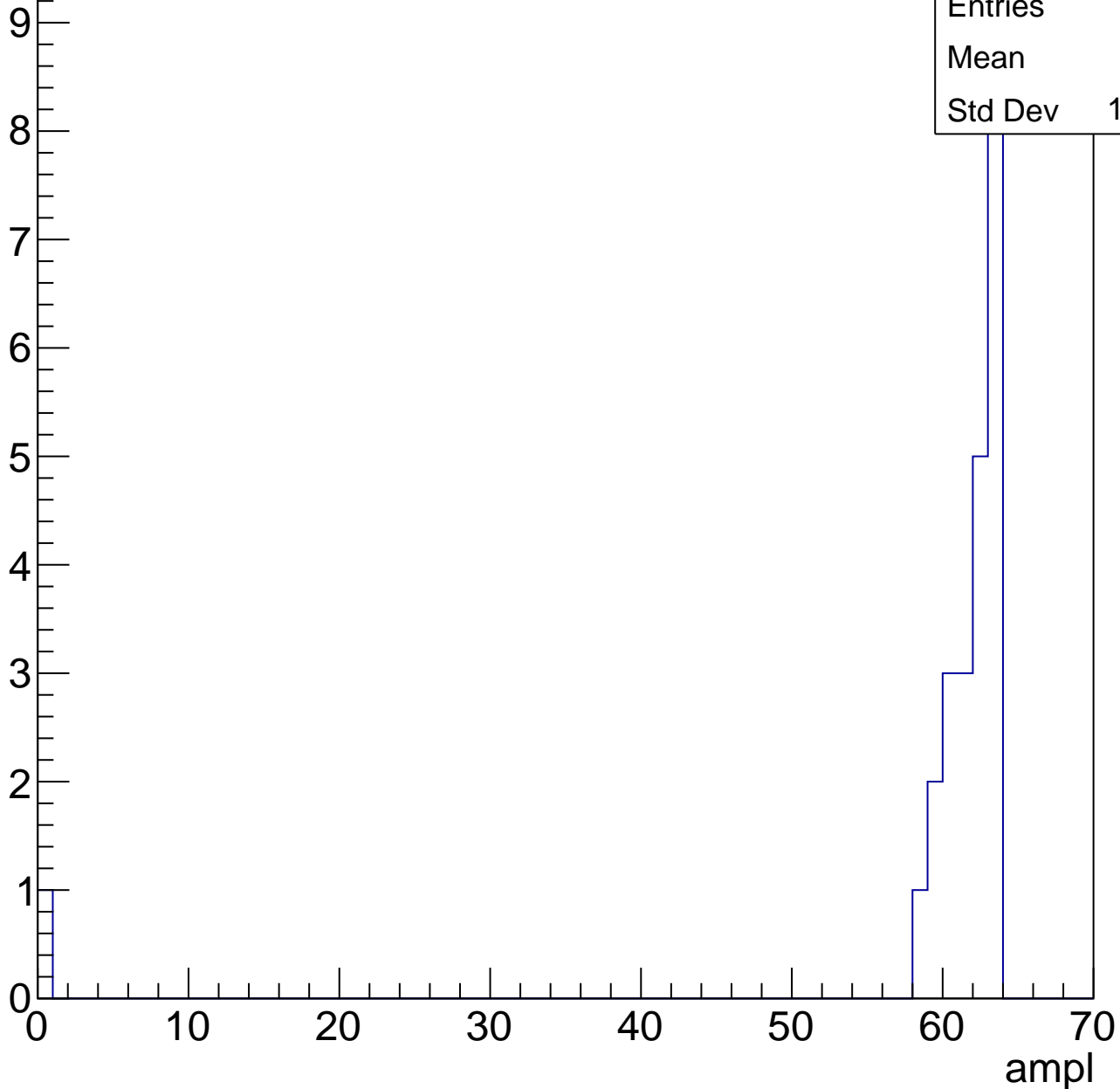


# B1L002S, U8-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	59
Std Dev	12.39

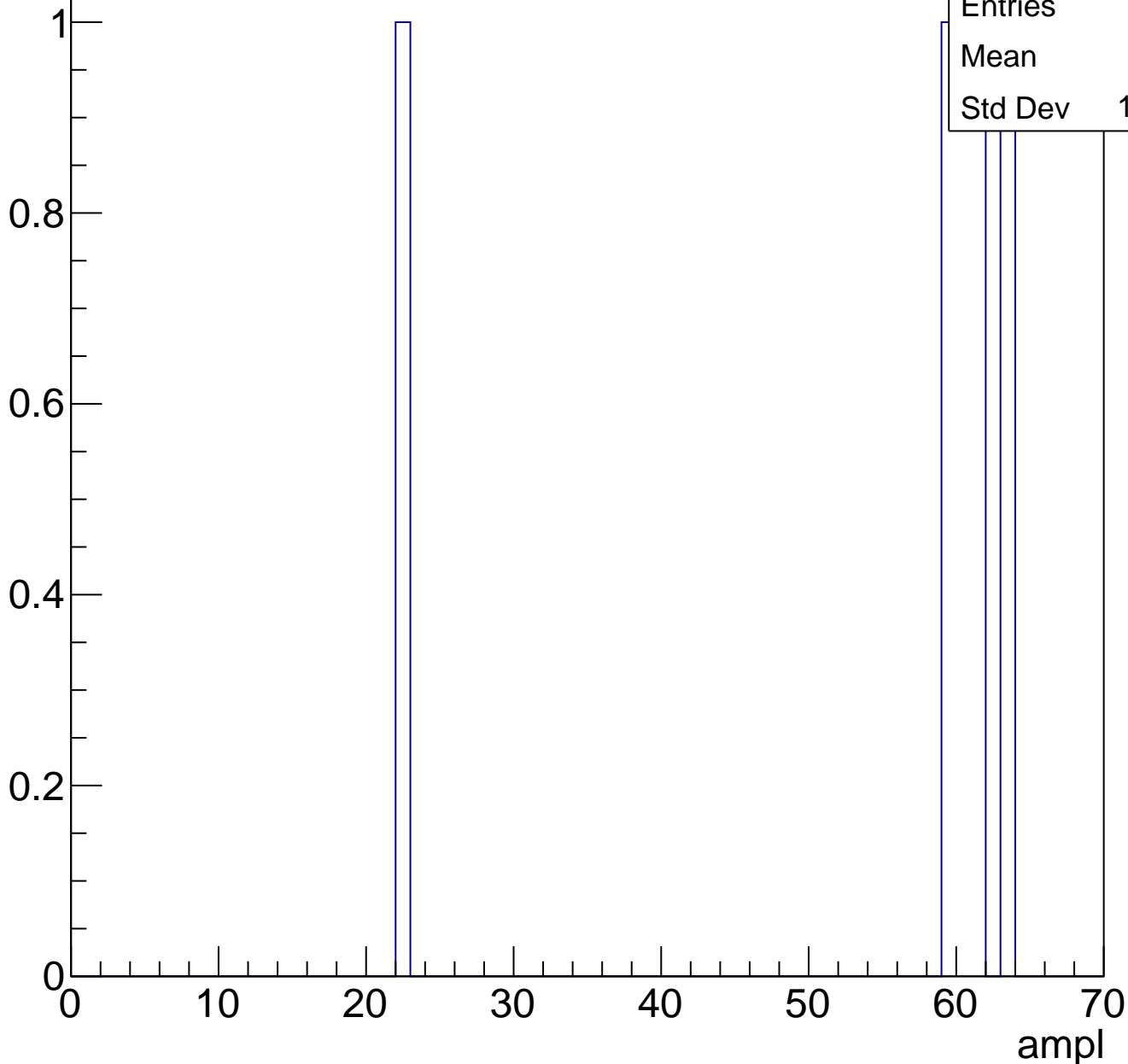




# B1L002S, U8-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch83, adc0

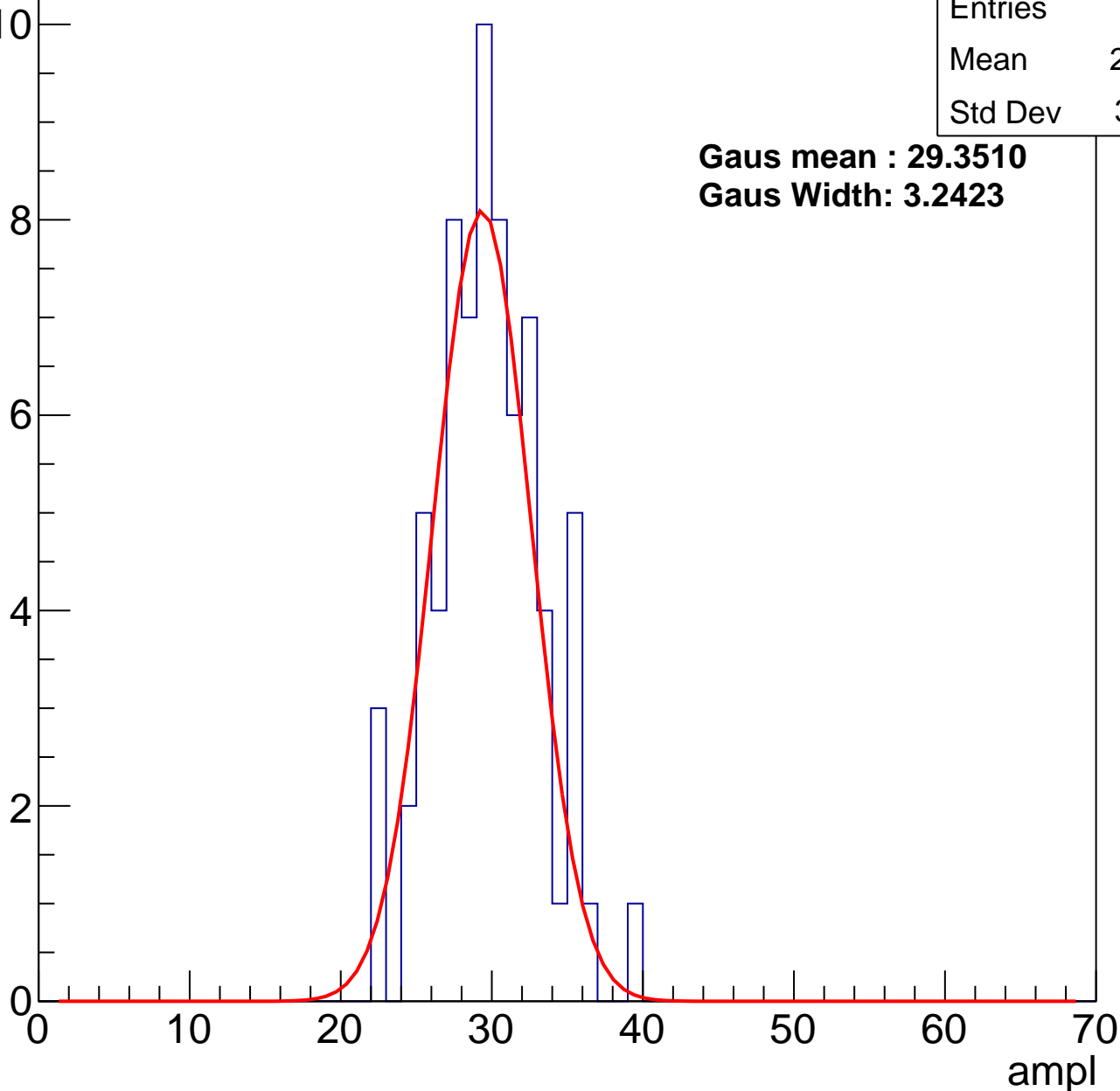
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	29.32
Std Dev	3.451

**Gaus mean : 29.3510**

**Gaus Width: 3.2423**



# B1L002S, U8-ch83, adc1

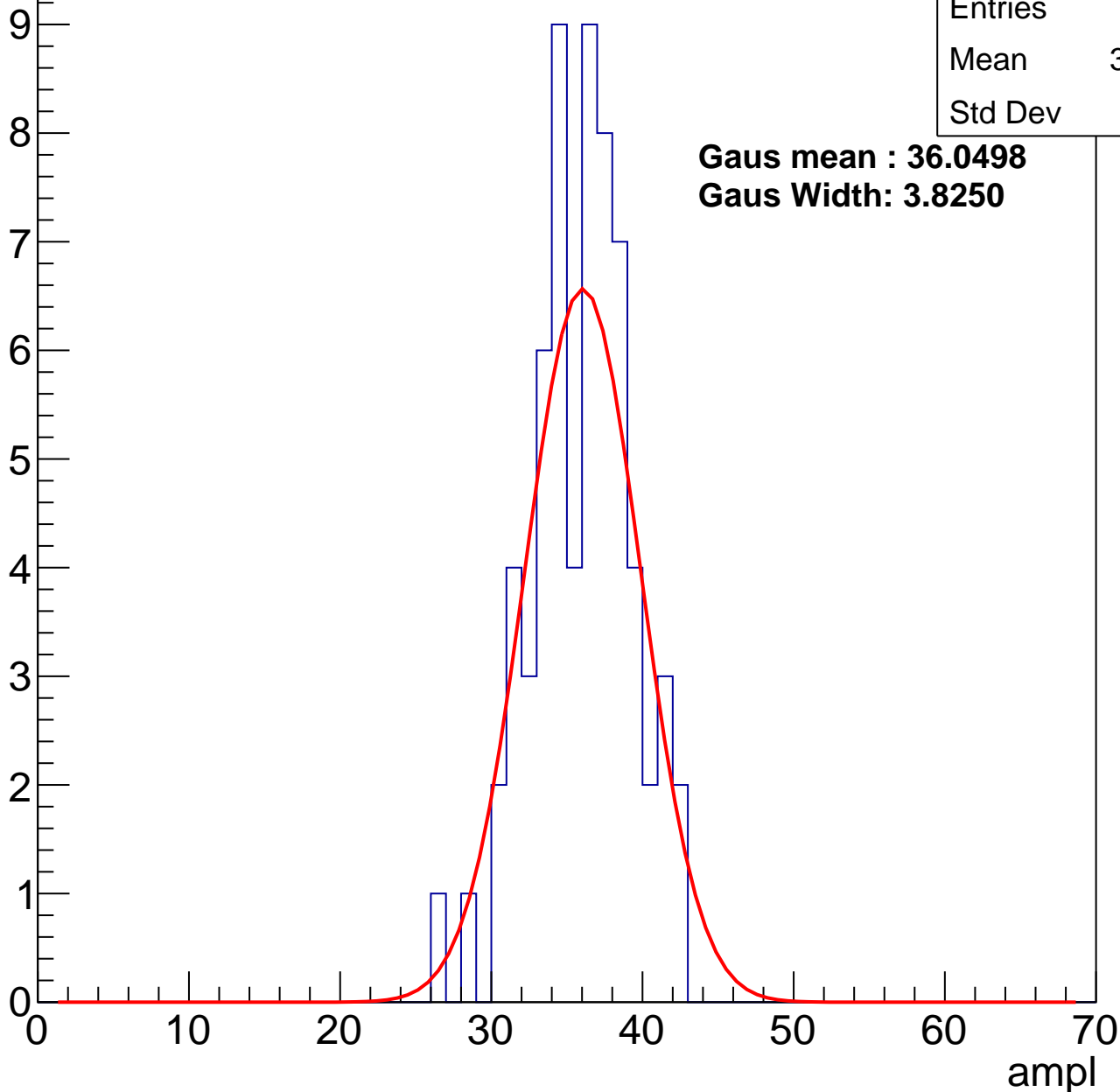
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35.49
Std Dev	3.31

**Gaus mean : 36.0498**

**Gaus Width: 3.8250**



# B1L002S, U8-ch83, adc2

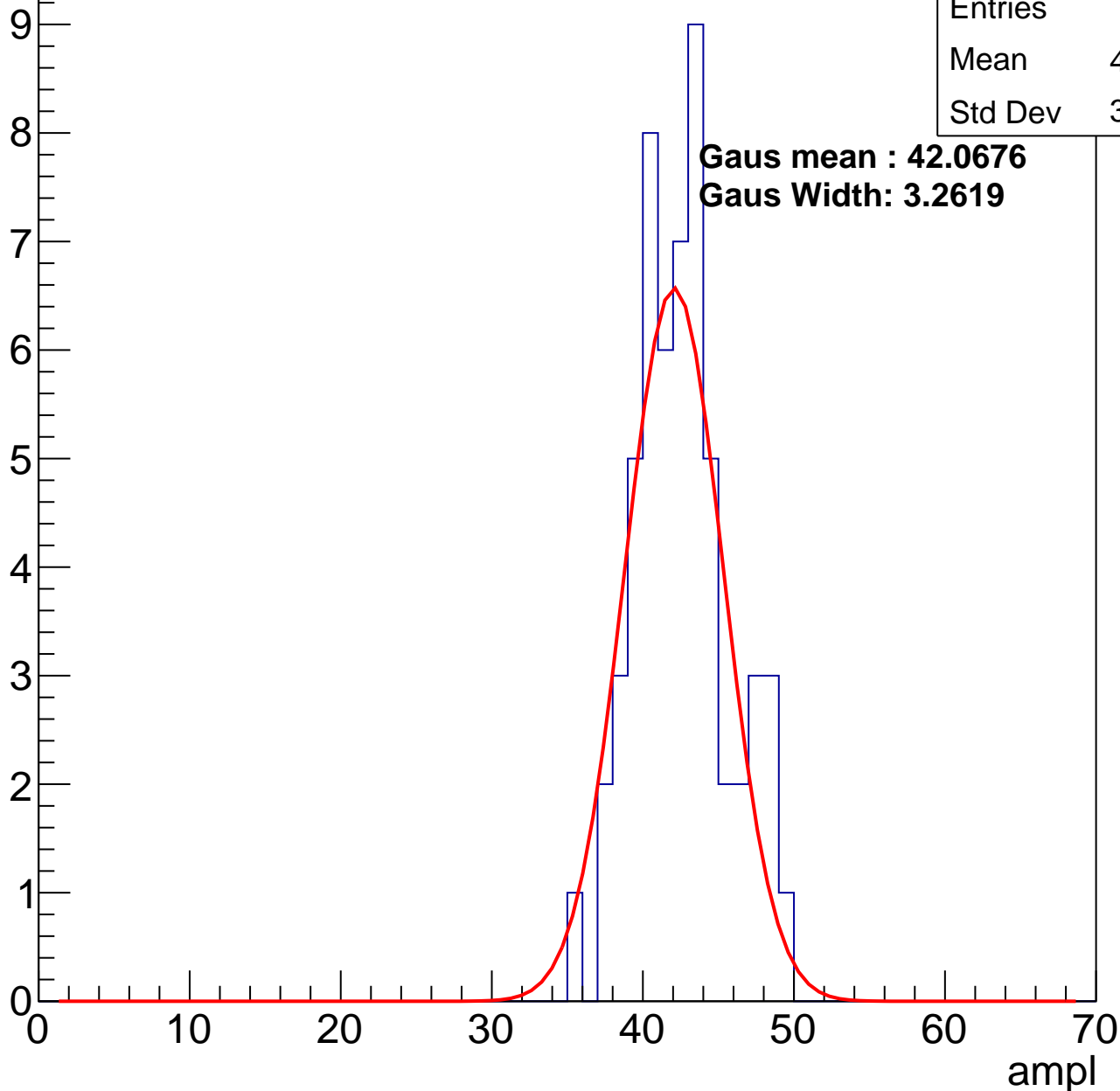
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	42.12
Std Dev	3.078

**Gaus mean : 42.0676**

**Gaus Width: 3.2619**

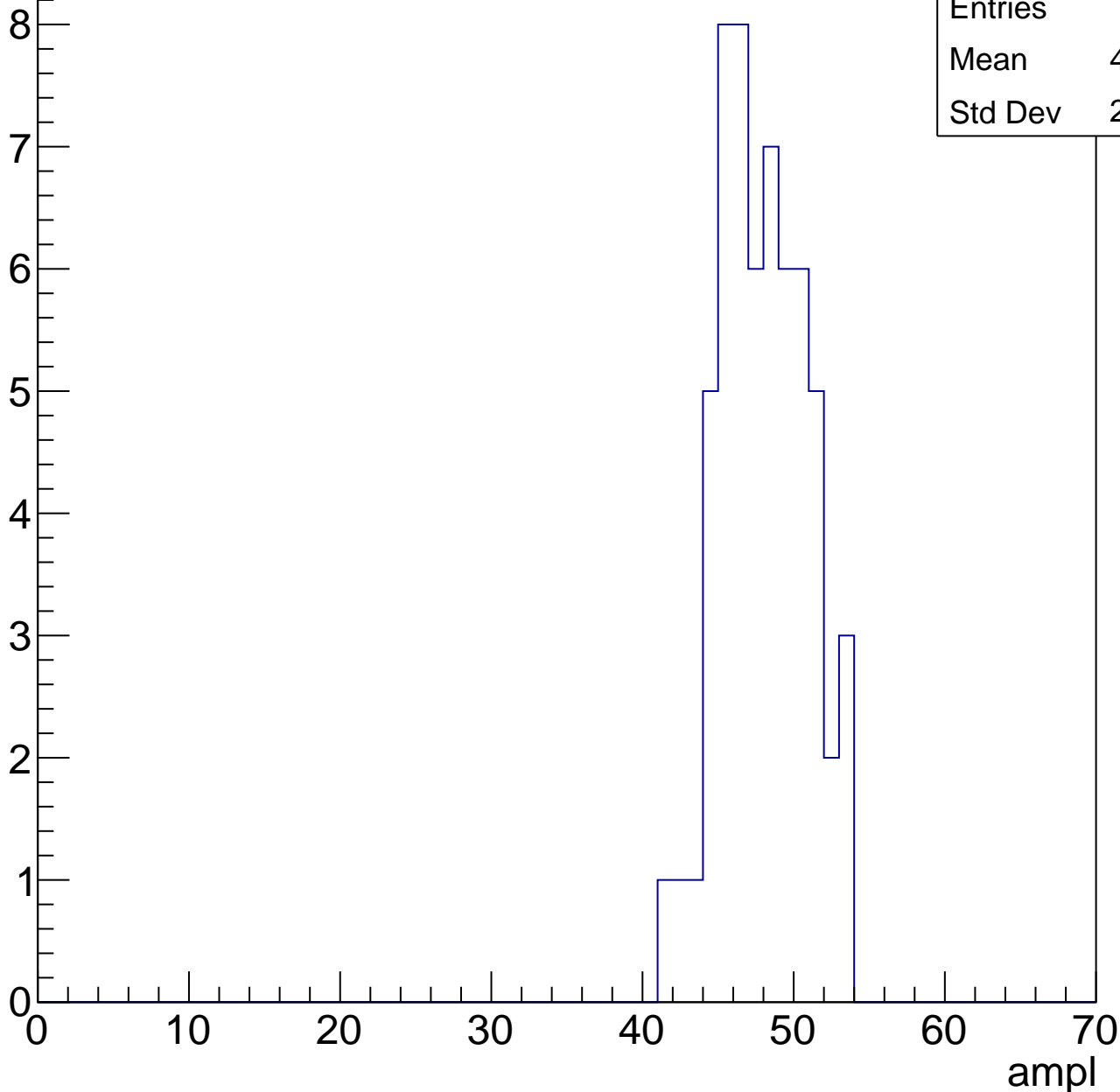


# B1L002S, U8-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

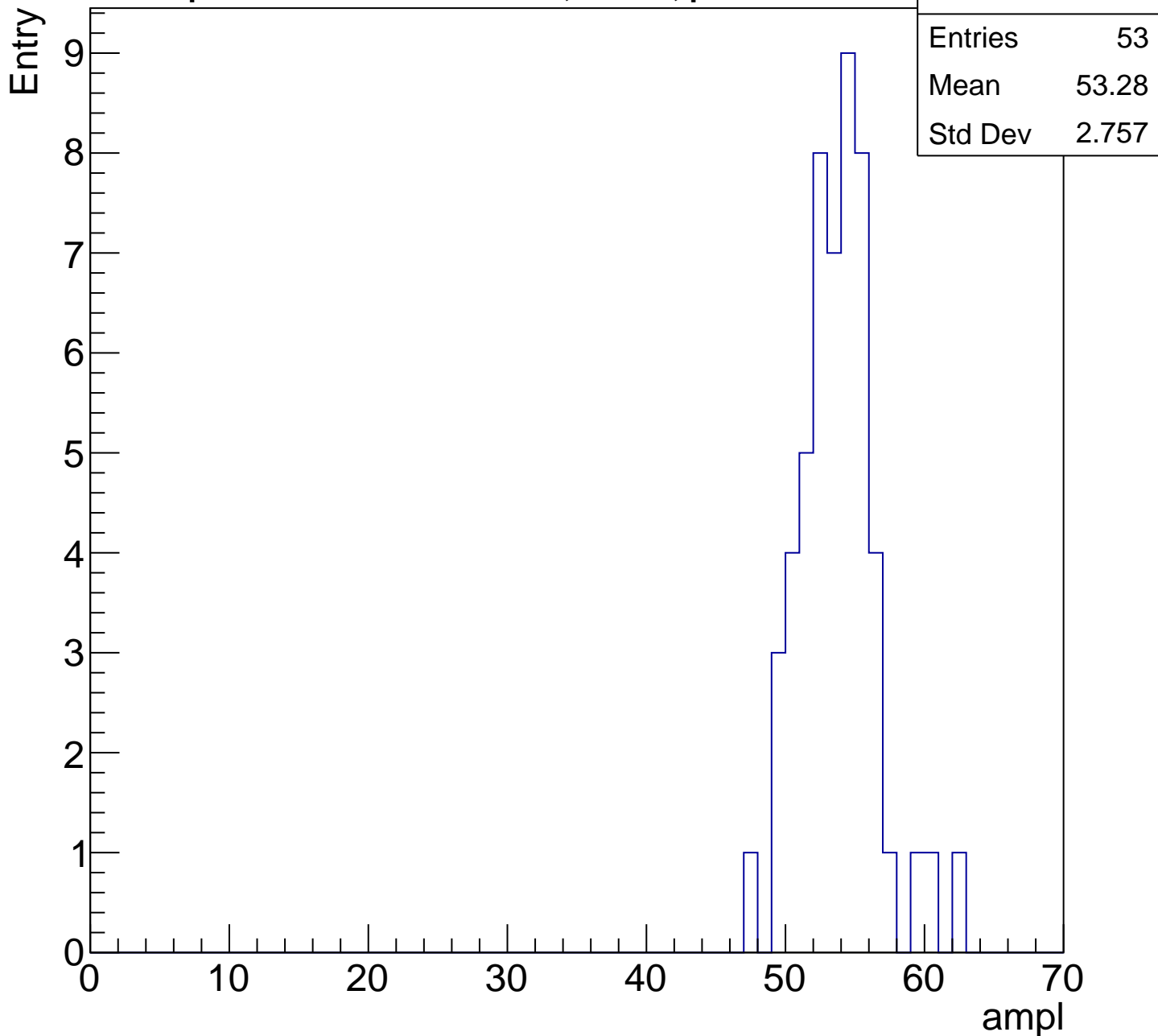
Entry

Entries	59
Mean	47.53
Std Dev	2.819



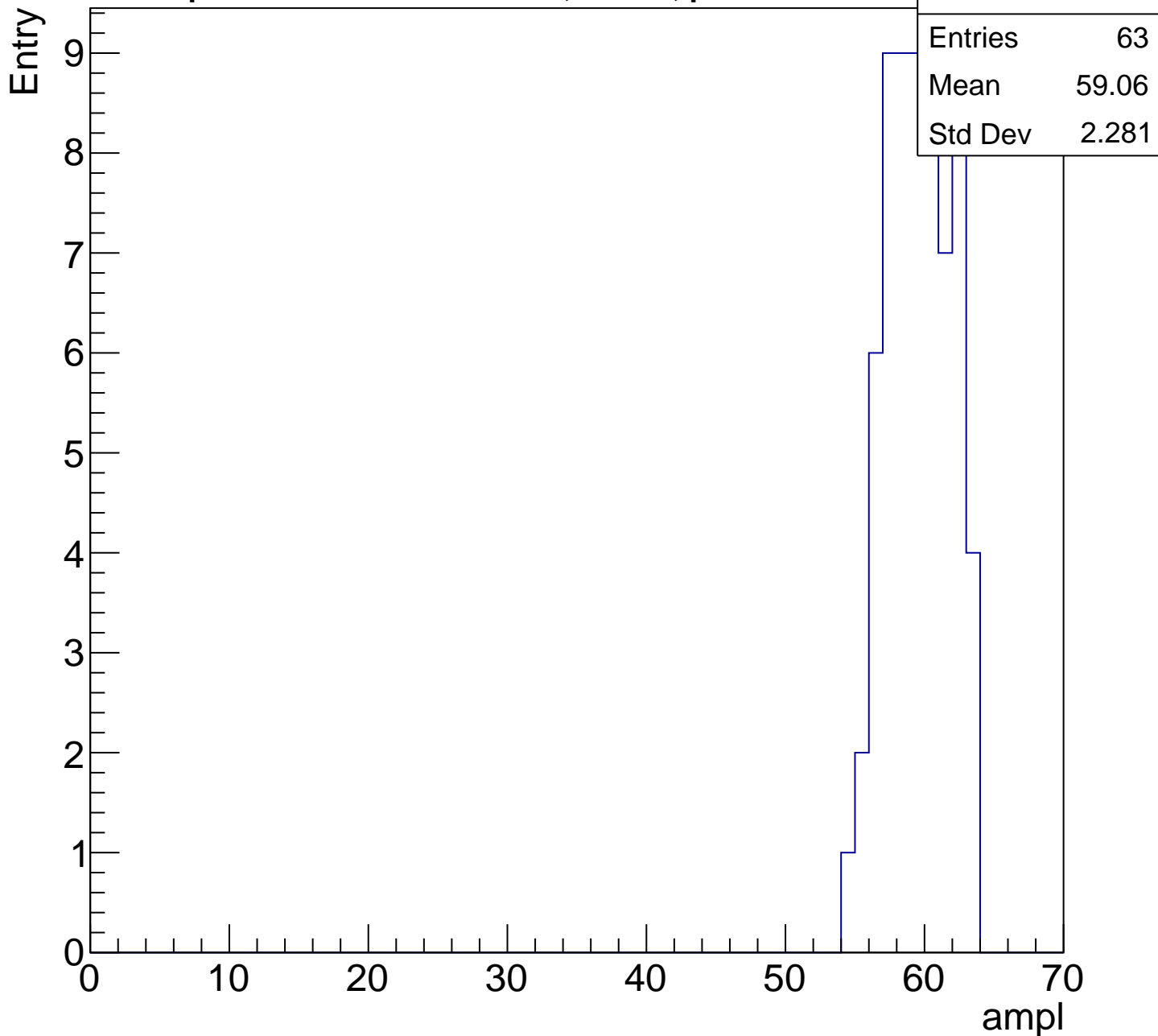
# B1L002S, U8-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

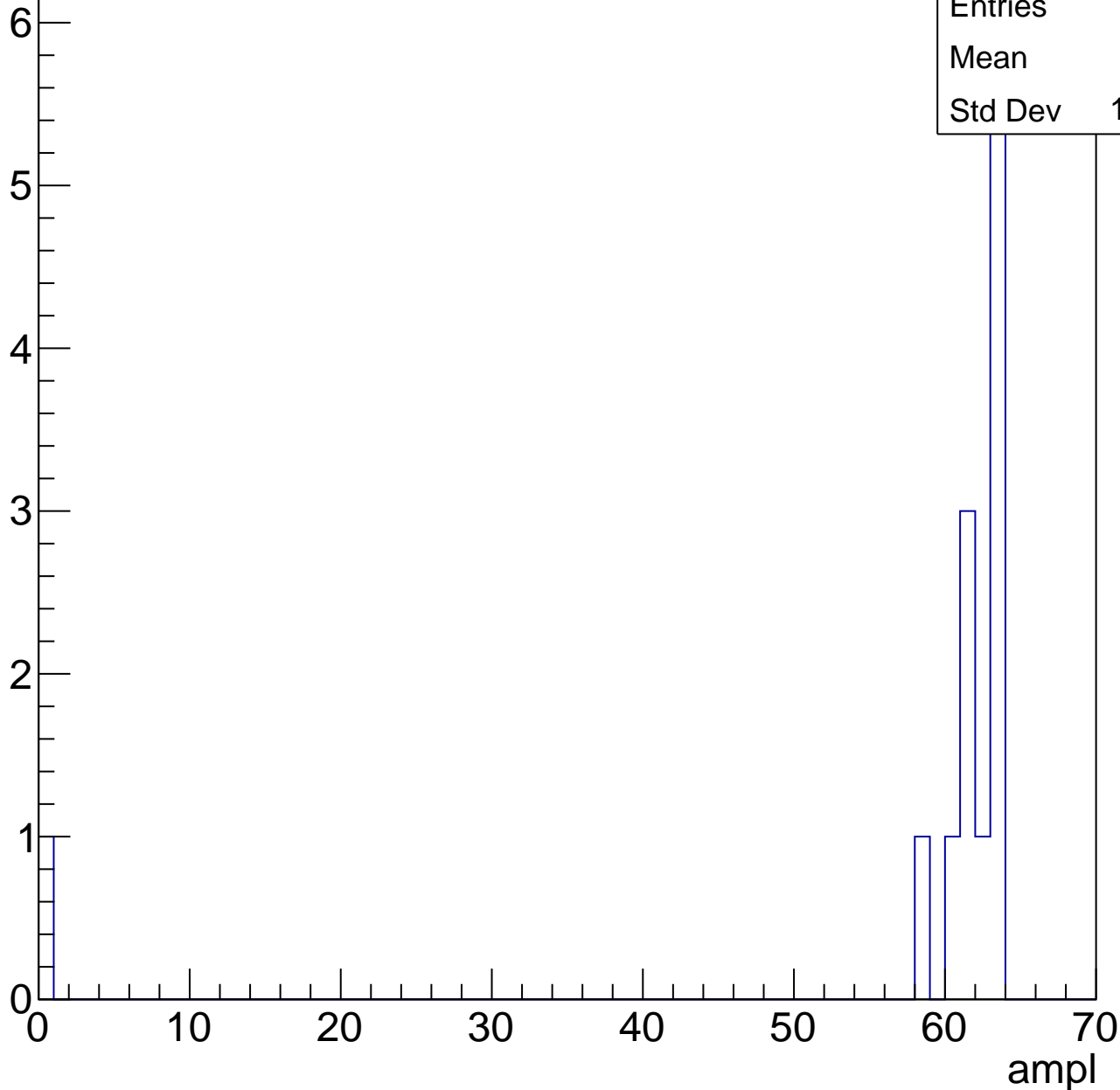


# B1L002S, U8-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	57
Std Dev	16.52





# B1L002S, U8-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L002S, U8-ch84, adc0

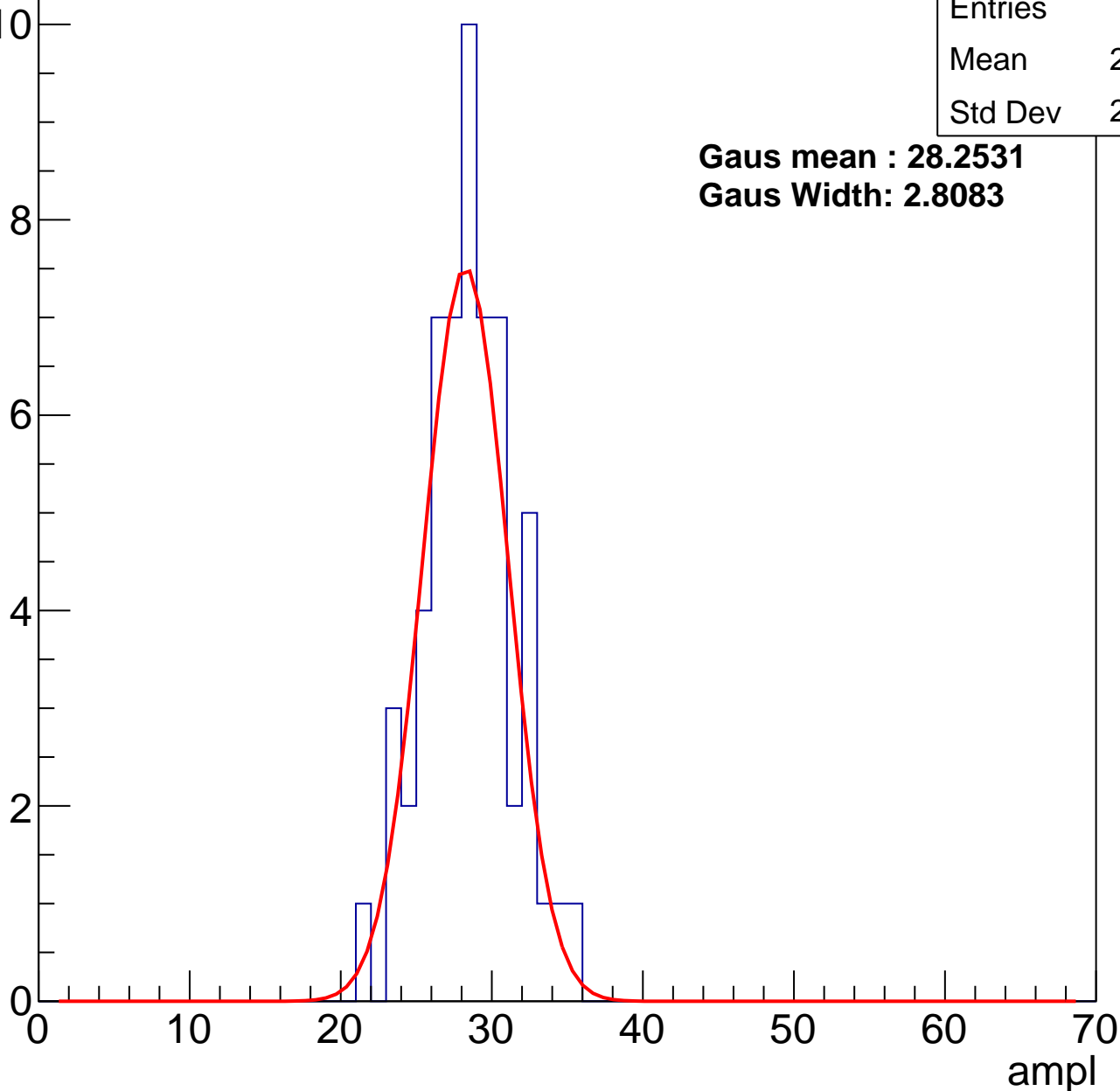
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	28.03
Std Dev	2.846

**Gaus mean : 28.2531**

**Gaus Width: 2.8083**



# B1L002S, U8-ch84, adc1

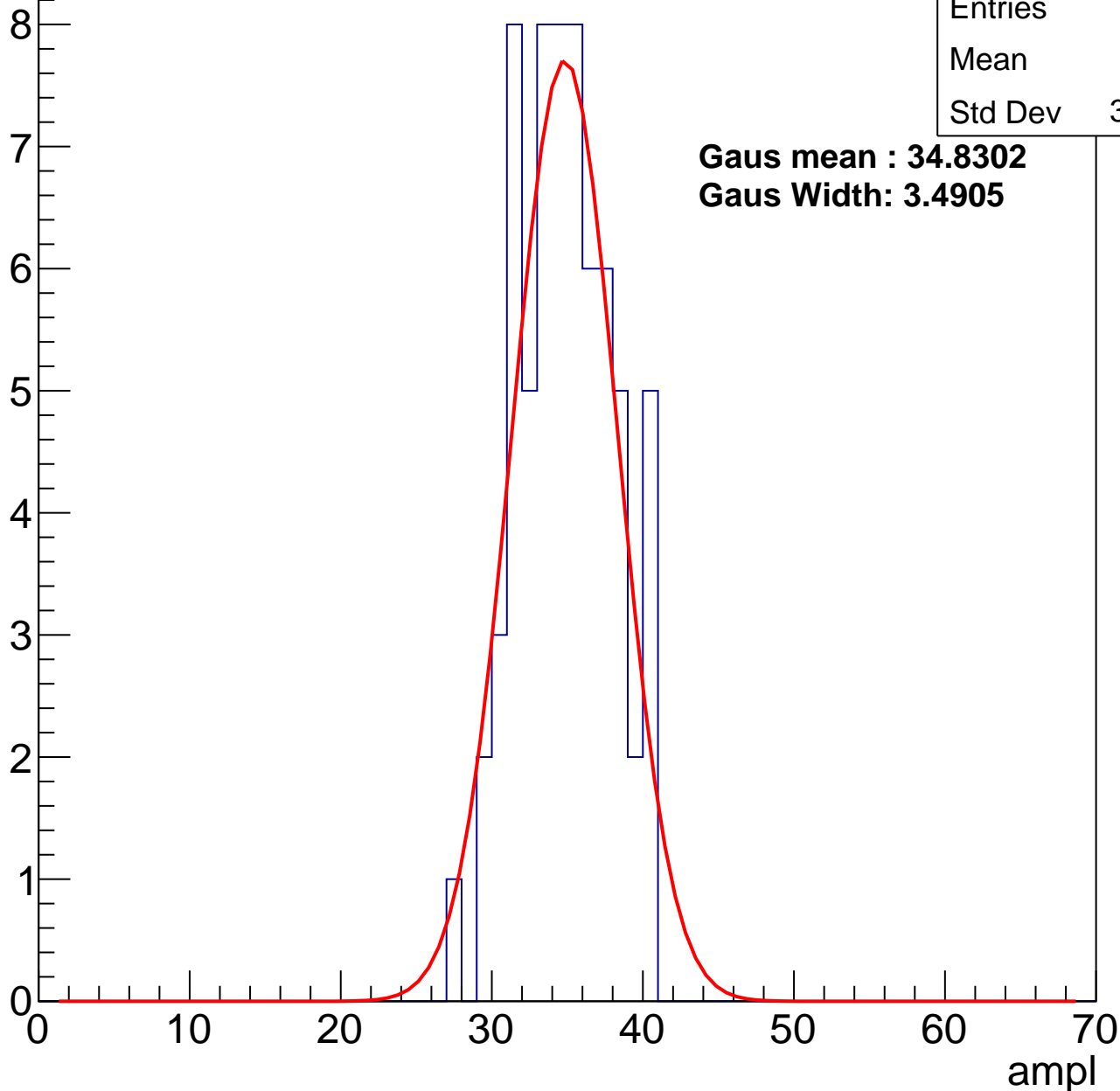
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.4
Std Dev	3.072

**Gaus mean : 34.8302**

**Gaus Width: 3.4905**



# B1L002S, U8-ch84, adc2

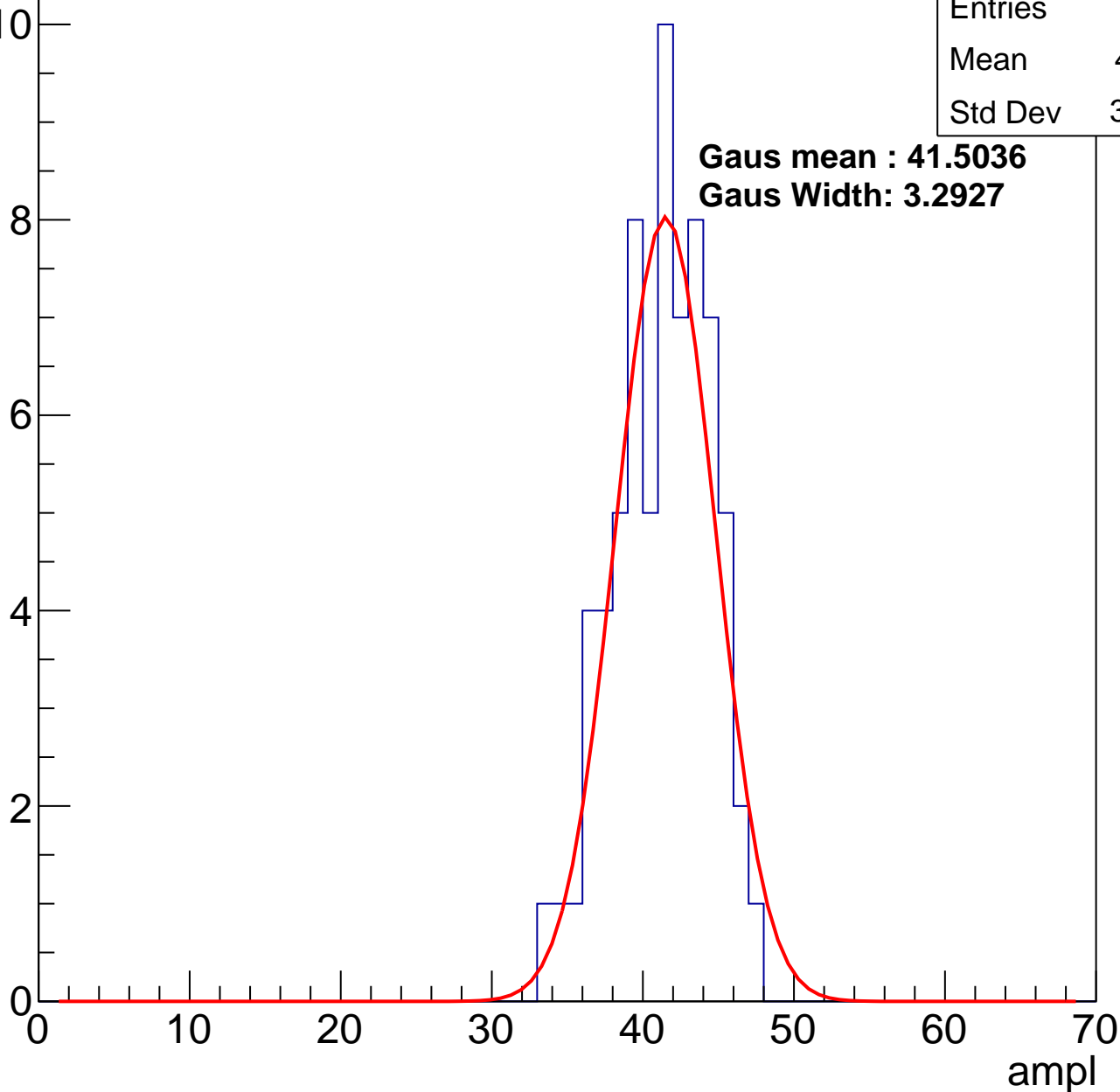
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.81
Std Dev	3.094

**Gaus mean : 41.5036**

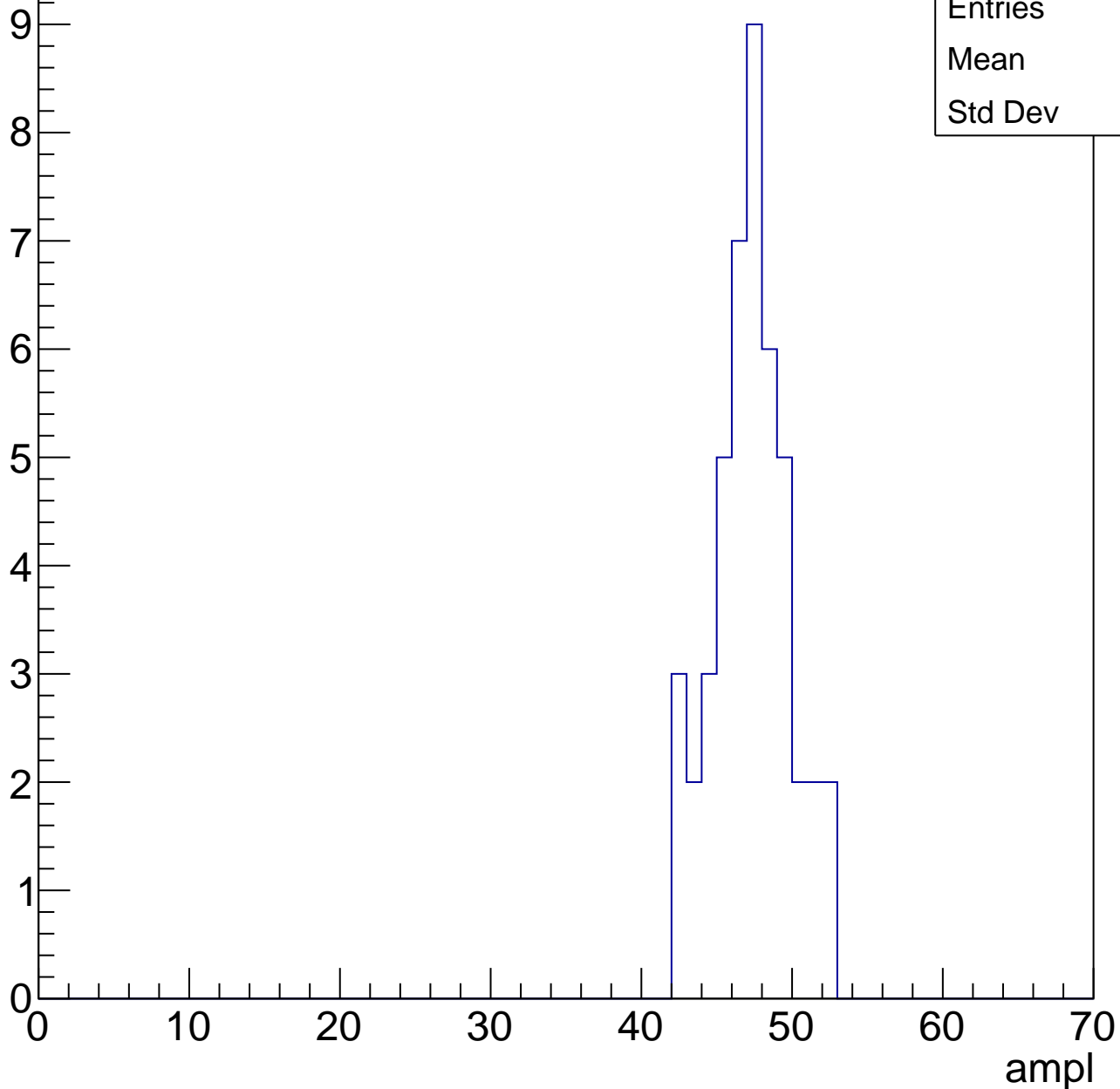
**Gaus Width: 3.2927**



# B1L002S, U8-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

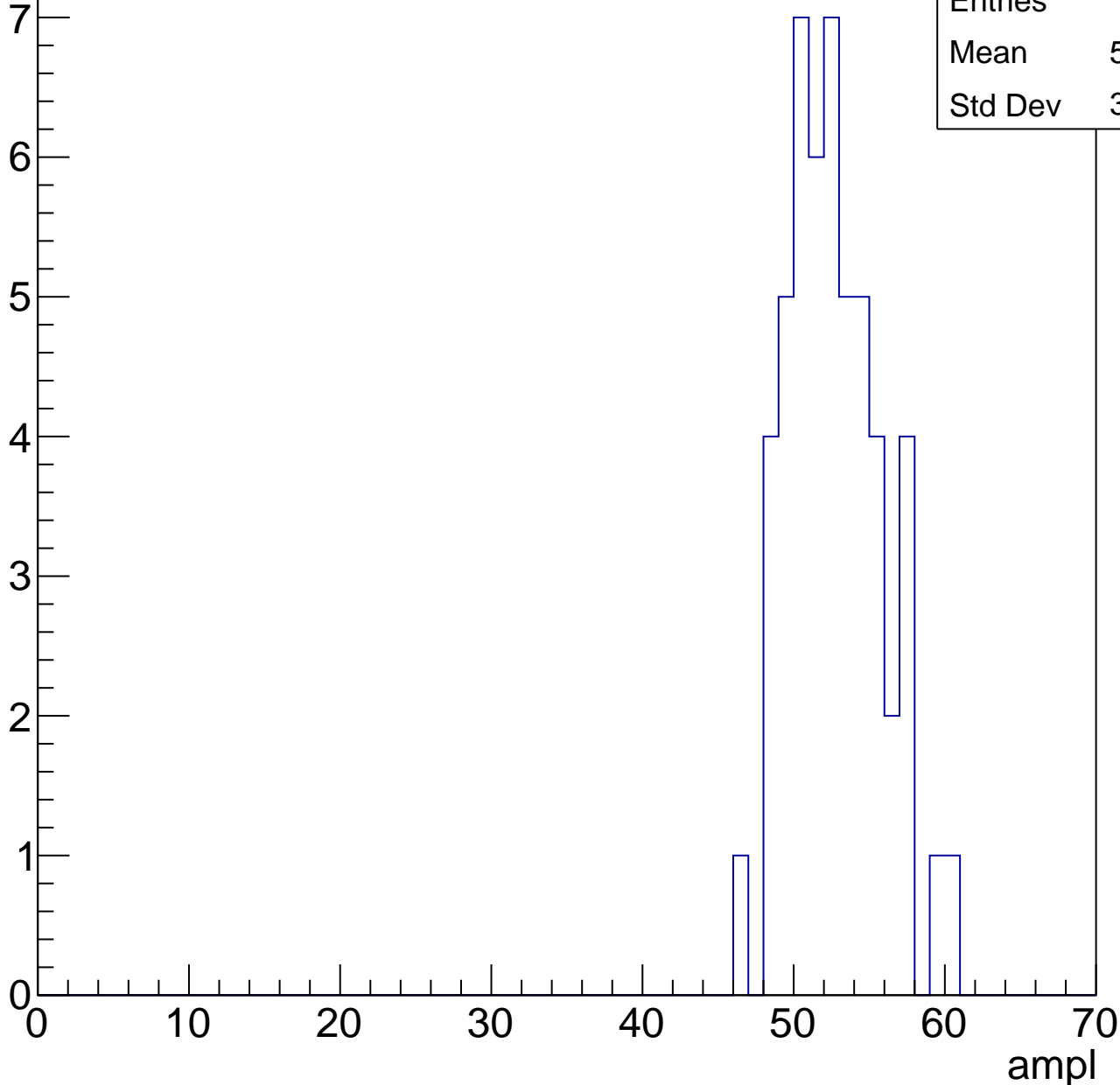


# B1L002S, U8-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

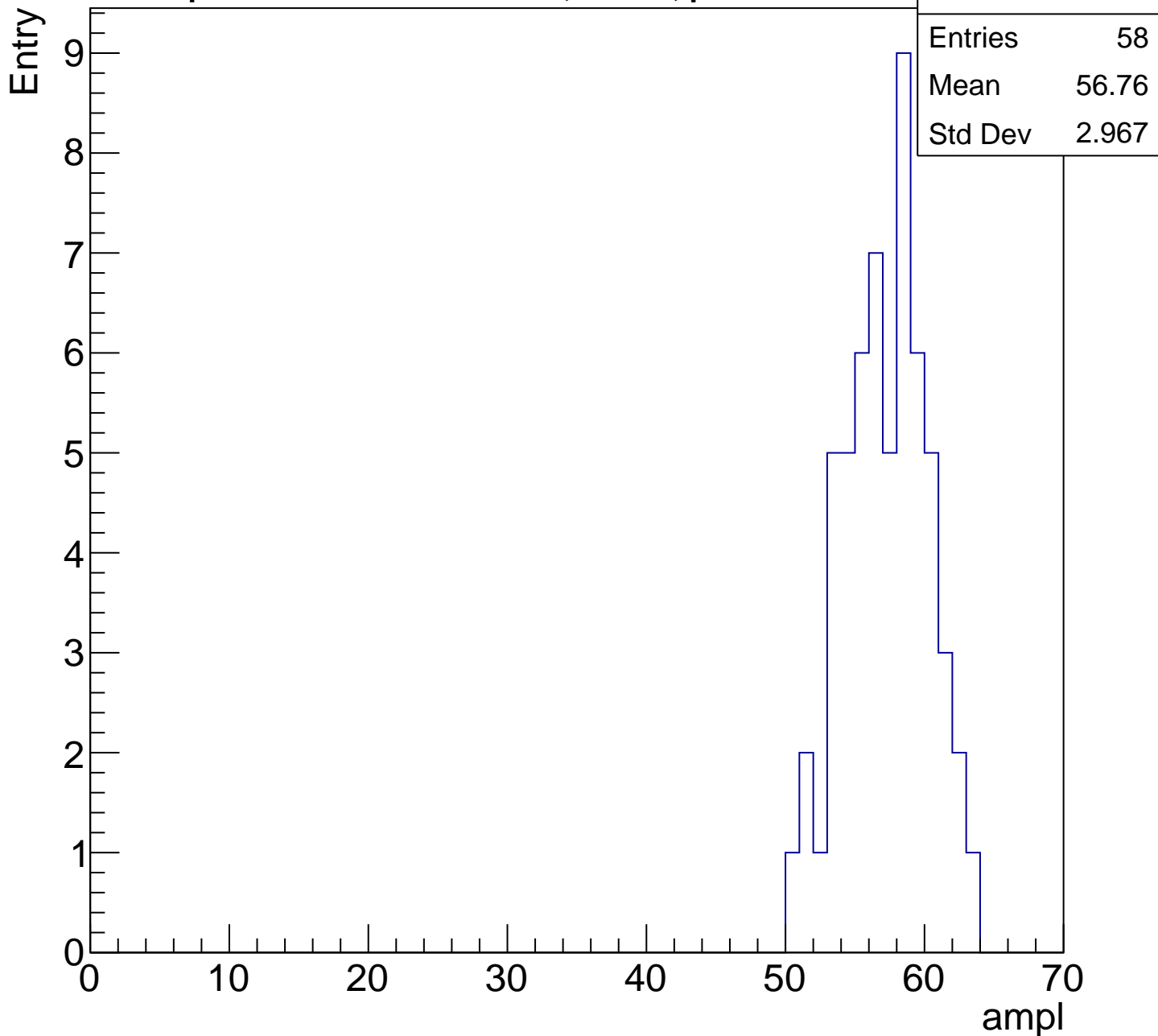
Entry

Entries	52
Mean	52.25
Std Dev	3.044



# B1L002S, U8-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

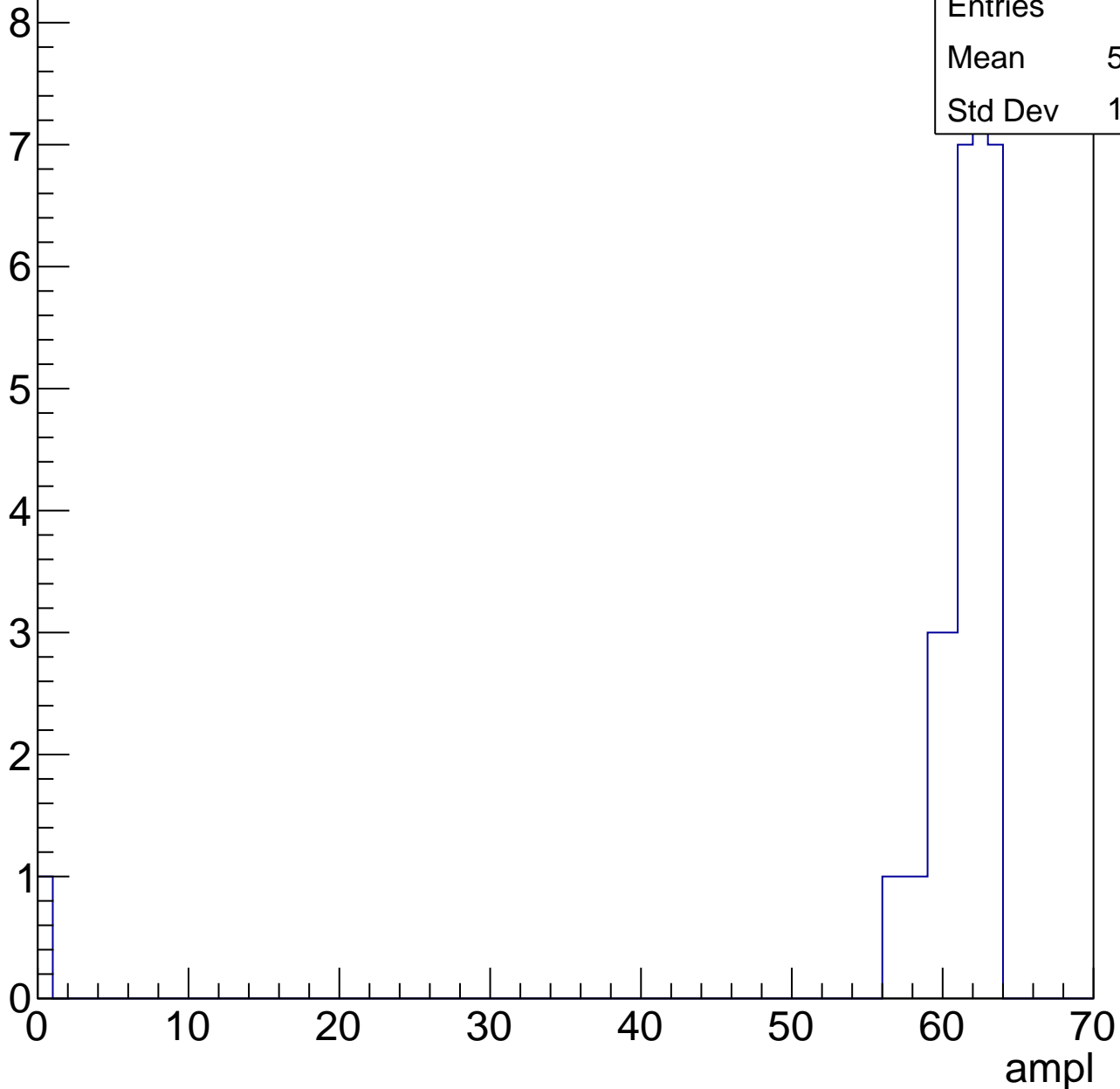


# B1L002S, U8-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.12
Std Dev	10.77

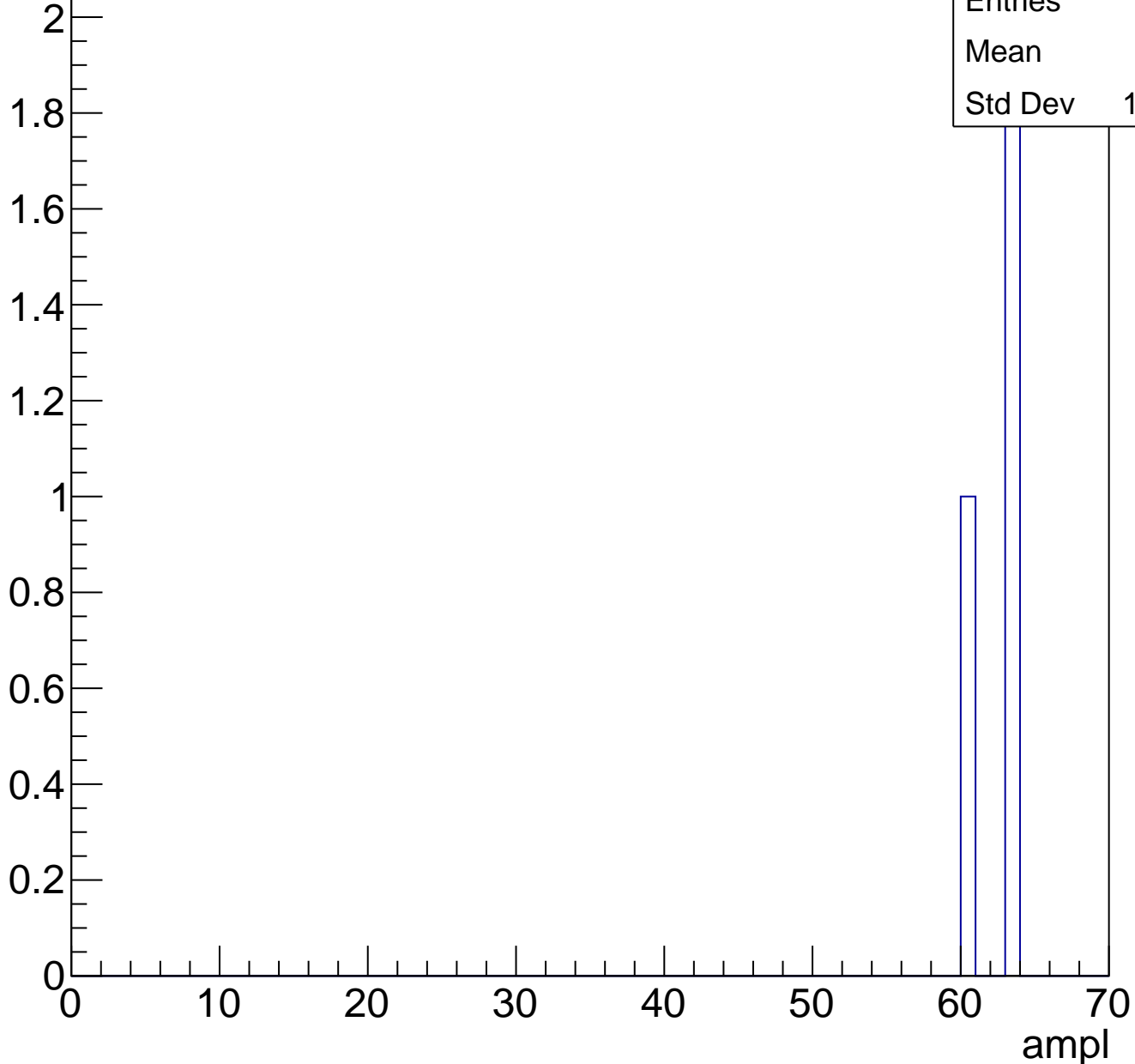




# B1L002S, U8-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch85, adc0

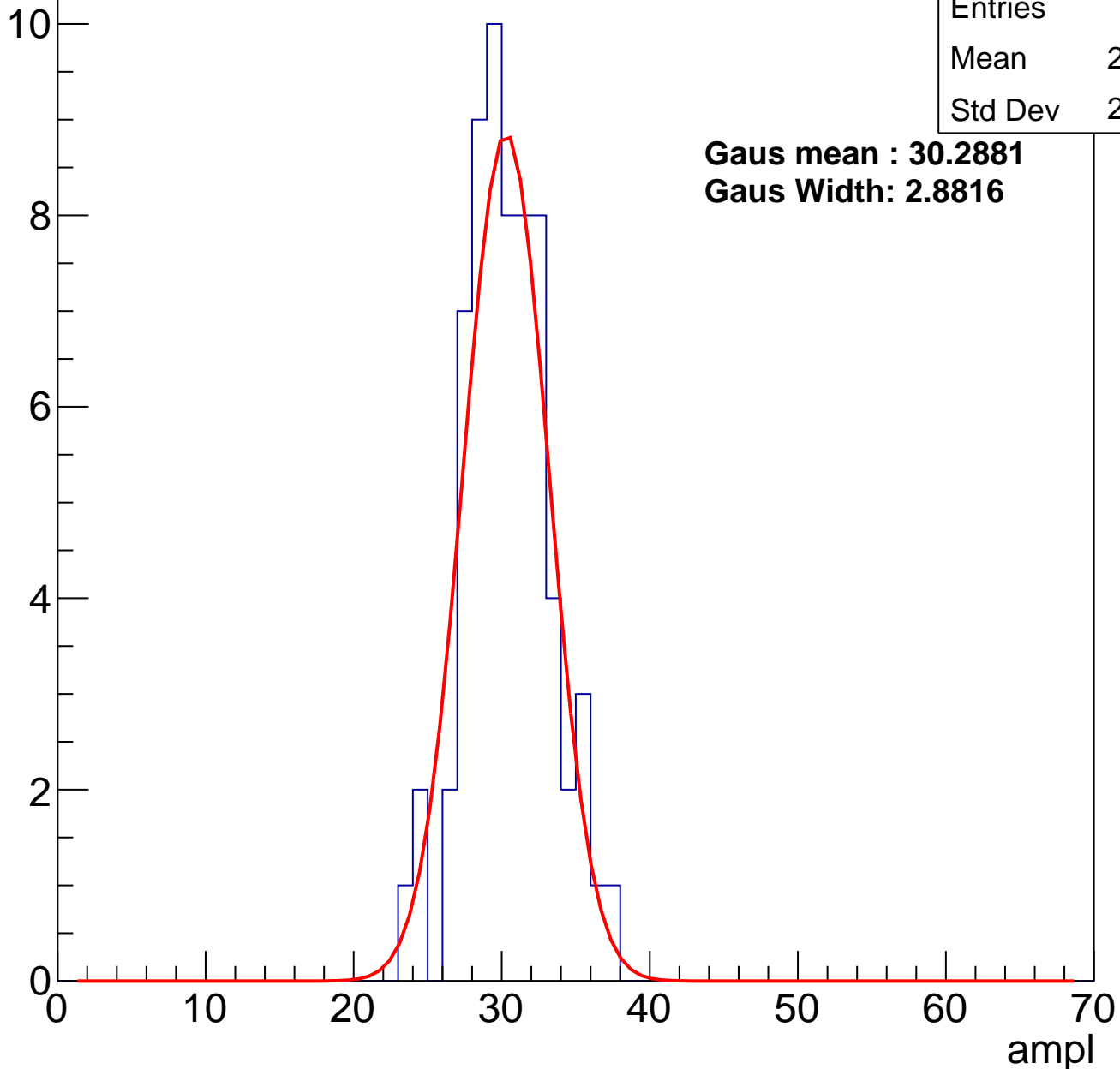
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	29.94
Std Dev	2.833

**Gaus mean : 30.2881**

**Gaus Width: 2.8816**

Entry



# B1L002S, U8-ch85, adc1

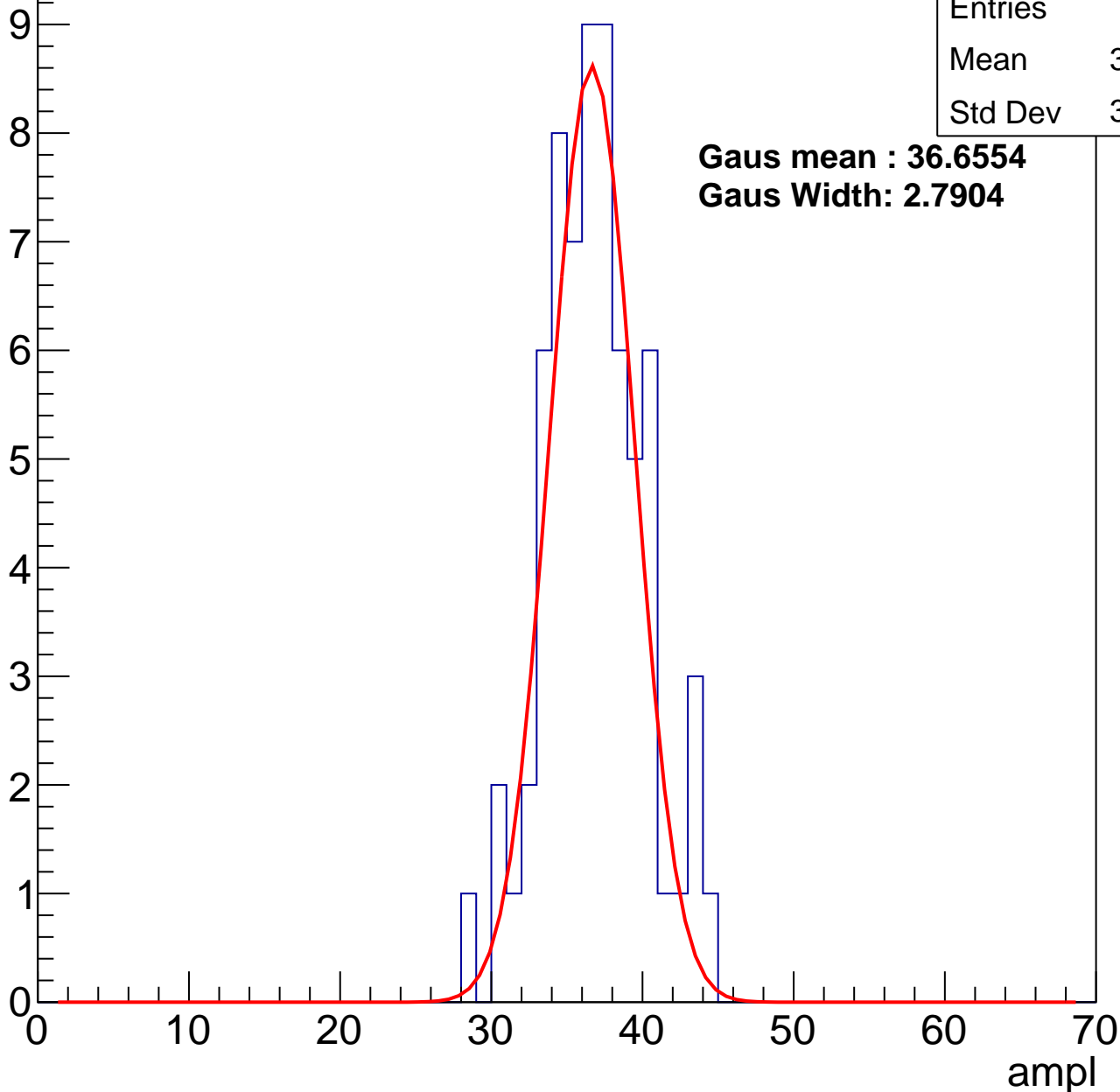
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	36.38
Std Dev	3.259

**Gaus mean : 36.6554**

**Gaus Width: 2.7904**



# B1L002S, U8-ch85, adc2

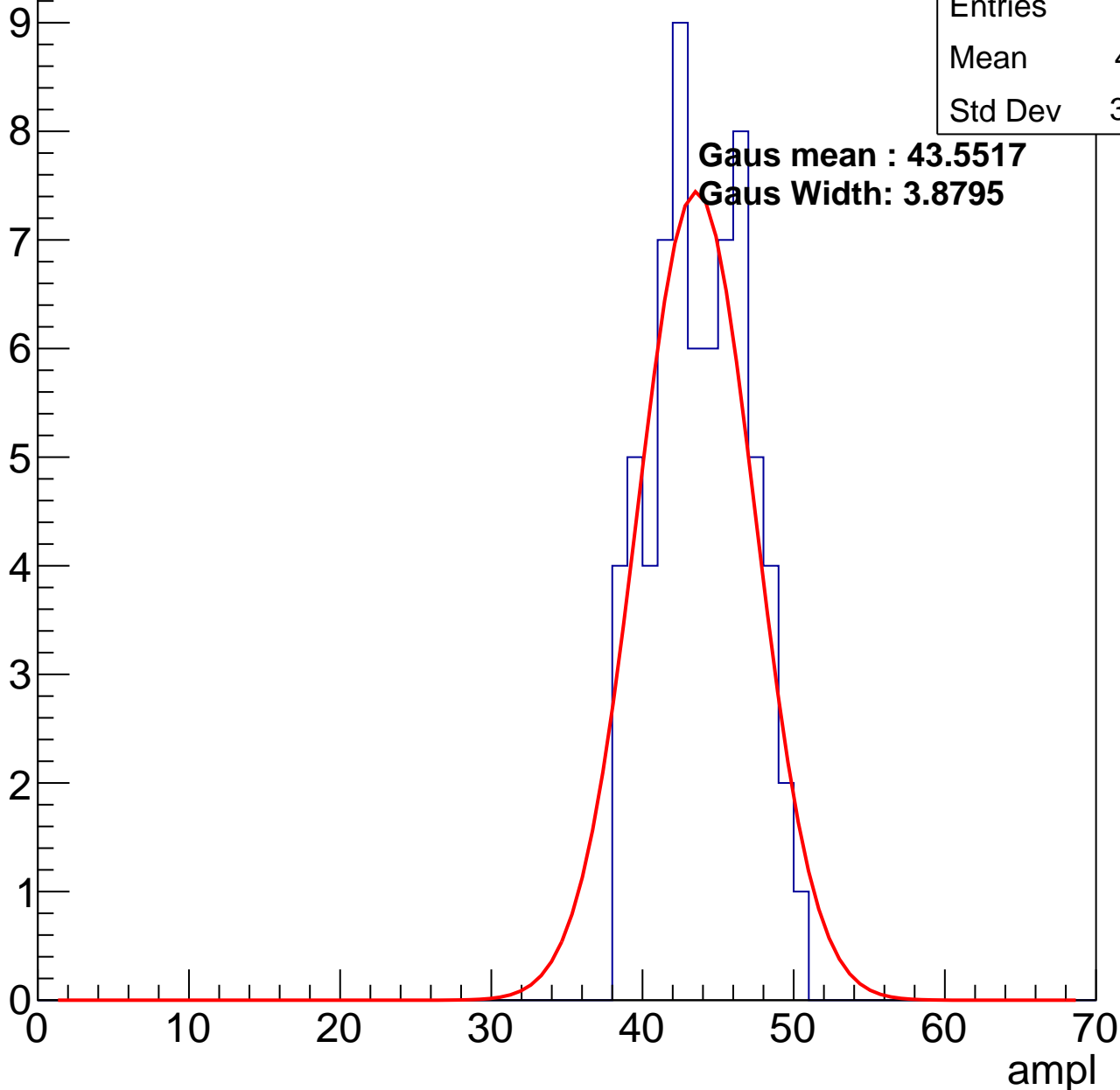
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	43.41
Std Dev	3.088

**Gaus mean : 43.5517**

**Gaus Width: 3.8795**

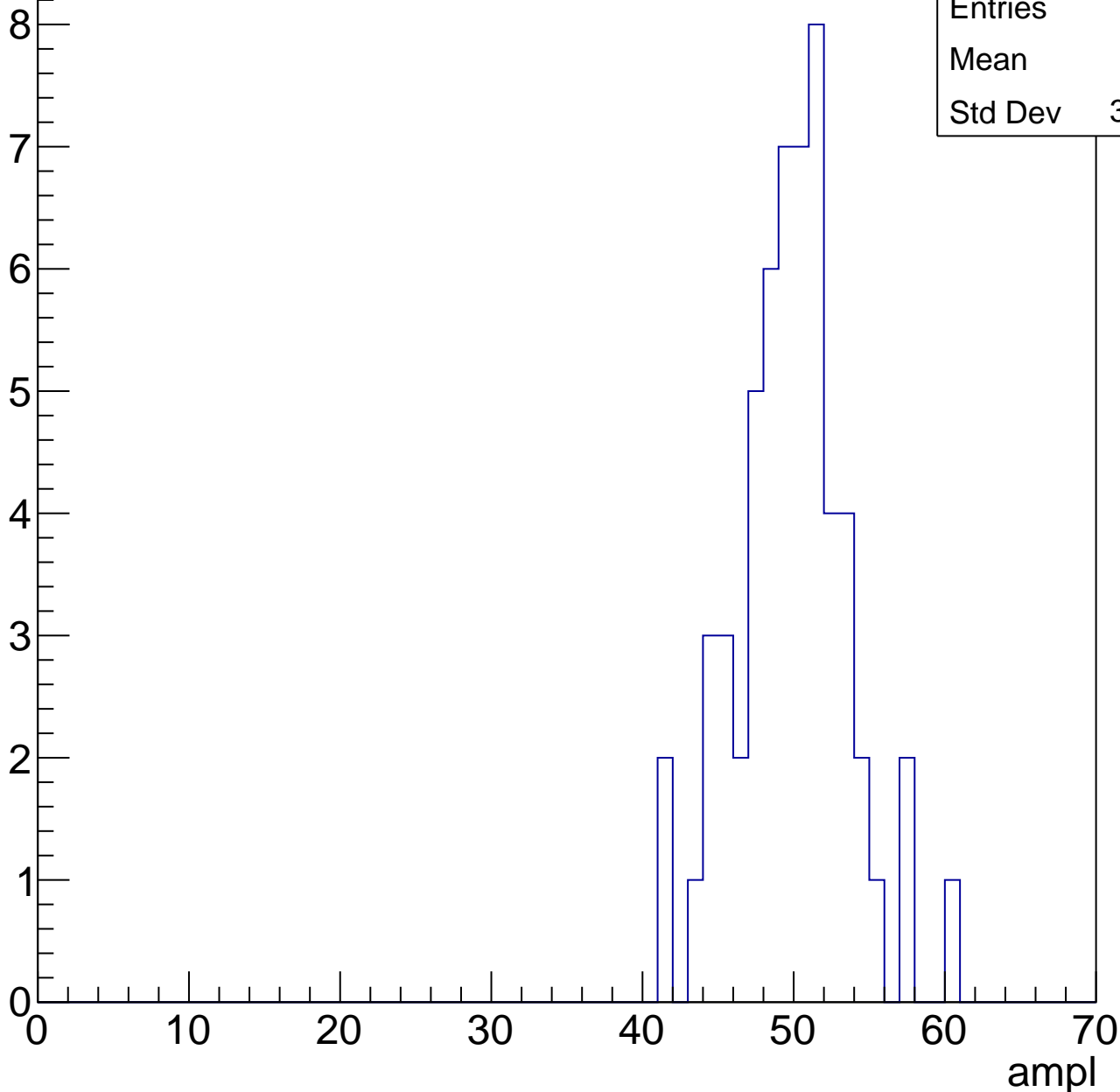


# B1L002S, U8-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

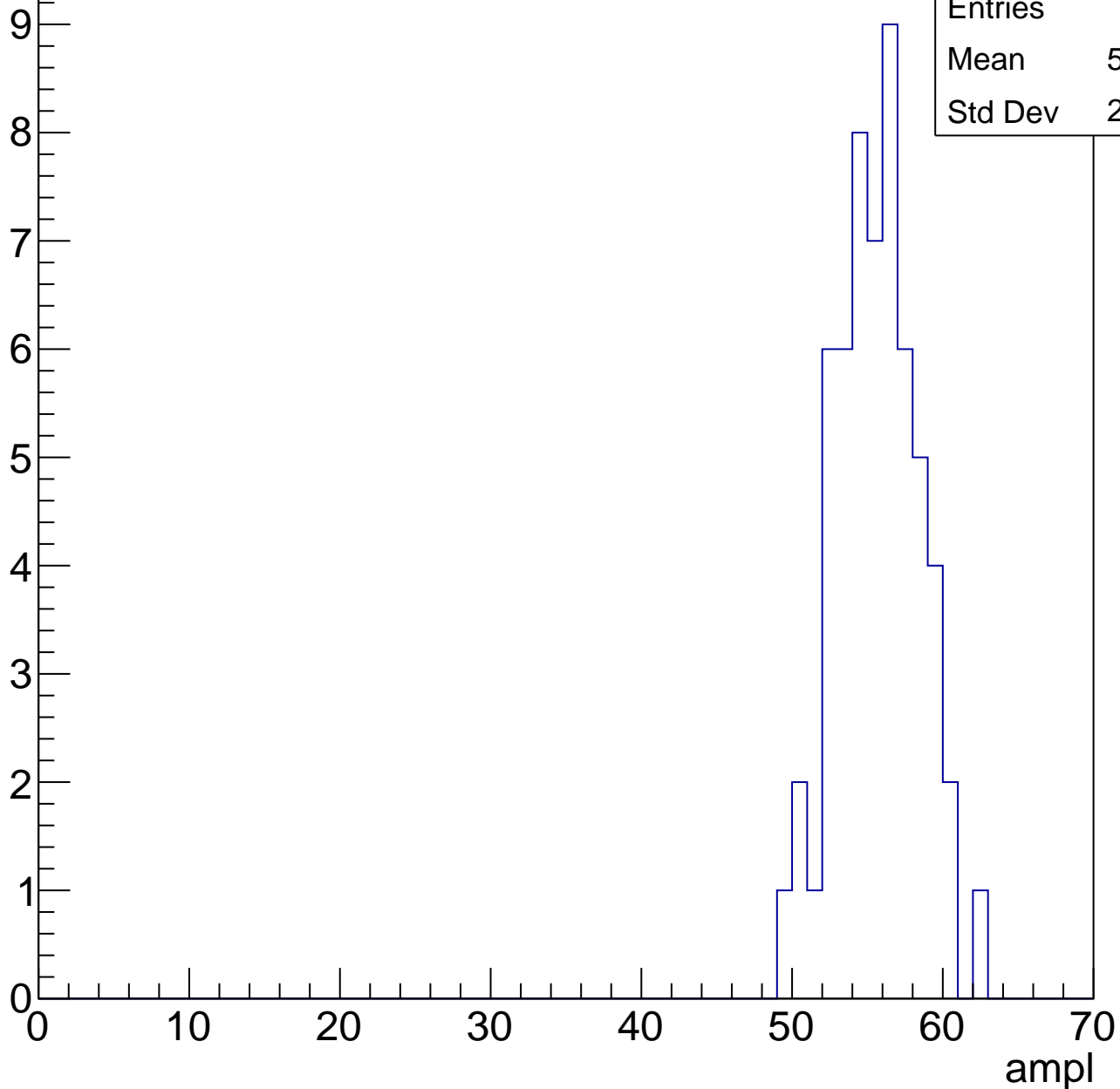
Entries	58
Mean	49.4
Std Dev	3.704



# B1L002S, U8-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



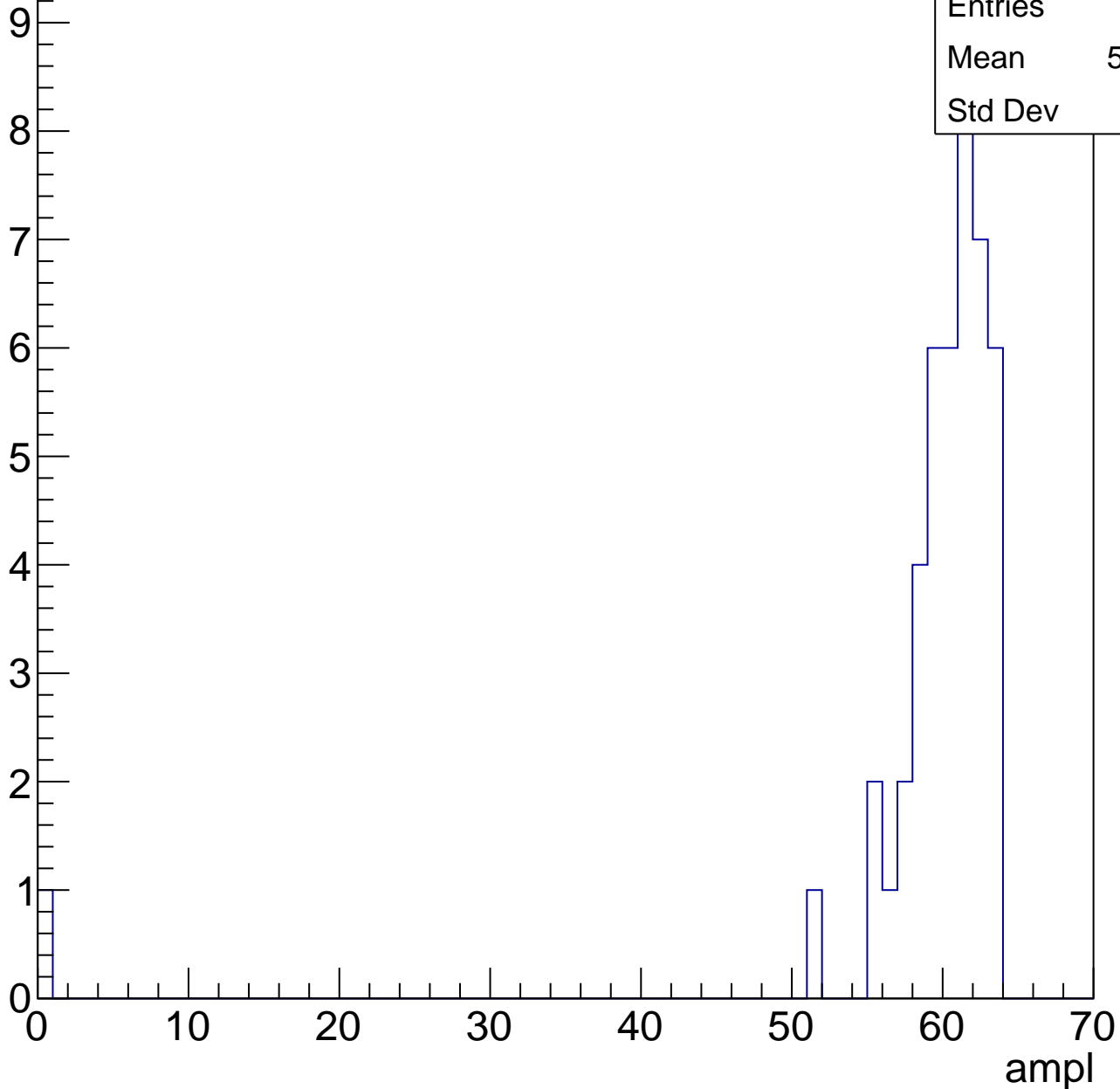
Entries	58
Mean	55.19
Std Dev	2.713

# B1L002S, U8-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

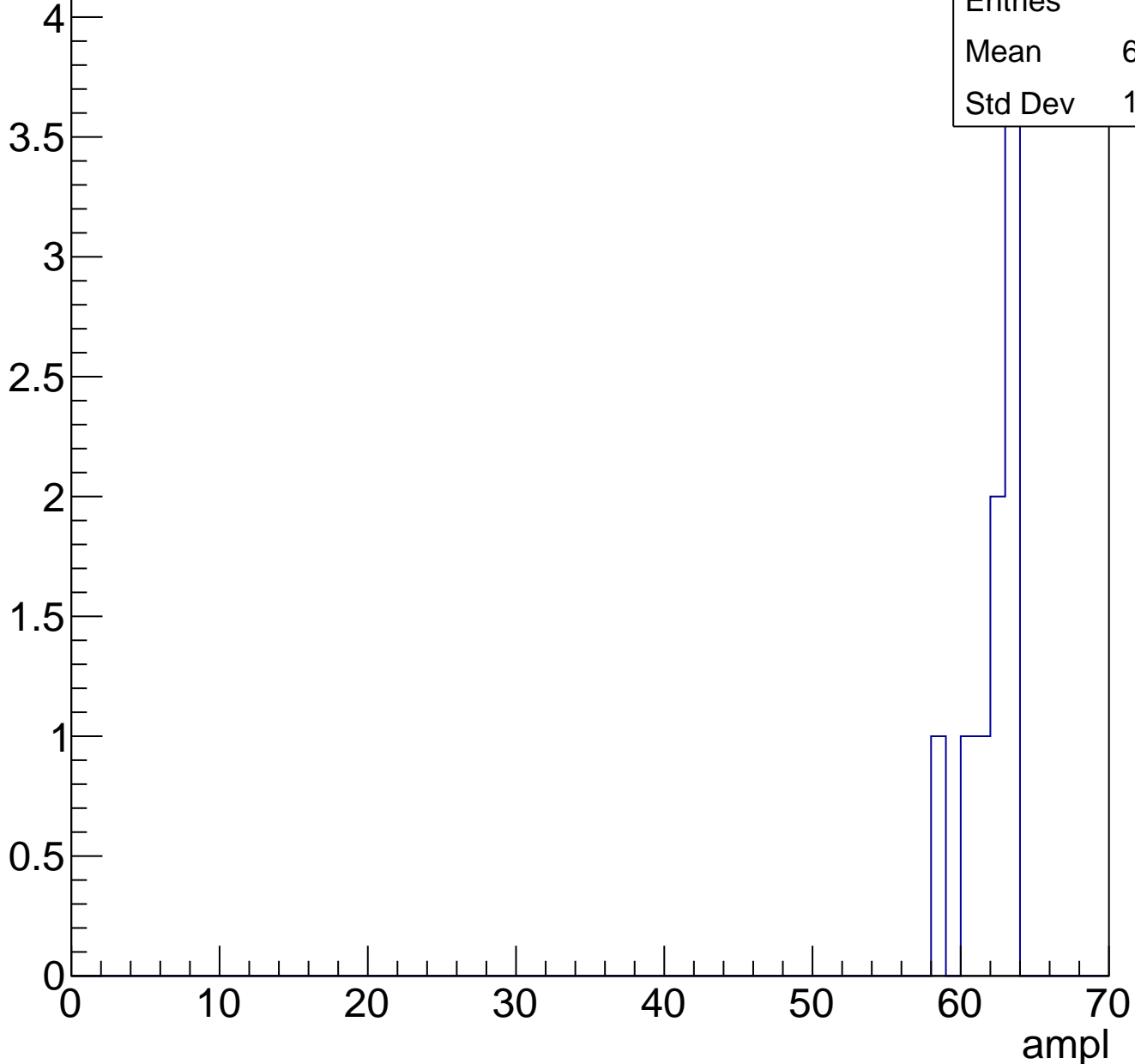
Entries	45
Mean	58.62
Std Dev	9.18



# B1L002S, U8-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch86, adc0

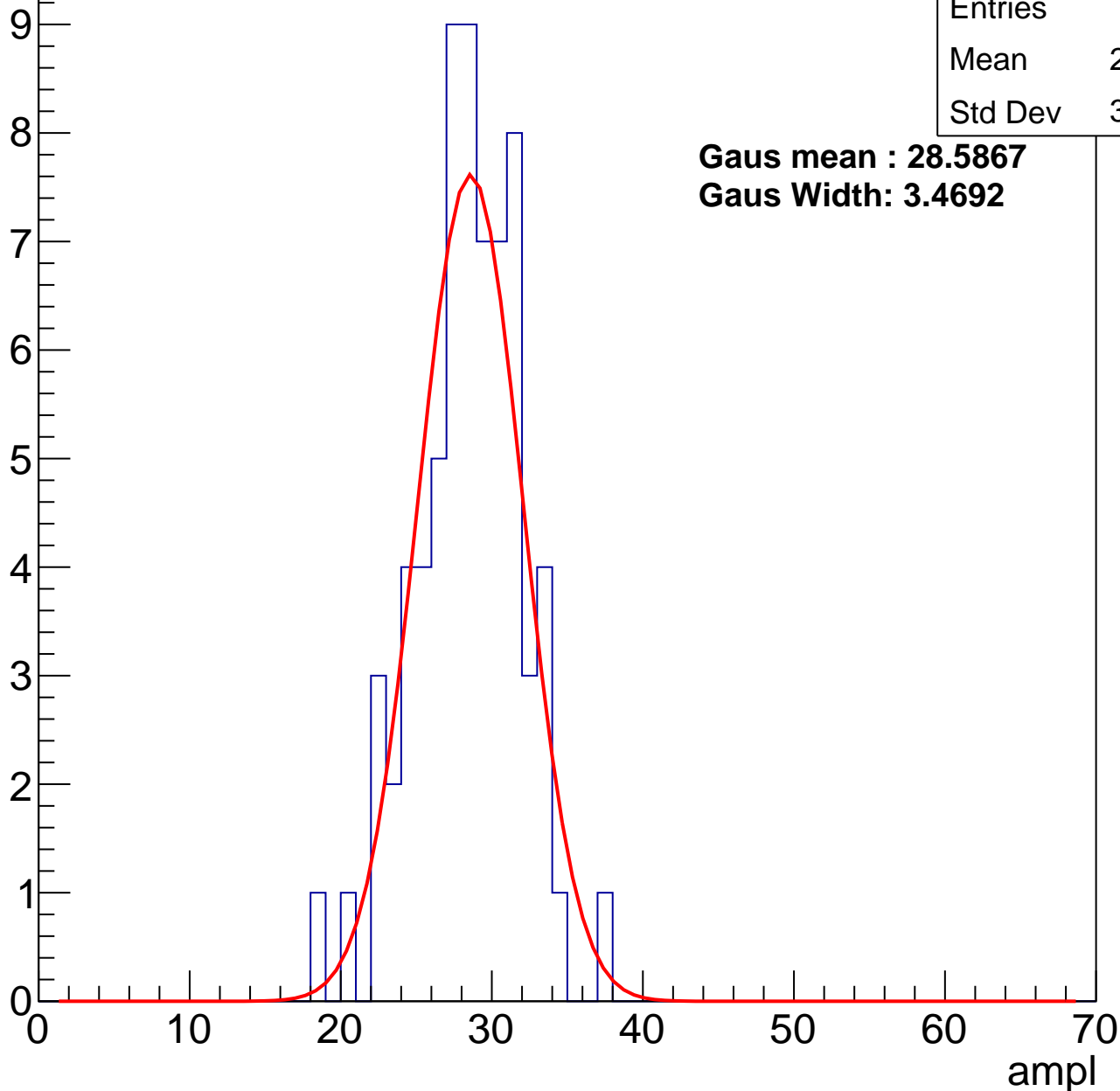
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.99
Std Dev	3.454

**Gaus mean : 28.5867**

**Gaus Width: 3.4692**



# B1L002S, U8-ch86, adc1

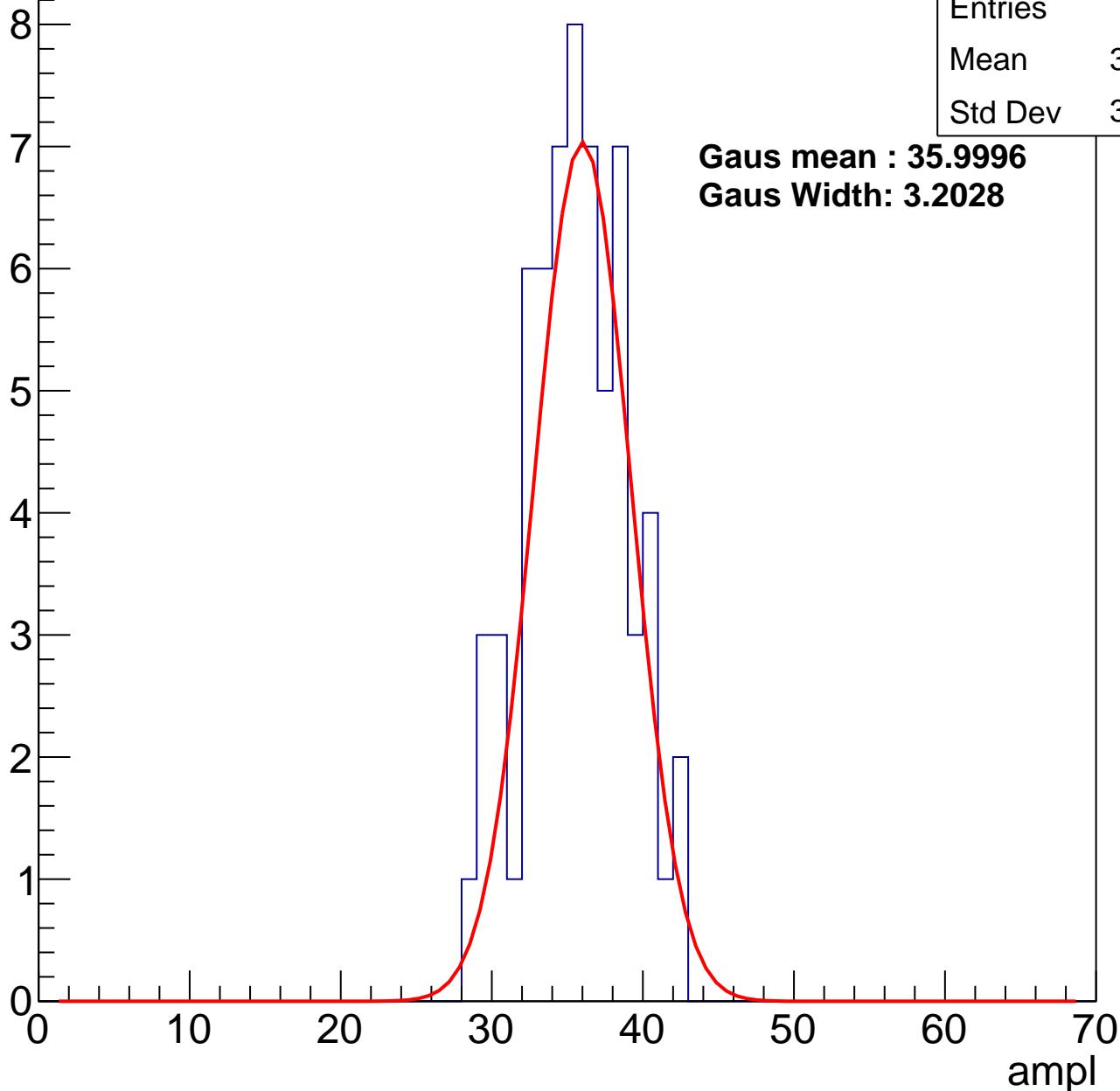
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.14
Std Dev	3.316

**Gaus mean : 35.9996**

**Gaus Width: 3.2028**



# B1L002S, U8-ch86, adc2

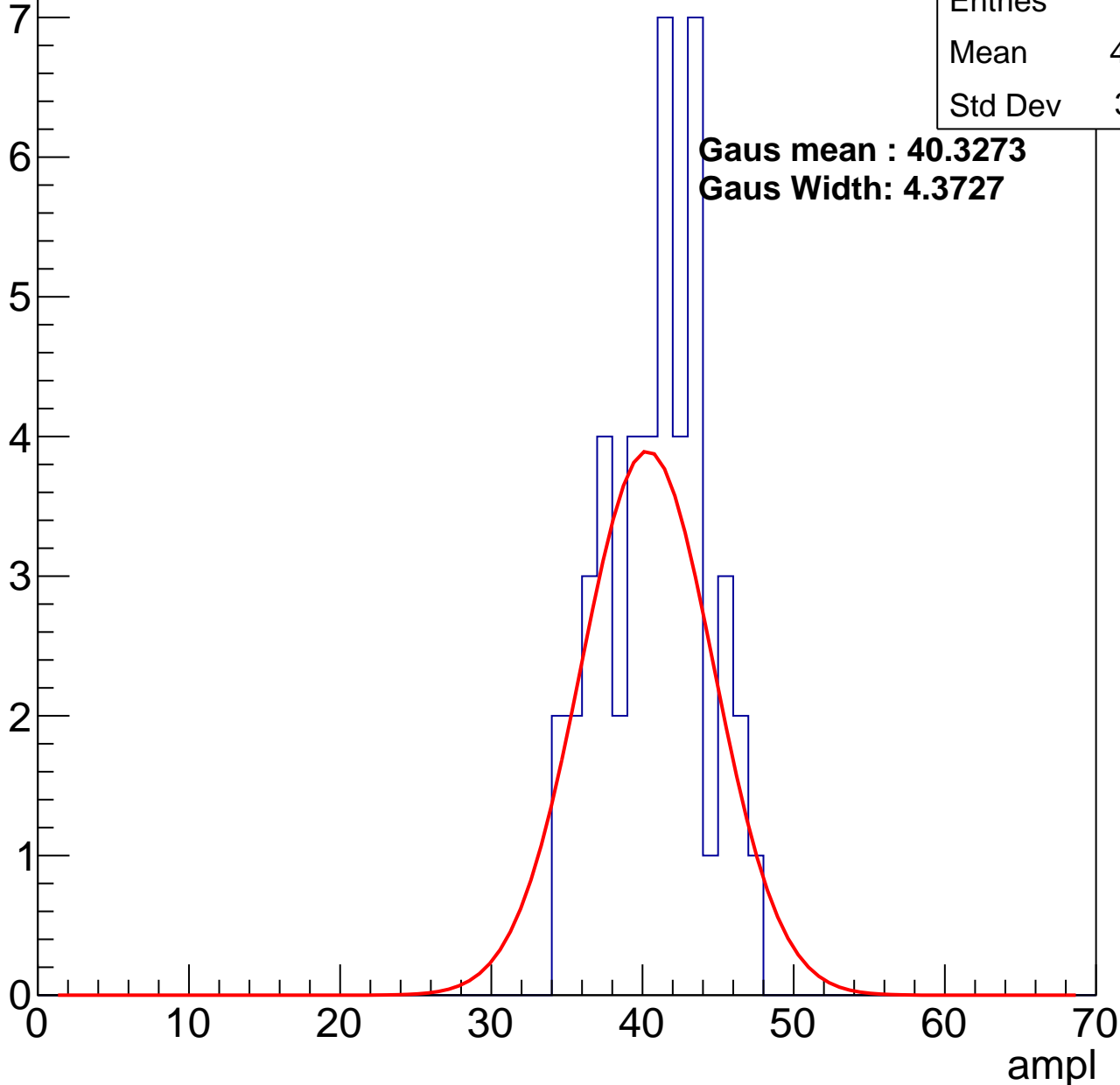
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	40.43
Std Dev	3.321

**Gaus mean : 40.3273**

**Gaus Width: 4.3727**



# B1L002S, U8-ch86, adc3

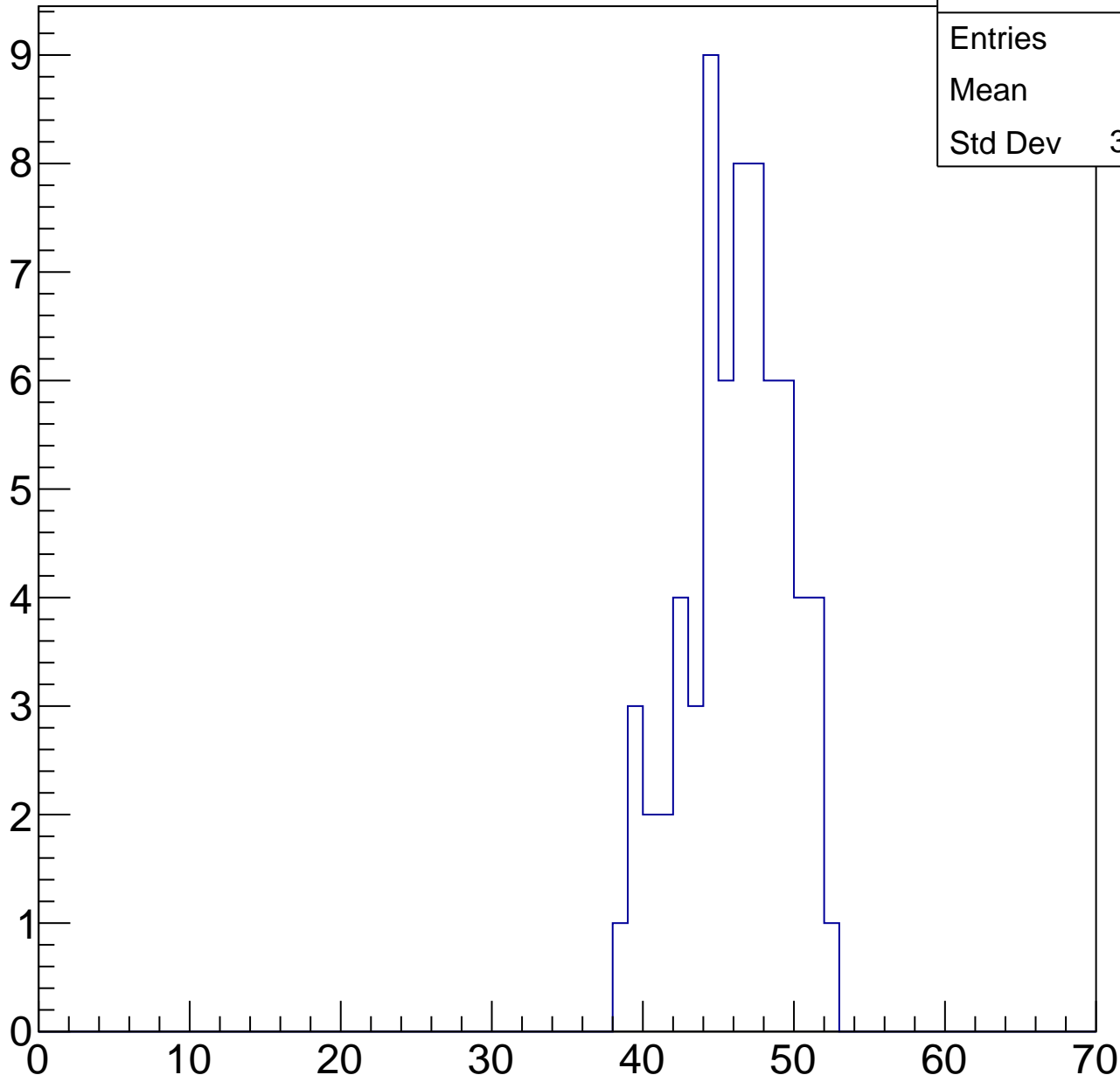
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	67
Mean	45.7
Std Dev	3.337

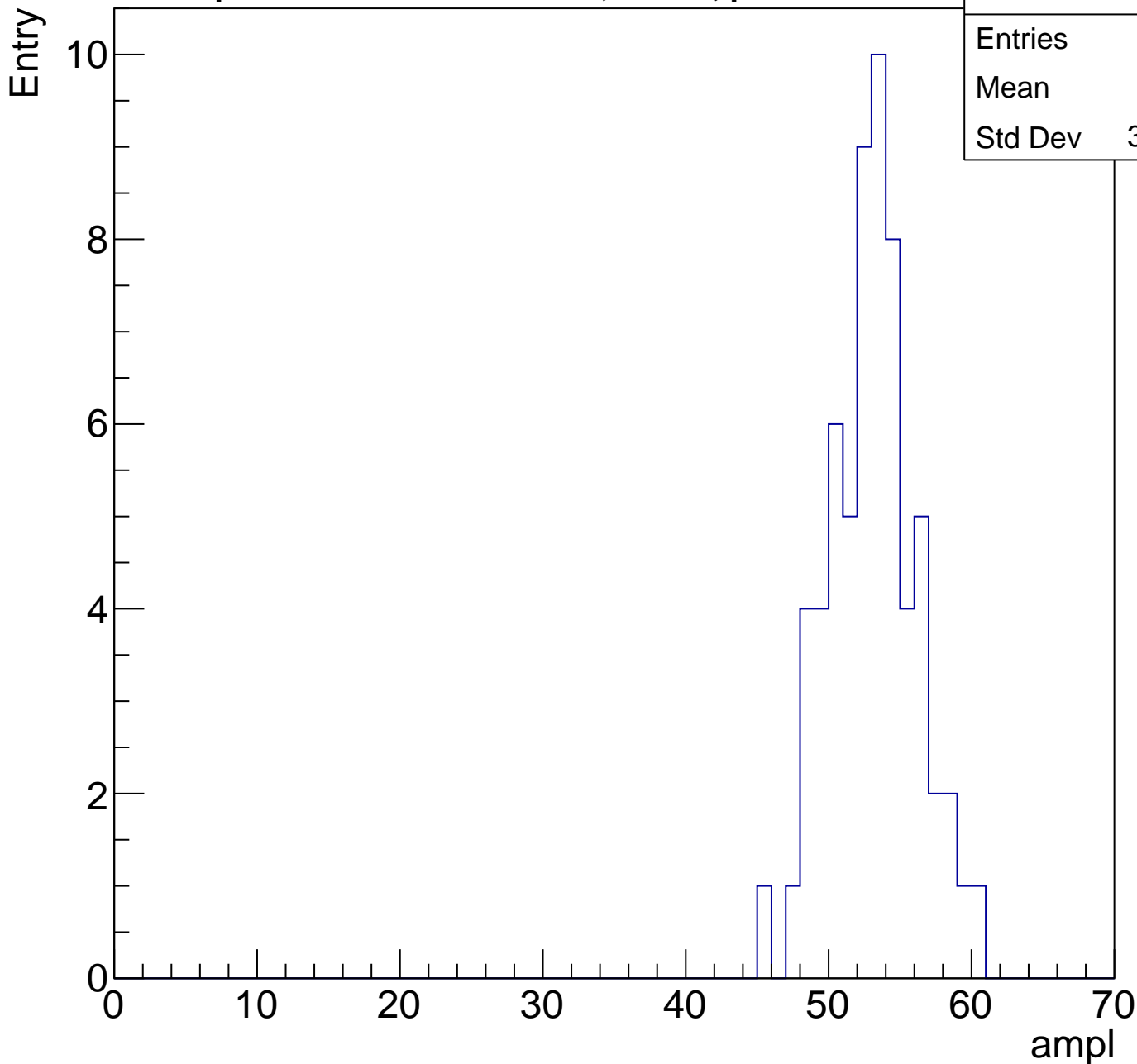
ampl



# B1L002S, U8-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	52.6
Std Dev	3.016

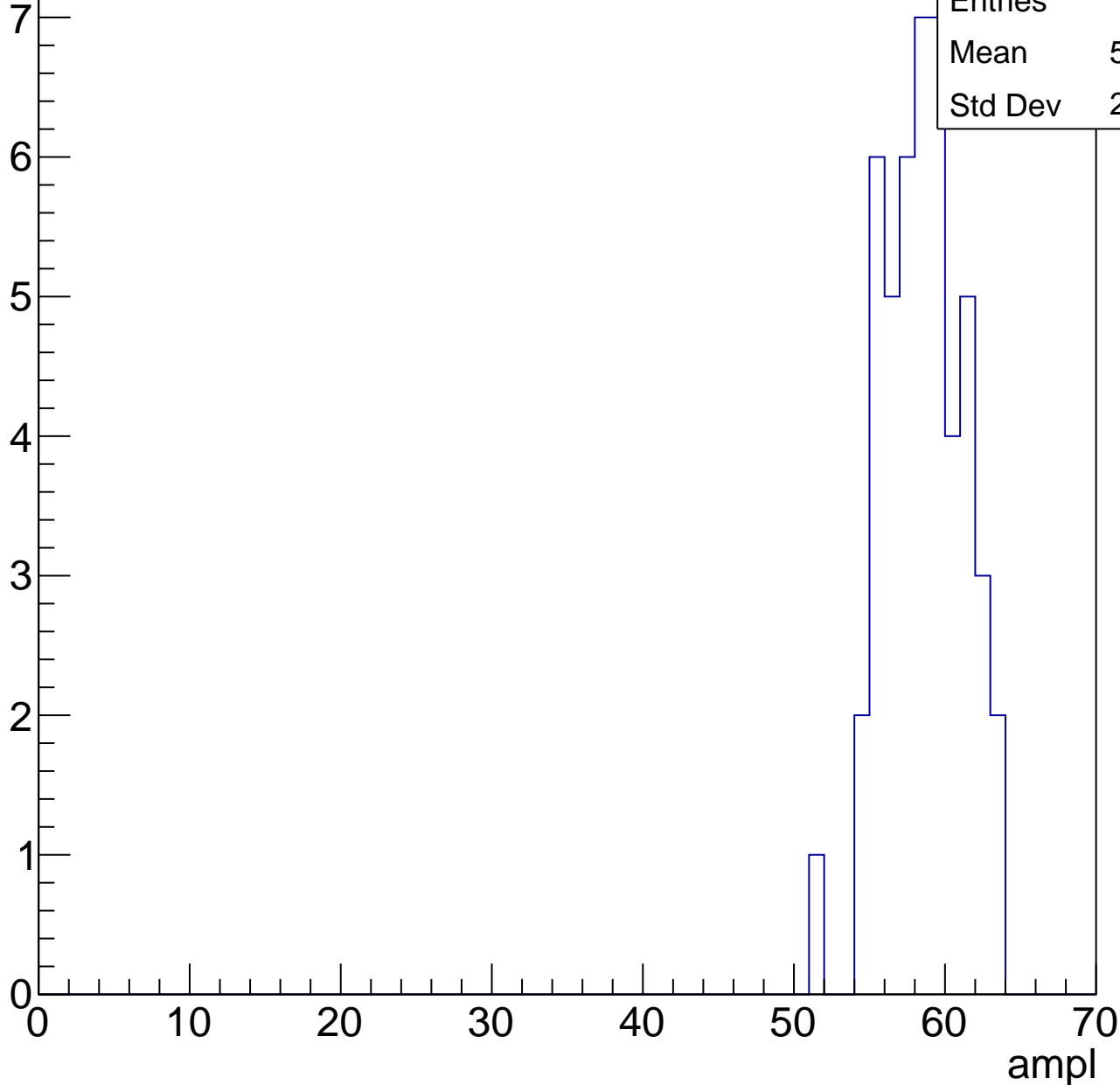


# B1L002S, U8-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.06
Std Dev	2.609

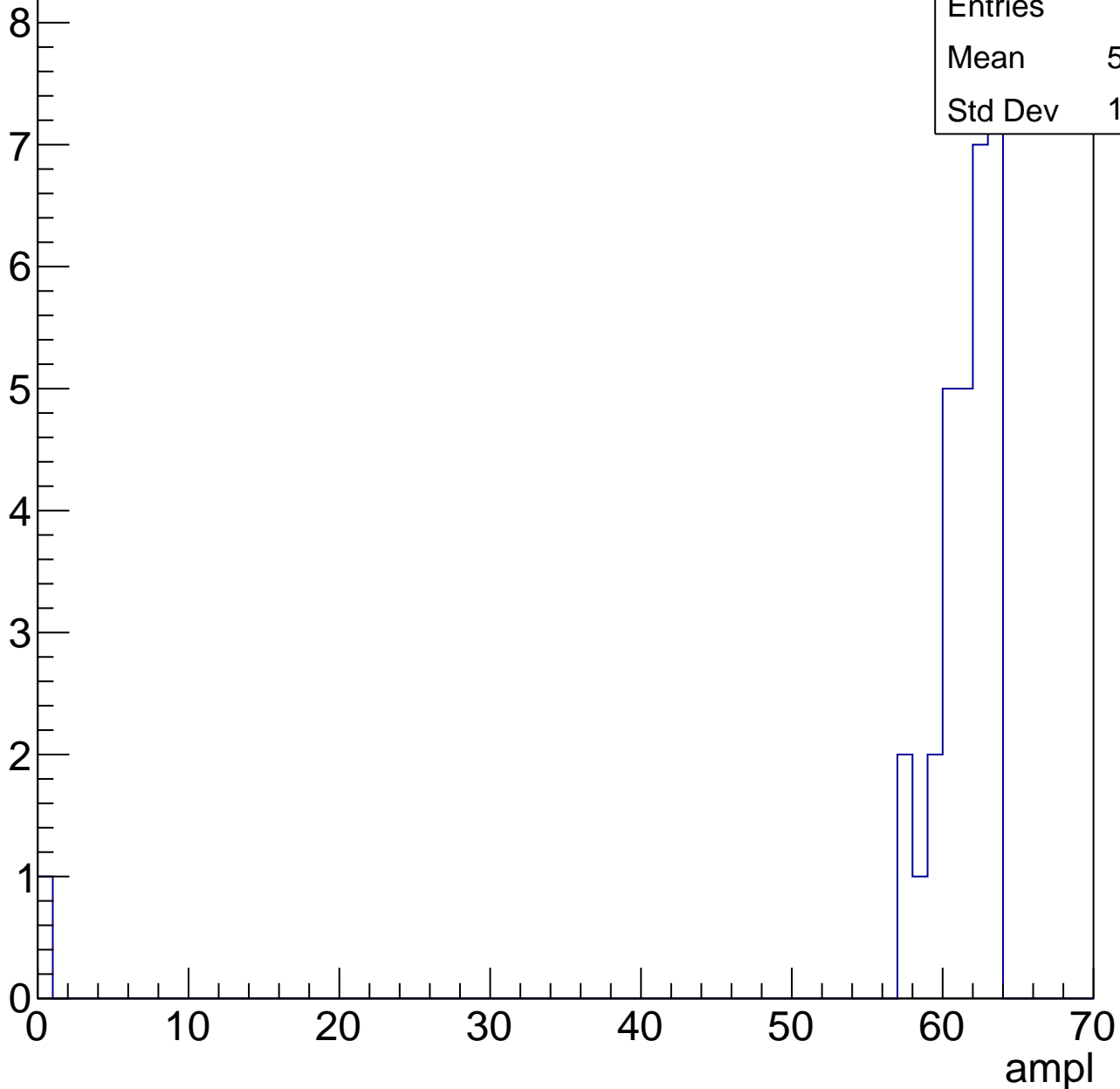


# B1L002S, U8-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	59.13
Std Dev	10.93





# B1L002S, U8-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch87, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	29.83
Std Dev	3.023

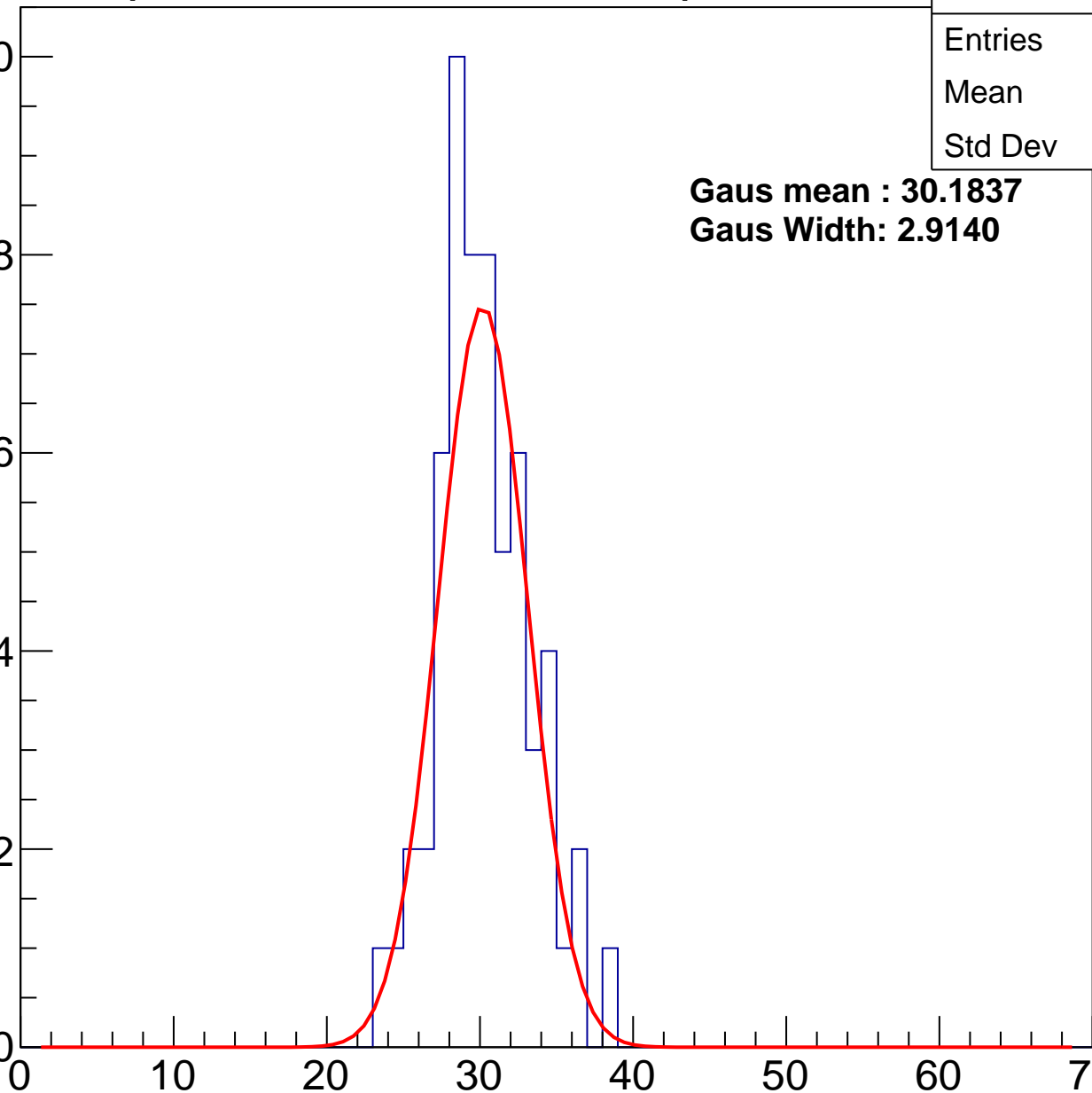
**Gaus mean : 30.1837**

**Gaus Width: 2.9140**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U8-ch87, adc1

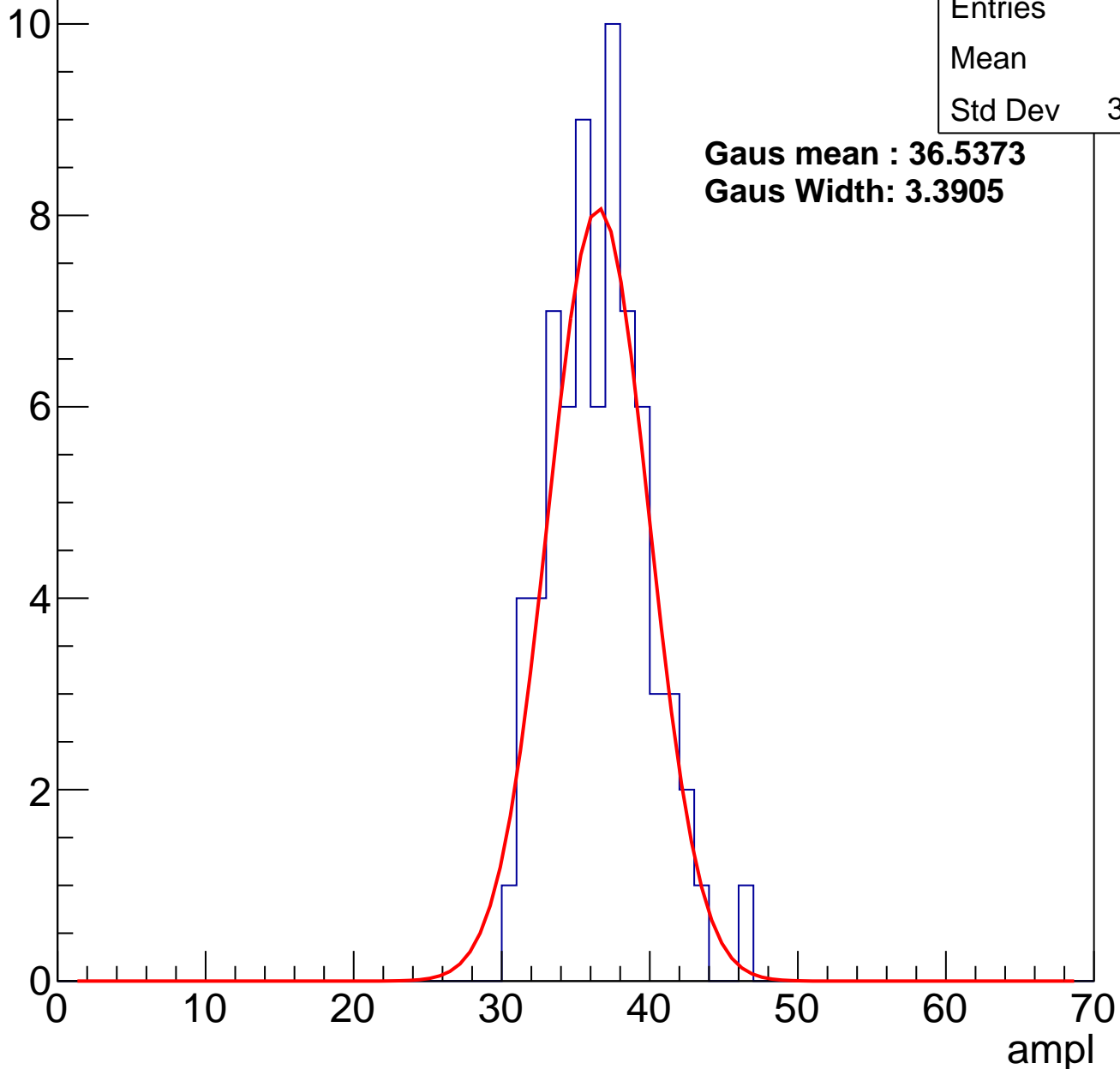
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	36.2
Std Dev	3.228

**Gaus mean : 36.5373**

**Gaus Width: 3.3905**

Entry



# B1L002S, U8-ch87, adc2

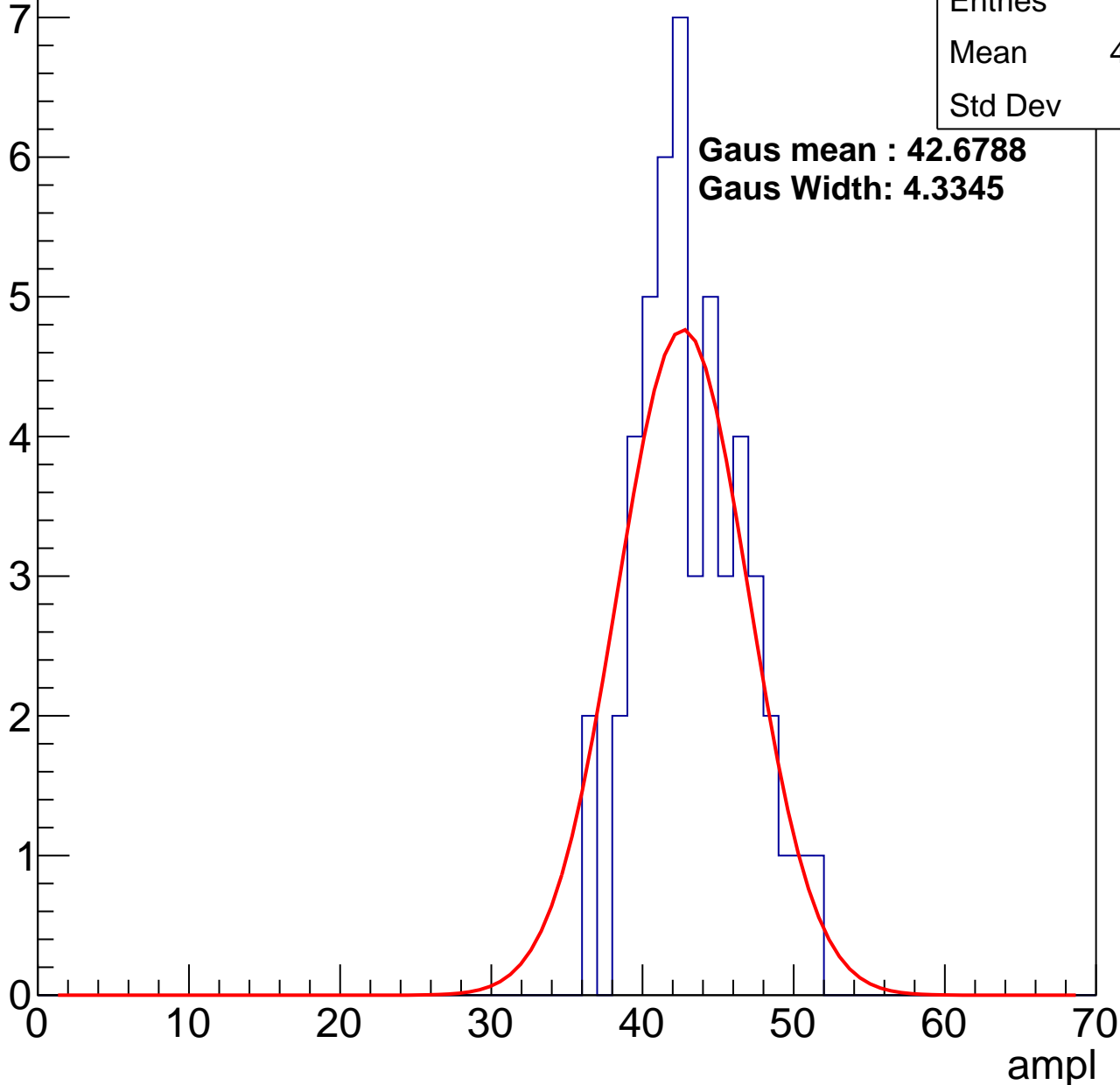
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	42.84
Std Dev	3.46

**Gaus mean : 42.6788**

**Gaus Width: 4.3345**

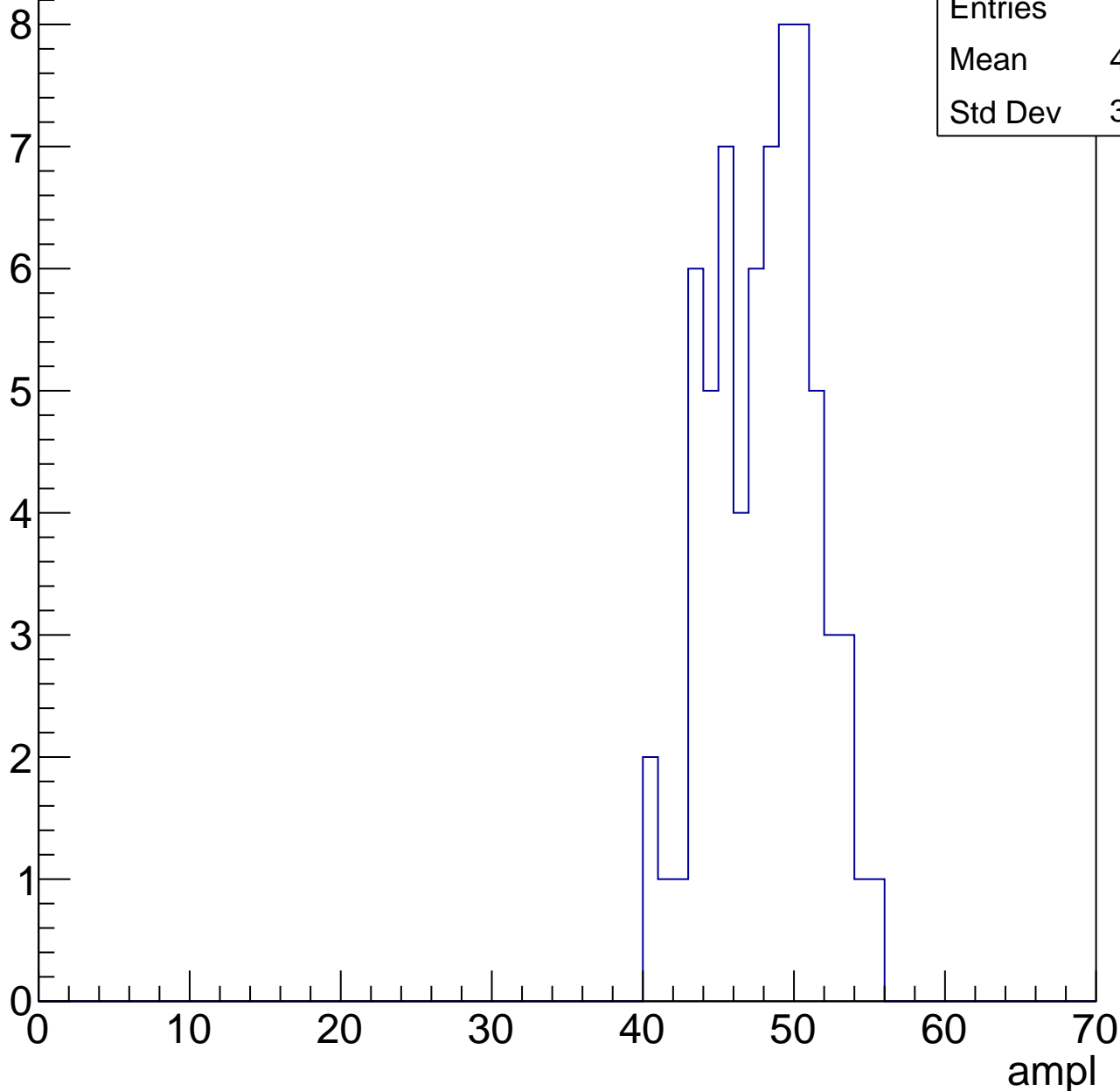


# B1L002S, U8-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

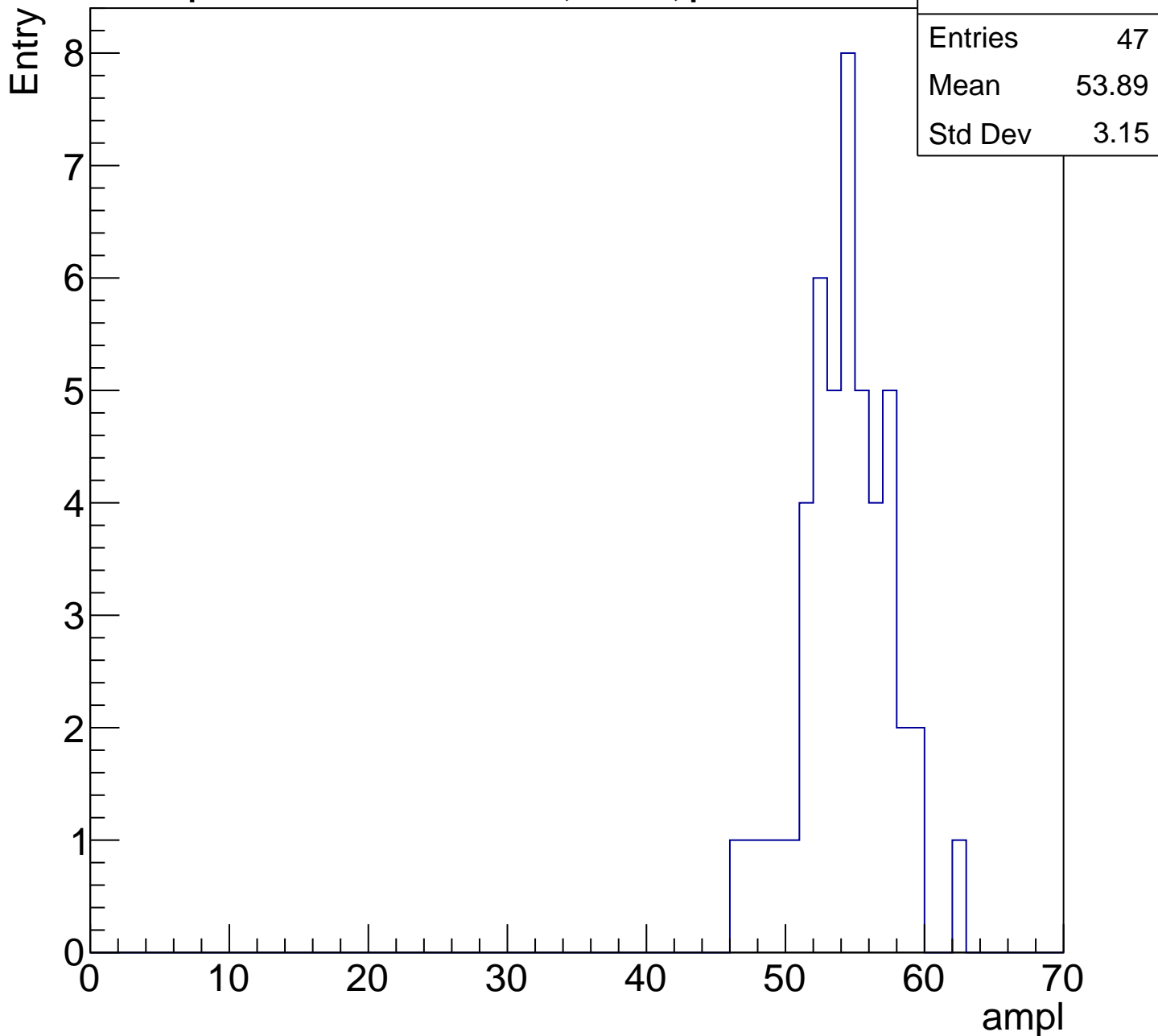
Entry

Entries	68
Mean	47.49
Std Dev	3.436



# B1L002S, U8-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

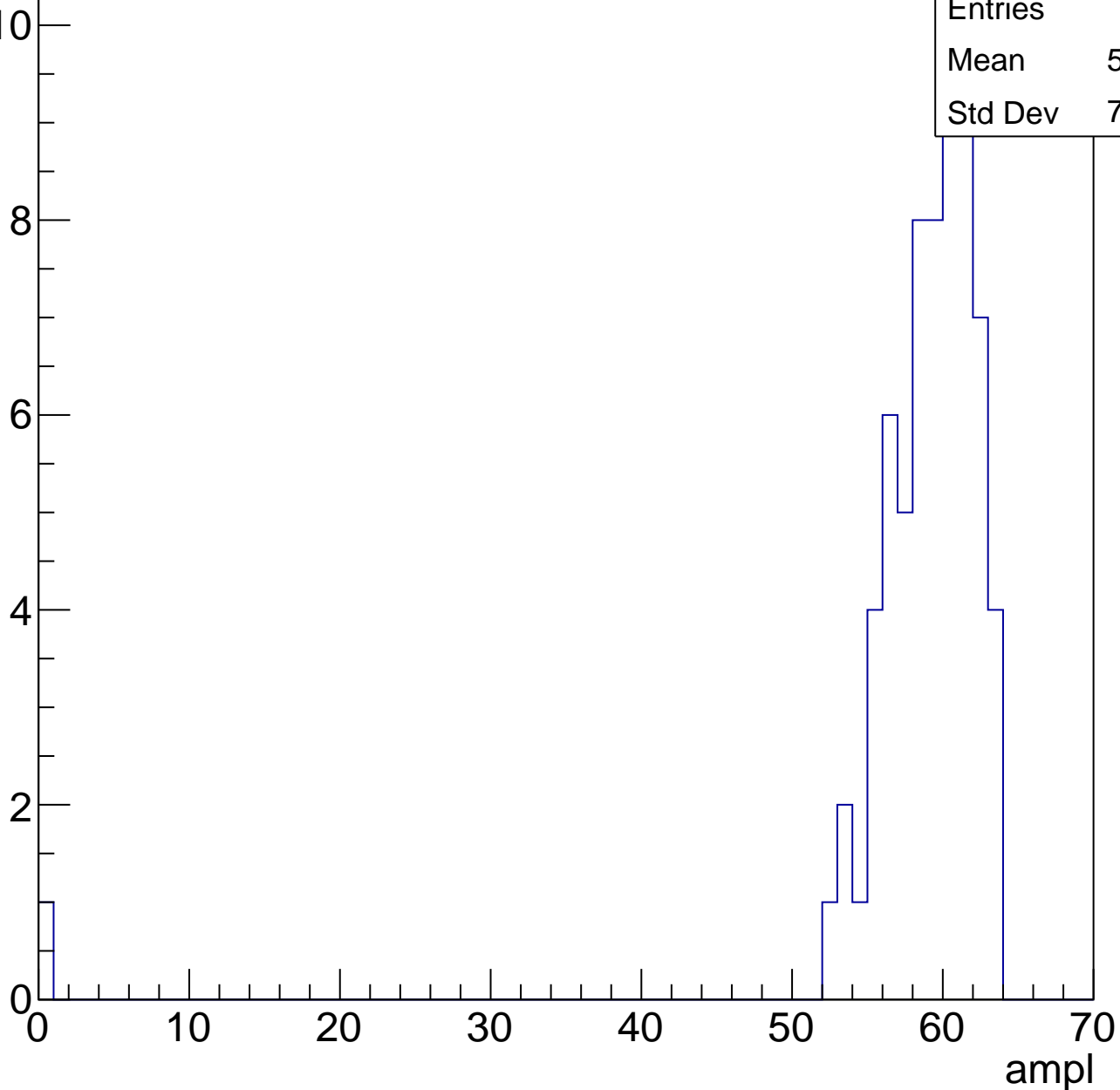


# B1L002S, U8-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.94
Std Dev	7.659

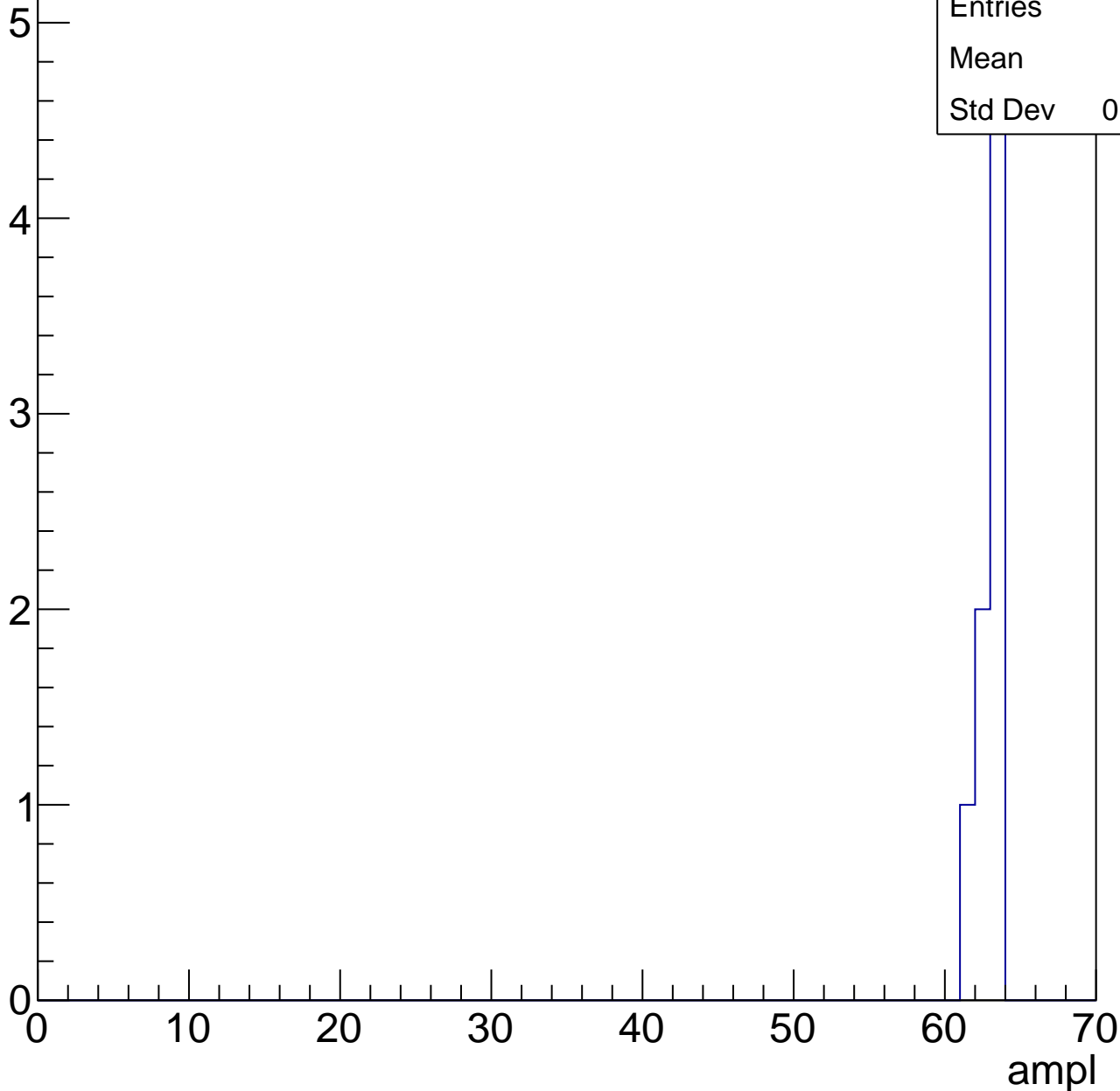


# B1L002S, U8-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	8
Mean	62.5
Std Dev	0.7071

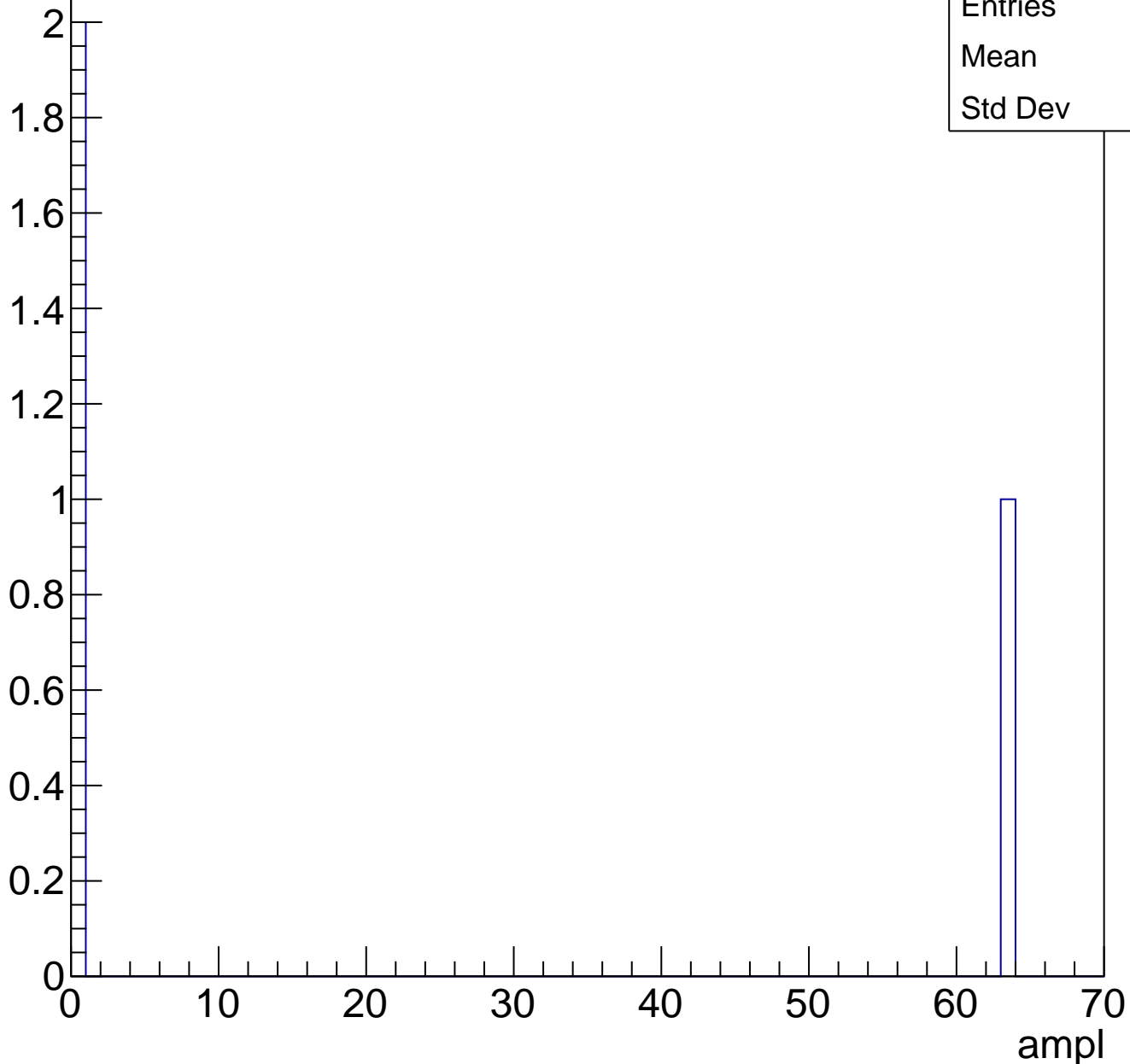




# B1L002S, U8-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch88, adc0

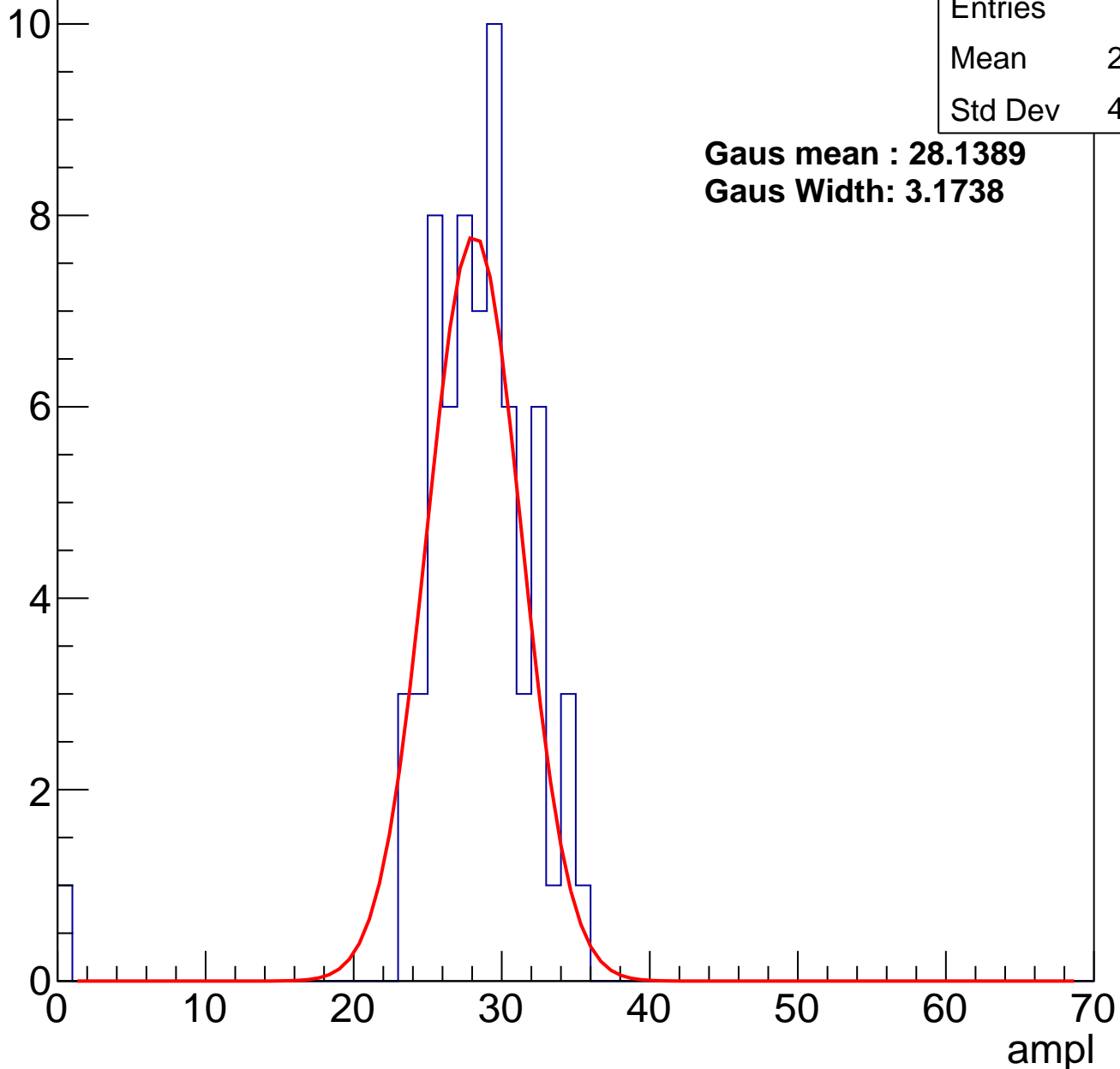
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	27.79
Std Dev	4.518

**Gaus mean : 28.1389**

**Gaus Width: 3.1738**

Entry



# B1L002S, U8-ch88, adc1

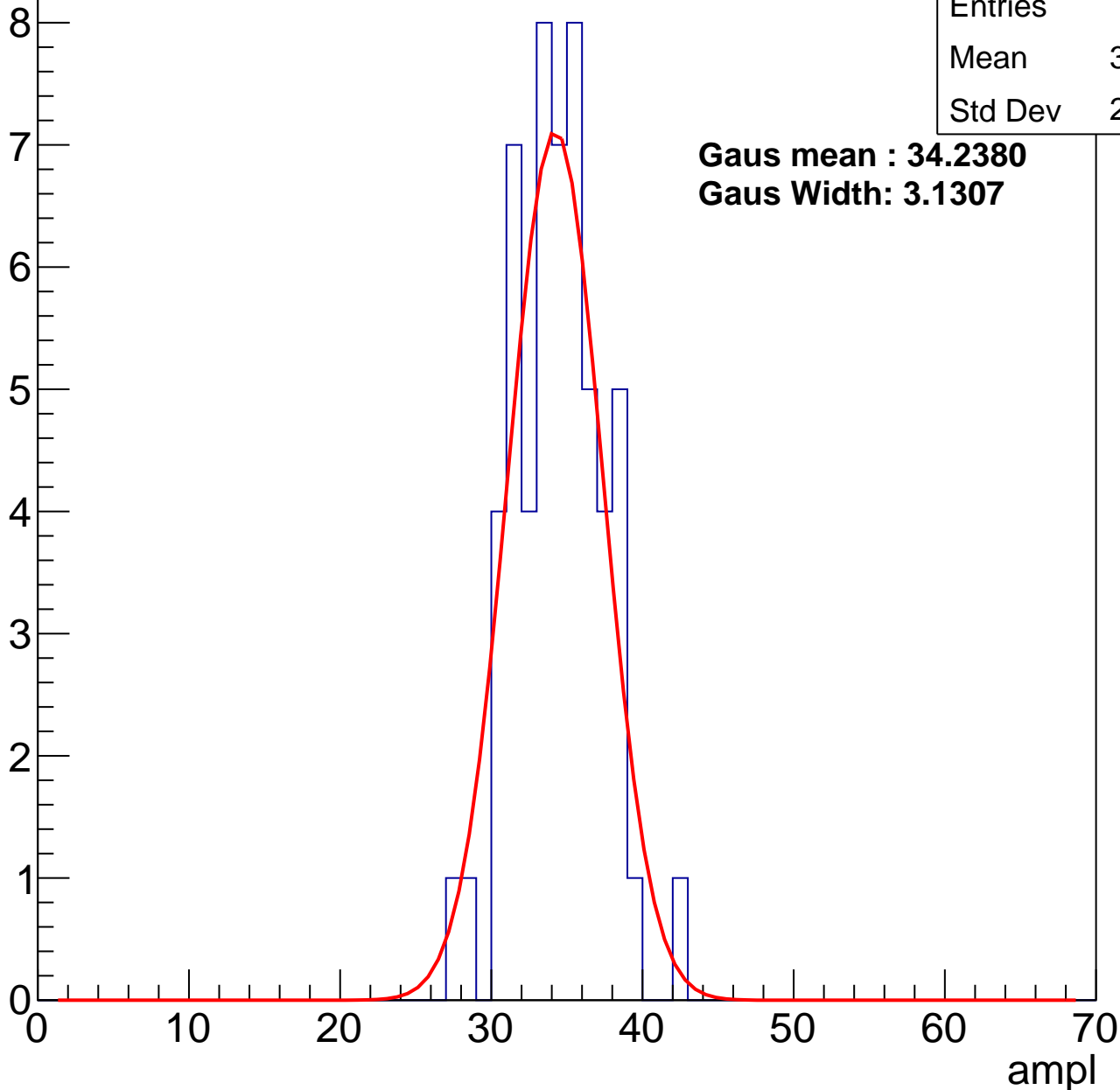
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.95
Std Dev	2.893

**Gaus mean : 34.2380**

**Gaus Width: 3.1307**



# B1L002S, U8-ch88, adc2

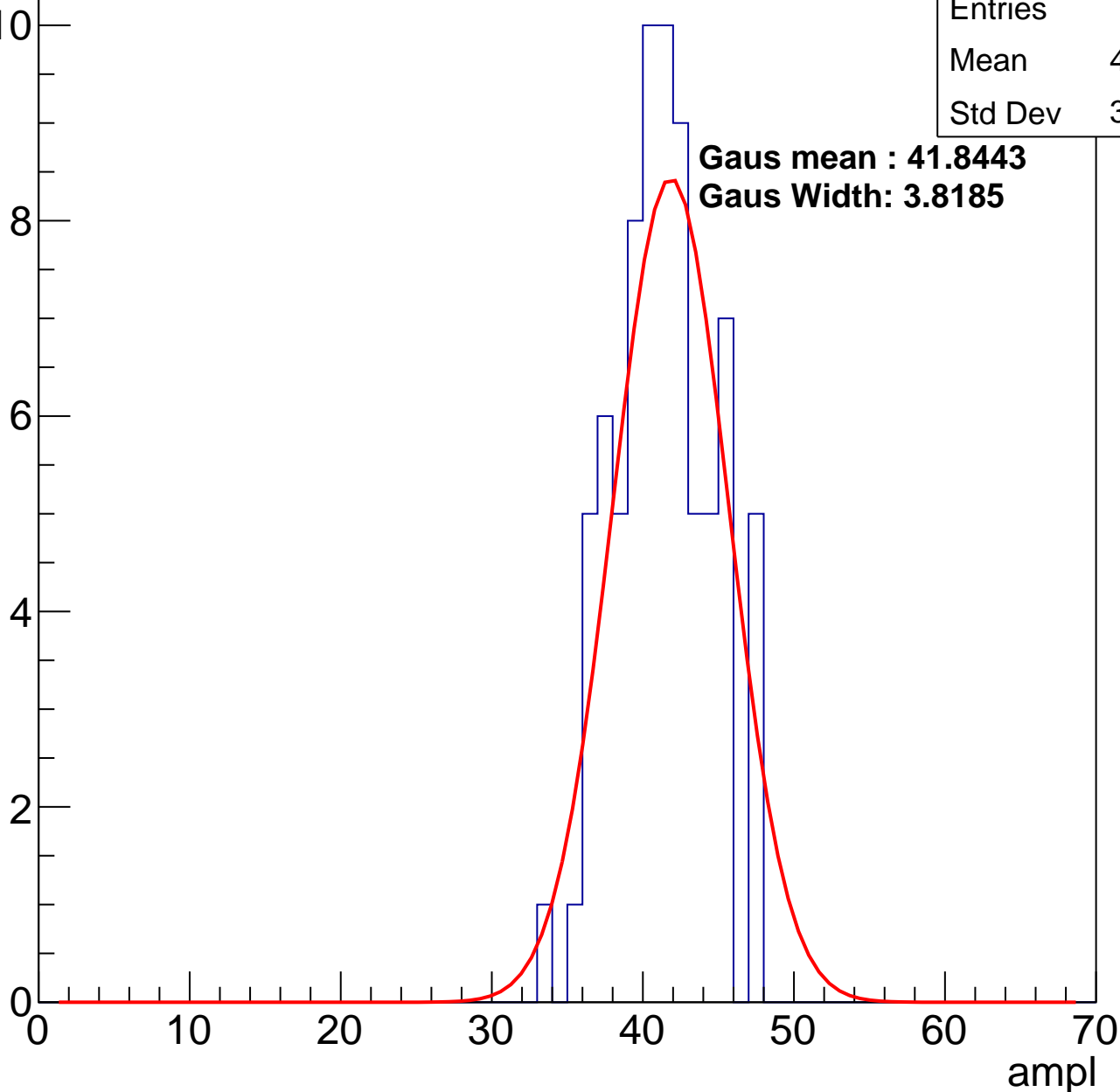
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	40.84
Std Dev	3.167

**Gaus mean : 41.8443**

**Gaus Width: 3.8185**



# B1L002S, U8-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

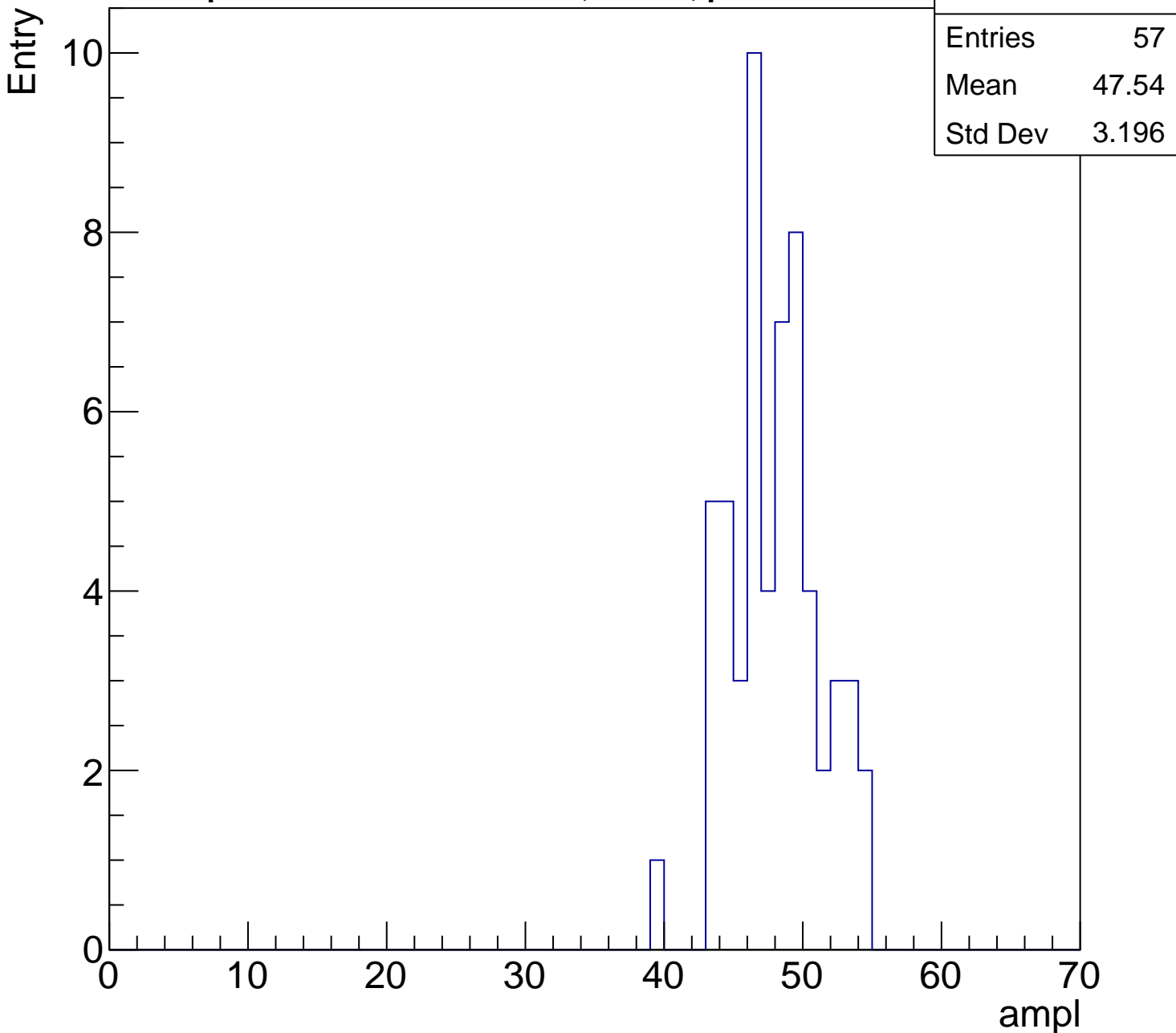
Entries	57
Mean	47.54
Std Dev	3.196

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

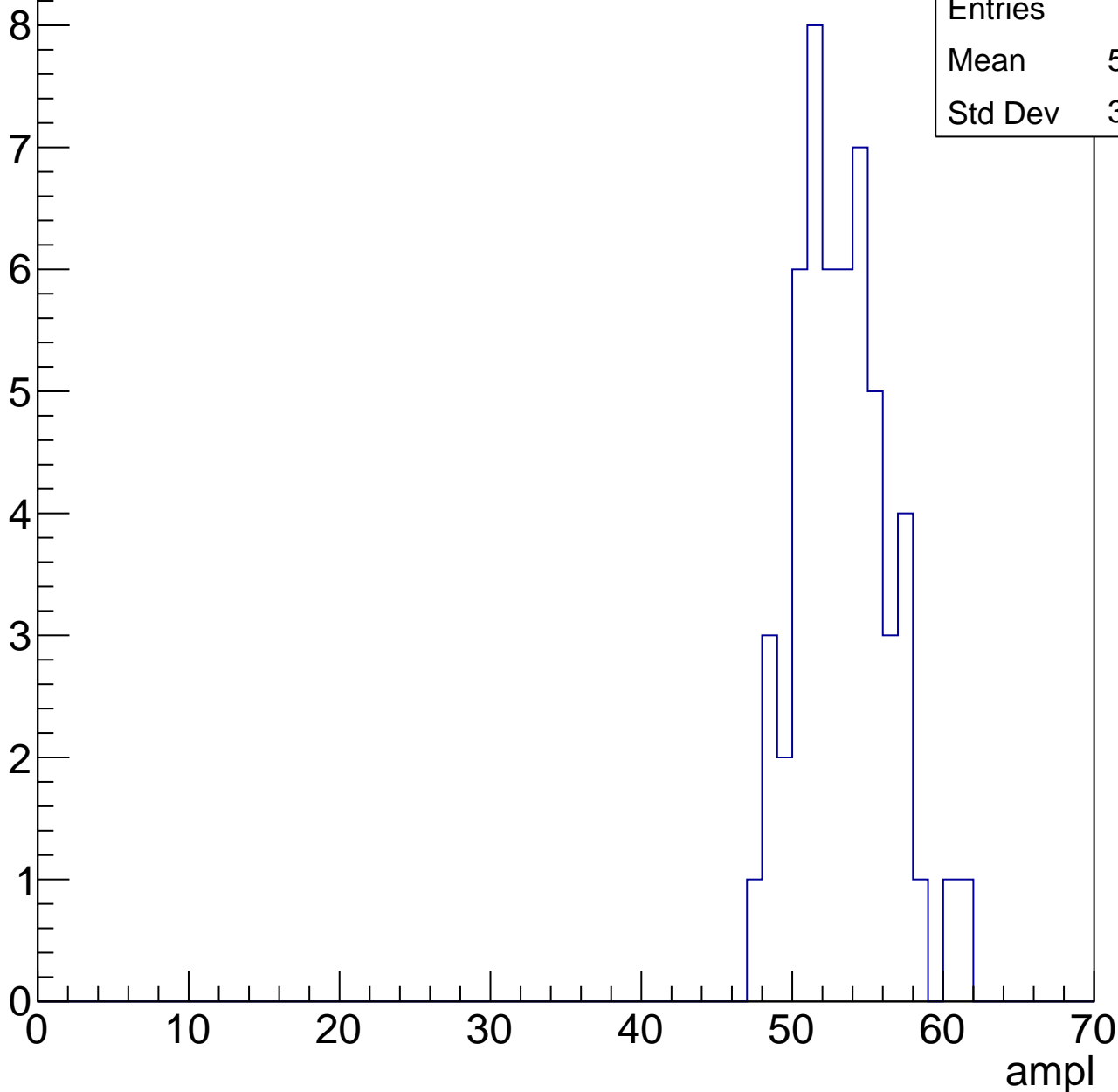


# B1L002S, U8-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

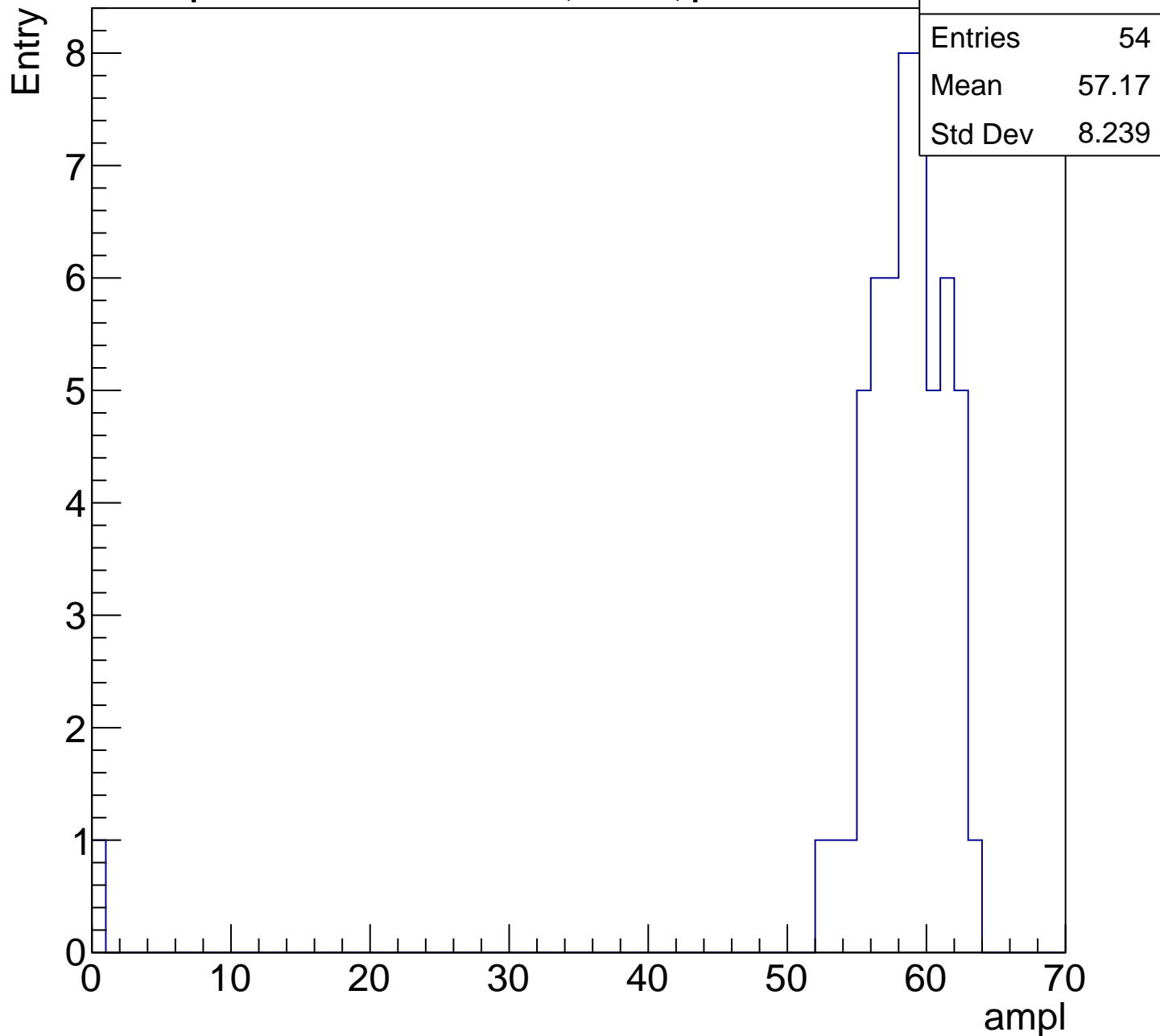
Entry

Entries	54
Mean	52.87
Std Dev	3.013



# B1L002S, U8-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

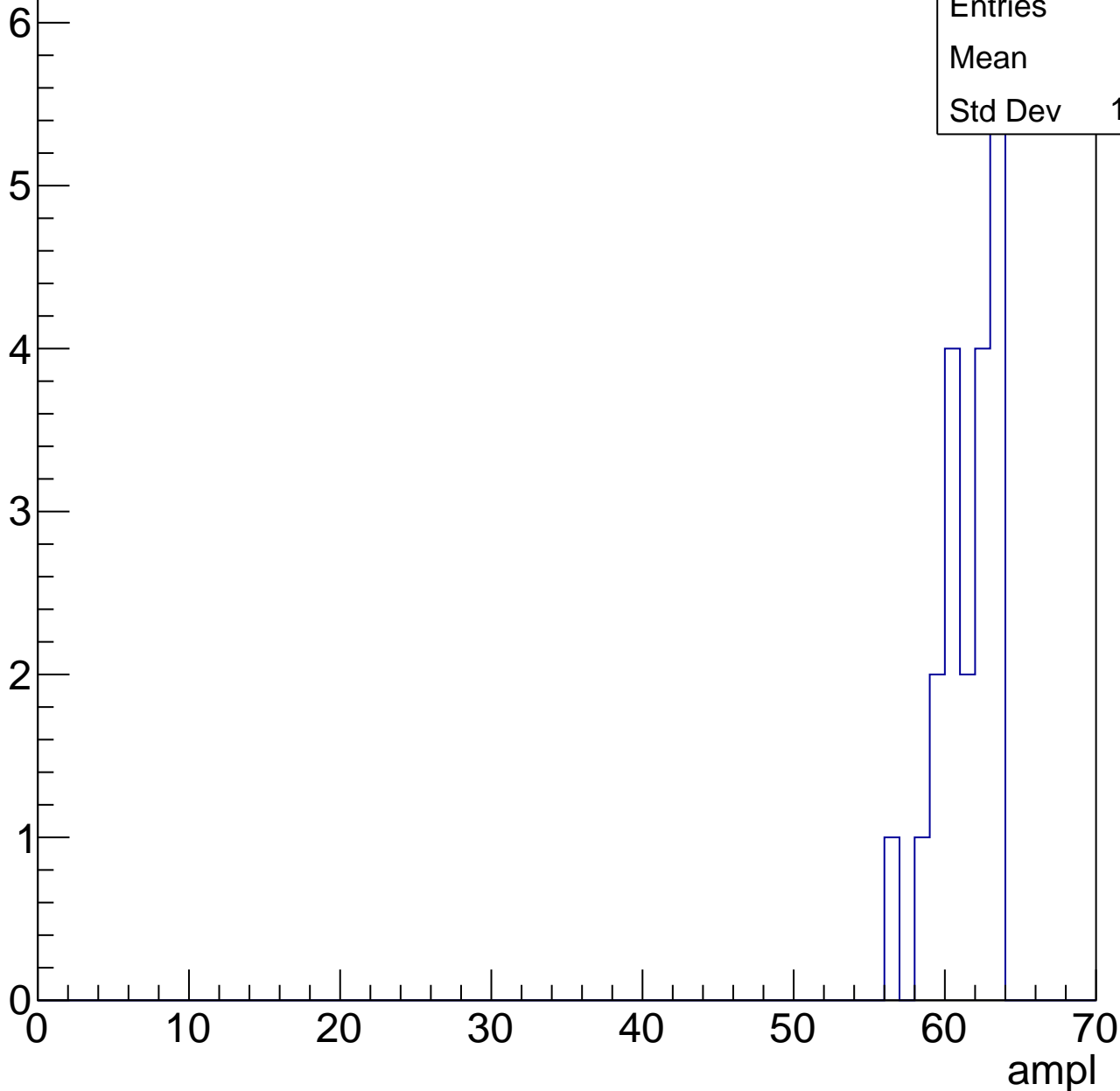


# B1L002S, U8-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61
Std Dev	1.924

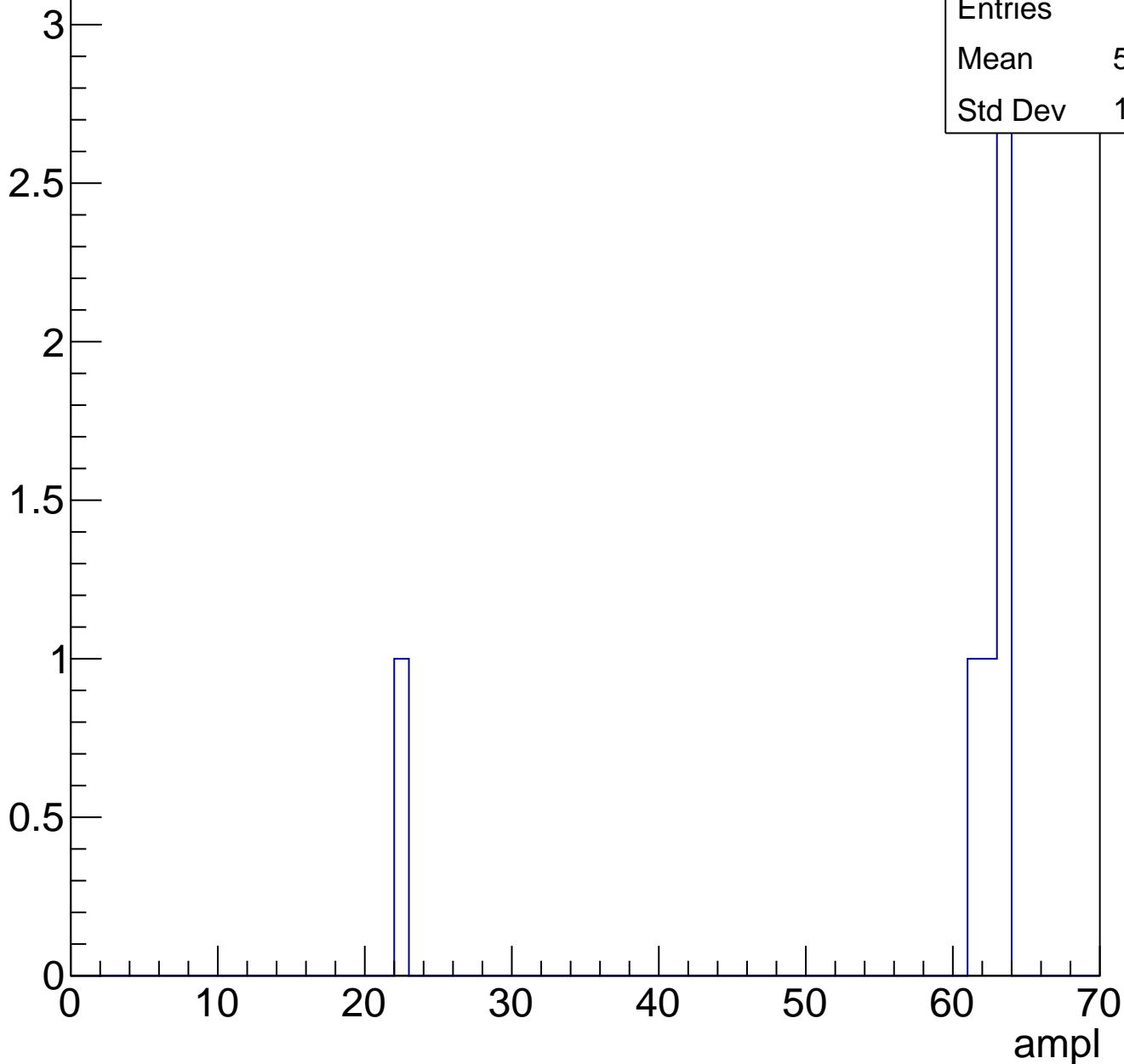




# B1L002S, U8-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	27.47
Std Dev	4.367

**Gaus mean : 28.4621**

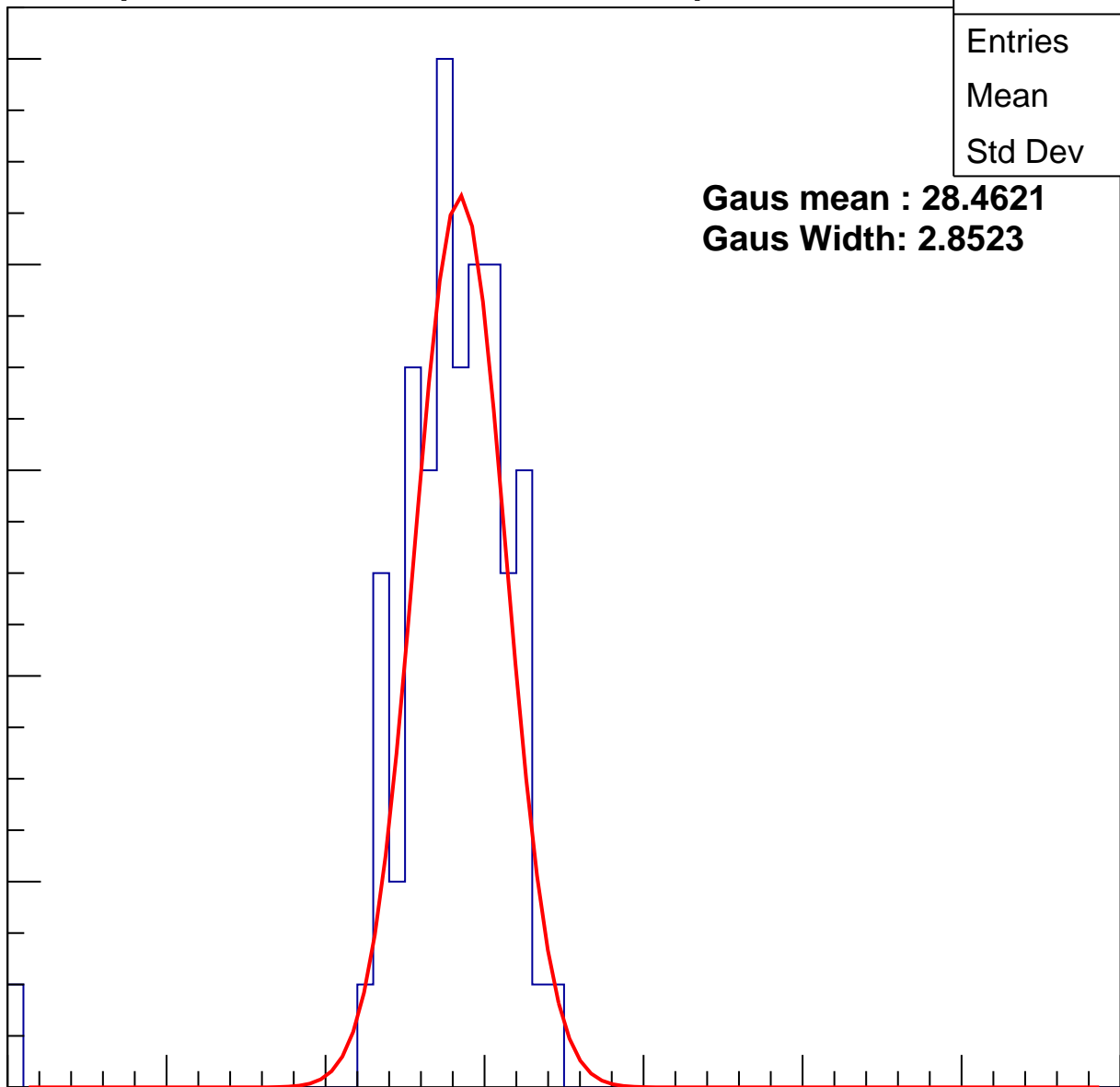
**Gaus Width: 2.8523**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch89, adc1

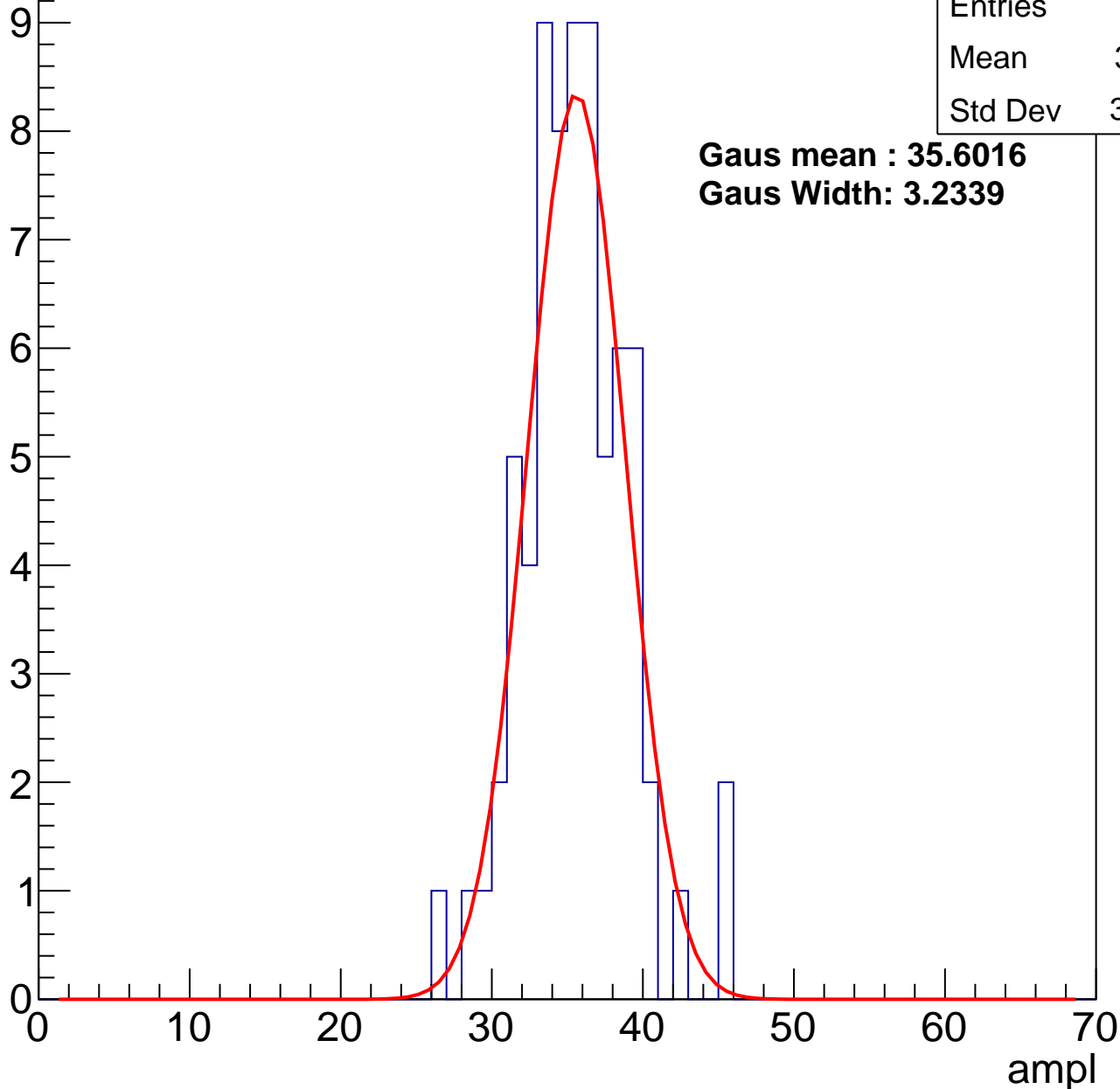
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	35.11
Std Dev	3.466

**Gaus mean : 35.6016**

**Gaus Width: 3.2339**



# B1L002S, U8-ch89, adc2

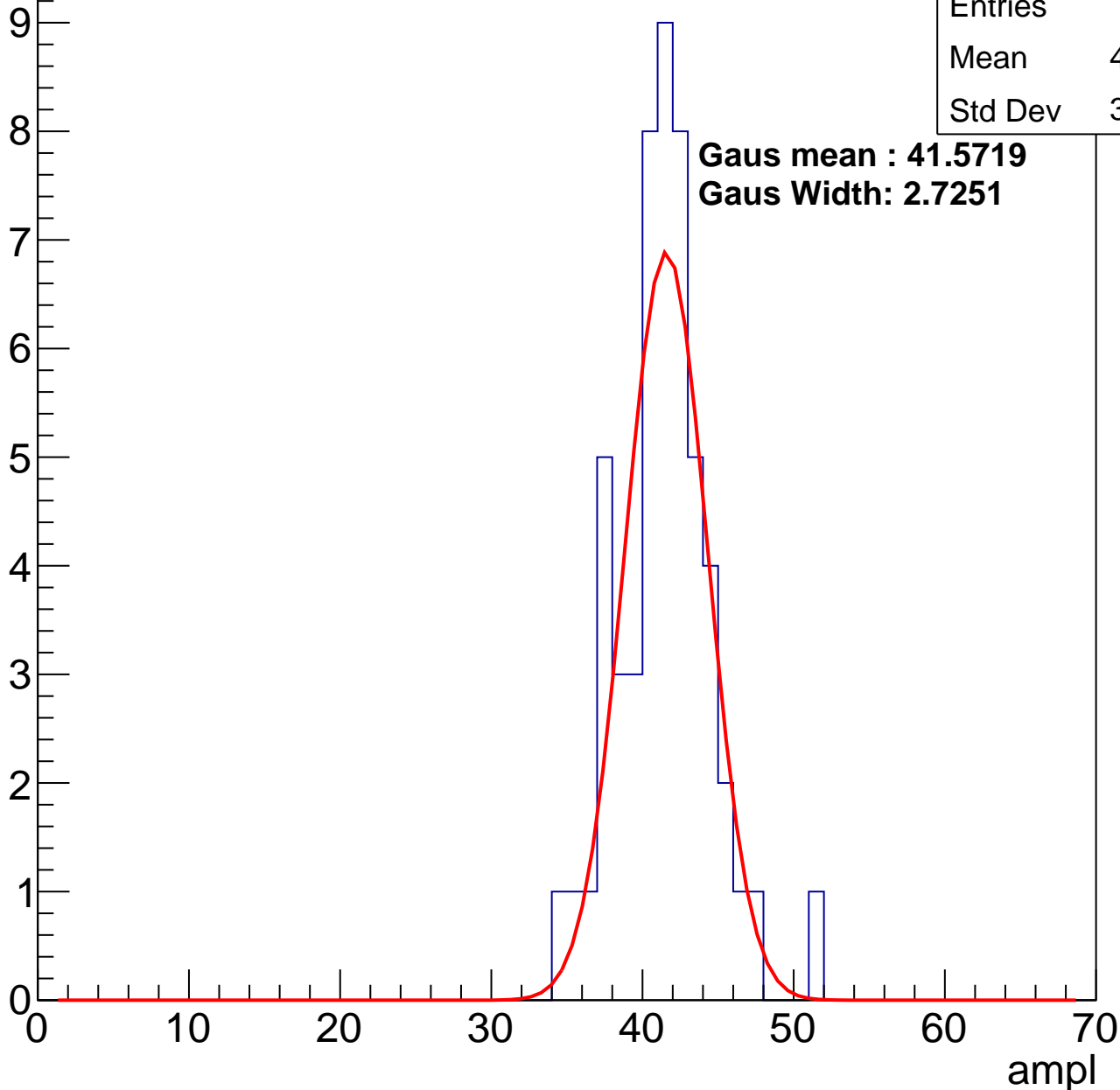
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.96
Std Dev	3.053

**Gaus mean : 41.5719**

**Gaus Width: 2.7251**

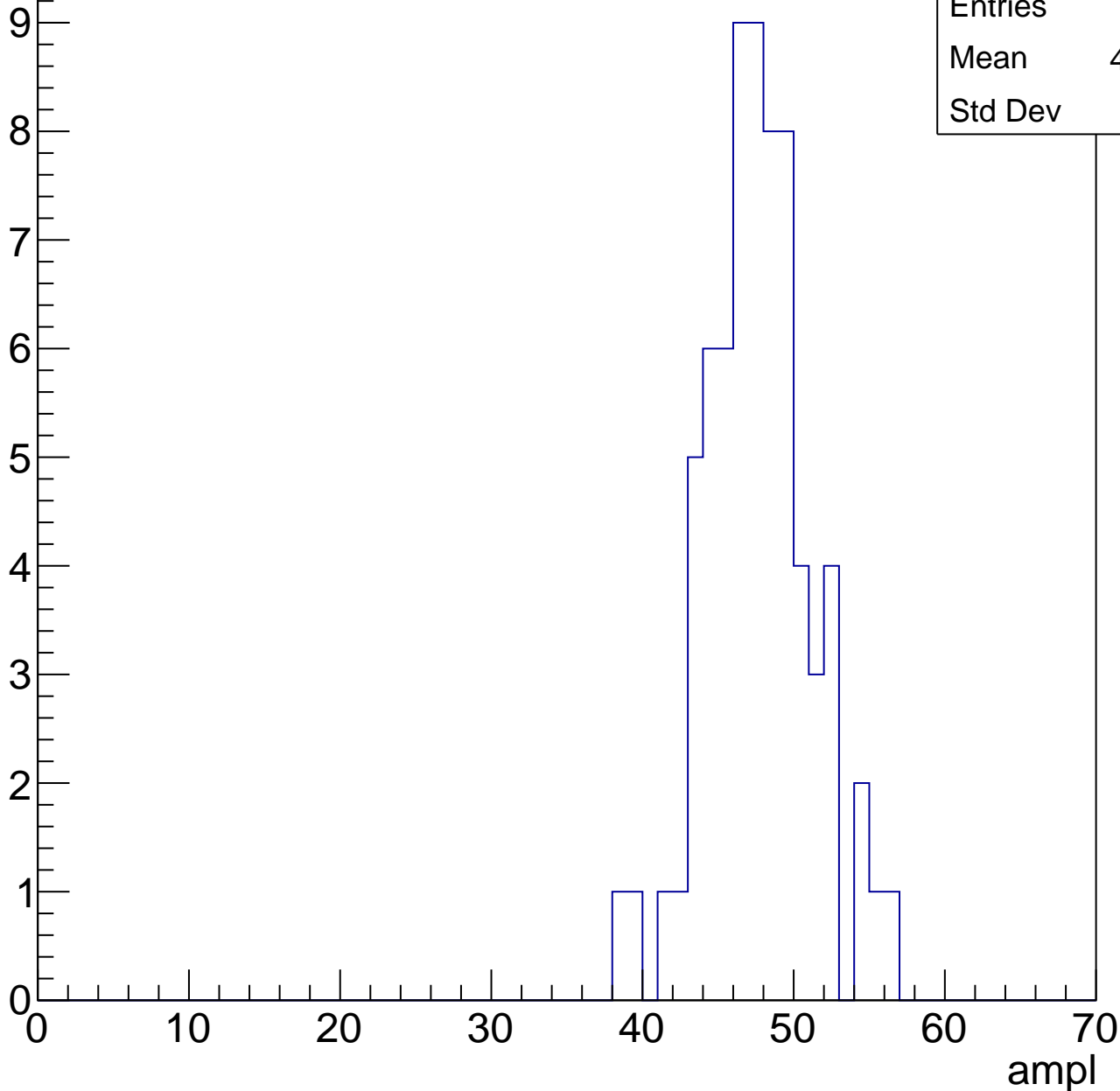


# B1L002S, U8-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

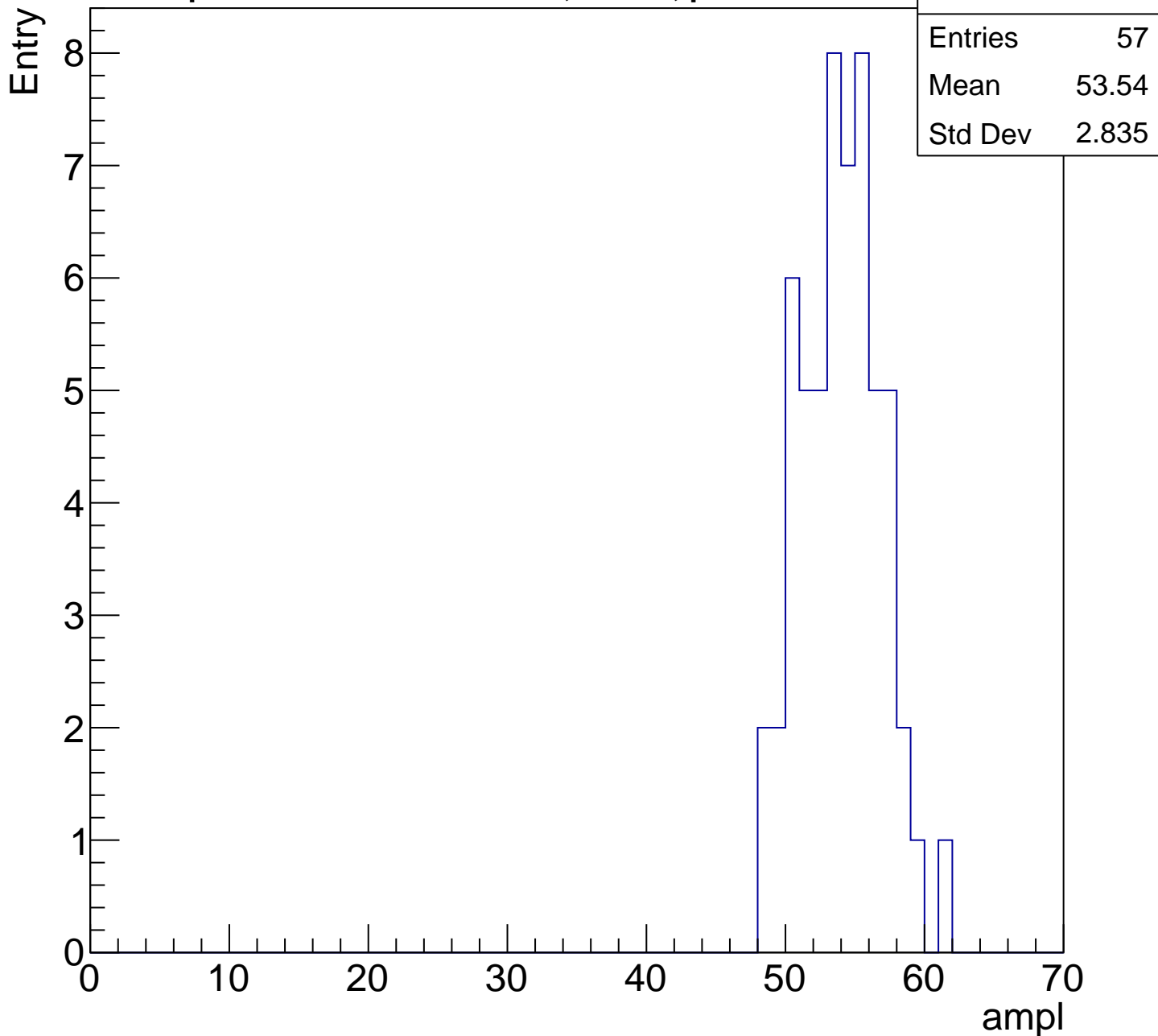
Entry

Entries	70
Mean	47.17
Std Dev	3.46



# B1L002S, U8-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

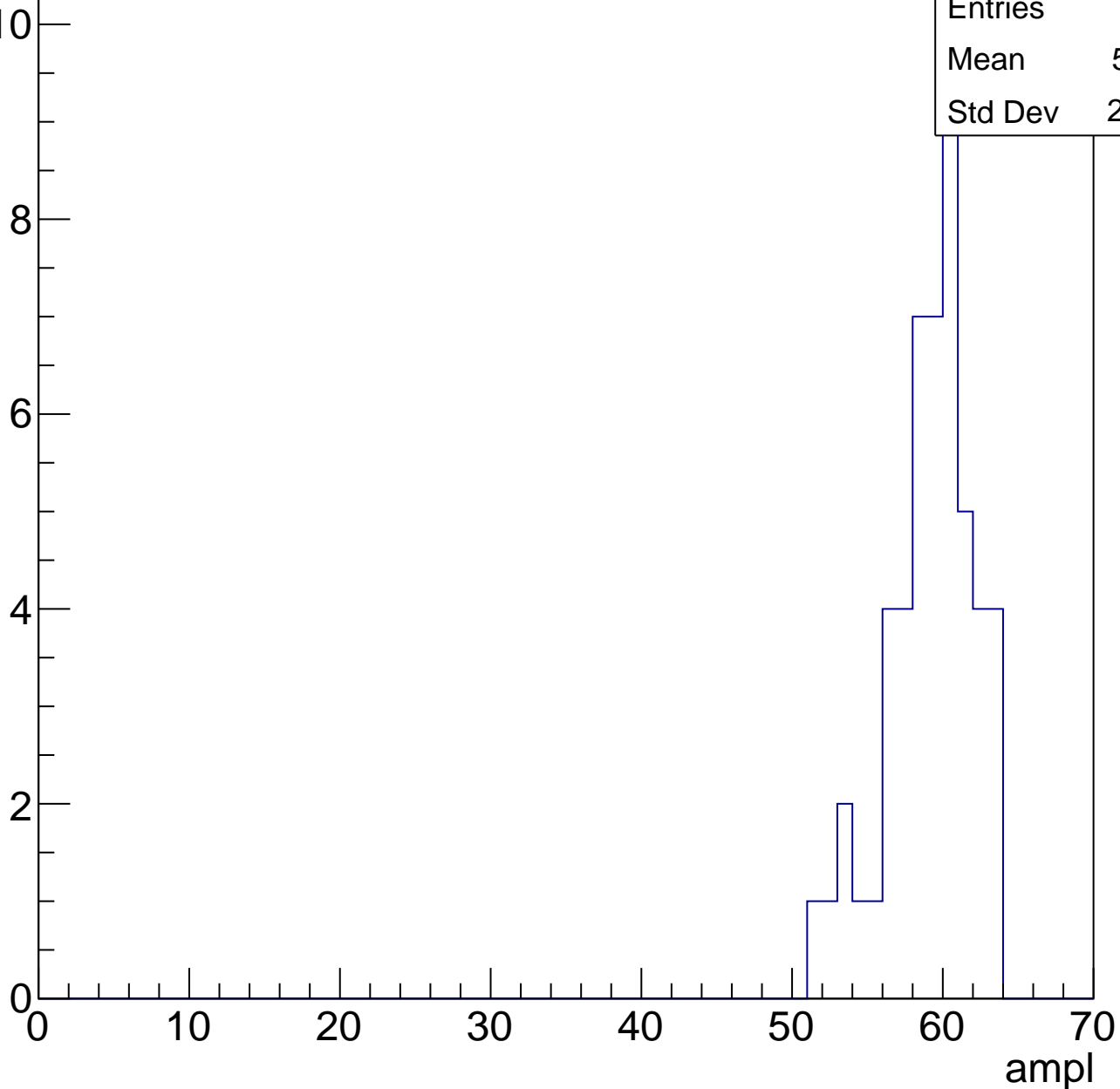


# B1L002S, U8-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.71
Std Dev	2.837

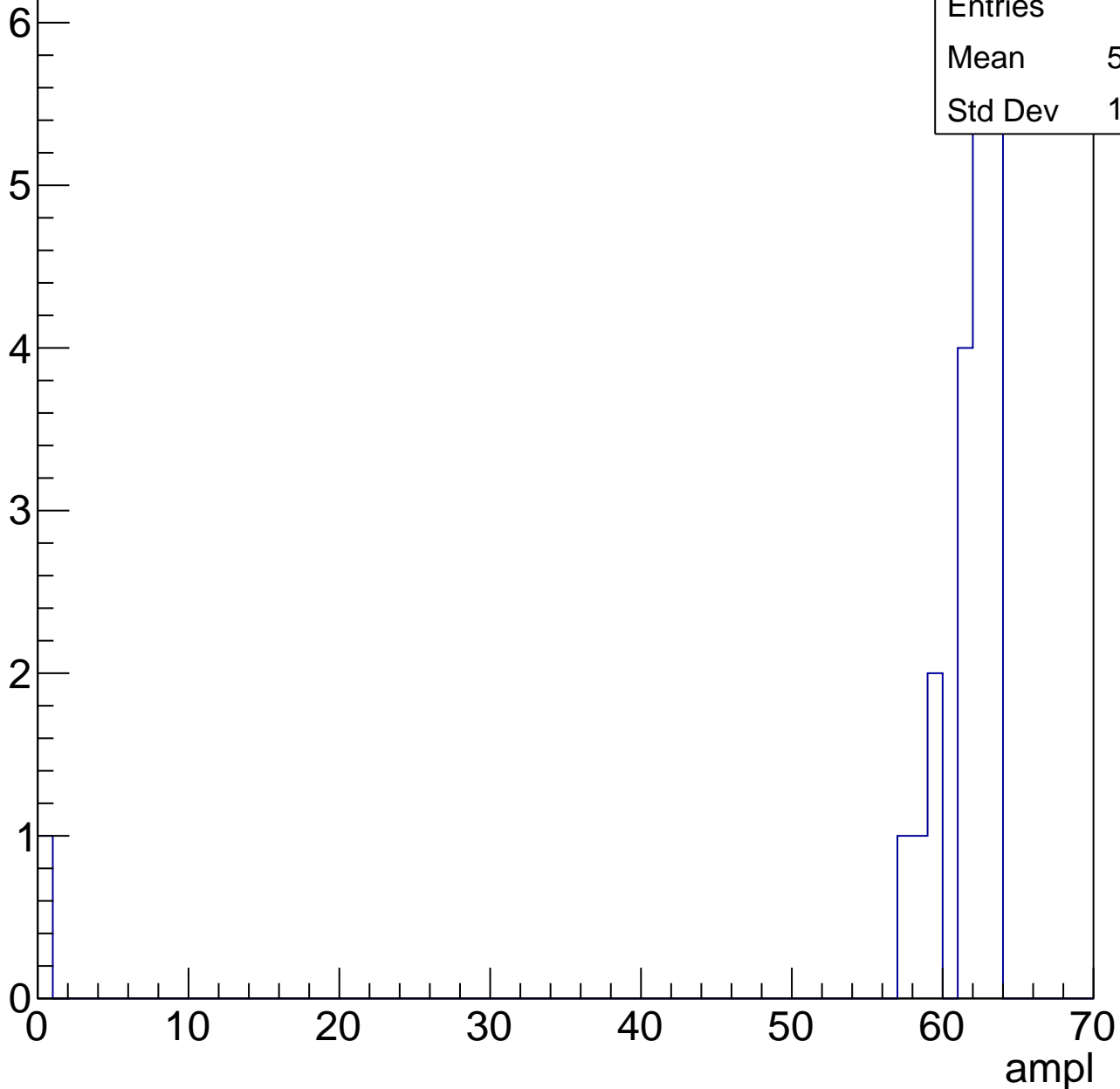


# B1L002S, U8-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.43
Std Dev	13.17





# B1L002S, U8-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch90, adc0

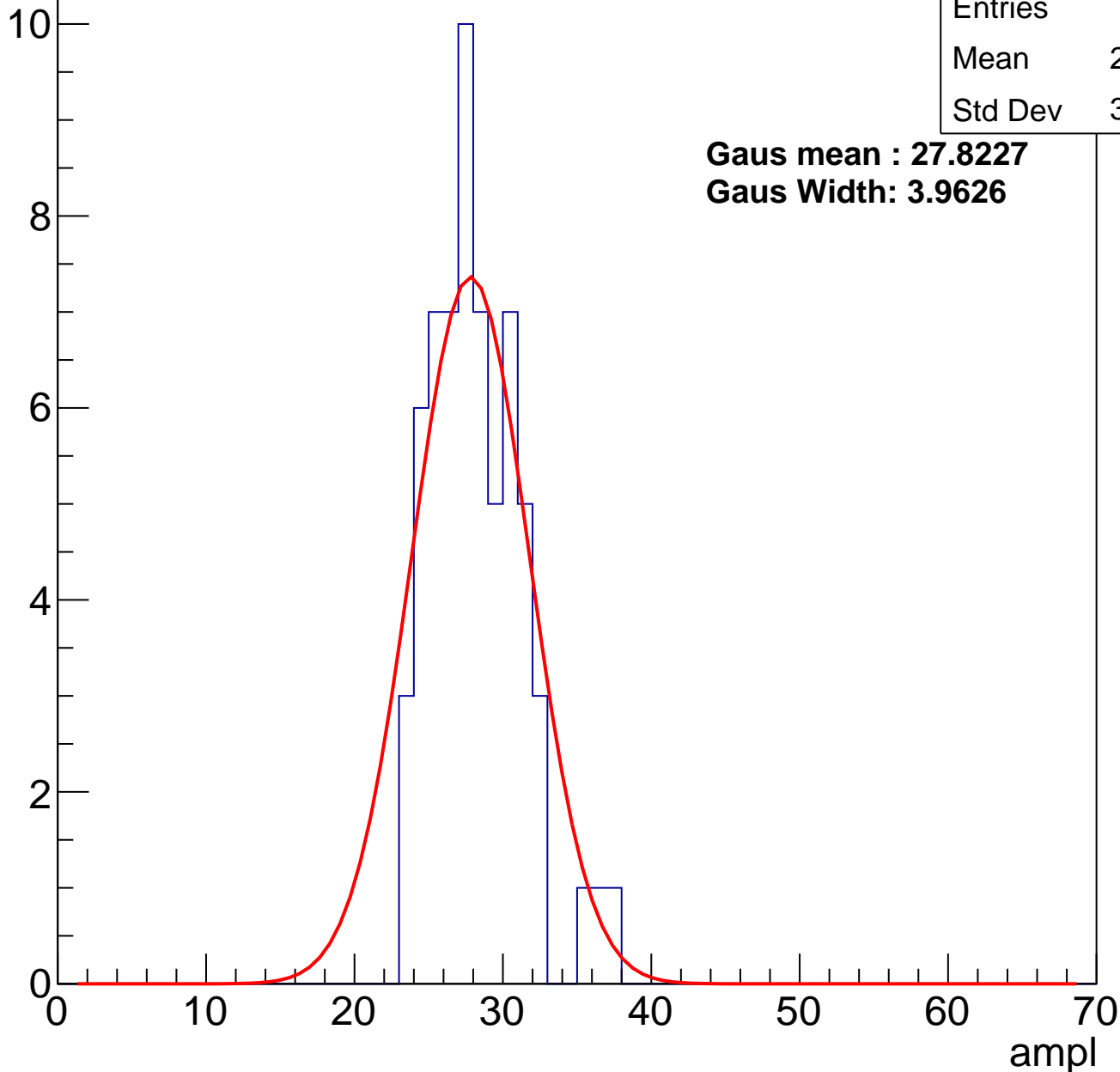
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	27.78
Std Dev	3.057

**Gaus mean : 27.8227**

**Gaus Width: 3.9626**

Entry



# B1L002S, U8-ch90, adc1

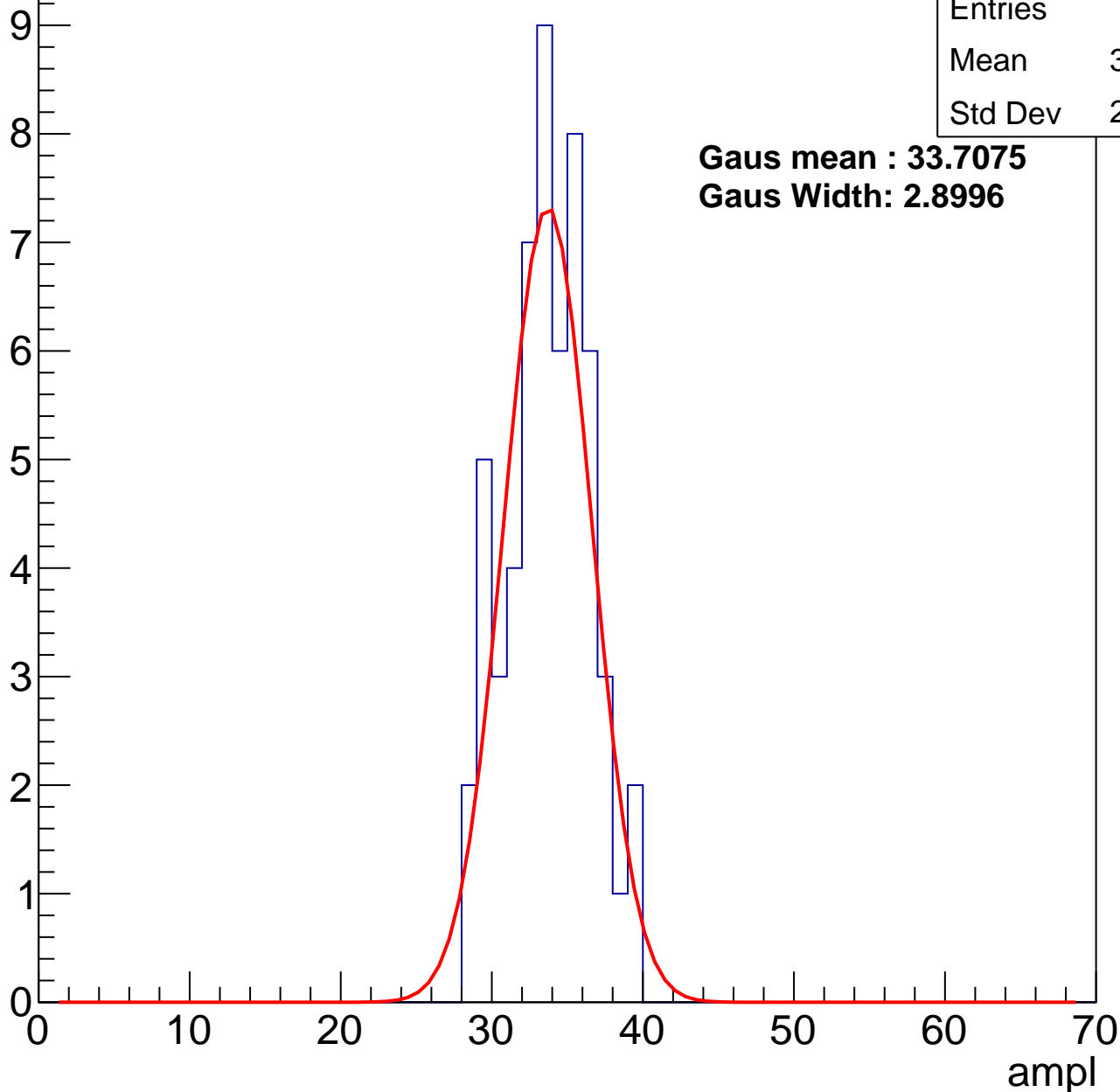
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.27
Std Dev	2.716

**Gaus mean : 33.7075**

**Gaus Width: 2.8996**



# B1L002S, U8-ch90, adc2

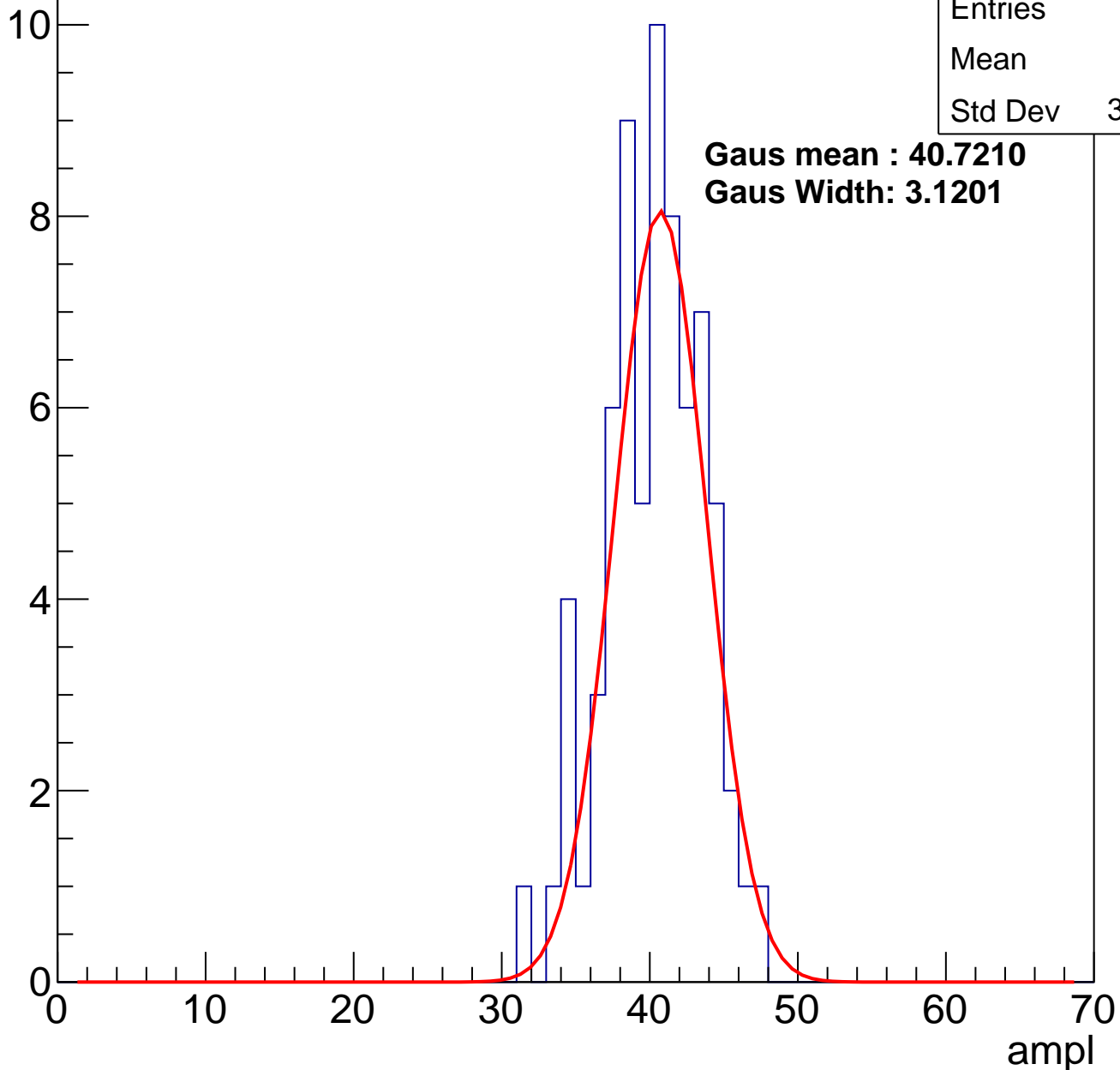
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	39.8
Std Dev	3.272

**Gaus mean : 40.7210**

**Gaus Width: 3.1201**

Entry

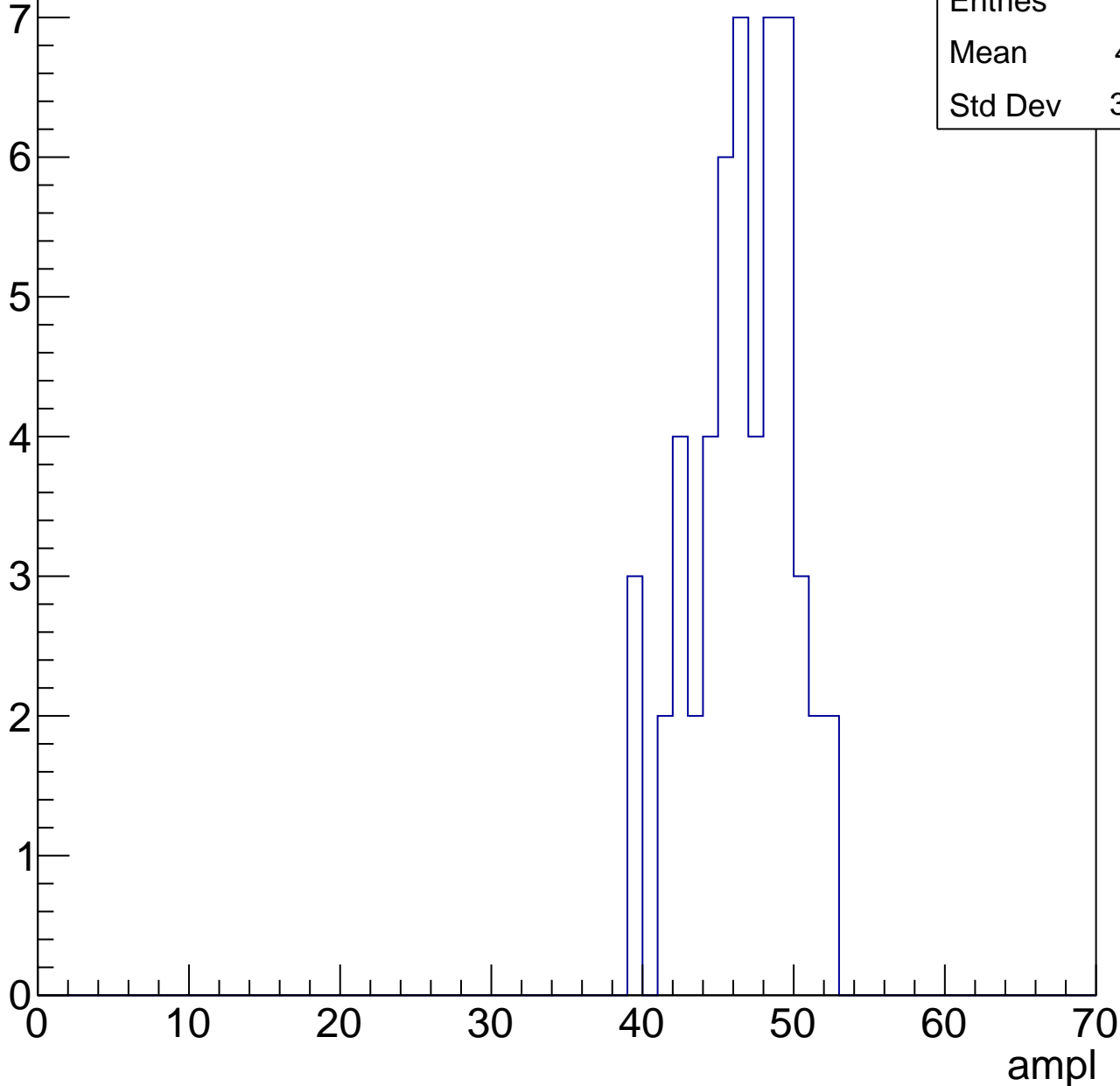


# B1L002S, U8-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.11
Std Dev	3.266

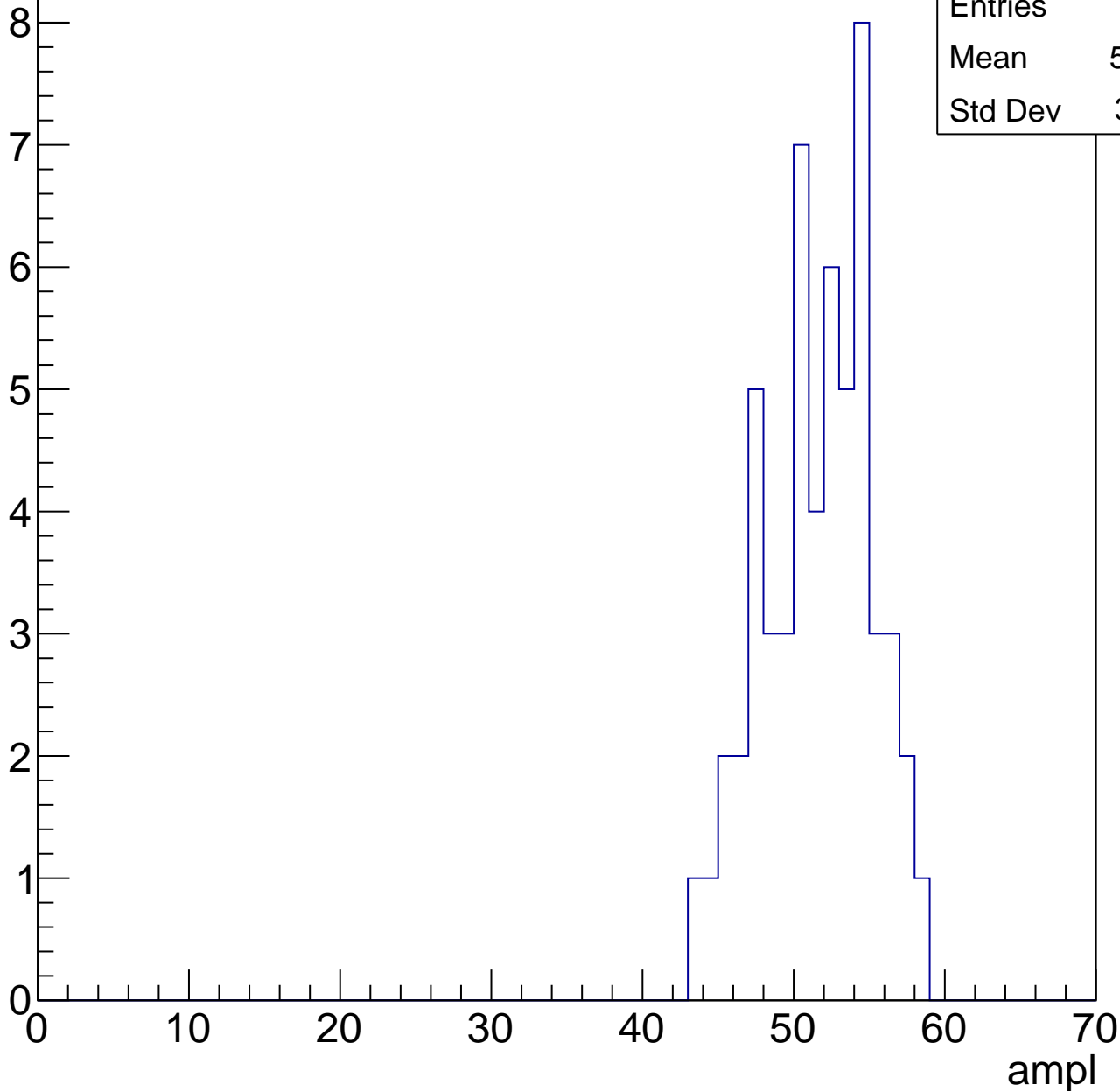


# B1L002S, U8-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

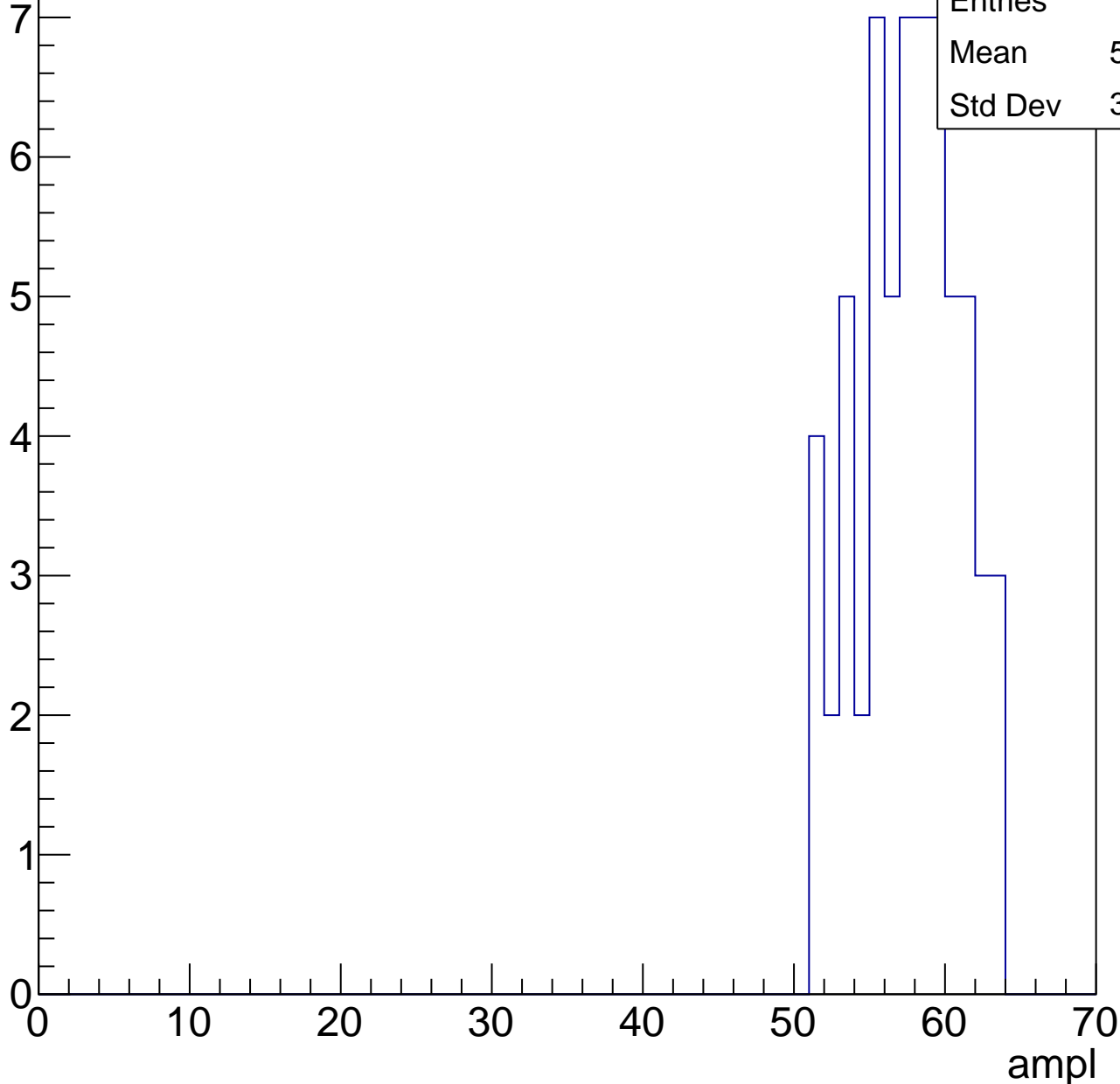
Entries	56
Mean	51.12
Std Dev	3.541



# B1L002S, U8-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

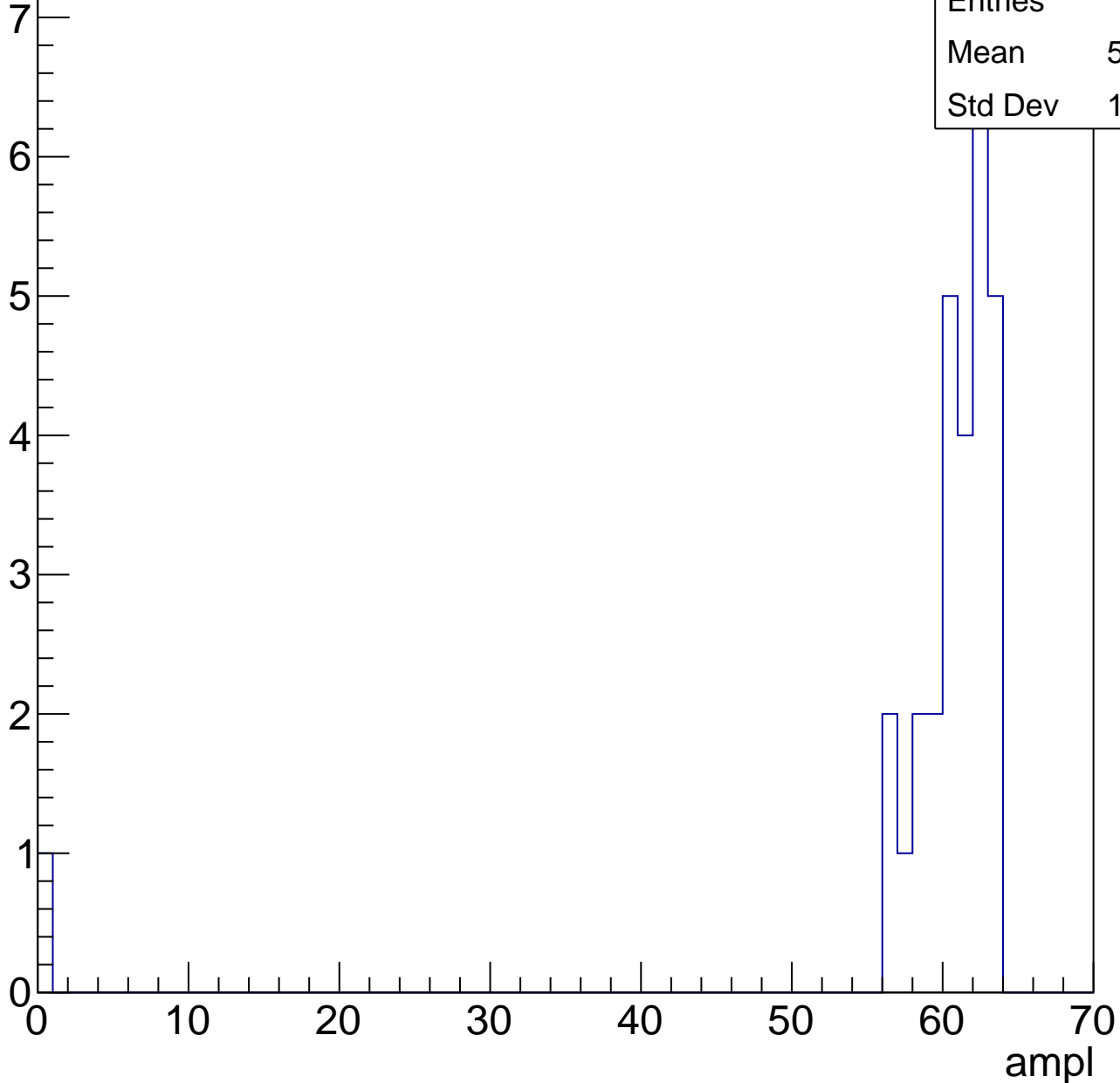


# B1L002S, U8-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.48
Std Dev	11.24

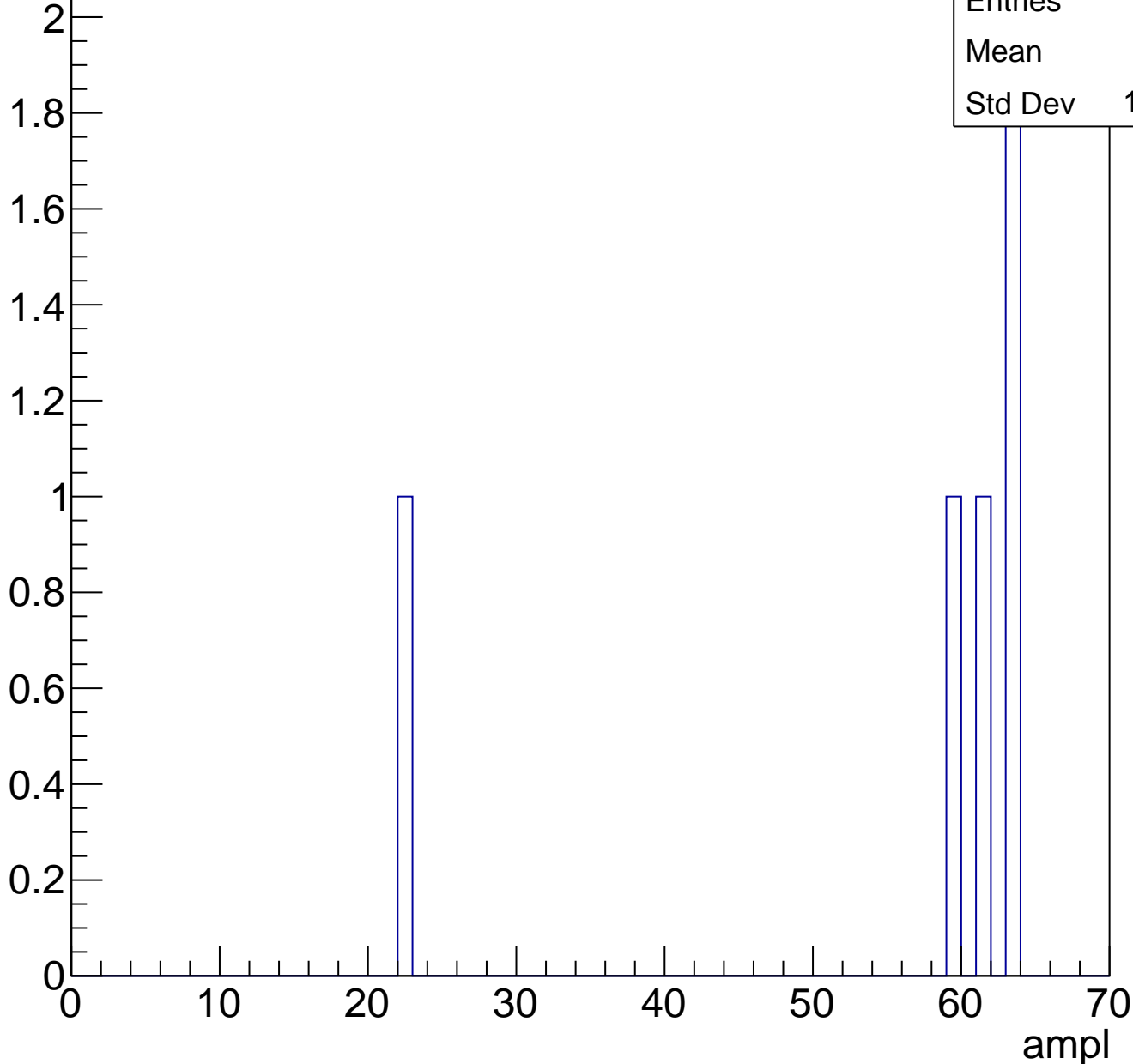




# B1L002S, U8-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	53.6
Std Dev	15.87

# B1L002S, U8-ch91, adc0

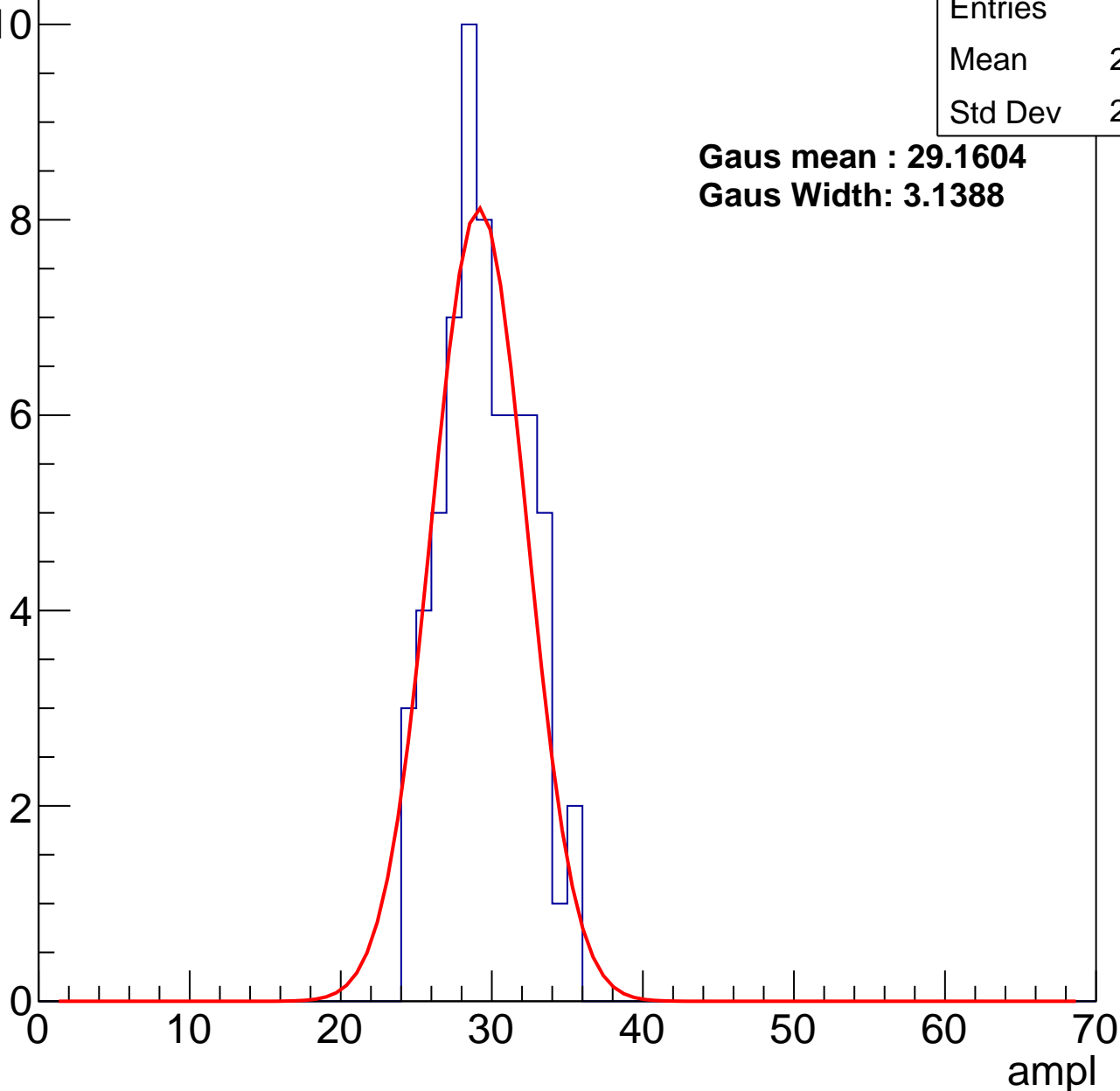
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	29.05
Std Dev	2.768

**Gaus mean : 29.1604**

**Gaus Width: 3.1388**



# B1L002S, U8-ch91, adc1

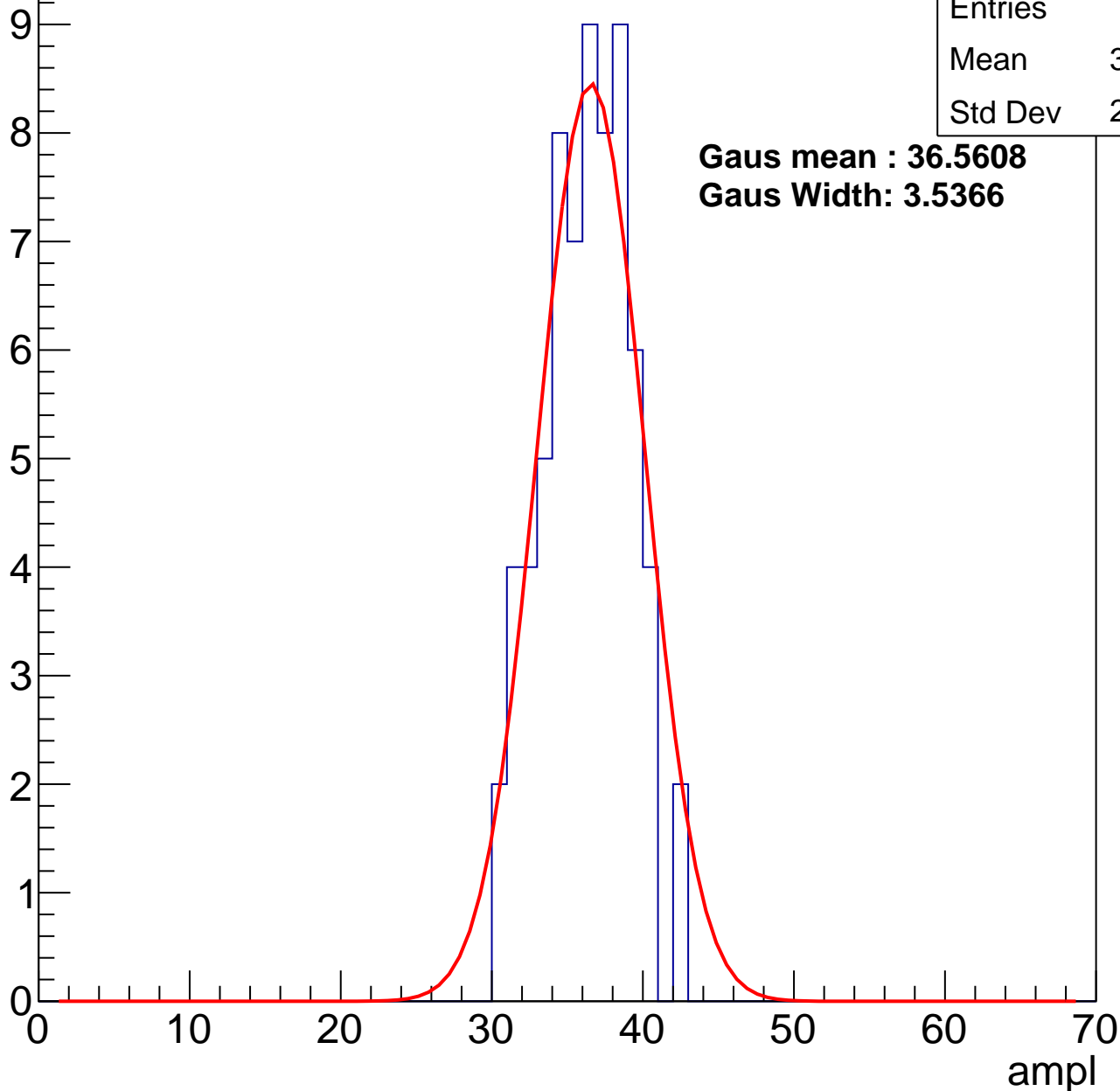
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.79
Std Dev	2.847

**Gaus mean : 36.5608**

**Gaus Width: 3.5366**



# B1L002S, U8-ch91, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	42.29
Std Dev	3.27

**Gaus mean : 42.9284**

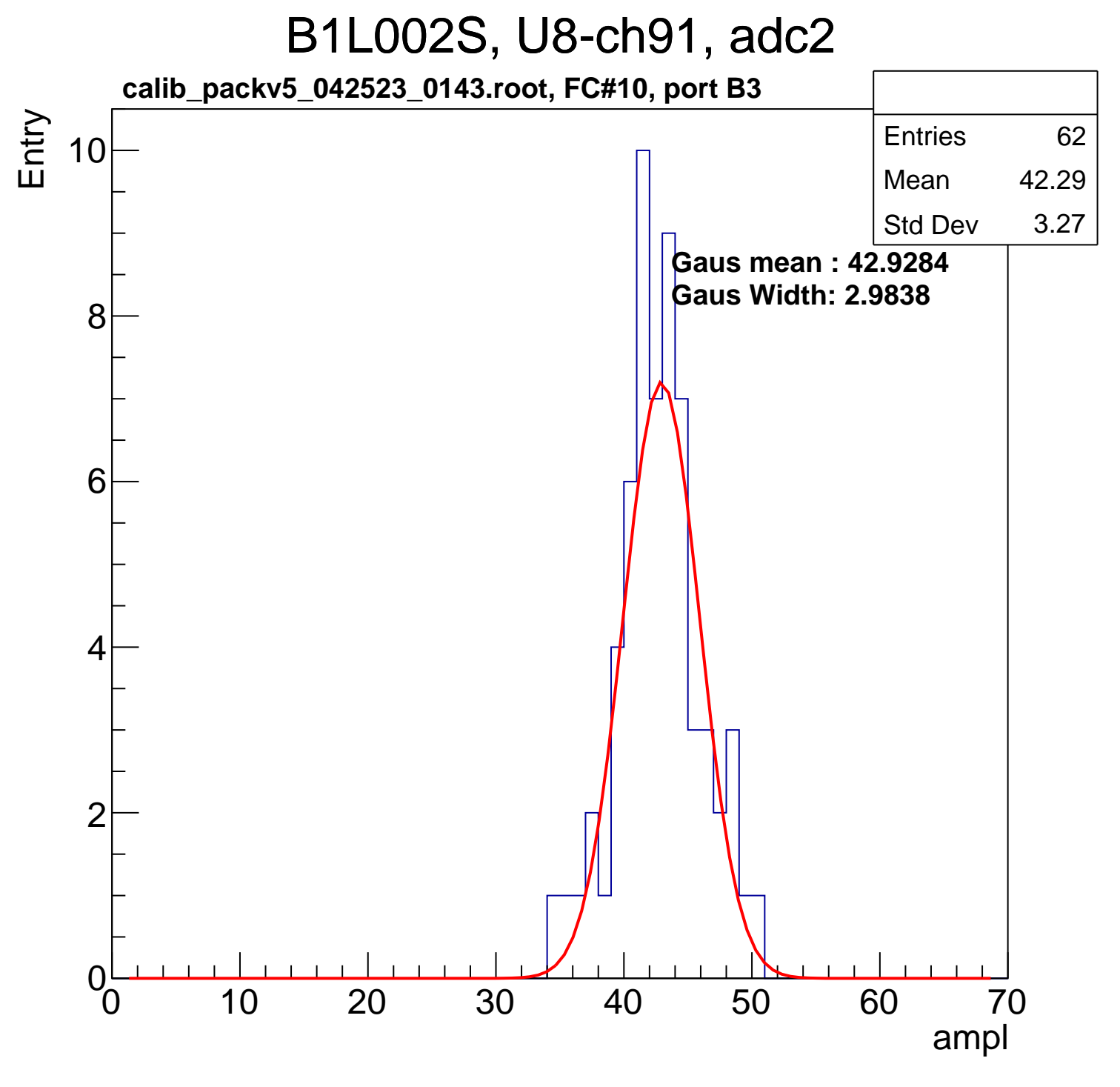
**Gaus Width: 2.9838**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

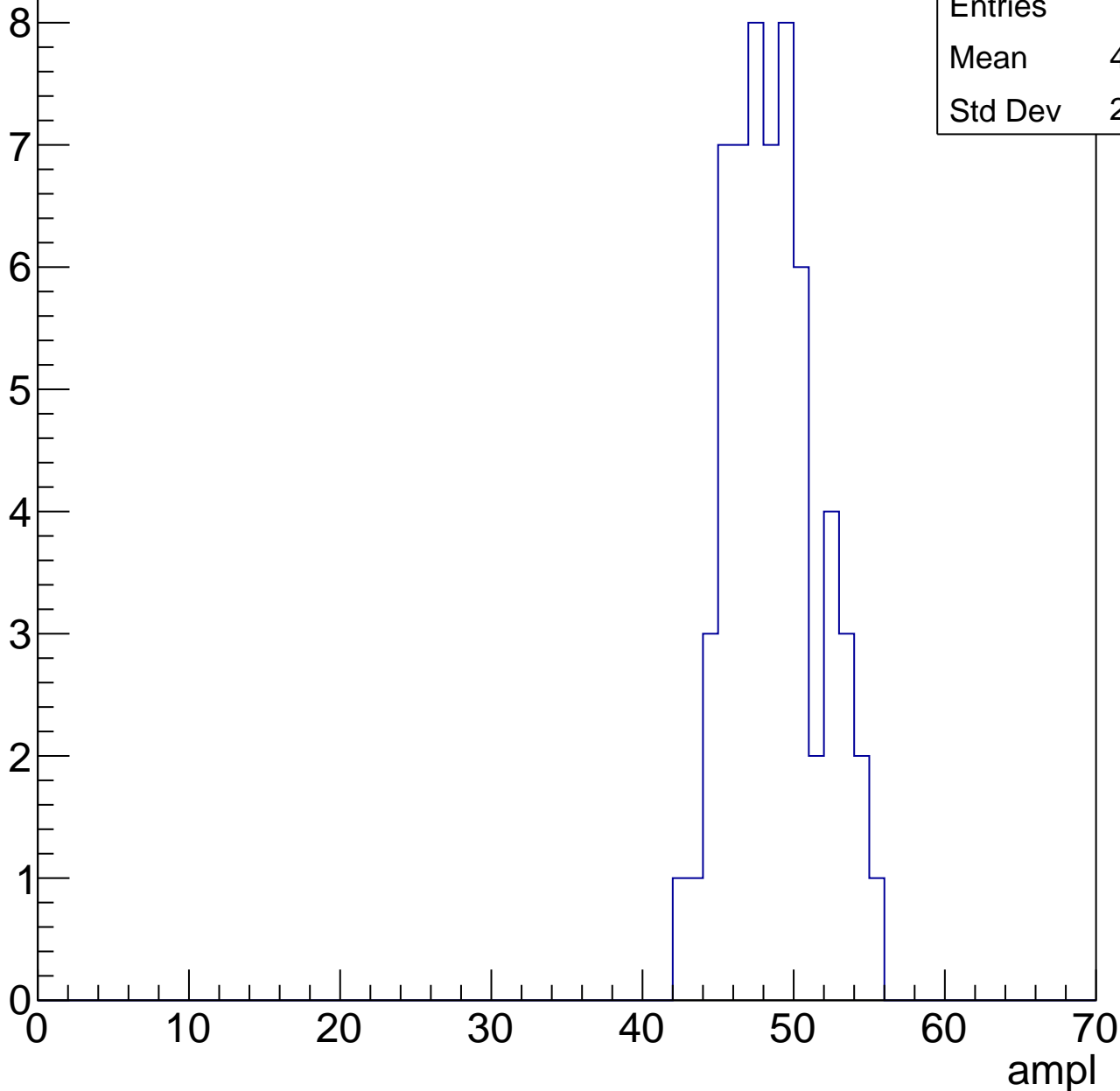


# B1L002S, U8-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

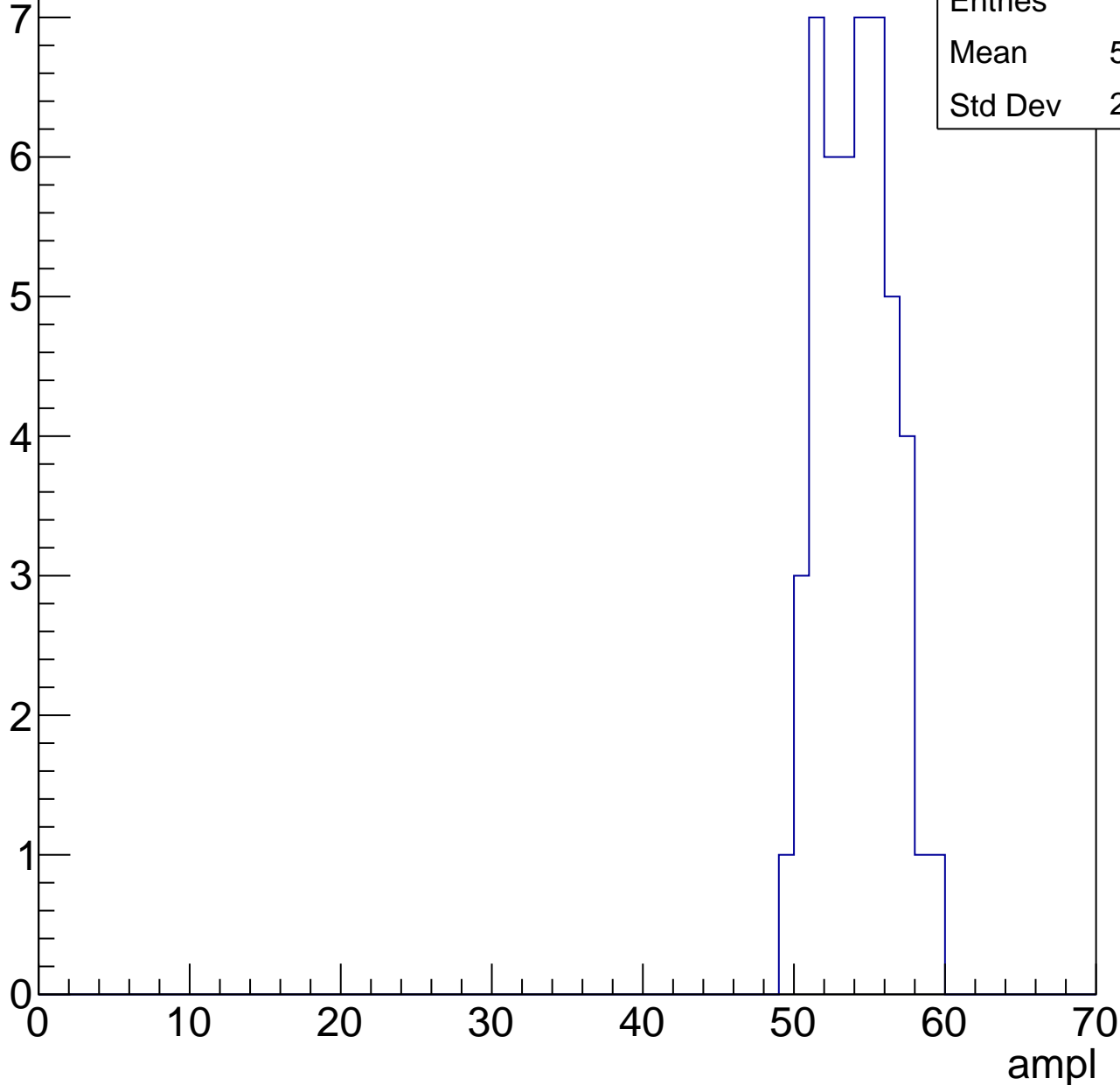
Entries	60
Mean	48.17
Std Dev	2.934



# B1L002S, U8-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

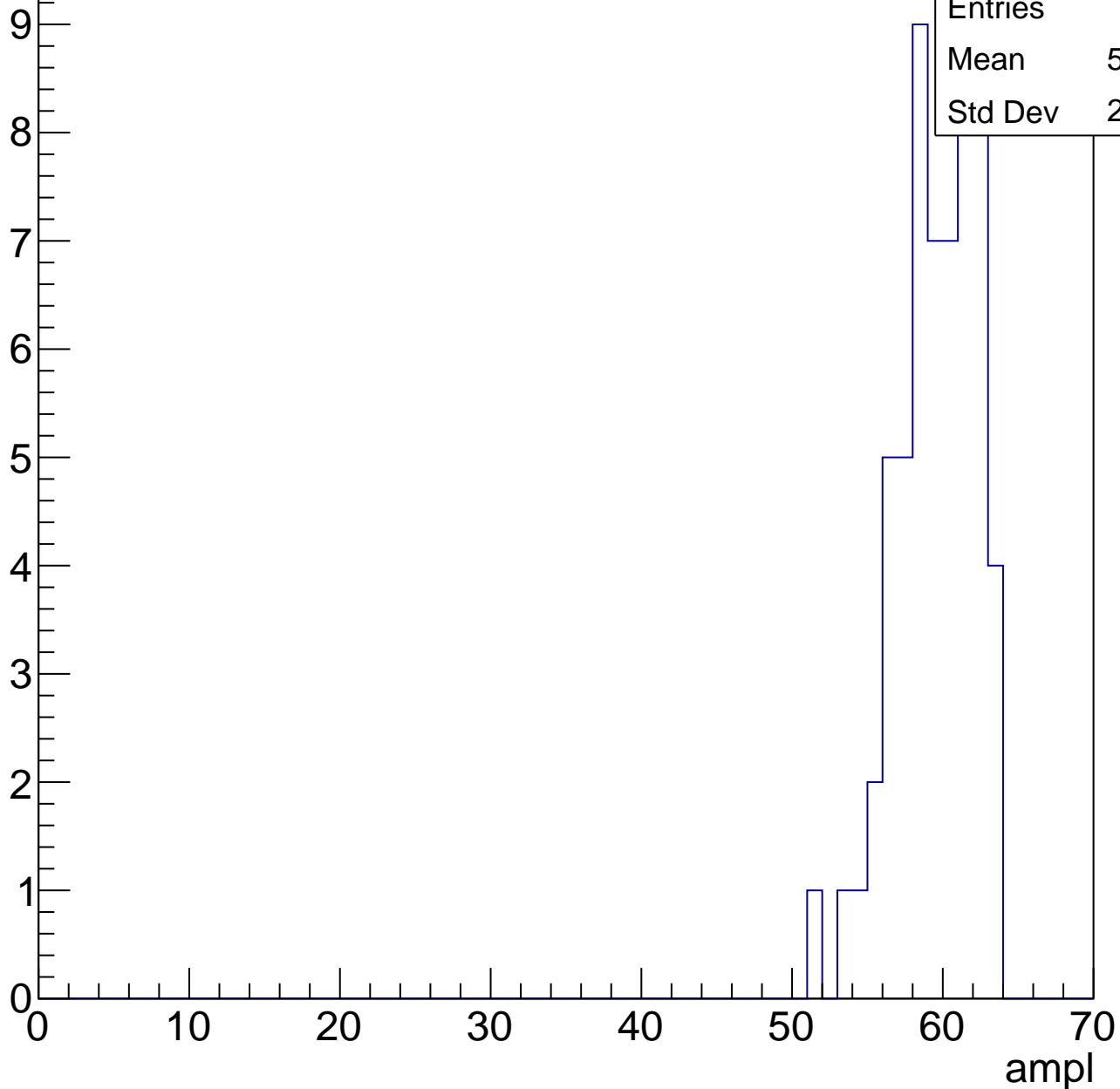


Entries	48
Mean	53.62
Std Dev	2.342

# B1L002S, U8-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



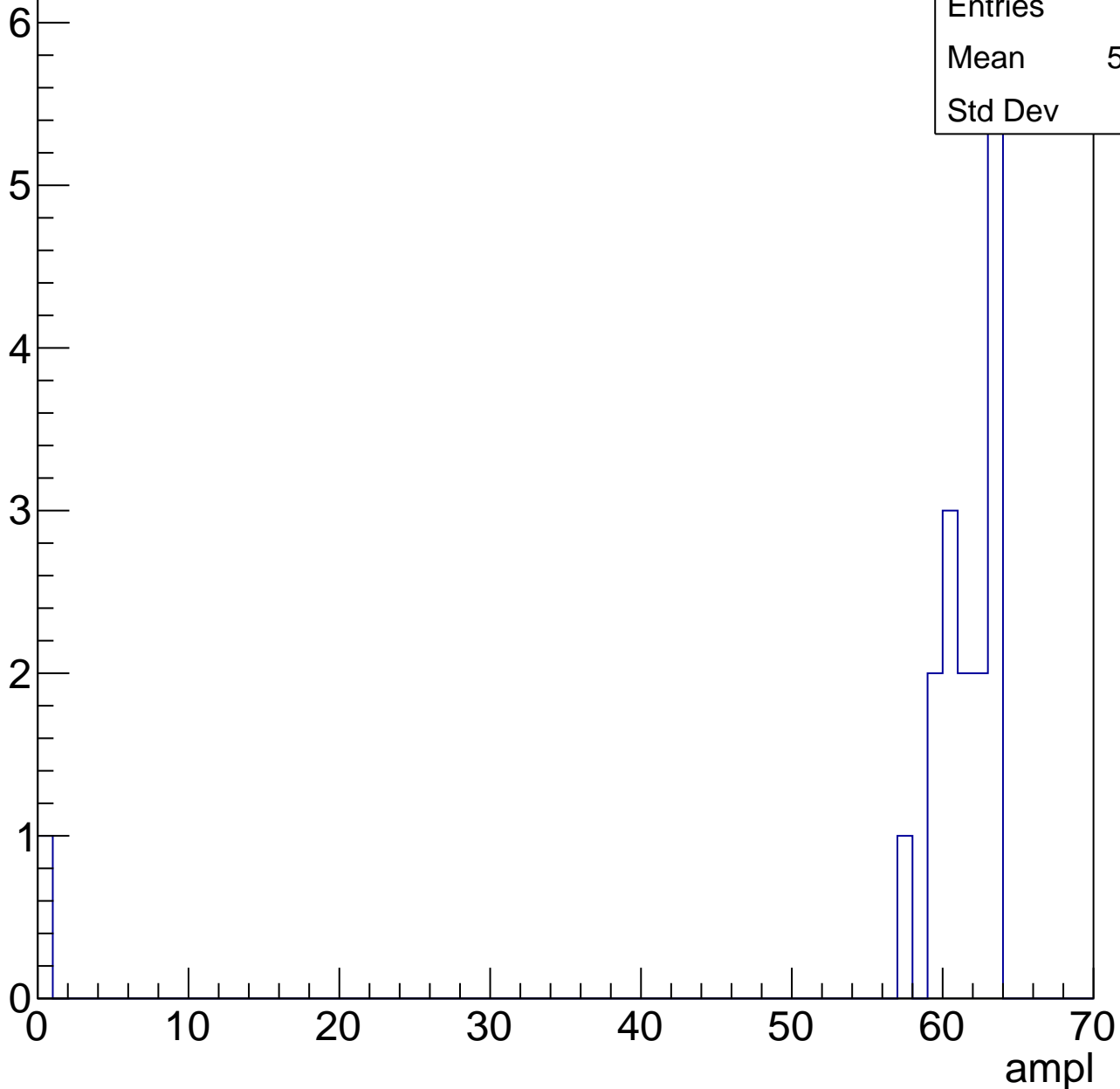
Entries	58
Mean	59.03
Std Dev	2.646

# B1L002S, U8-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	57.59
Std Dev	14.5

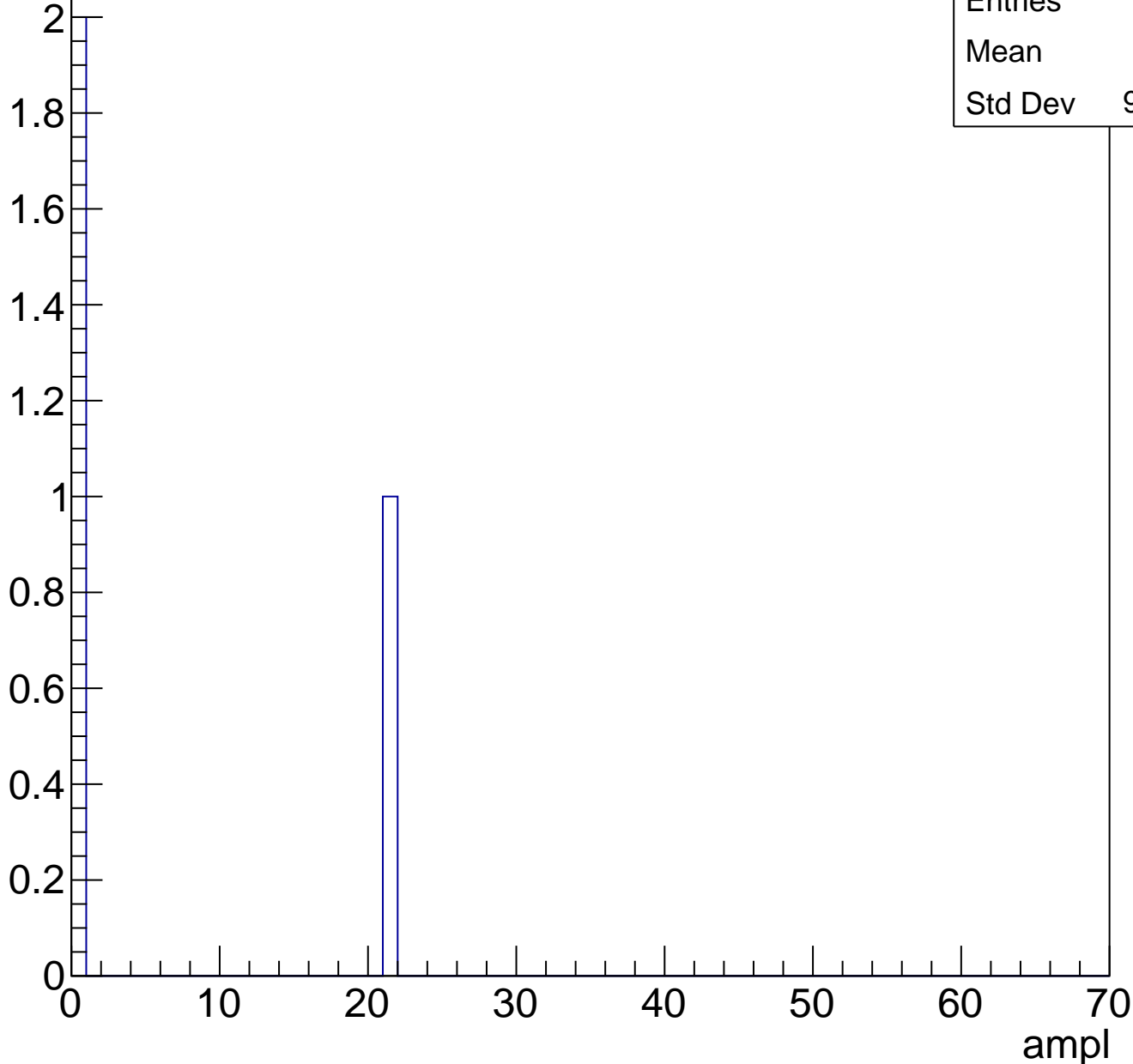




# B1L002S, U8-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L002S, U8-ch92, adc0

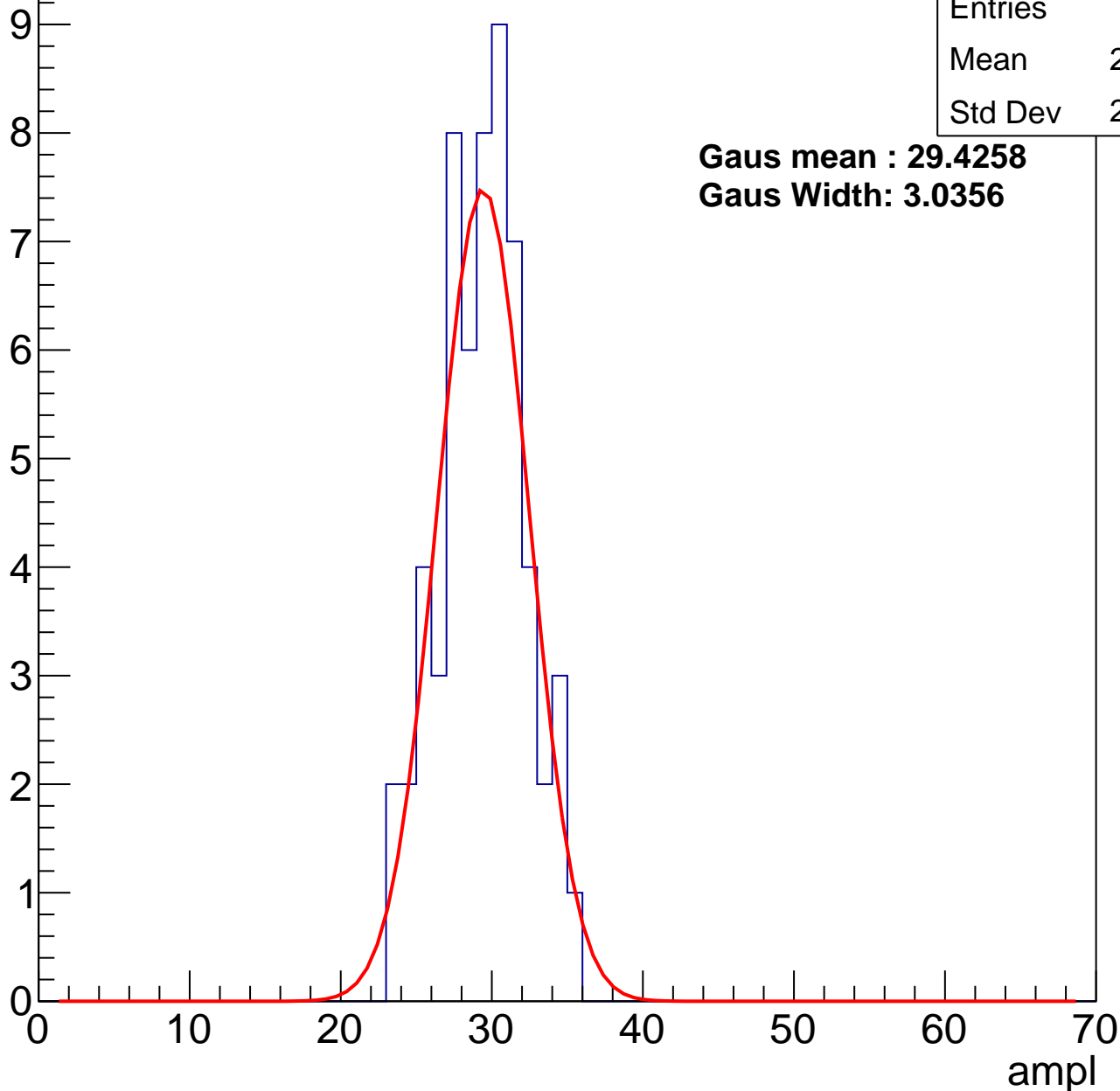
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.92
Std Dev	2.812

**Gaus mean : 29.4258**

**Gaus Width: 3.0356**



# B1L002S, U8-ch92, adc1

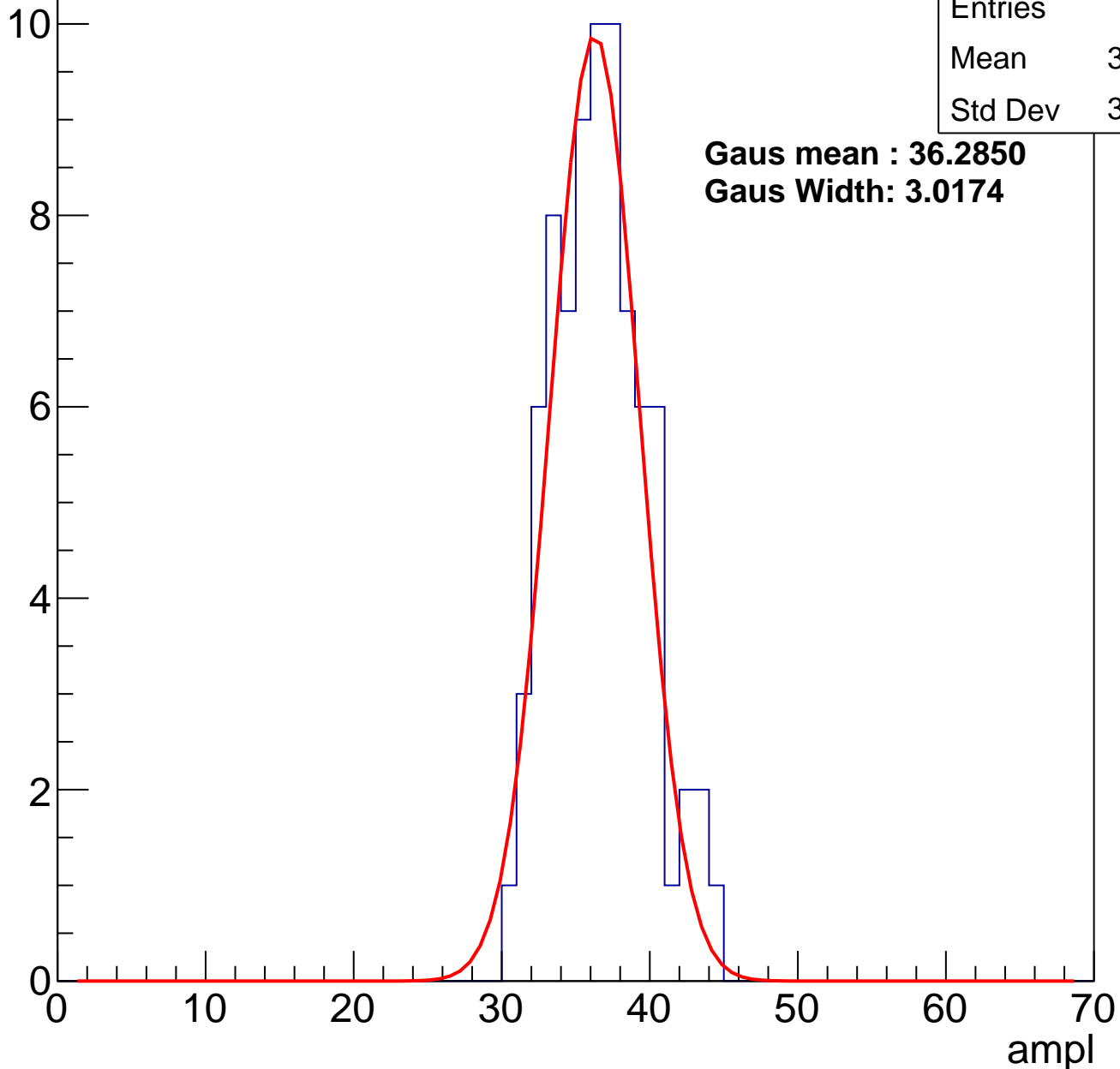
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	36.16
Std Dev	3.103

**Gaus mean : 36.2850**

**Gaus Width: 3.0174**

Entry



# B1L002S, U8-ch92, adc2

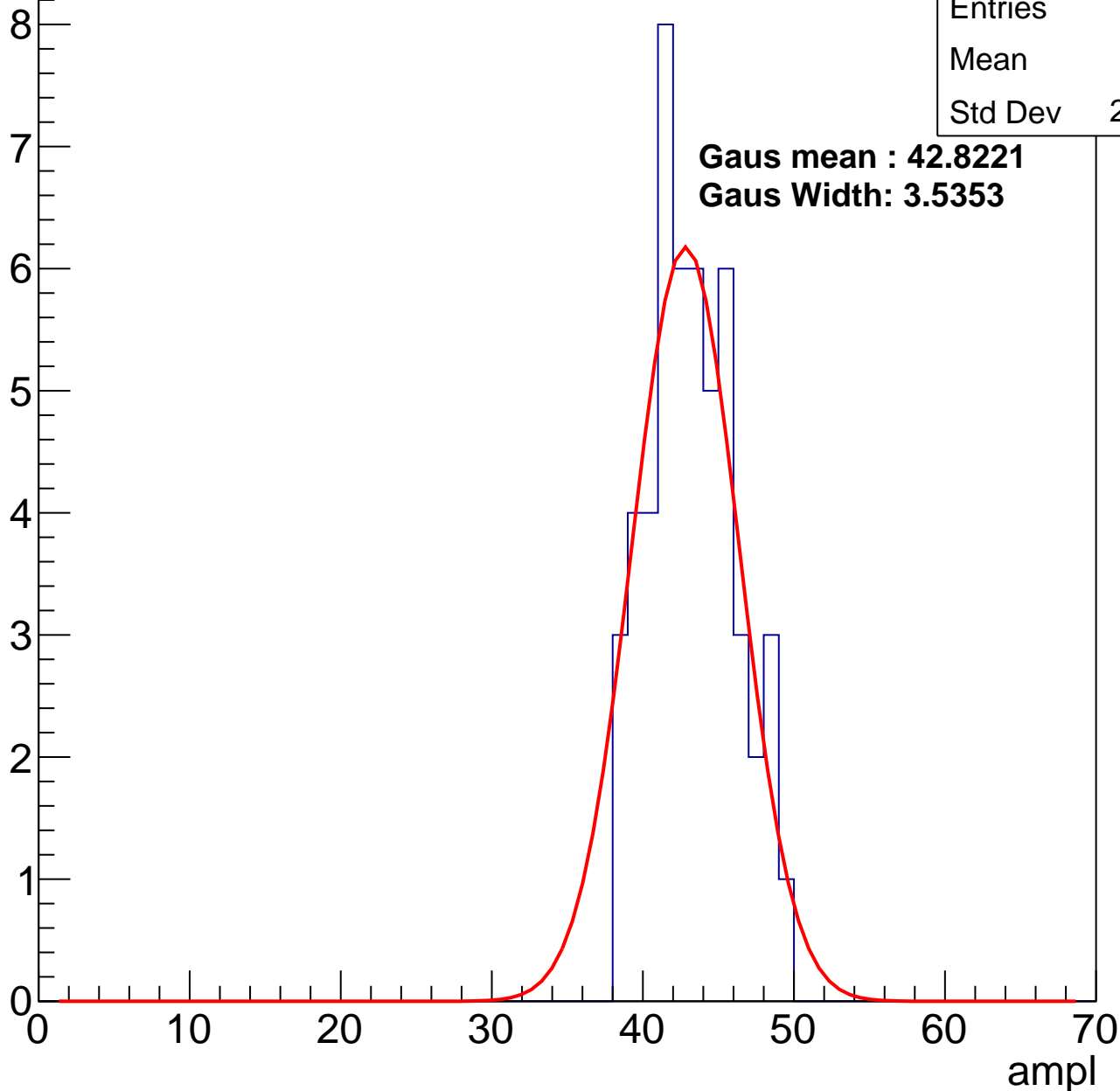
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	42.8
Std Dev	2.835

**Gaus mean : 42.8221**

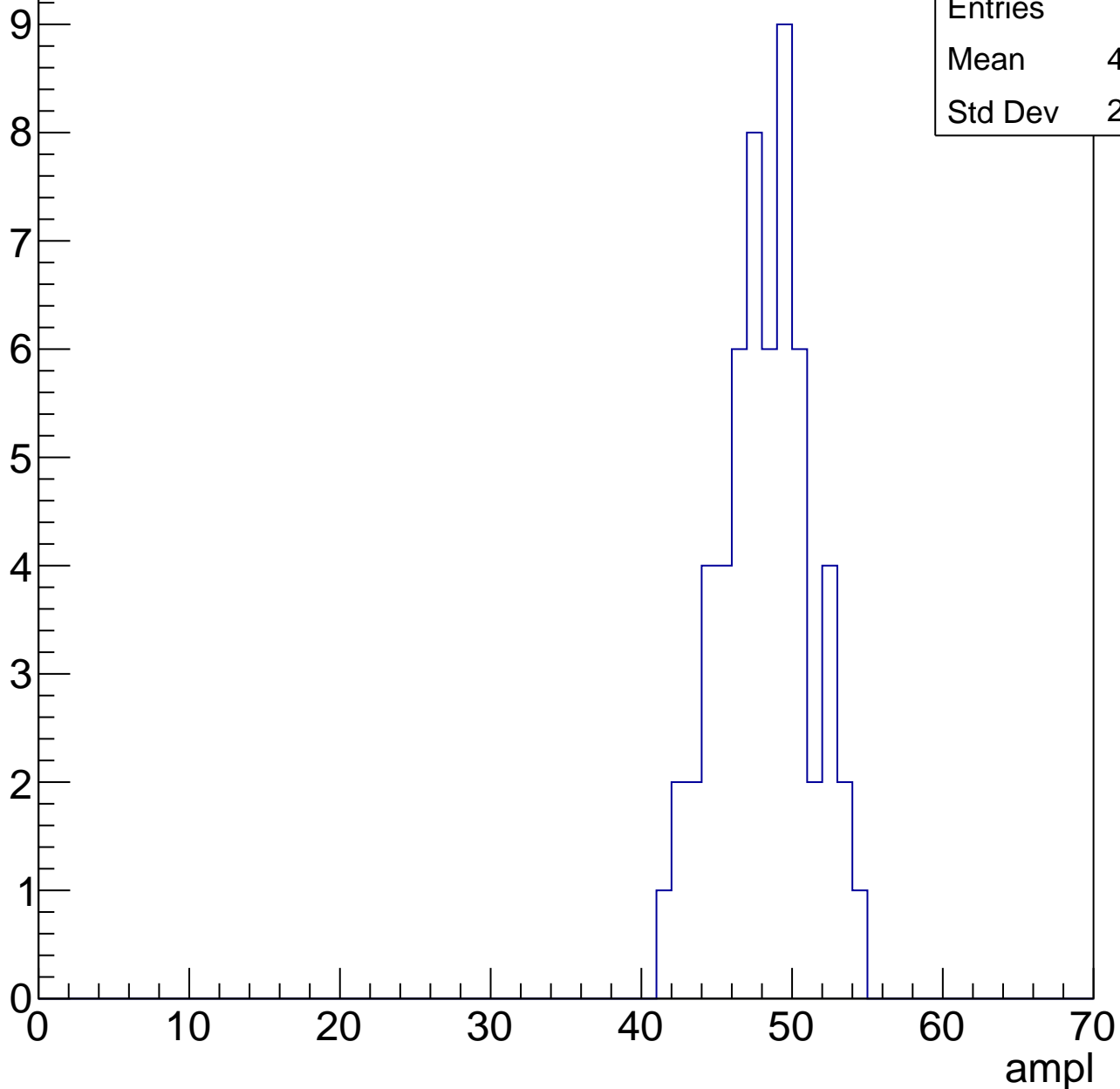
**Gaus Width: 3.5353**



# B1L002S, U8-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

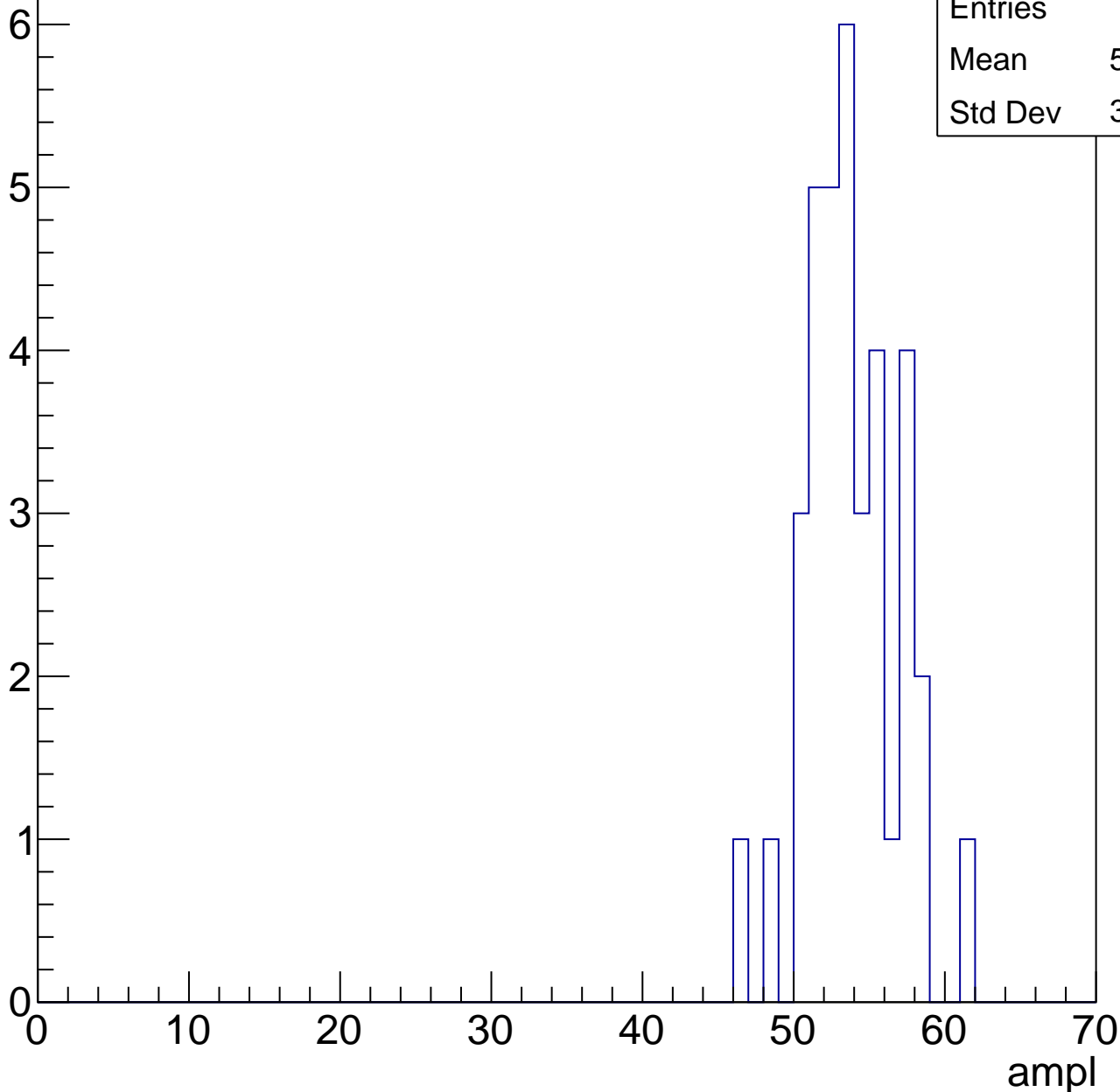


# B1L002S, U8-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	53.33
Std Dev	3.018

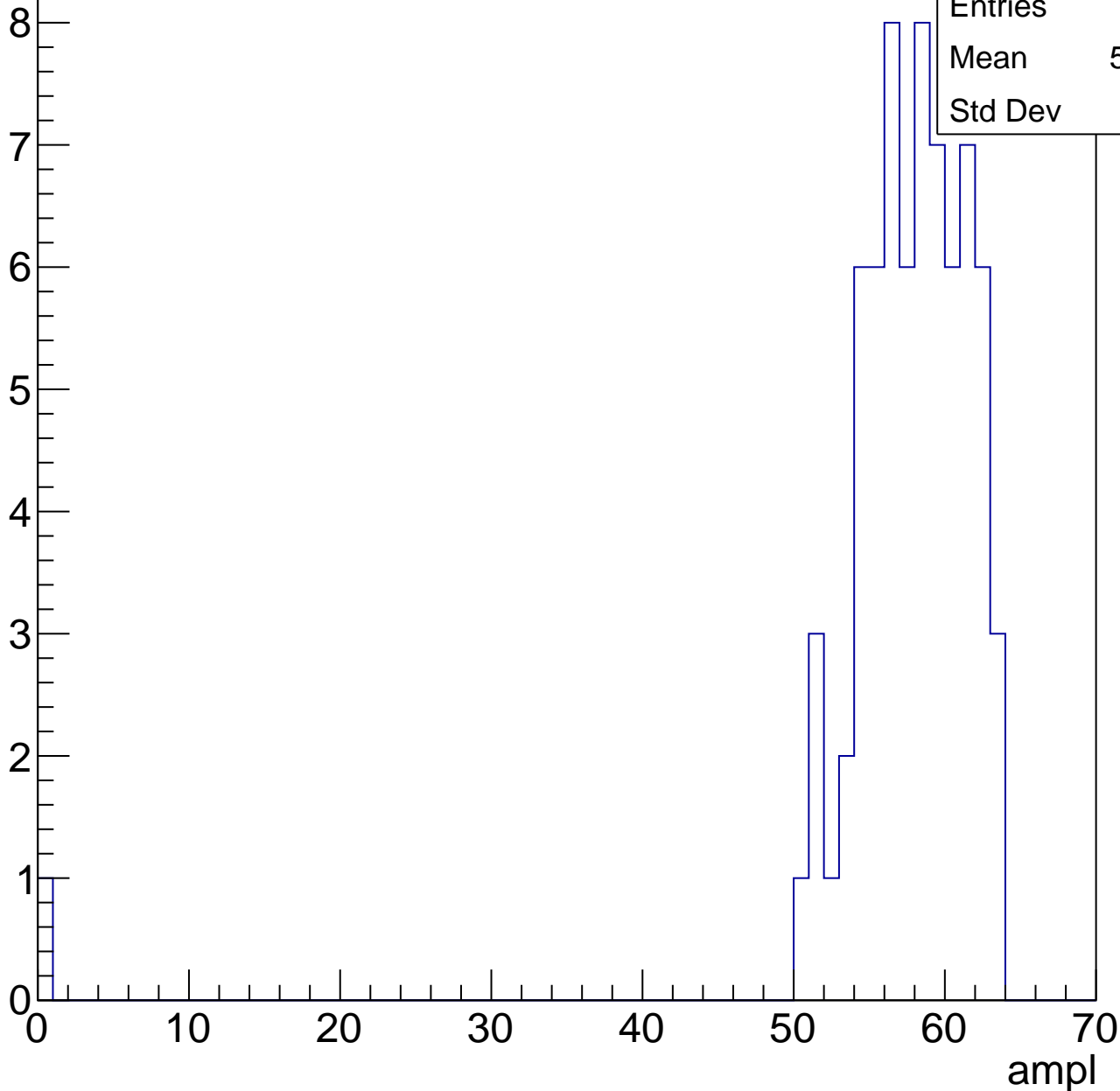


# B1L002S, U8-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	56.76
Std Dev	7.51

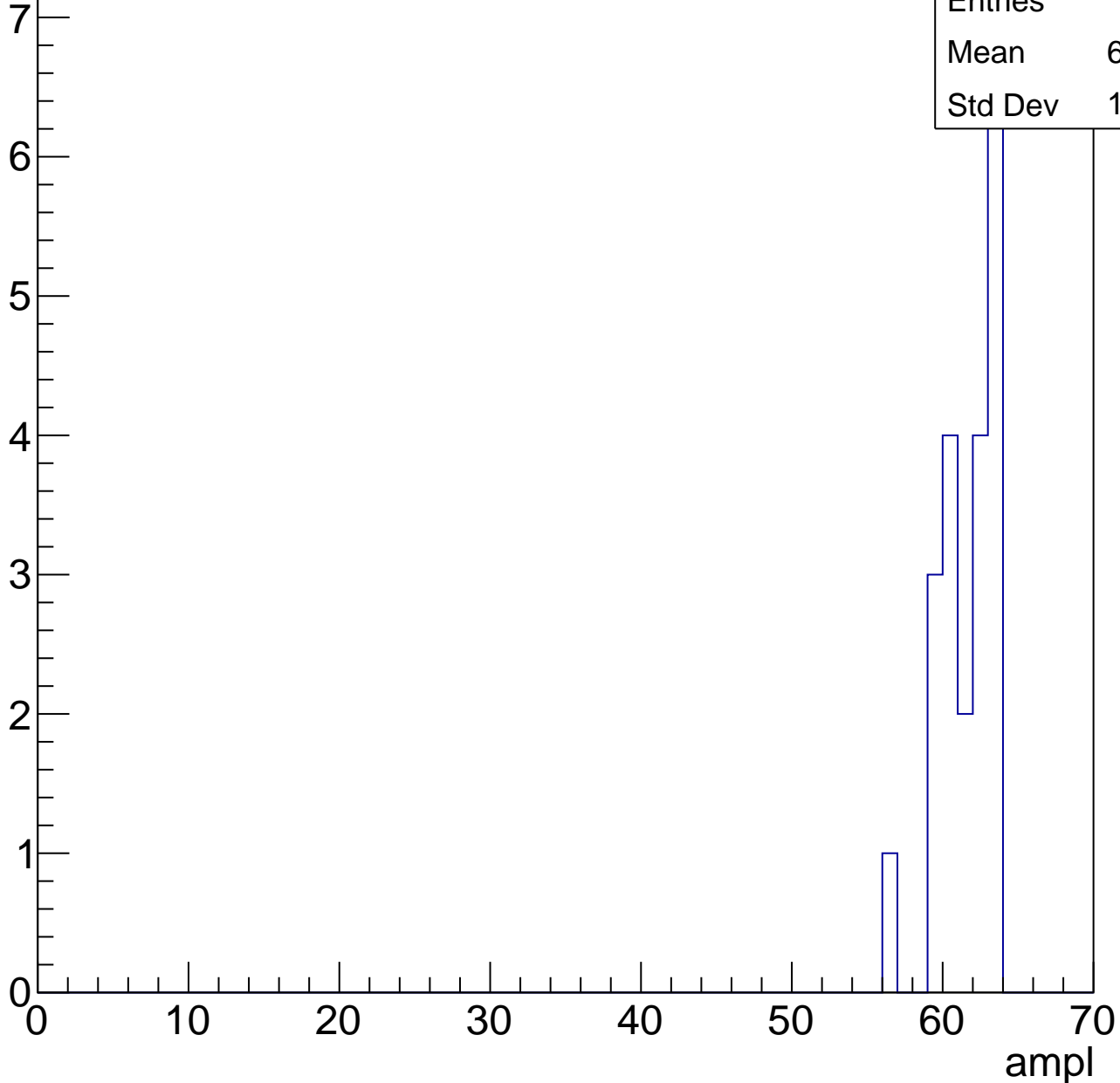


# B1L002S, U8-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.14
Std Dev	1.859





# B1L002S, U8-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch93, adc0

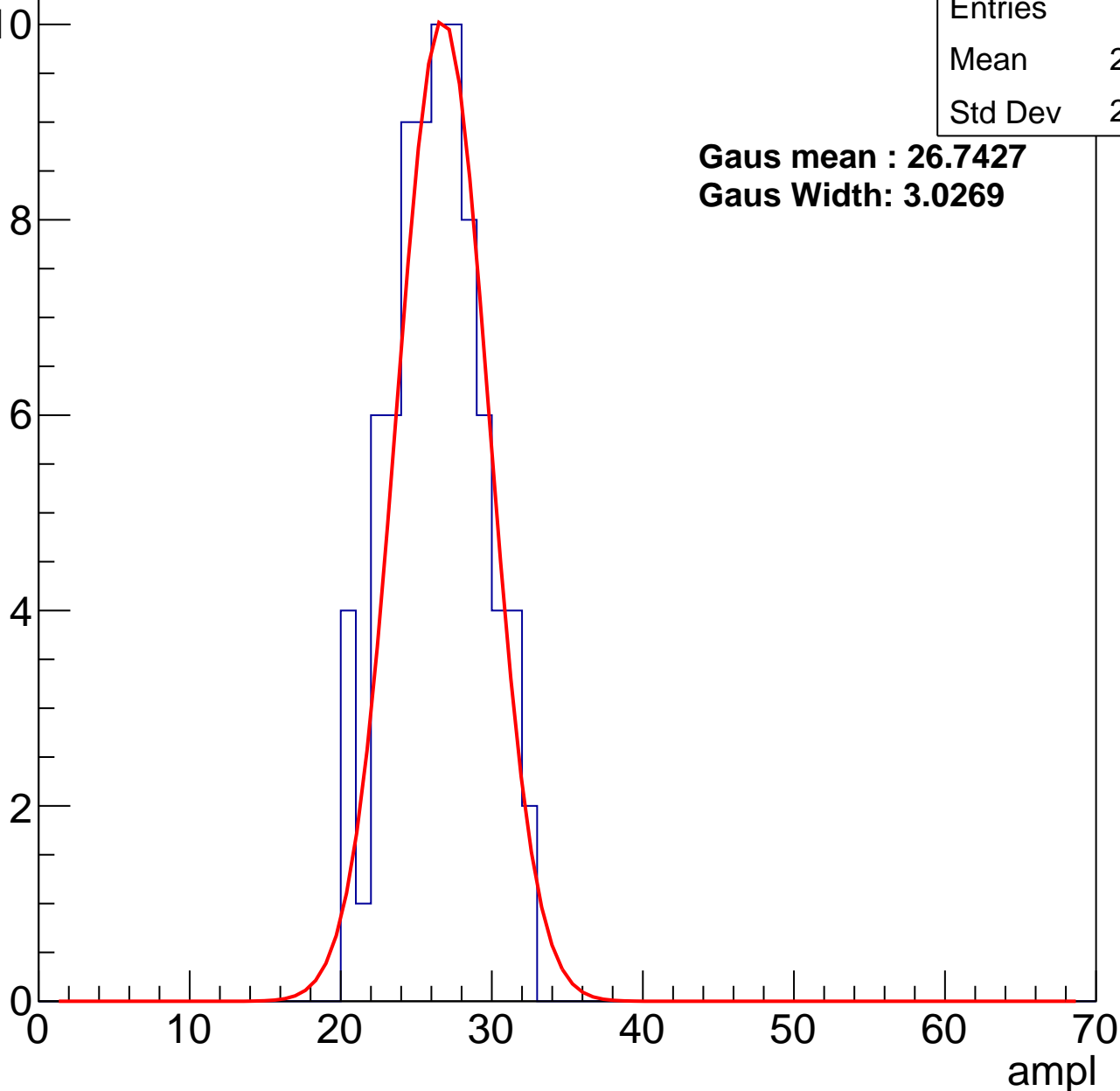
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	25.92
Std Dev	2.967

**Gaus mean : 26.7427**

**Gaus Width: 3.0269**



# B1L002S, U8-ch93, adc1

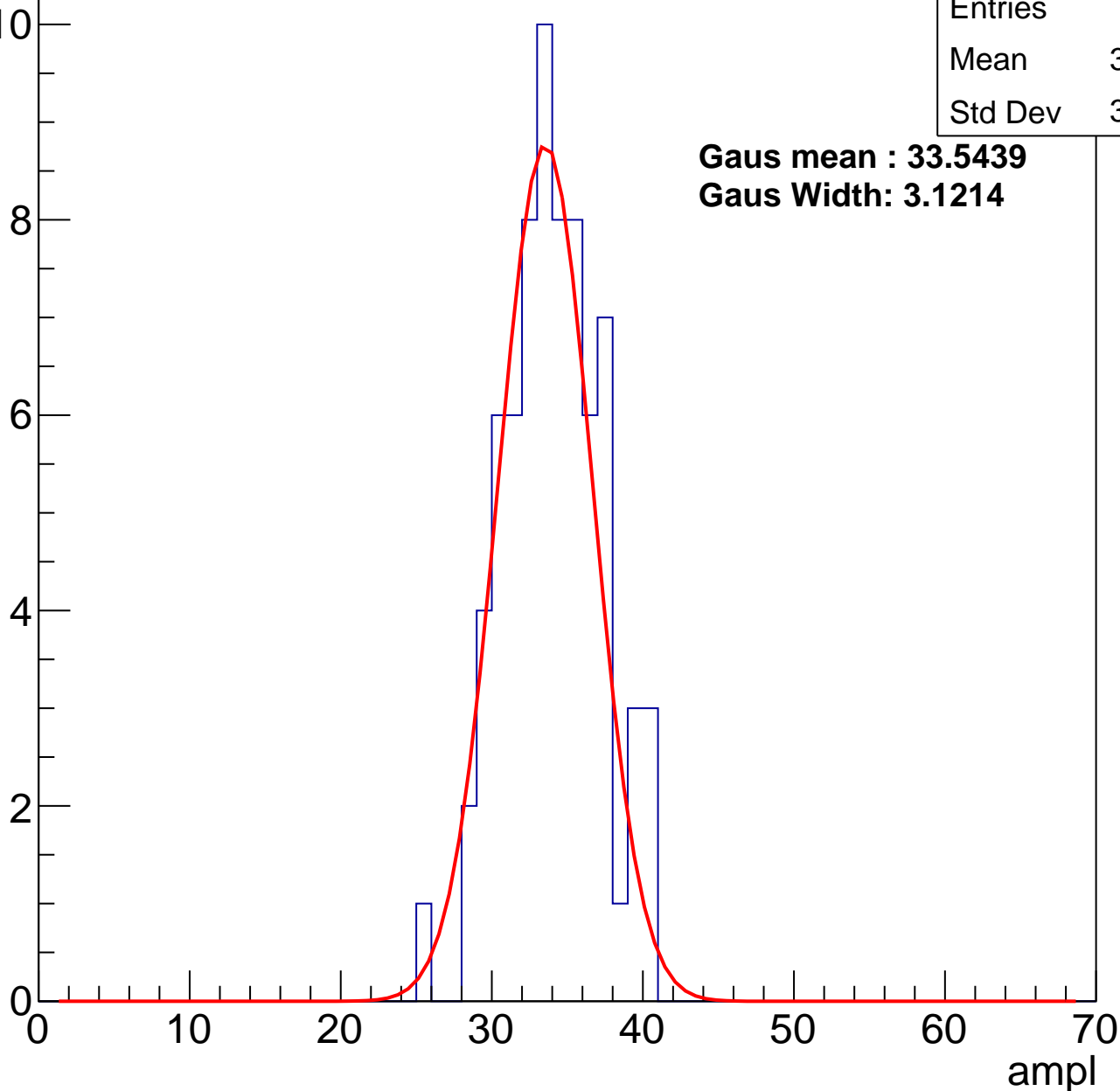
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	33.58
Std Dev	3.153

**Gaus mean : 33.5439**

**Gaus Width: 3.1214**



# B1L002S, U8-ch93, adc2

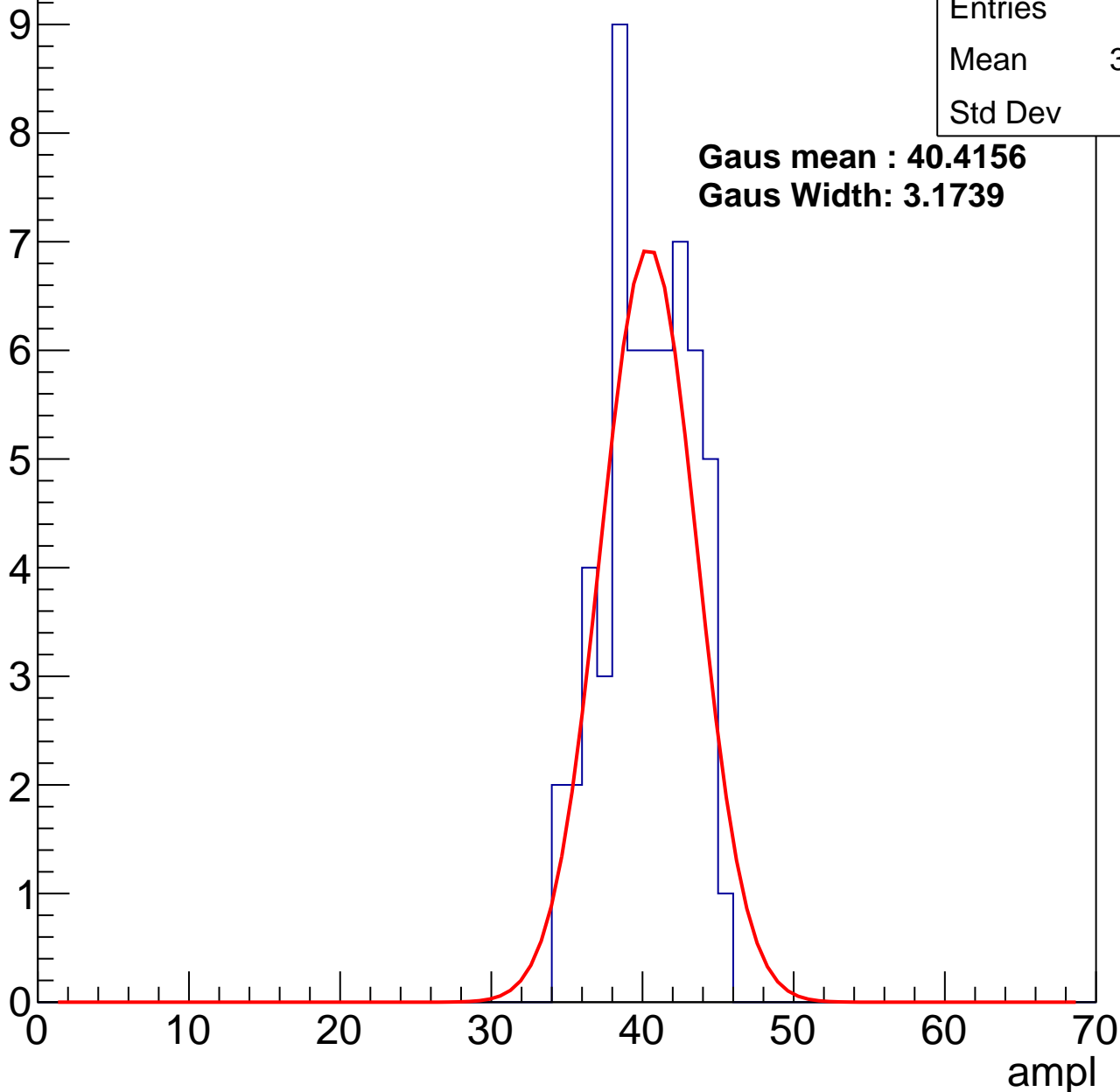
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.86
Std Dev	2.8

**Gaus mean : 40.4156**

**Gaus Width: 3.1739**

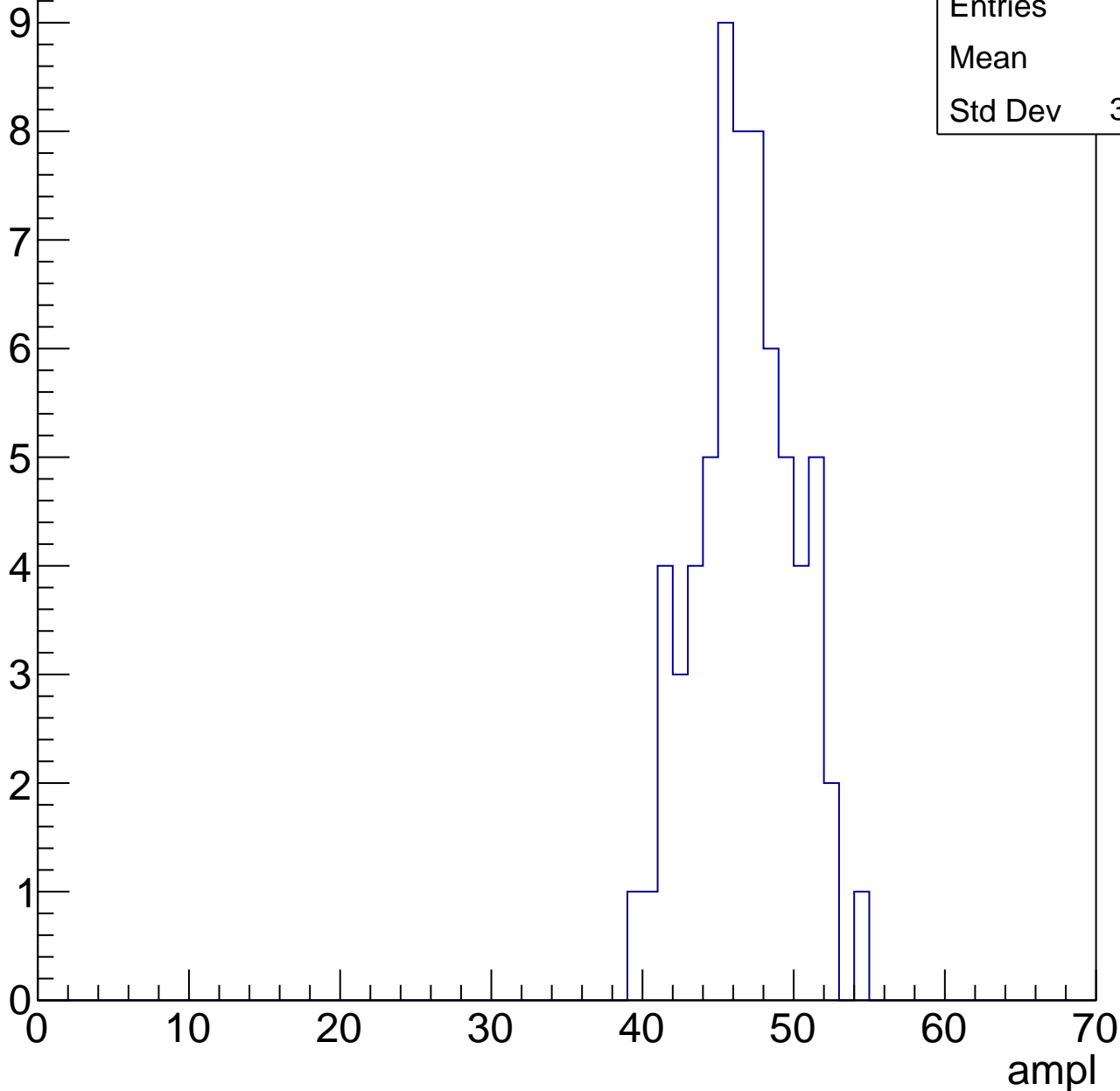


# B1L002S, U8-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.3
Std Dev	3.243

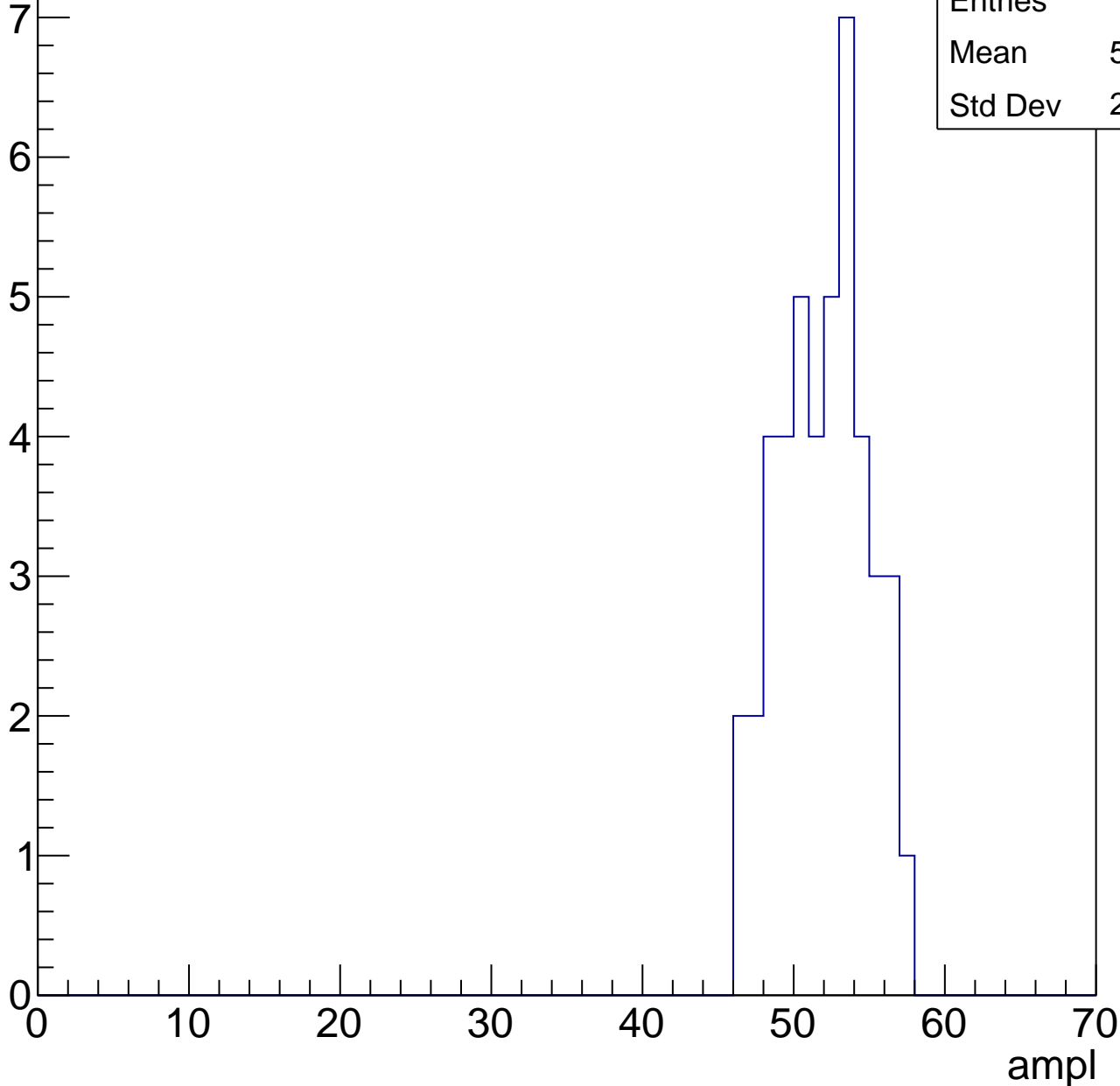


# B1L002S, U8-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.48
Std Dev	2.848

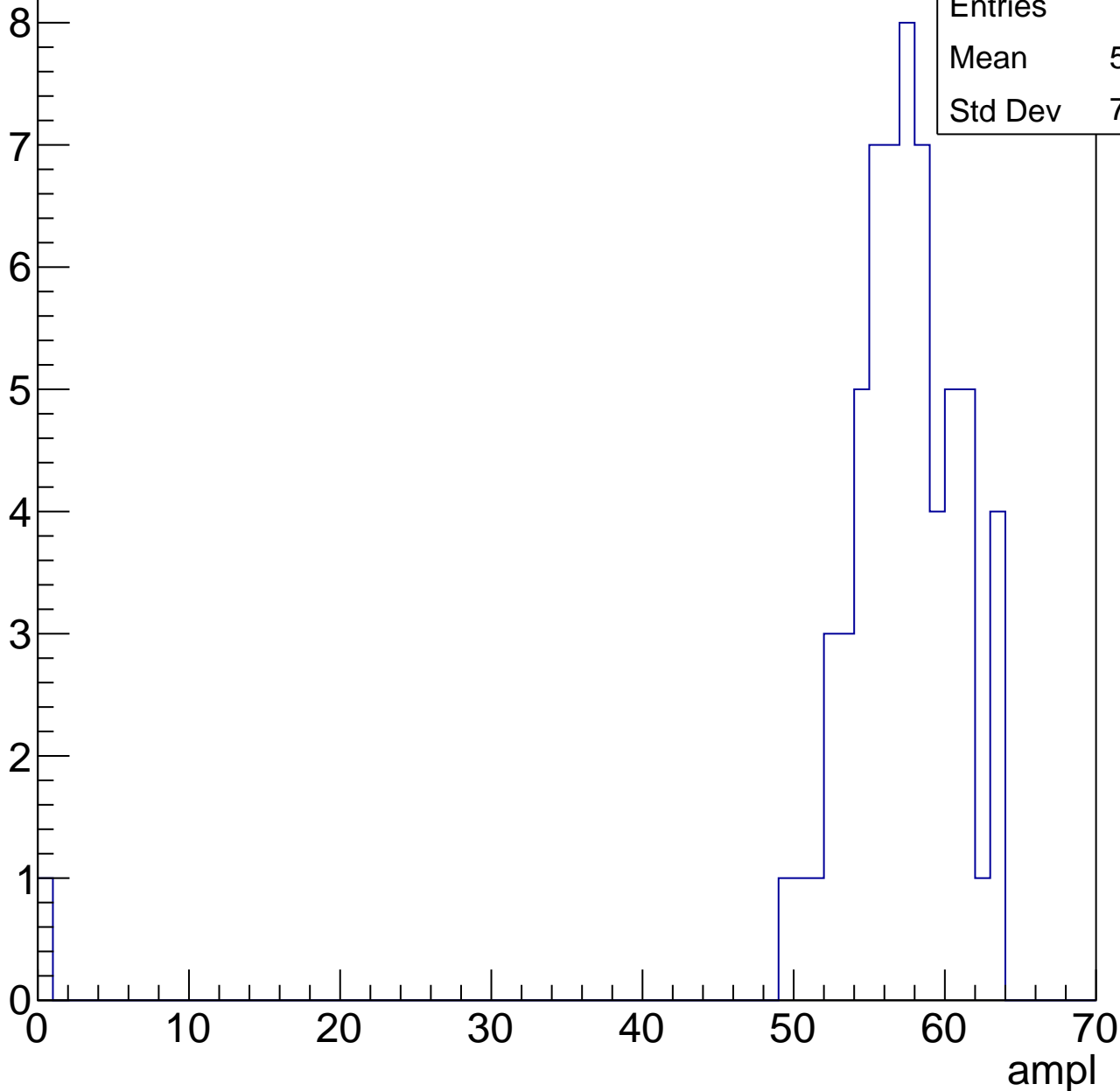


# B1L002S, U8-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	56.02
Std Dev	7.824

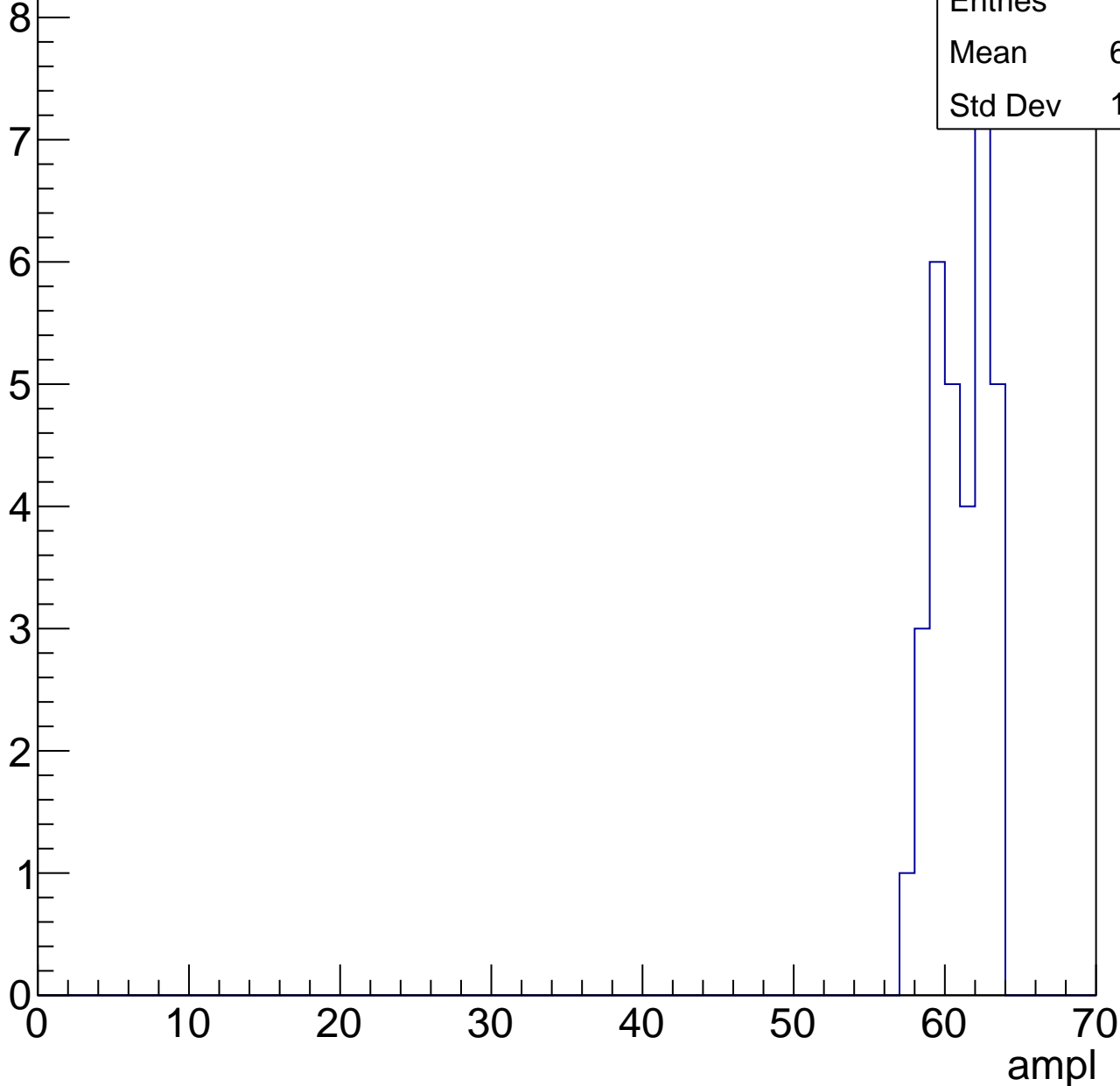


# B1L002S, U8-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	60.62
Std Dev	1.728

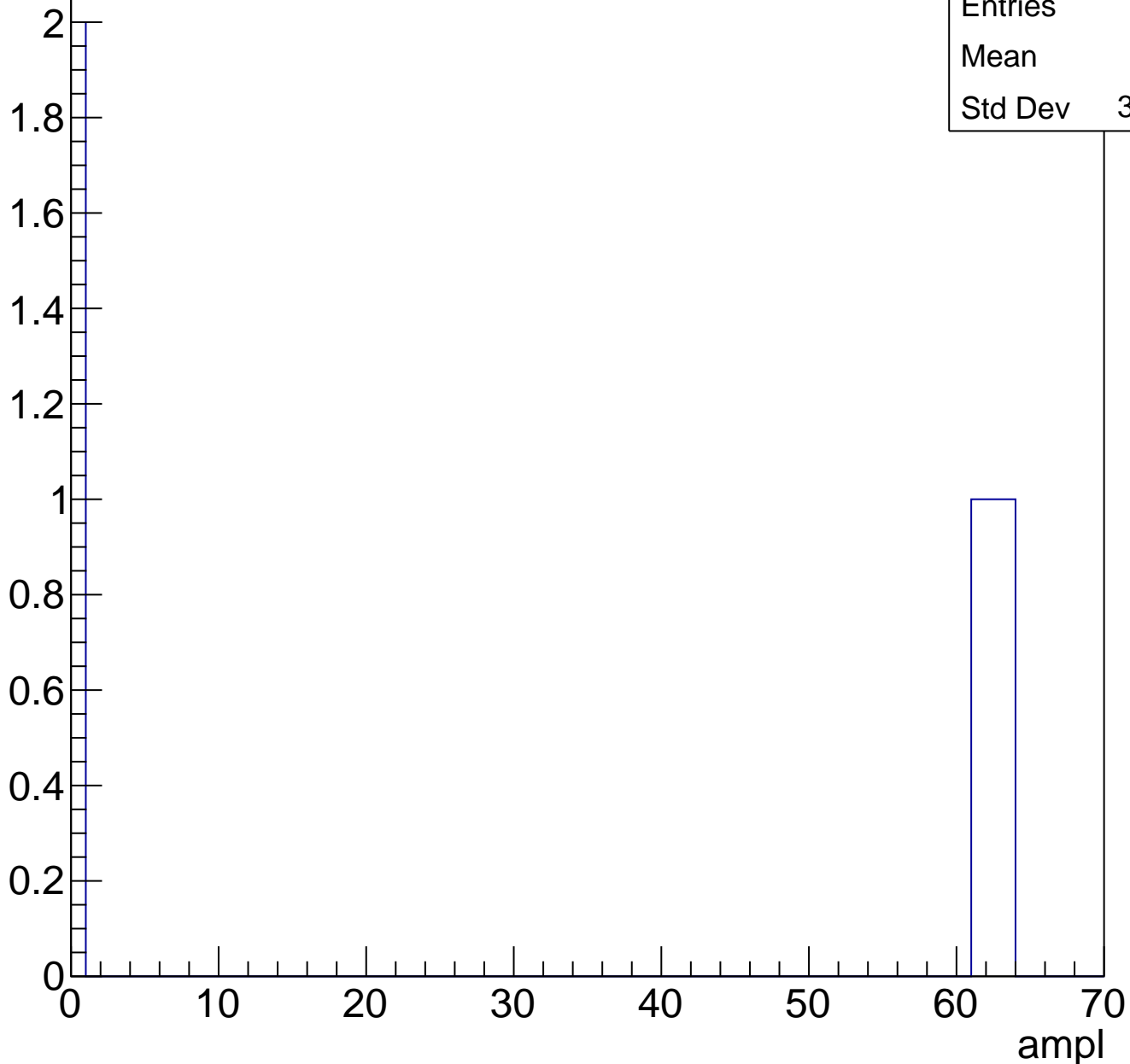




# B1L002S, U8-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch94, adc0

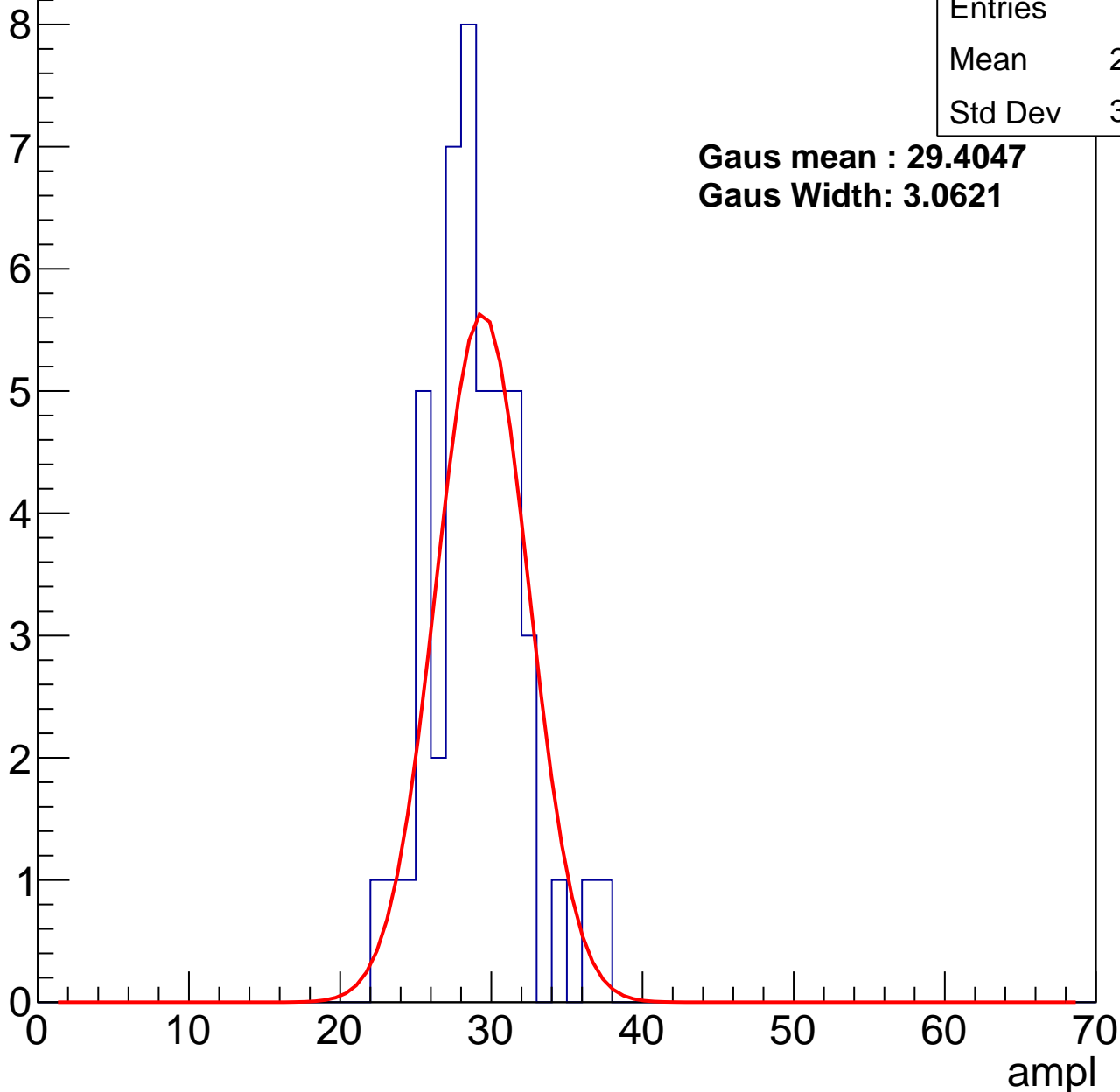
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	28.52
Std Dev	3.034

**Gaus mean : 29.4047**

**Gaus Width: 3.0621**



# B1L002S, U8-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	56
Mean	33.25
Std Dev	3.13

**Gaus mean : 33.6973**

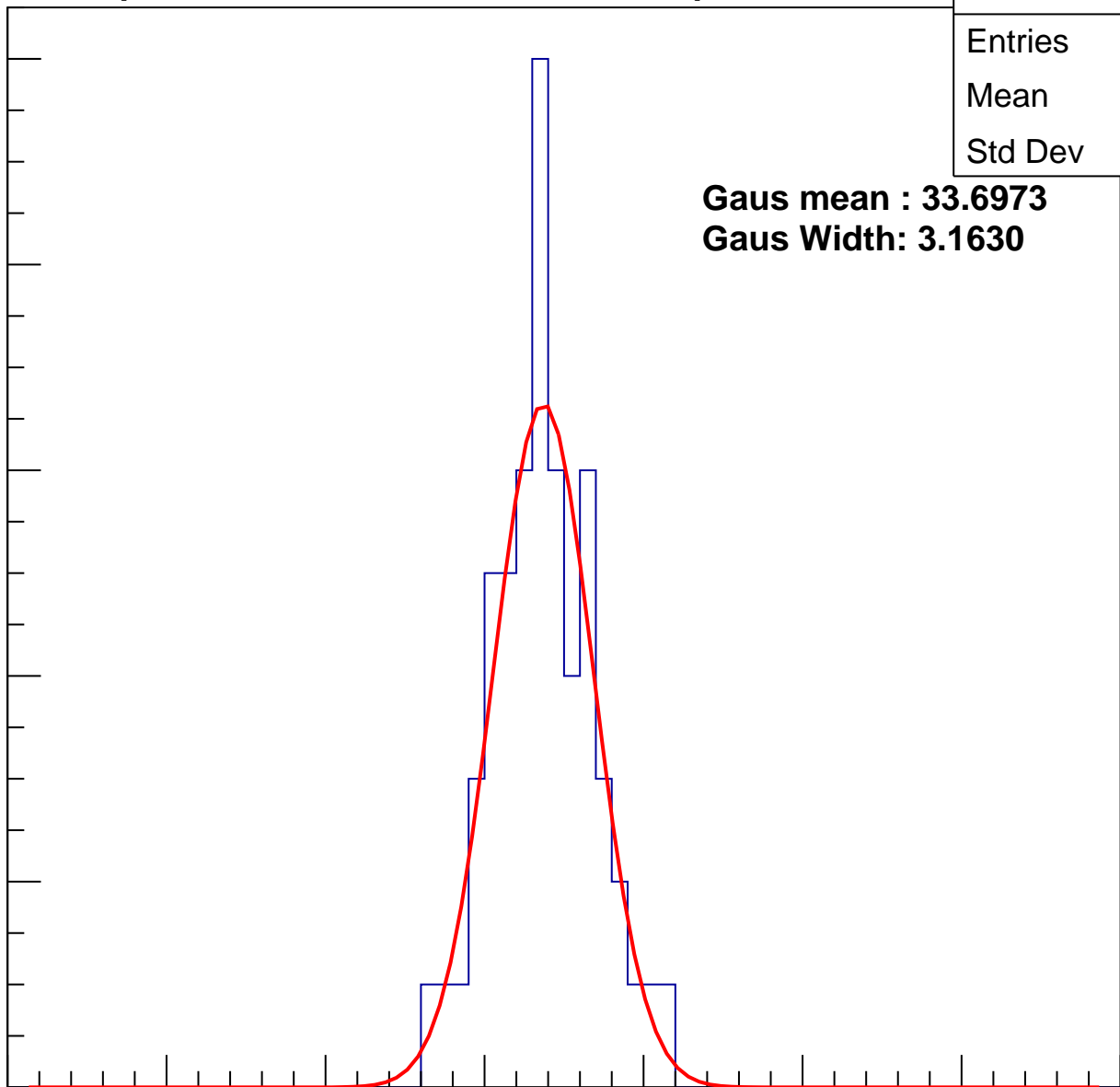
**Gaus Width: 3.1630**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch94, adc2

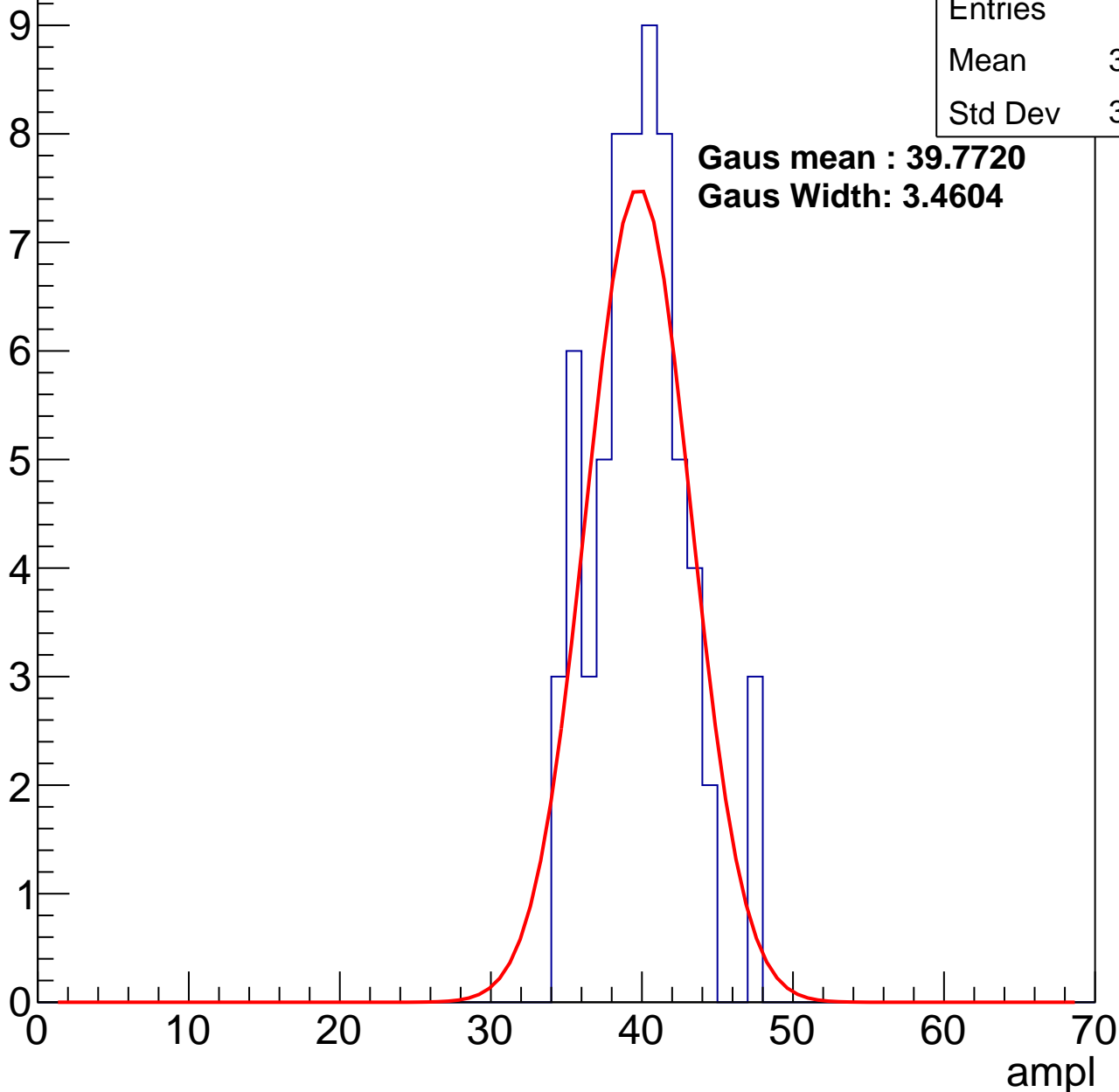
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	39.38
Std Dev	3.085

**Gaus mean : 39.7720**

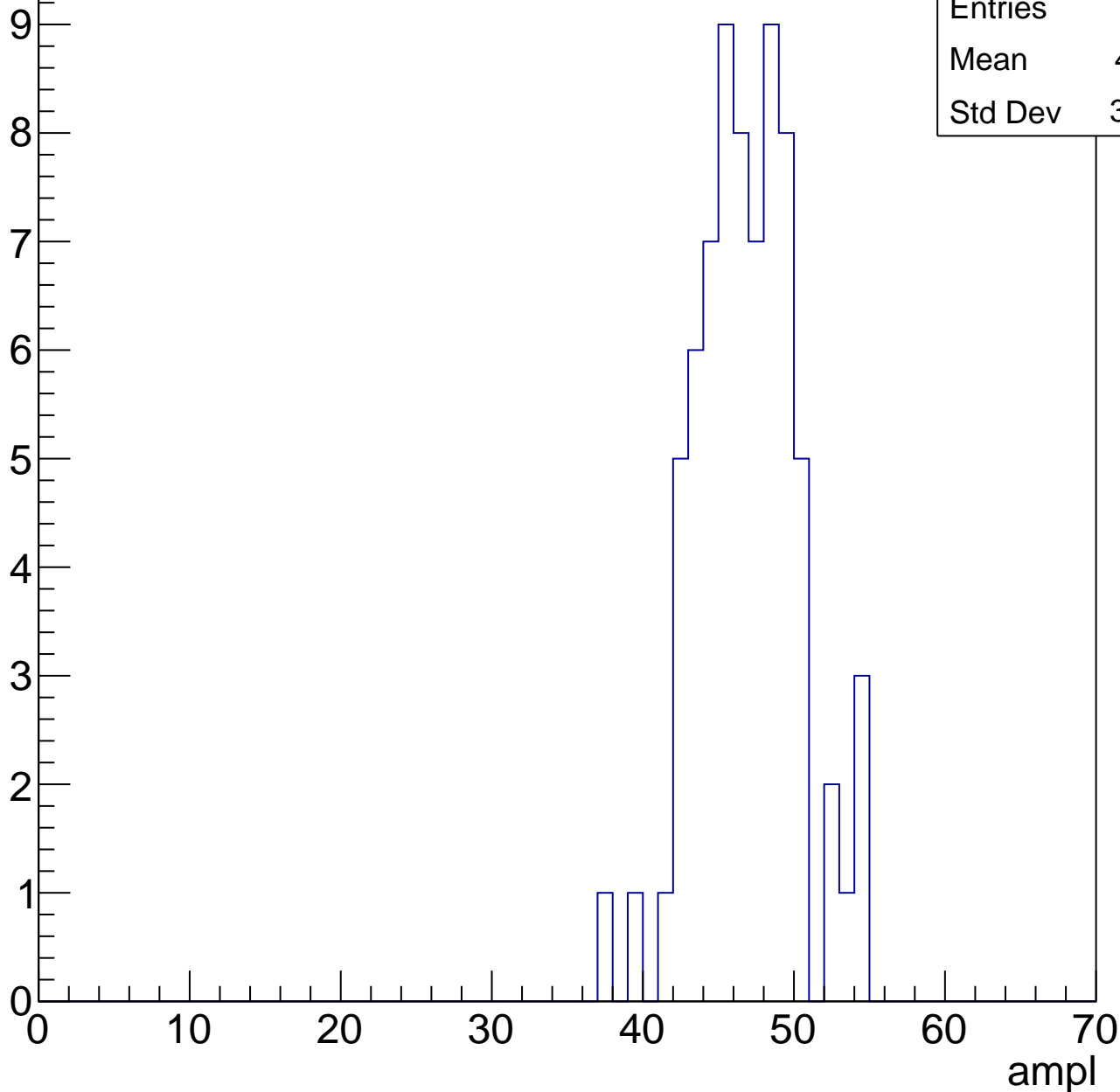
**Gaus Width: 3.4604**



# B1L002S, U8-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



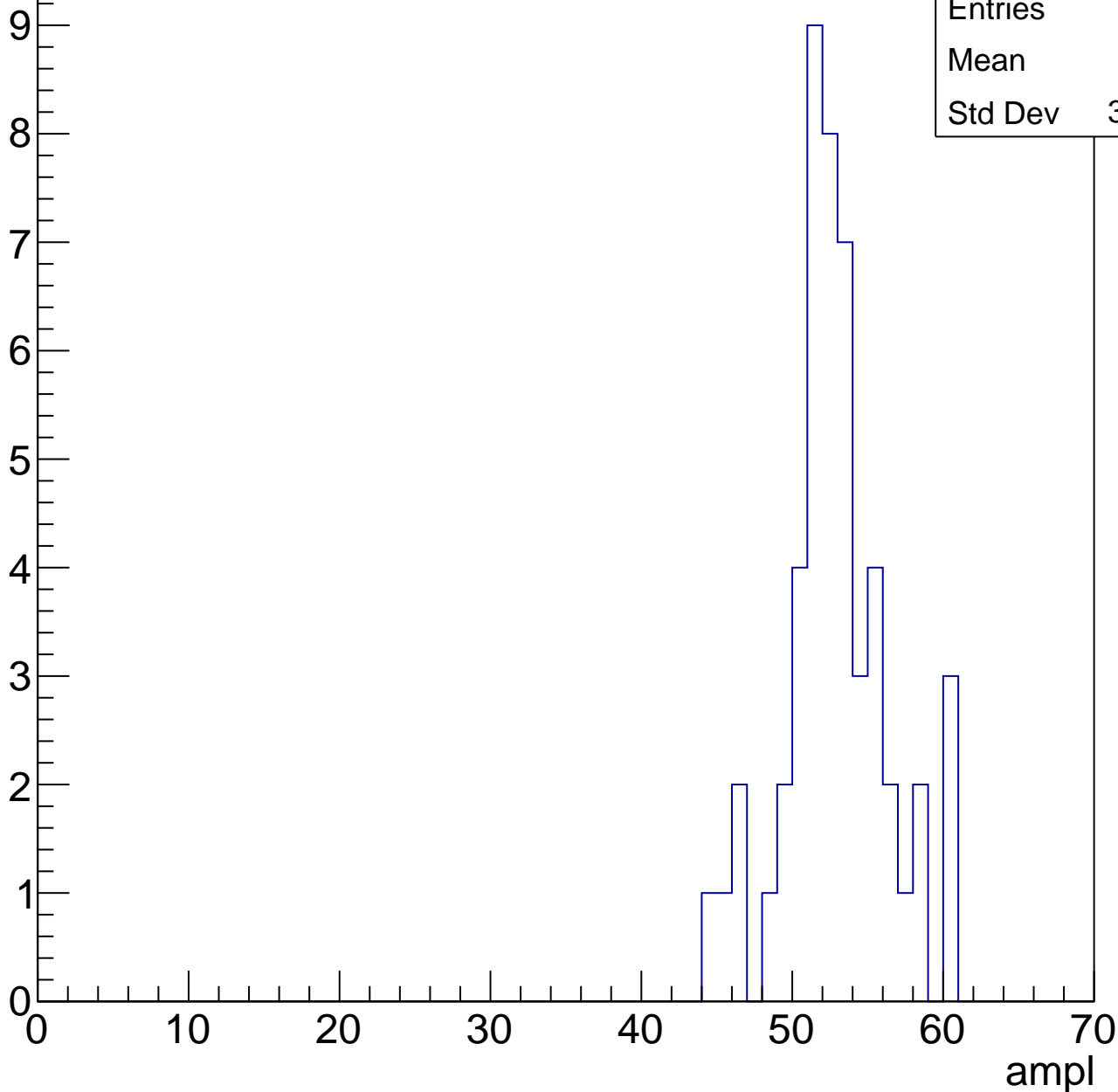
Entries	73
Mean	46.41
Std Dev	3.355

# B1L002S, U8-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

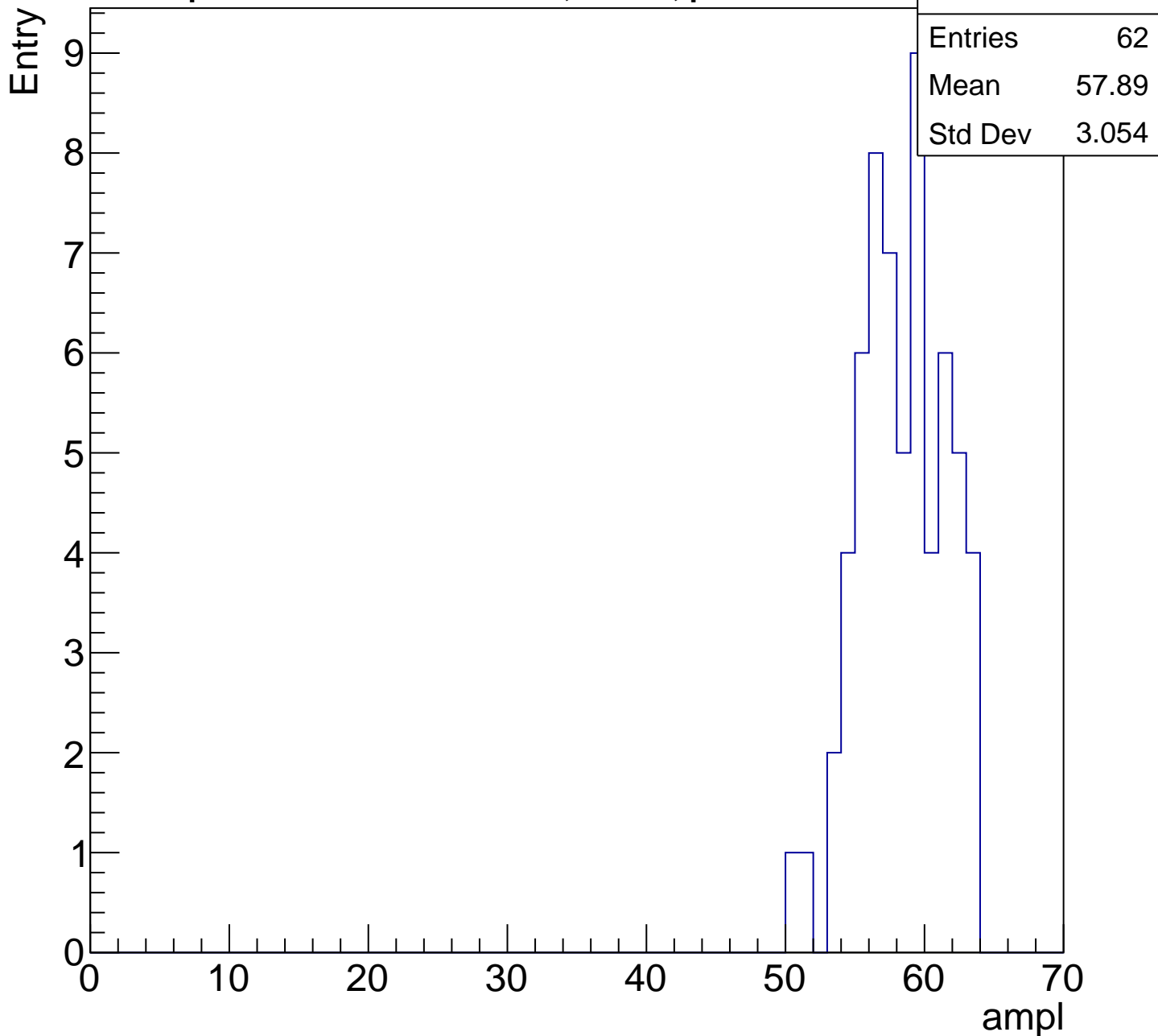
Entry

Entries	50
Mean	52.4
Std Dev	3.499



# B1L002S, U8-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

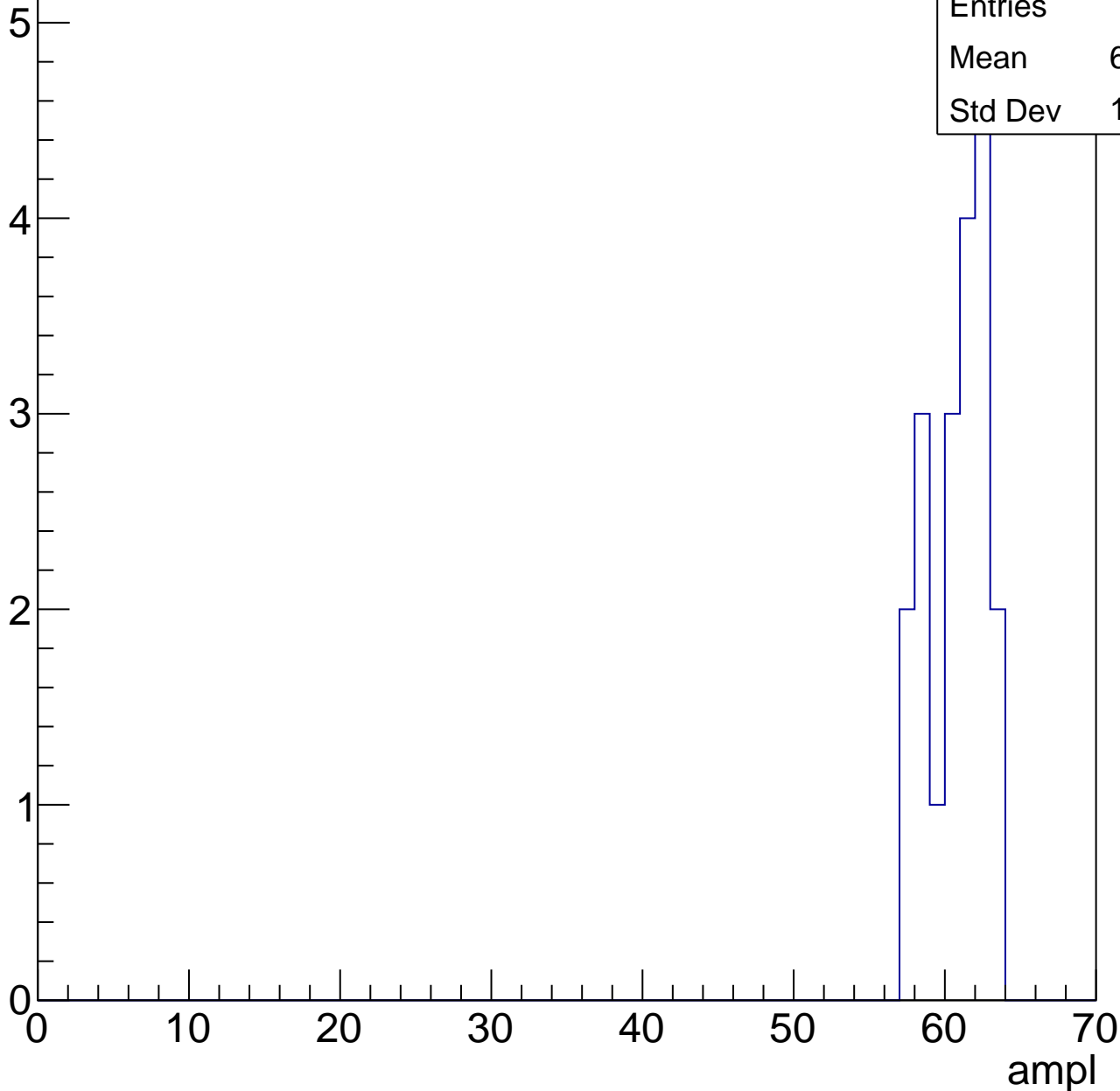


# B1L002S, U8-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	60.35
Std Dev	1.878

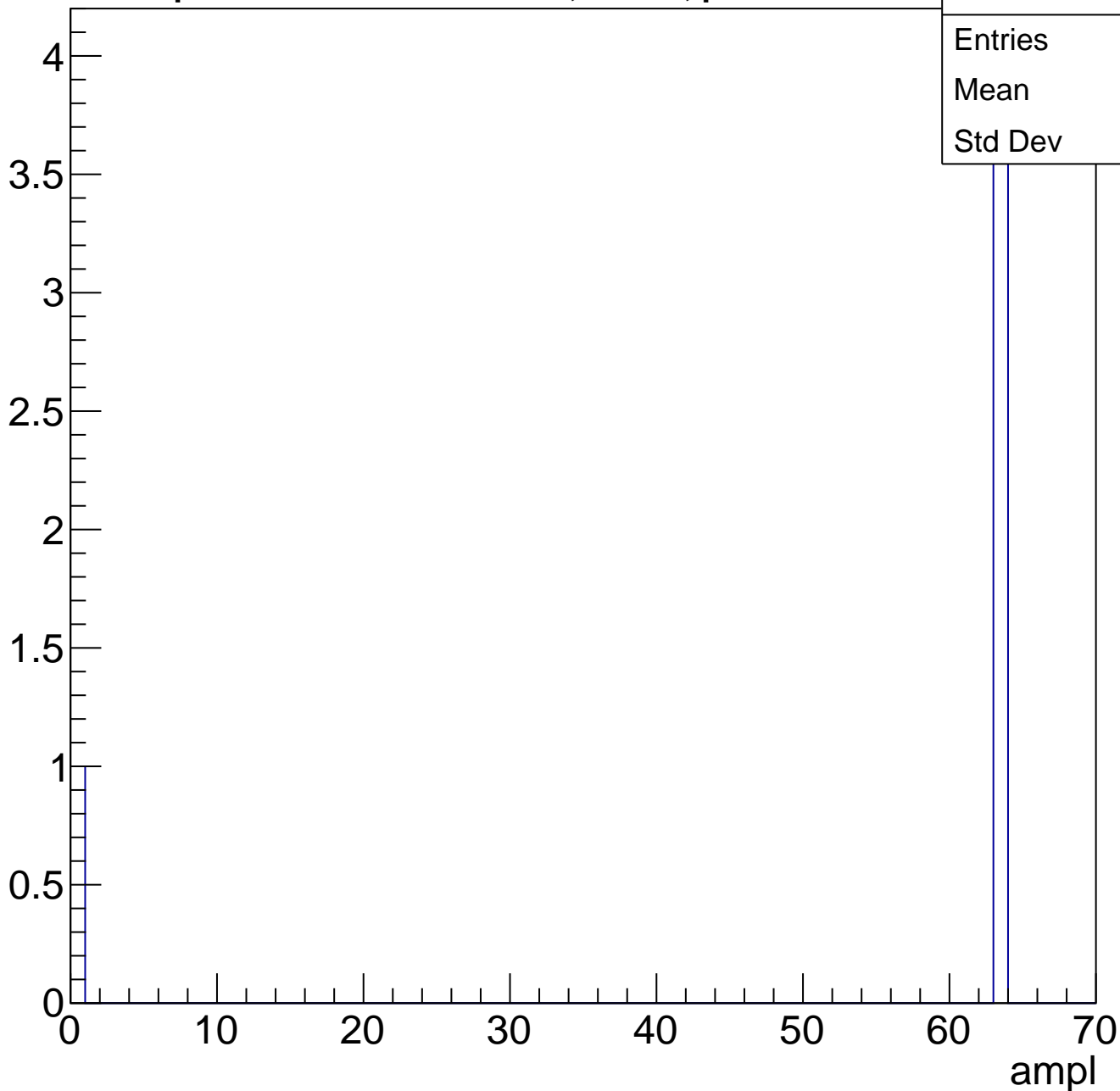




# B1L002S, U8-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch95, adc0

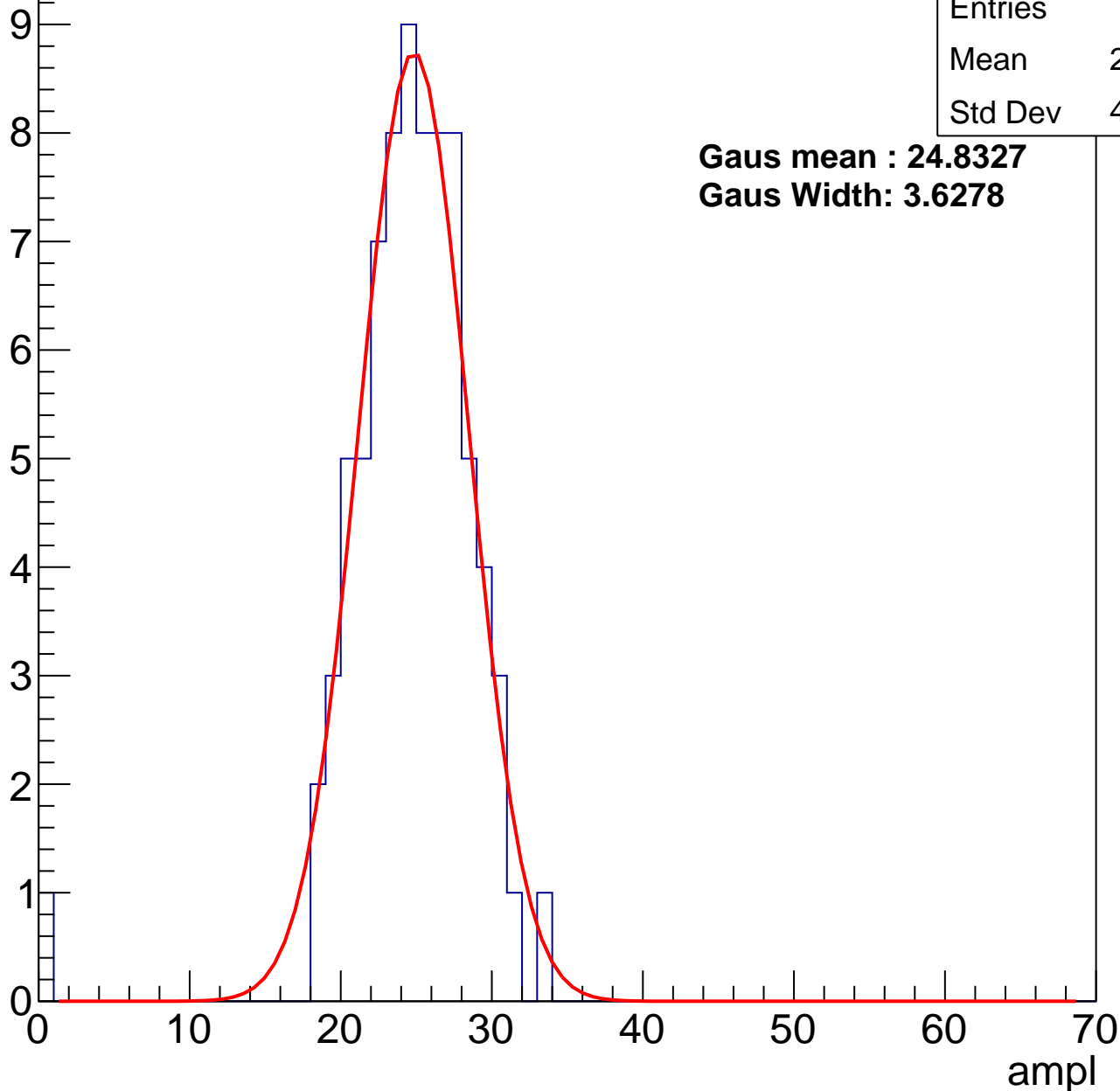
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	24.18
Std Dev	4.254

**Gaus mean : 24.8327**

**Gaus Width: 3.6278**



# B1L002S, U8-ch95, adc1

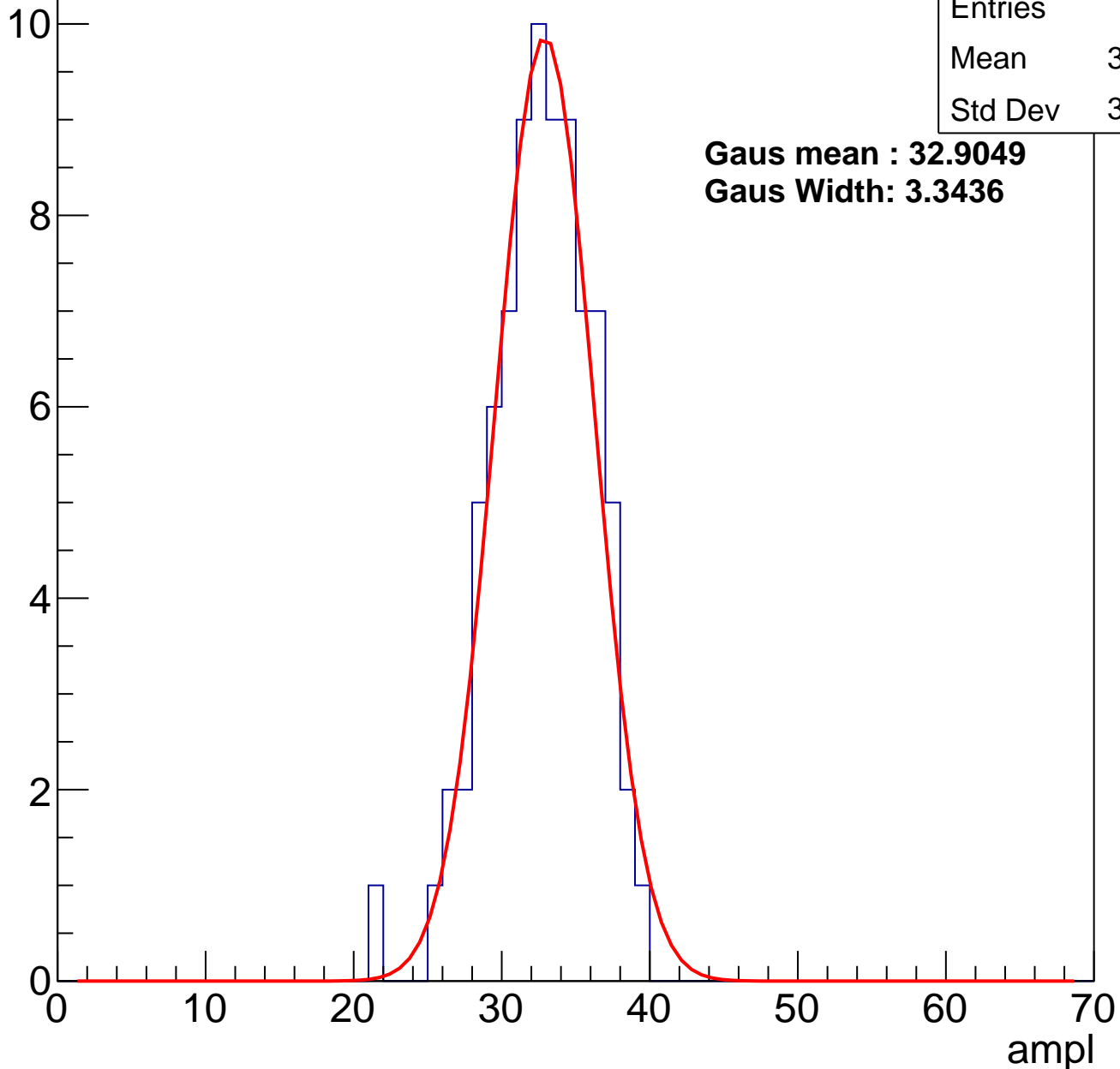
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	32.23
Std Dev	3.338

**Gaus mean : 32.9049**

**Gaus Width: 3.3436**

Entry



# B1L002S, U8-ch95, adc2

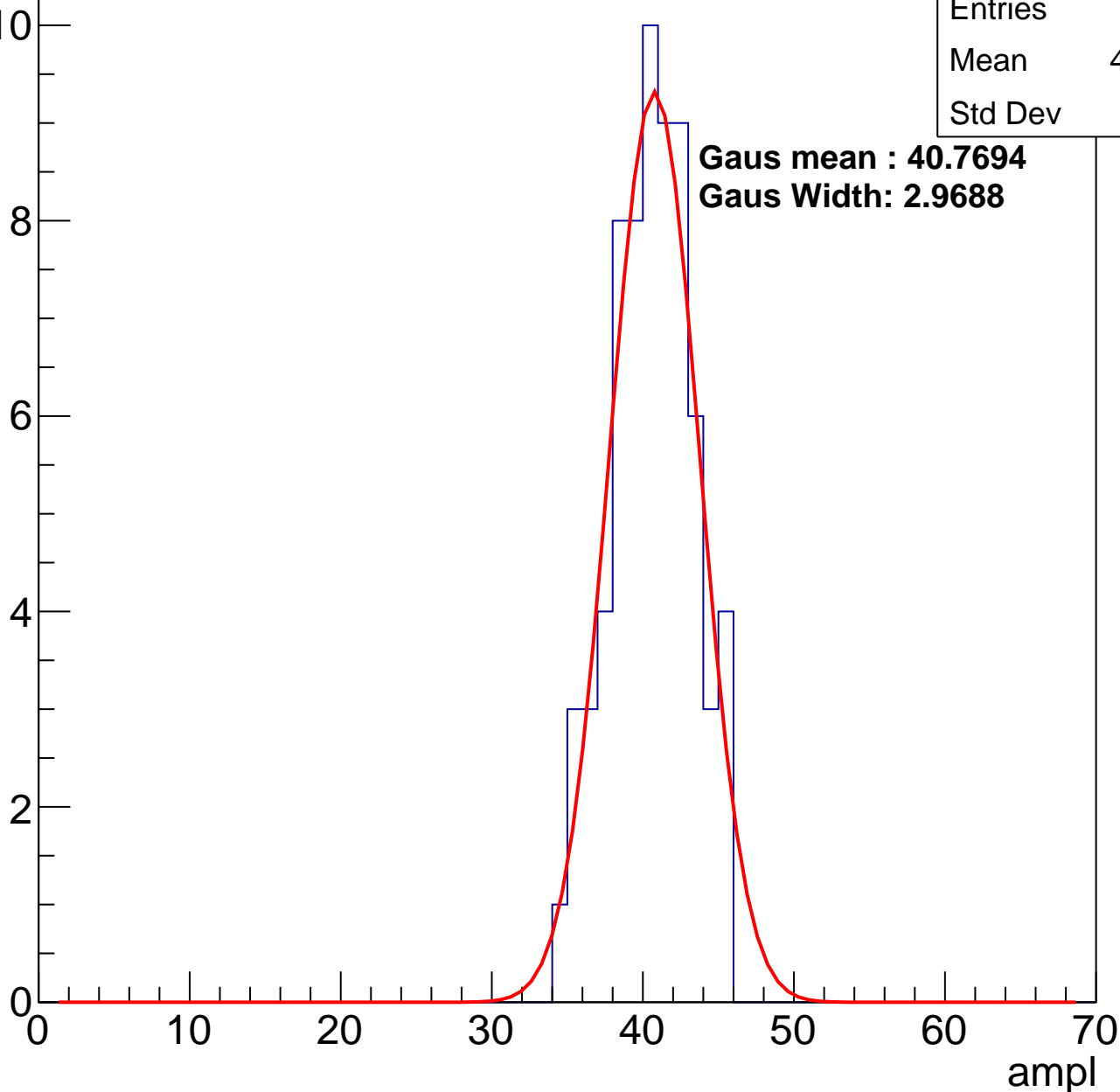
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.12
Std Dev	2.66

**Gaus mean : 40.7694**

**Gaus Width: 2.9688**

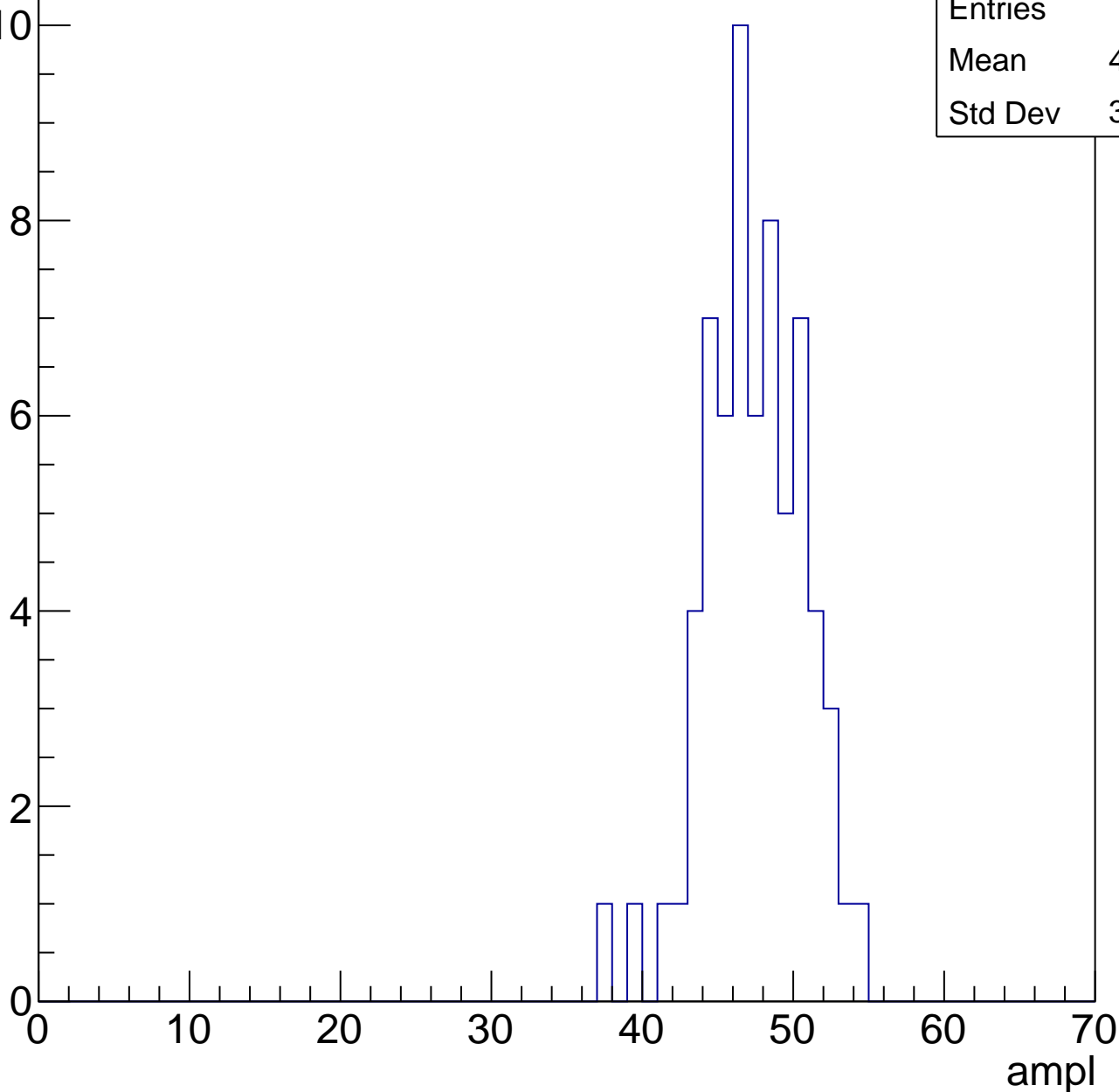


# B1L002S, U8-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.92
Std Dev	3.258

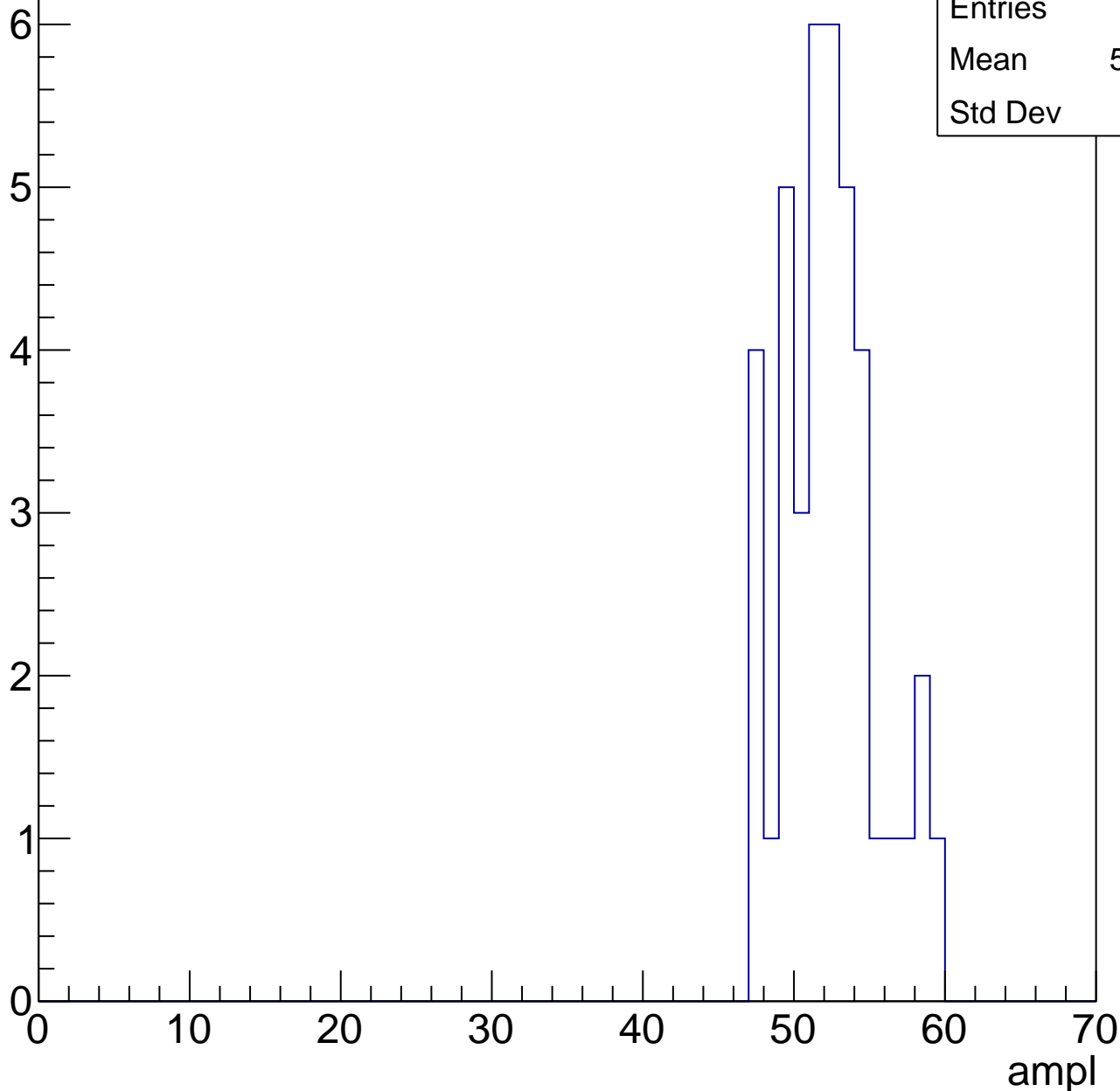


# B1L002S, U8-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

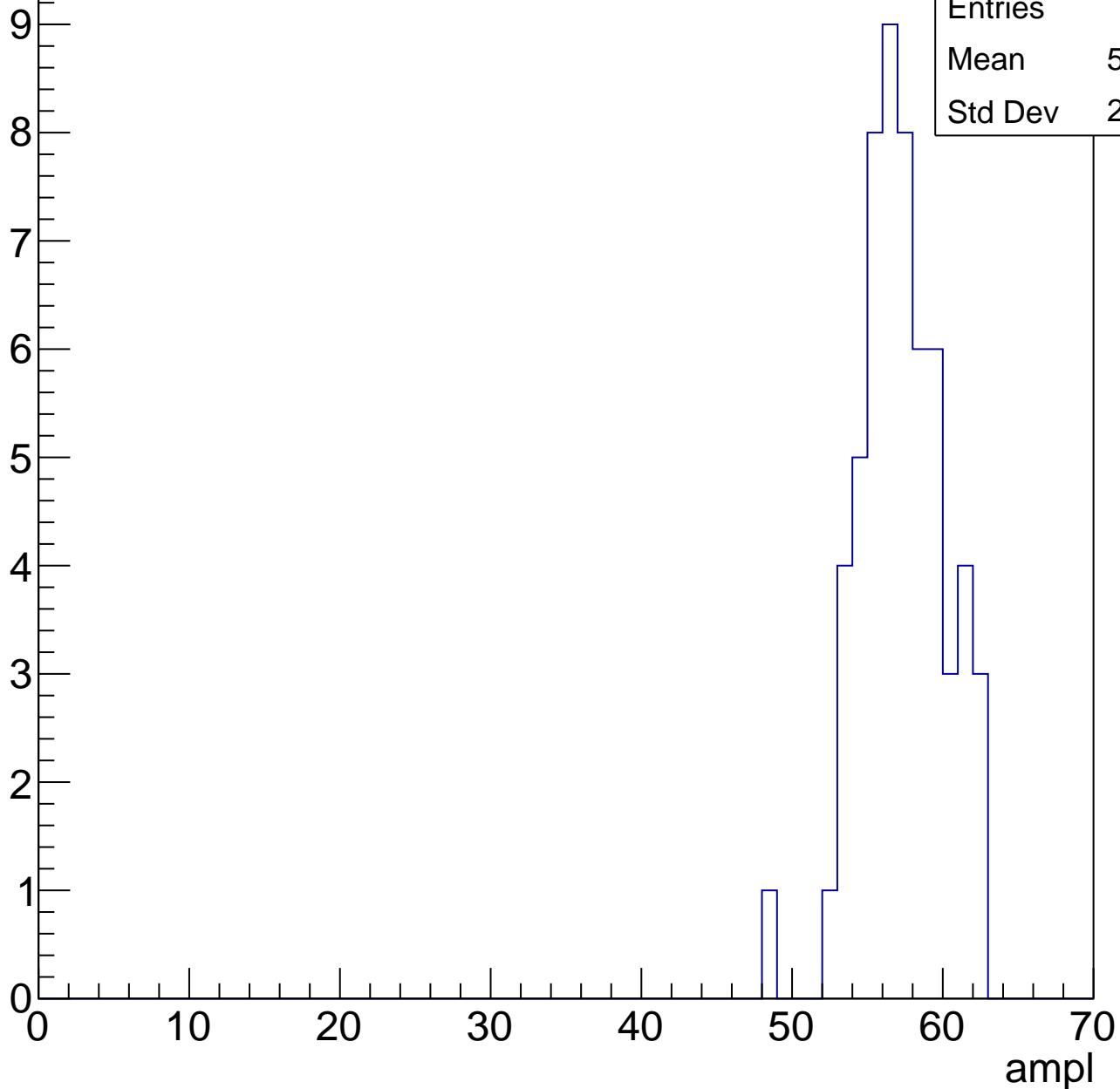
Entries	40
Mean	51.83
Std Dev	3.04



# B1L002S, U8-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

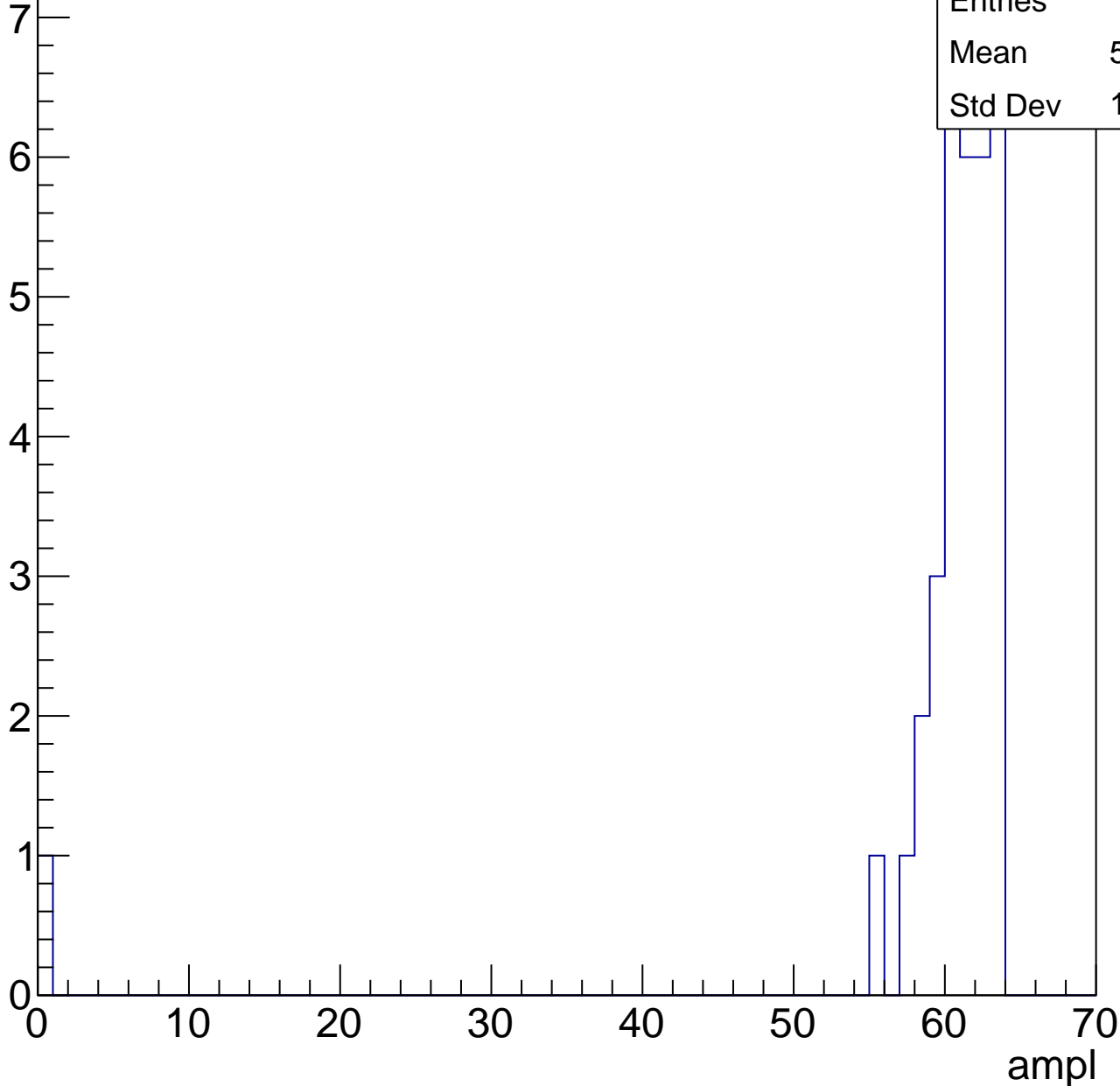


# B1L002S, U8-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	58.94
Std Dev	10.43

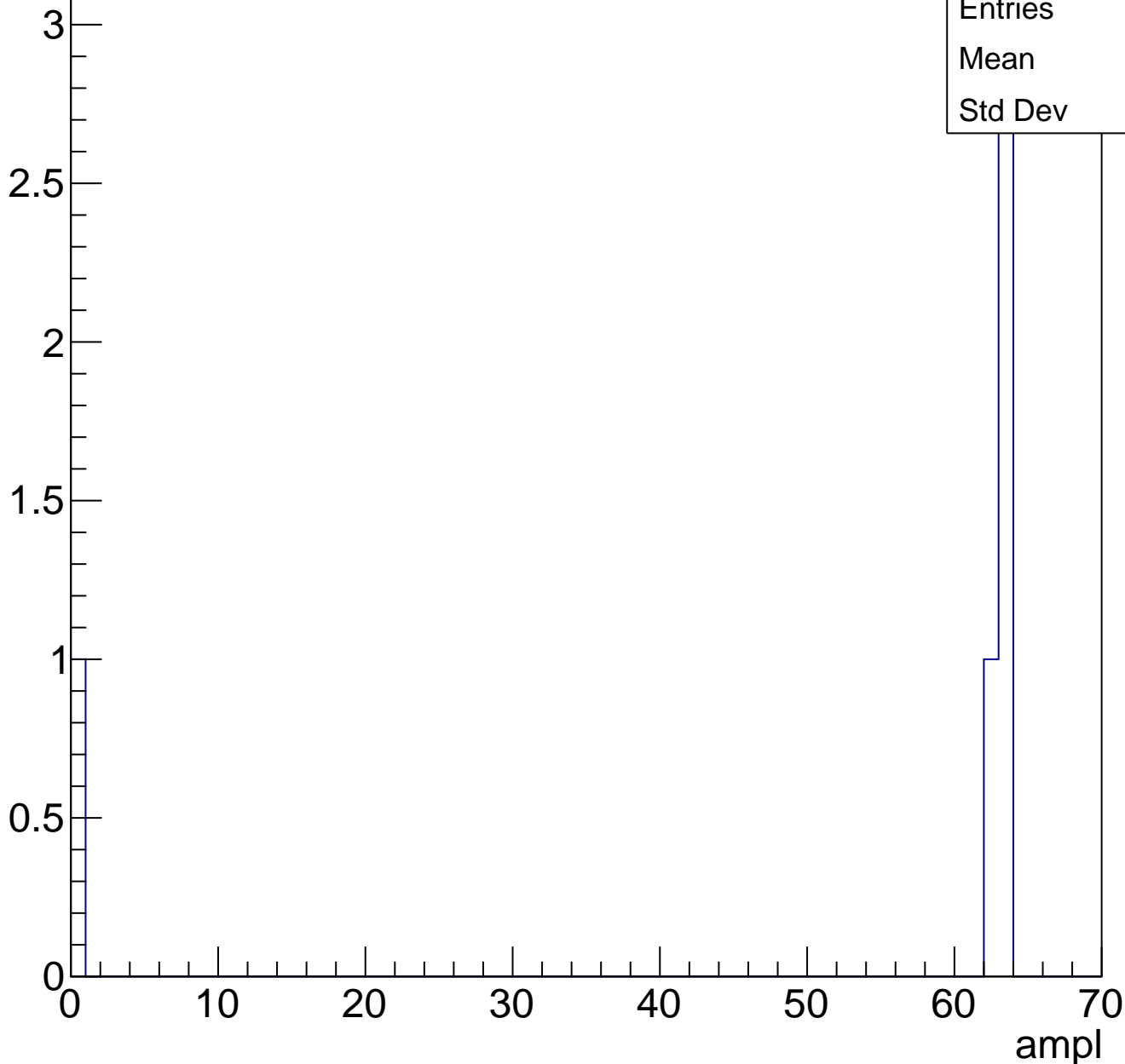




# B1L002S, U8-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch96, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	27.31
Std Dev	4.575

**Gaus mean : 28.4586**

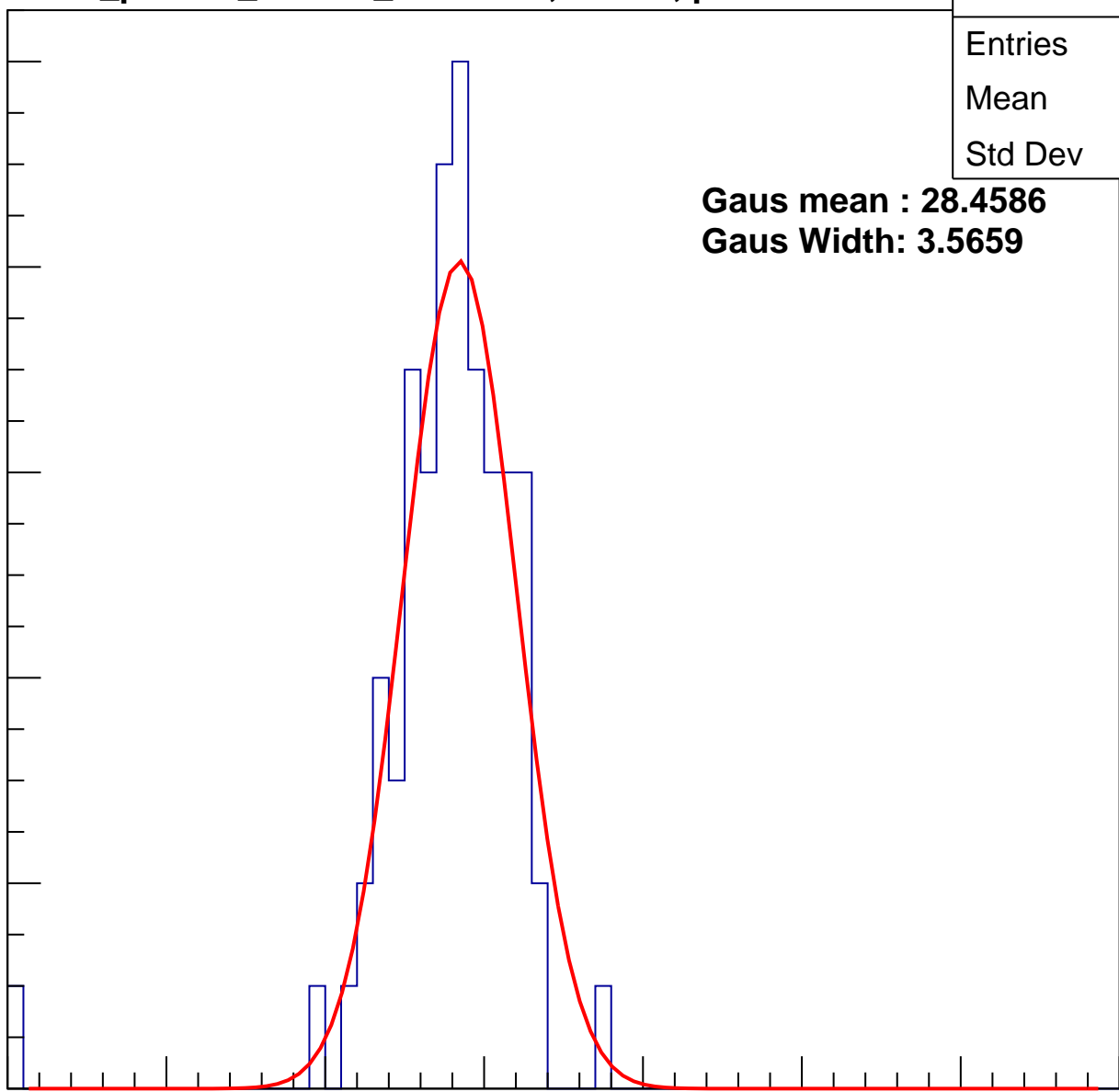
**Gaus Width: 3.5659**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	34.68
Std Dev	2.888

**Gaus mean : 35.1797**

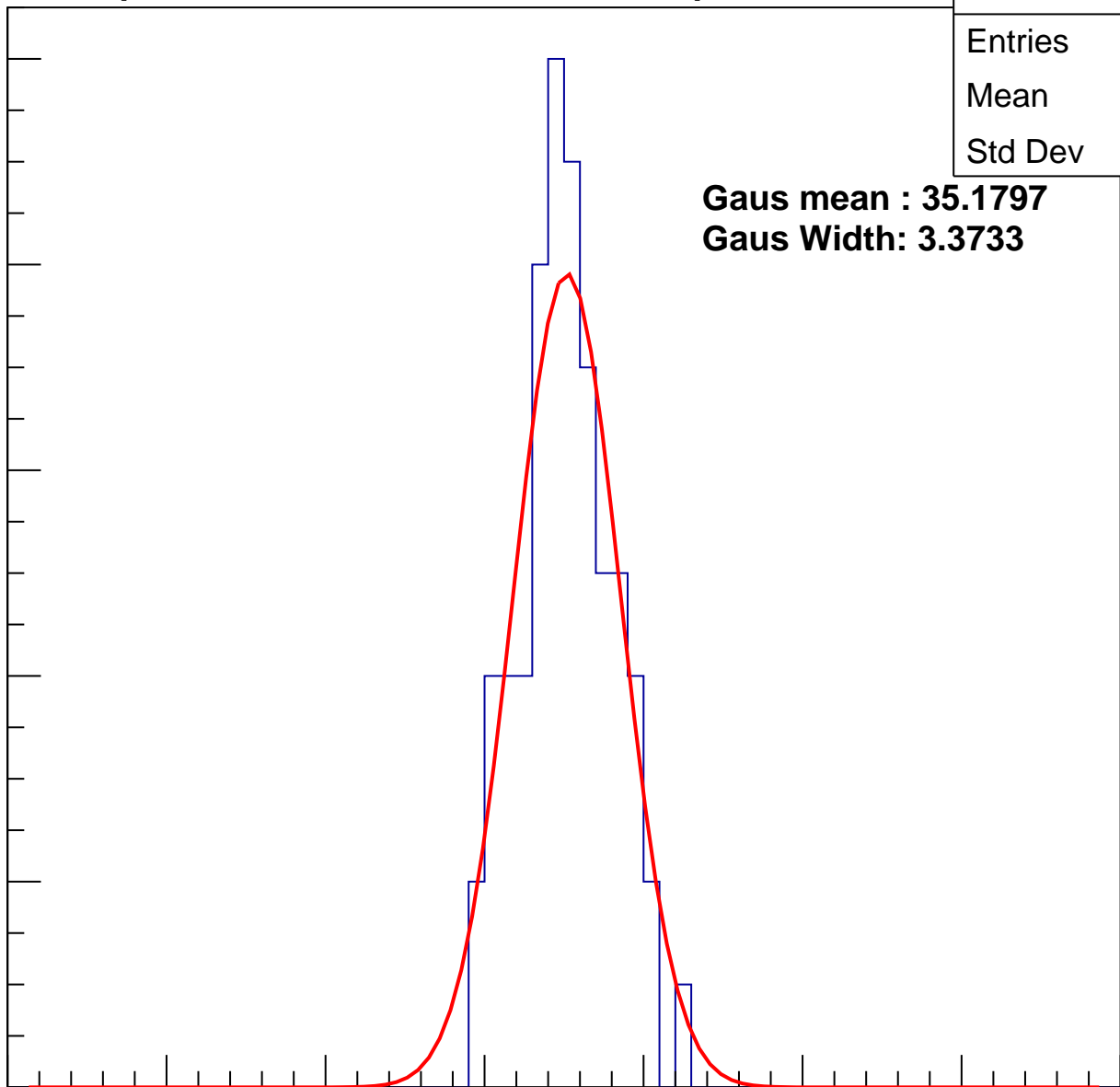
**Gaus Width: 3.3733**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch96, adc2

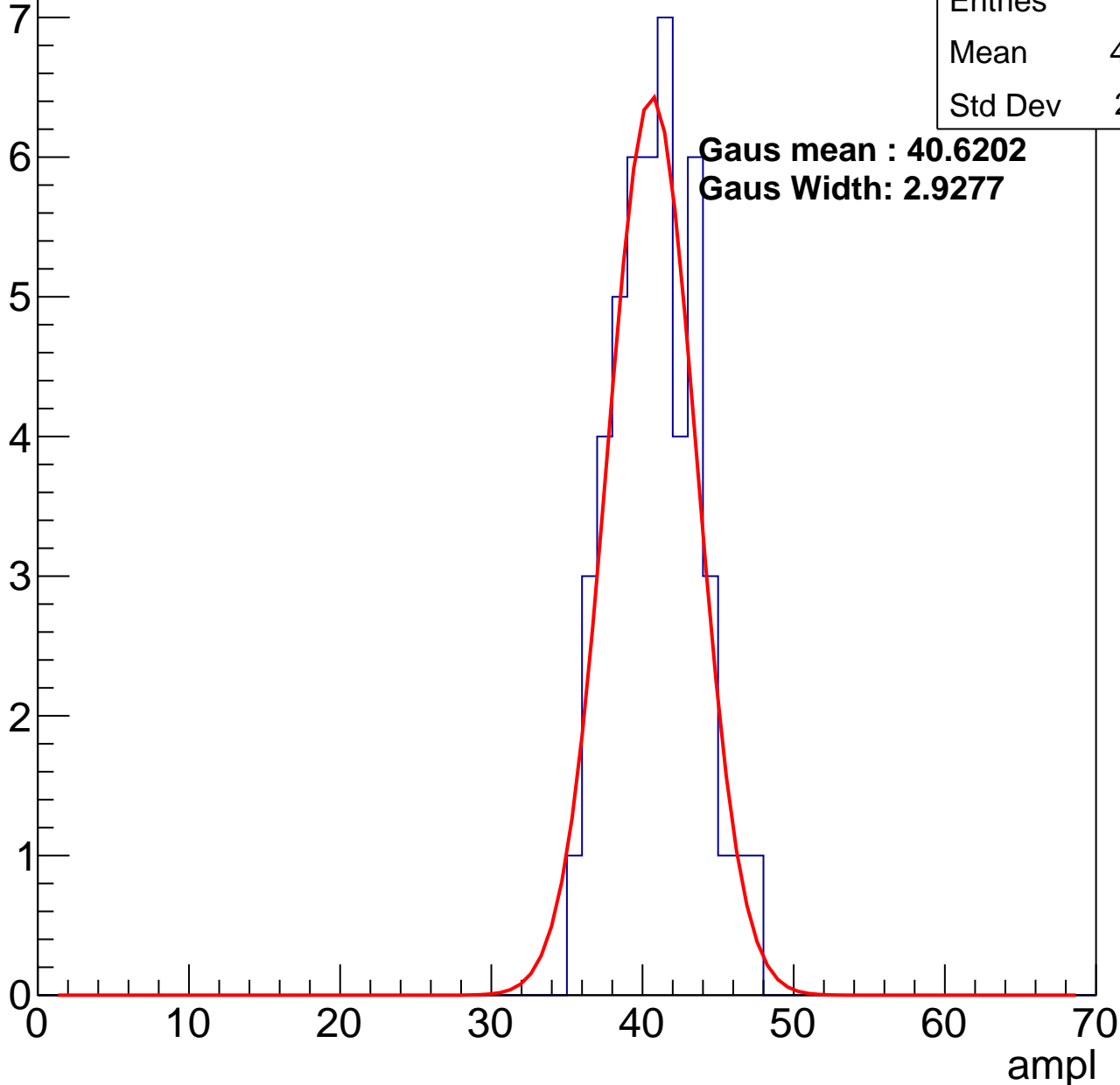
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	40.38
Std Dev	2.751

**Gaus mean : 40.6202**

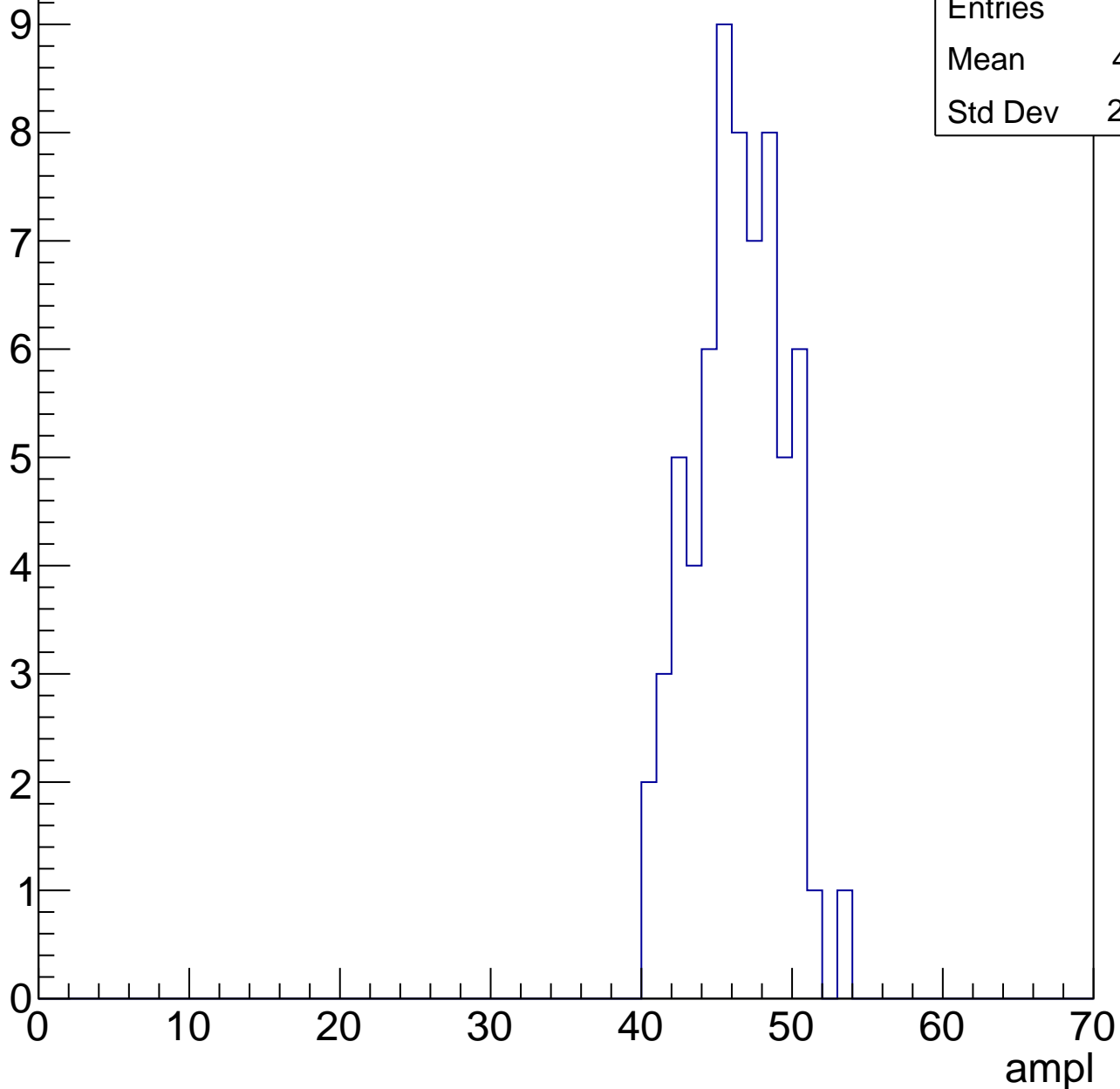
**Gaus Width: 2.9277**



# B1L002S, U8-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



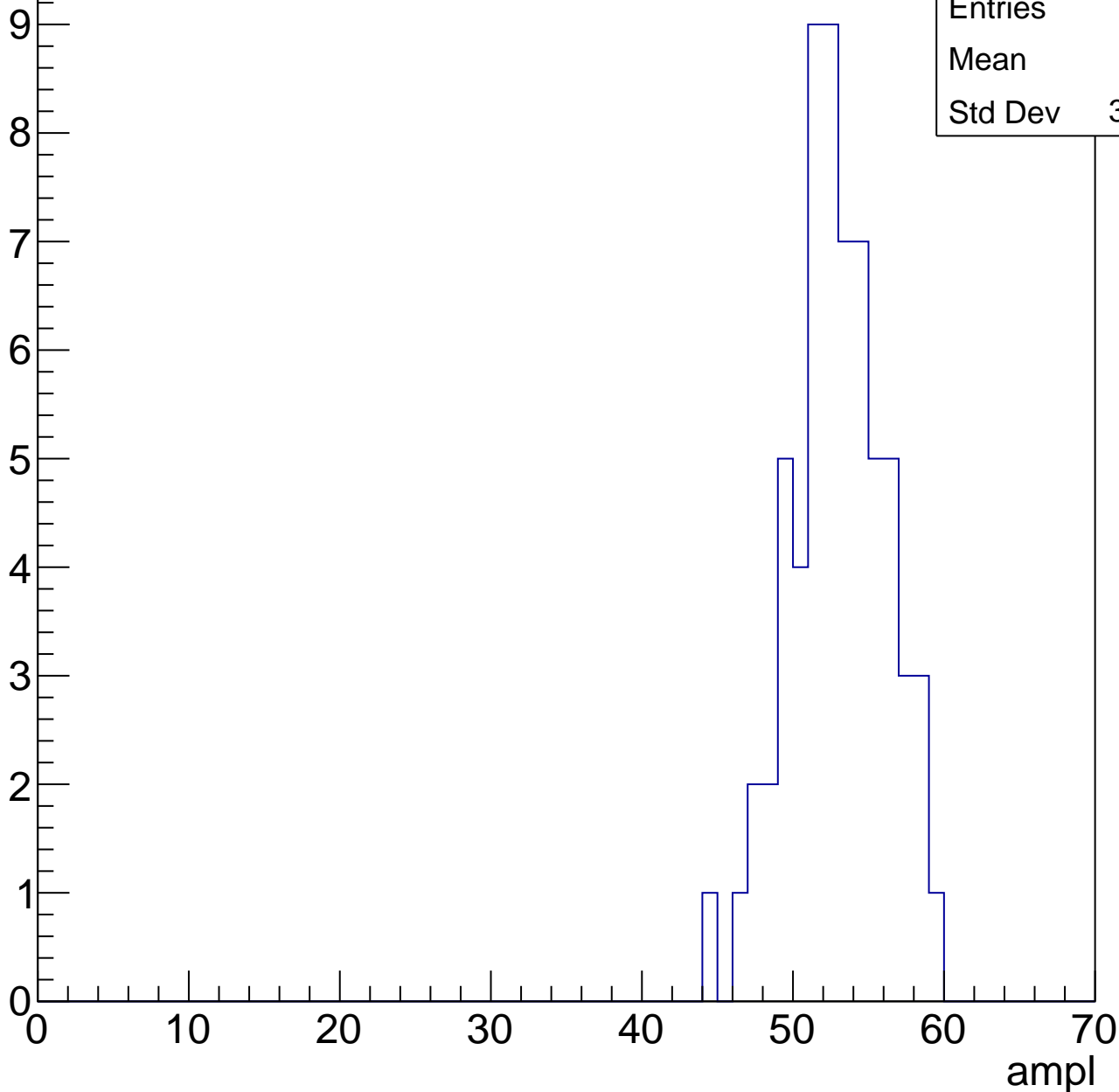
Entries	65
Mean	45.91
Std Dev	2.907

# B1L002S, U8-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

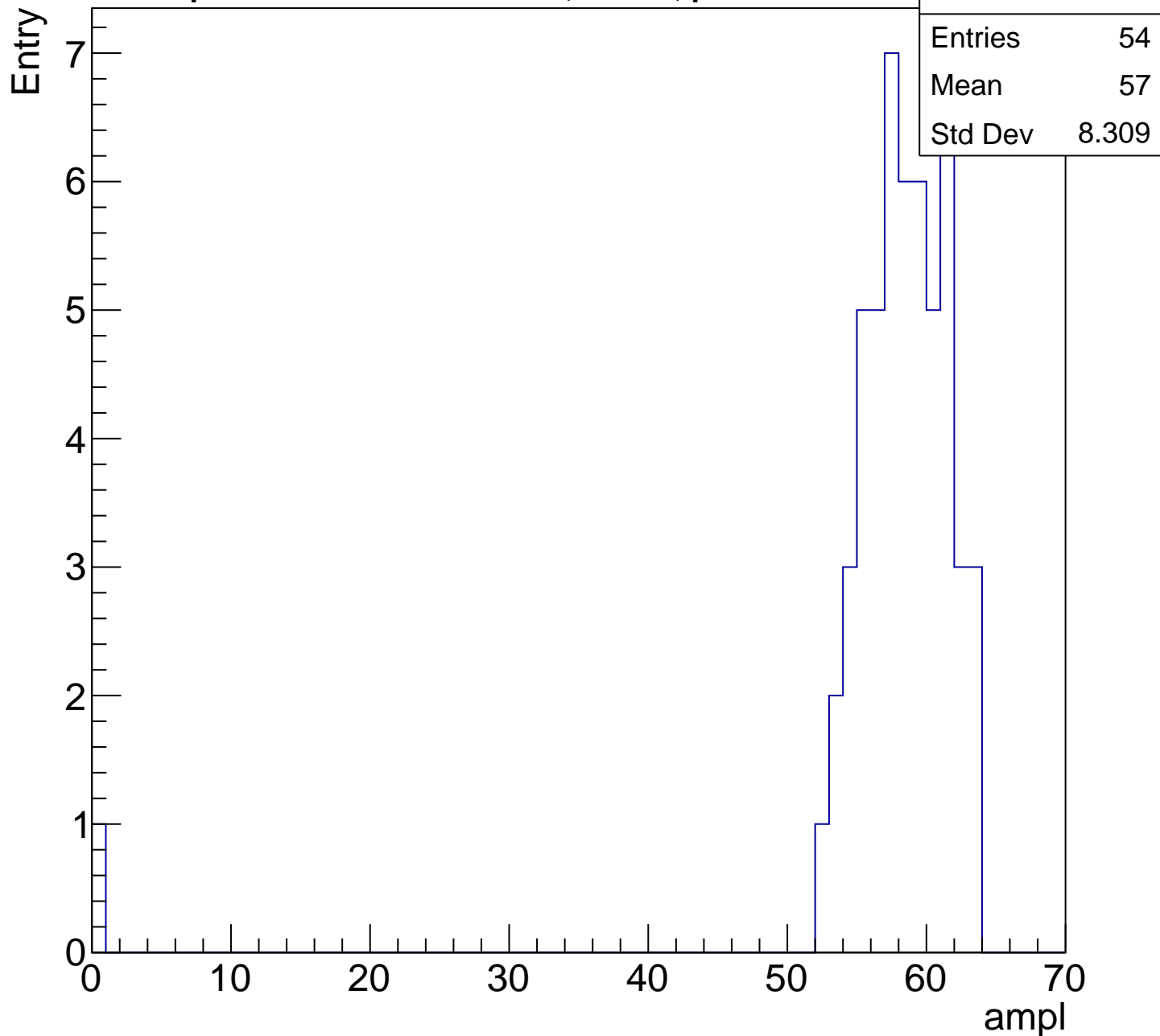
Entry

Entries	64
Mean	52.5
Std Dev	3.132



# B1L002S, U8-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

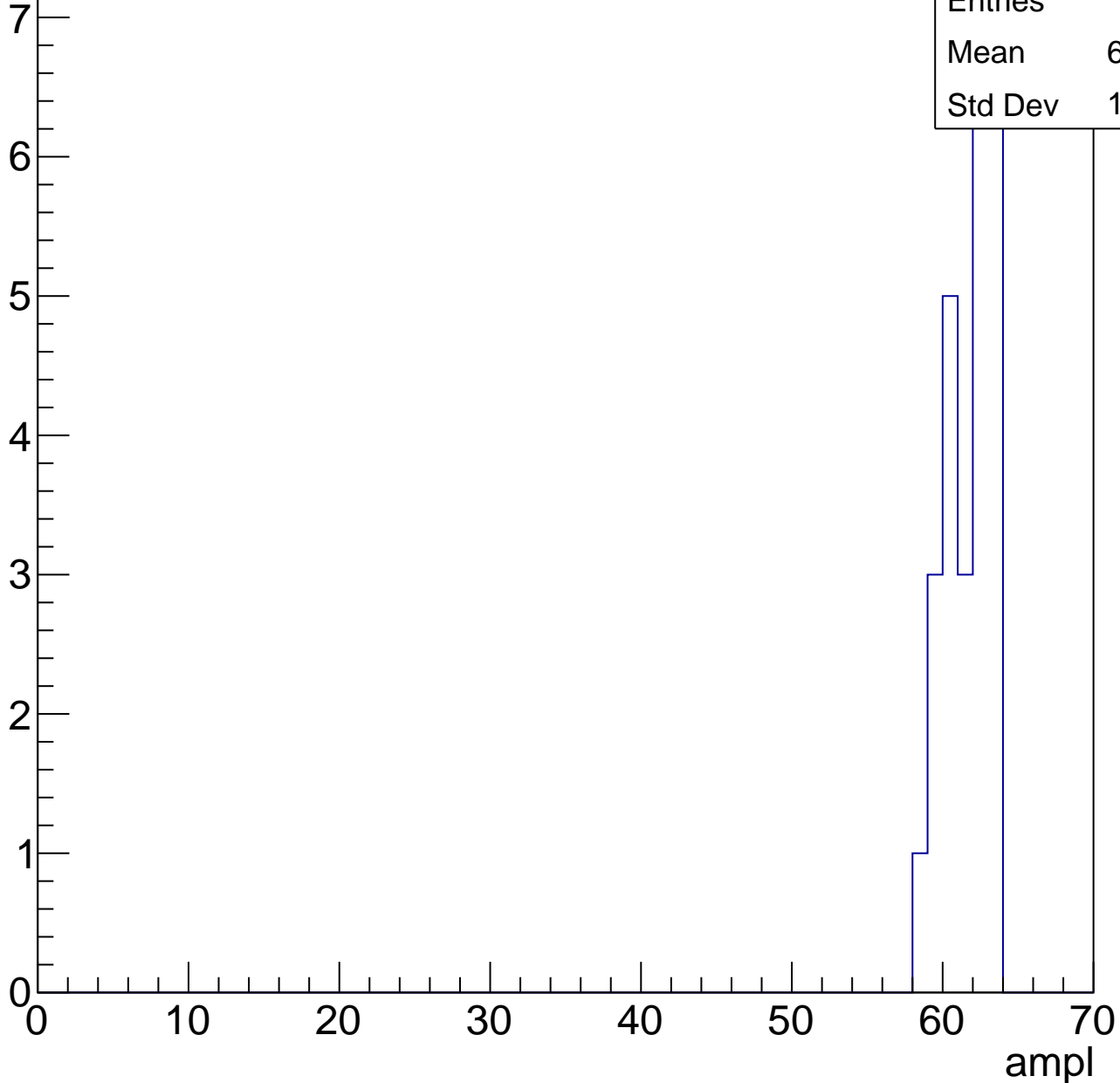


# B1L002S, U8-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.27
Std Dev	1.508





# B1L002S, U8-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U8-ch97, adc0

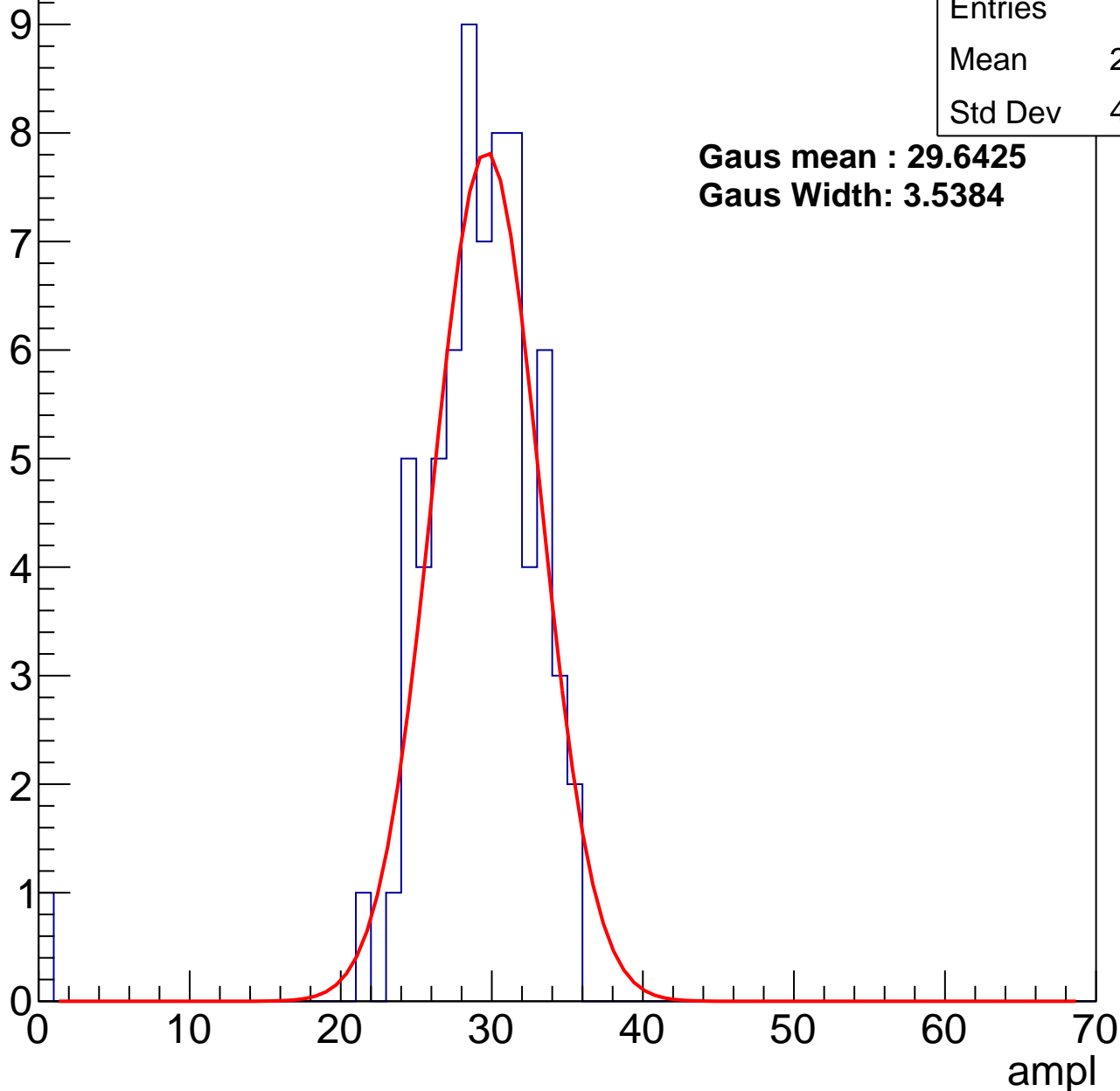
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.53
Std Dev	4.647

**Gaus mean : 29.6425**

**Gaus Width: 3.5384**



# B1L002S, U8-ch97, adc1

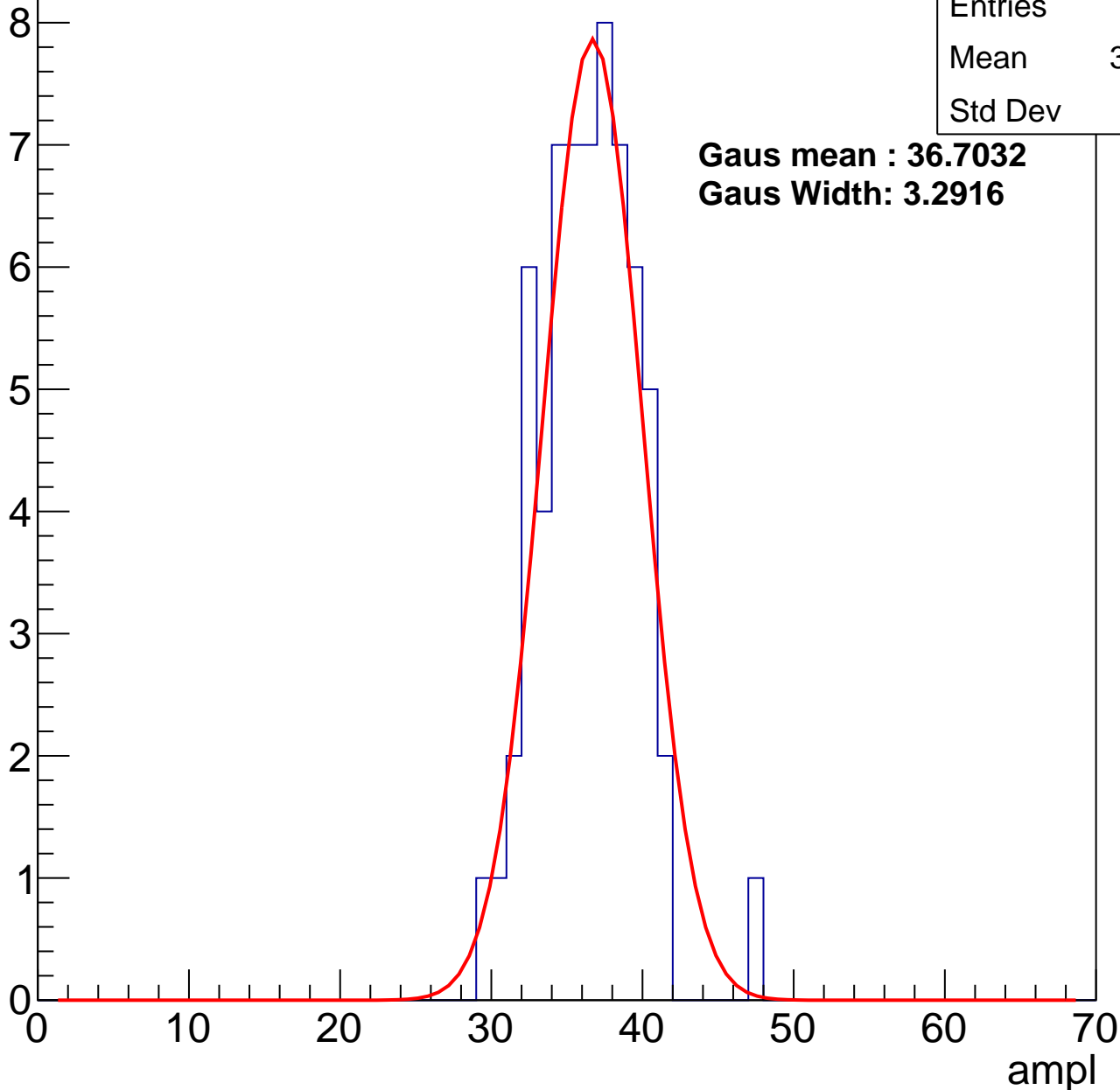
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	36.02
Std Dev	3.17

**Gaus mean : 36.7032**

**Gaus Width: 3.2916**



# B1L002S, U8-ch97, adc2

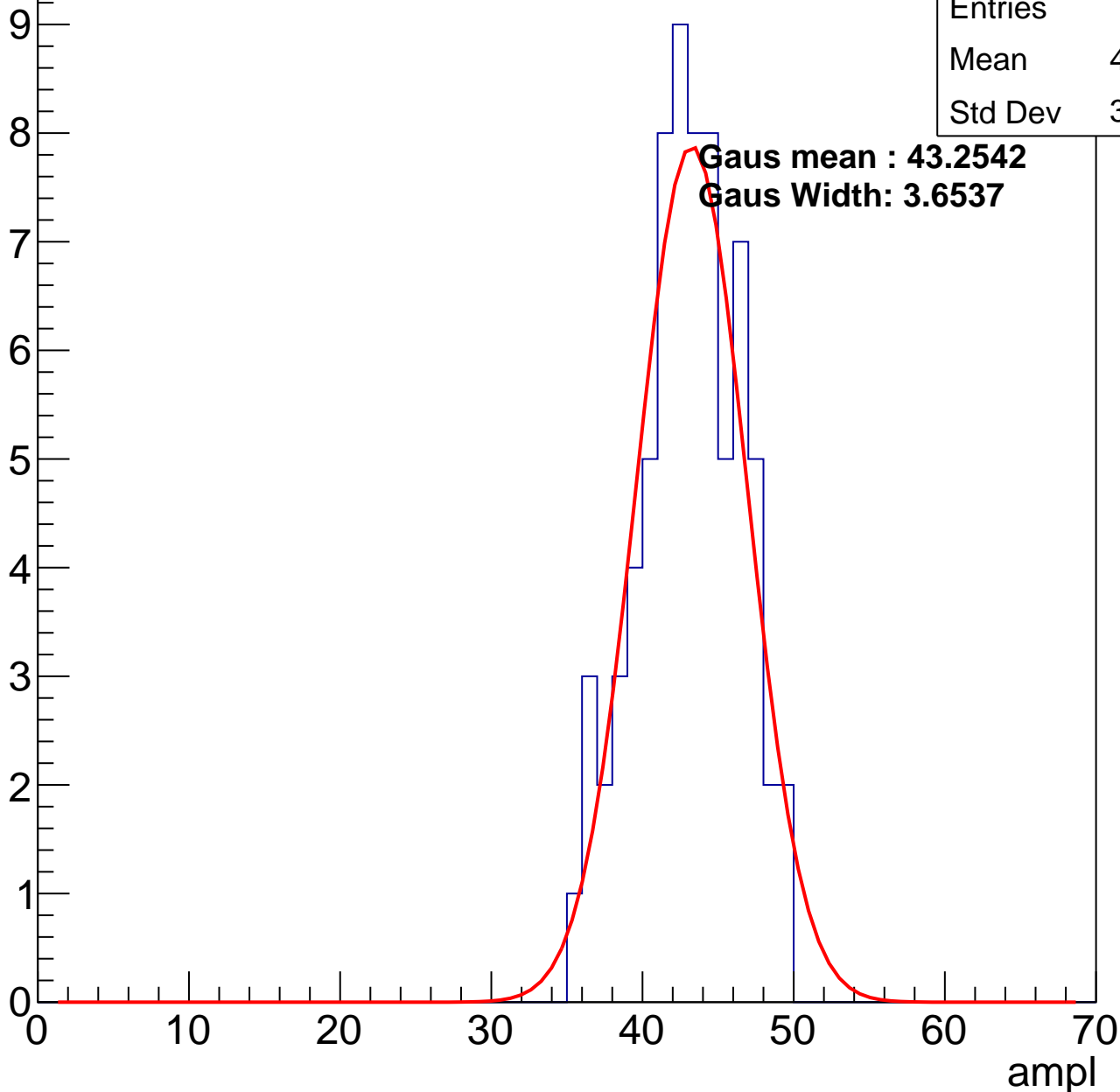
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	42.57
Std Dev	3.308

**Gaus mean : 43.2542**

**Gaus Width: 3.6537**

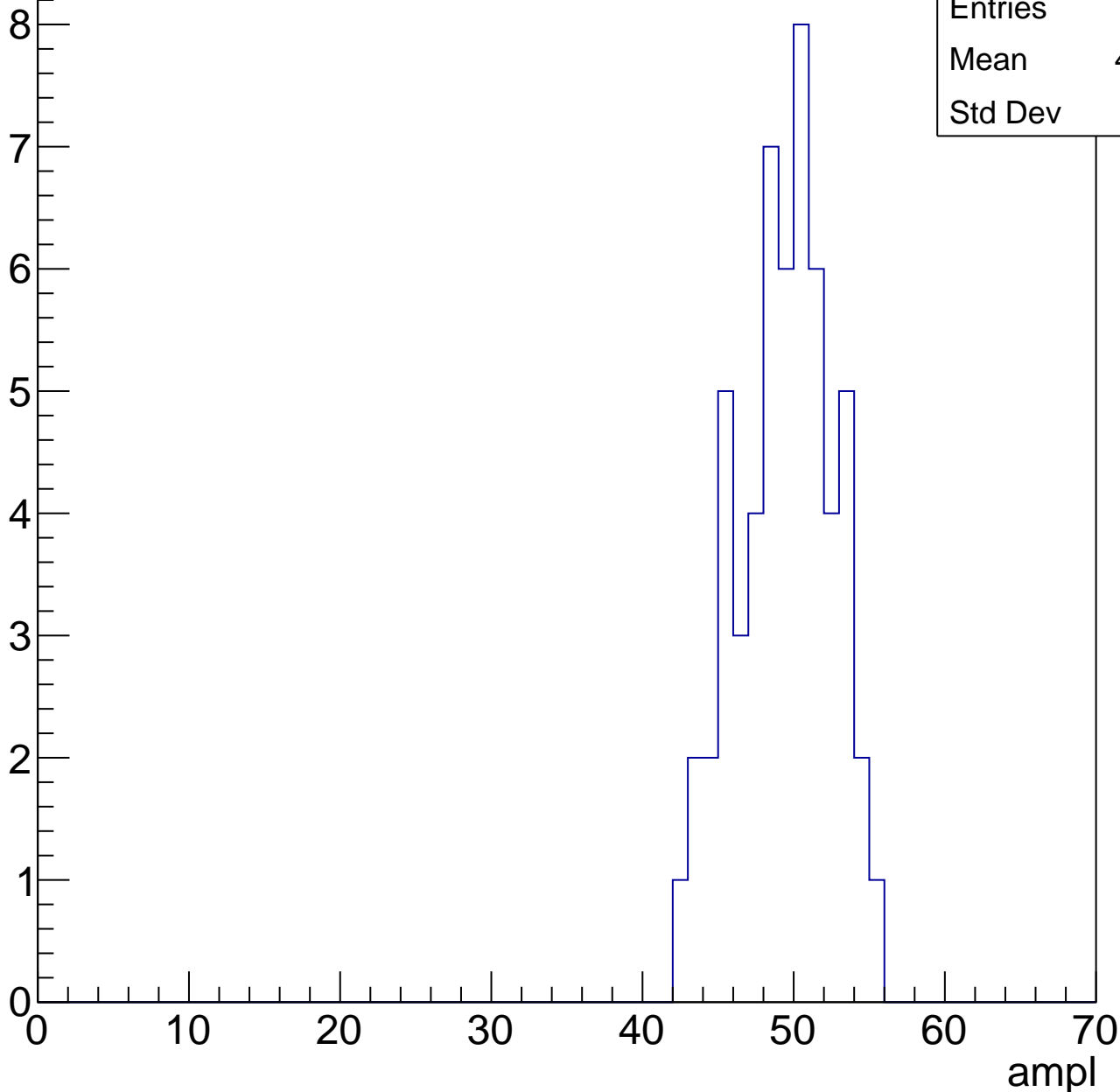


# B1L002S, U8-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

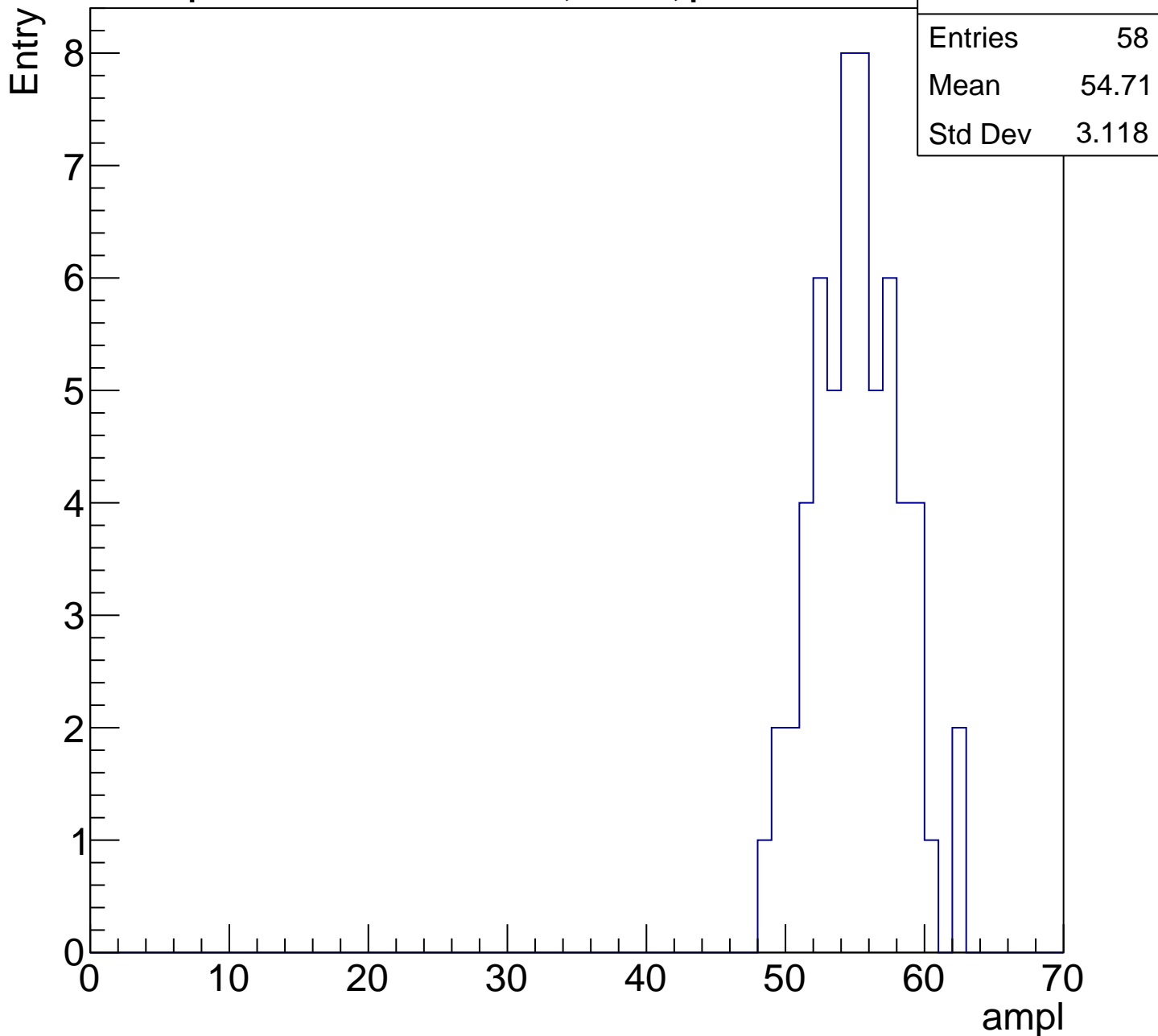
Entry

Entries	56
Mean	48.91
Std Dev	3.09



# B1L002S, U8-ch97, adc4

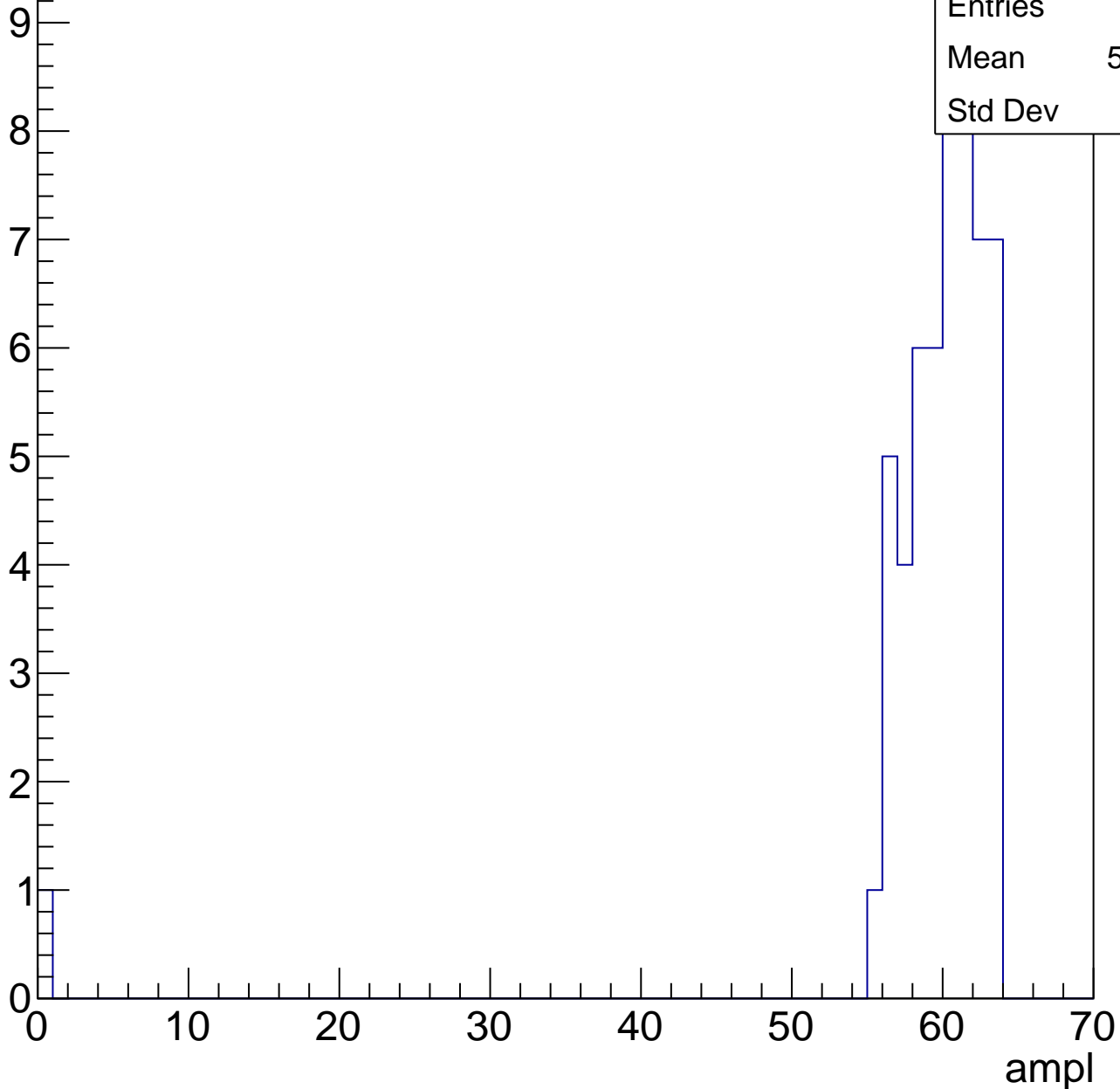
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U8-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	62.4
Std Dev	0.8



# B1L002S, U8-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch98, adc0

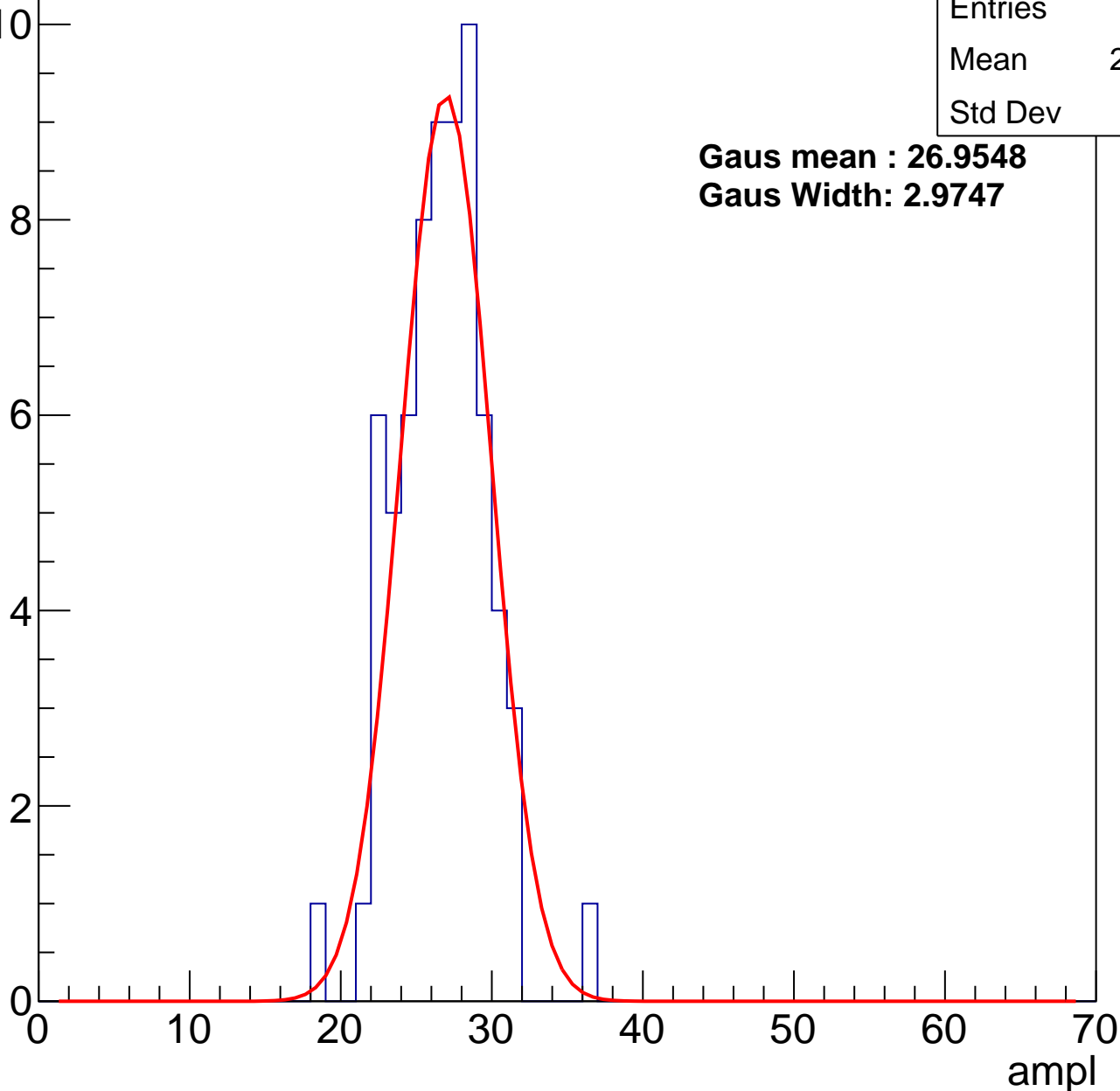
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	26.23
Std Dev	2.95

**Gaus mean : 26.9548**

**Gaus Width: 2.9747**



# B1L002S, U8-ch98, adc1

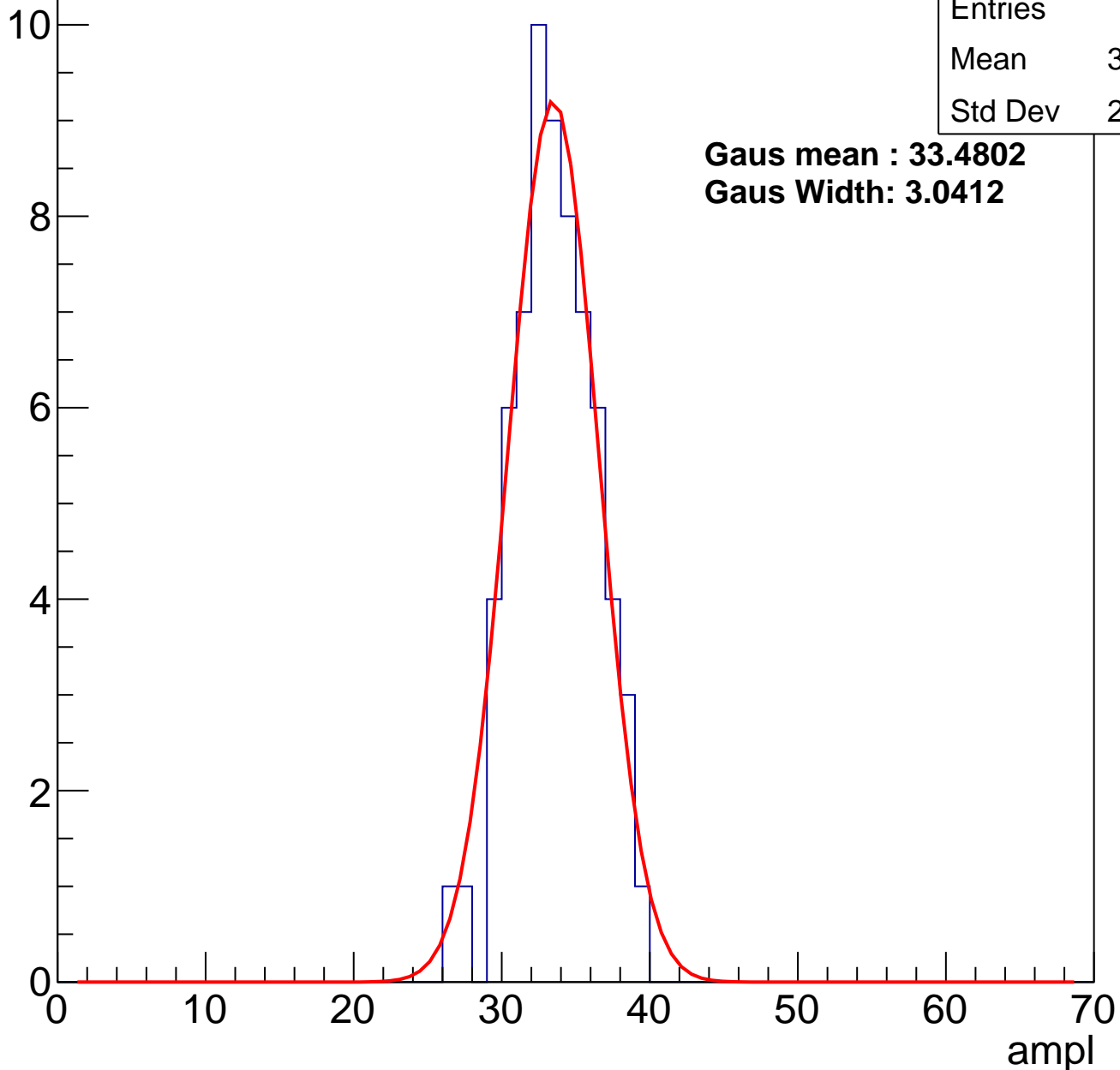
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	33.09
Std Dev	2.747

**Gaus mean : 33.4802**

**Gaus Width: 3.0412**

Entry



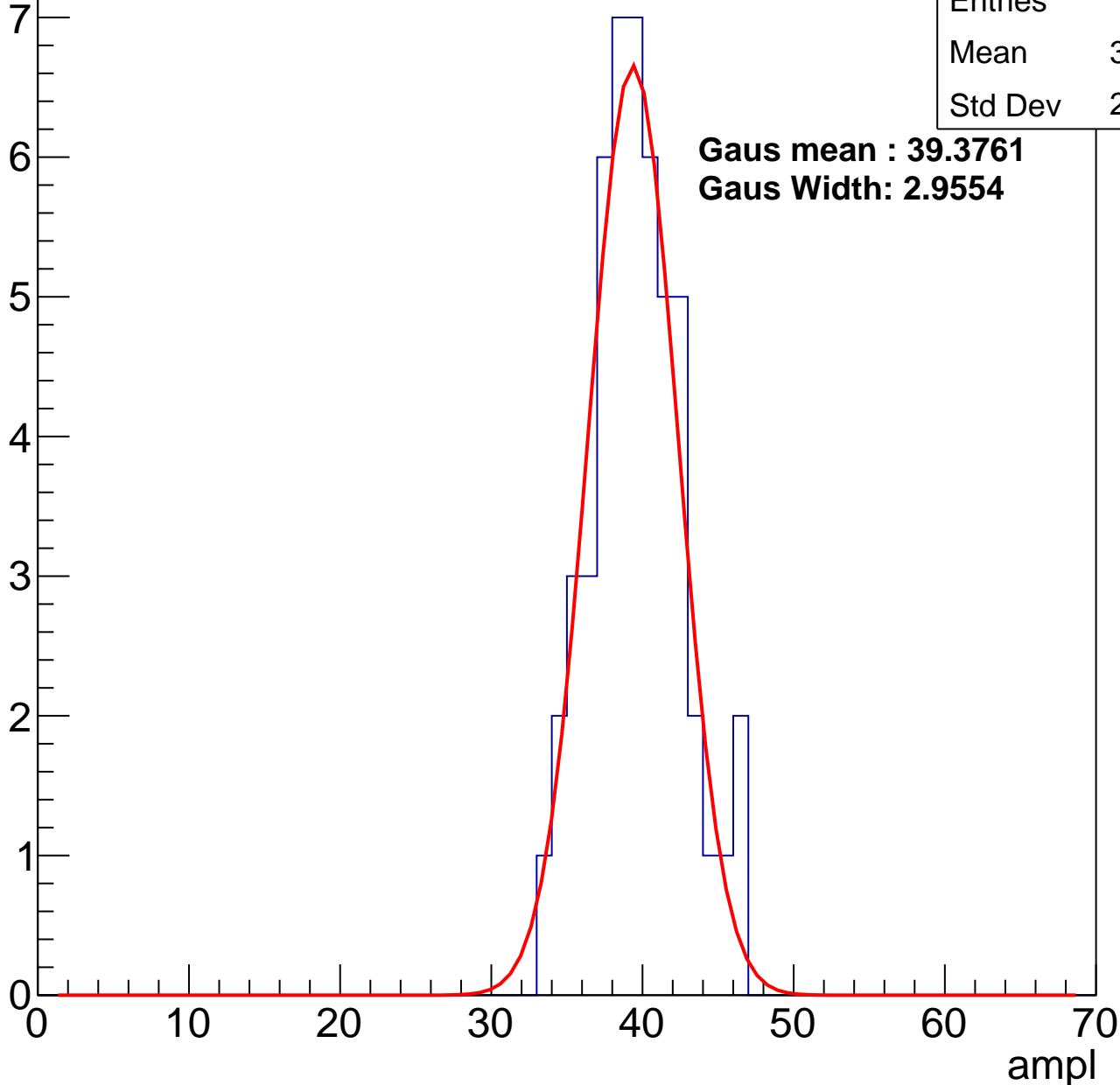
# B1L002S, U8-ch98, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	39.16
Std Dev	2.979

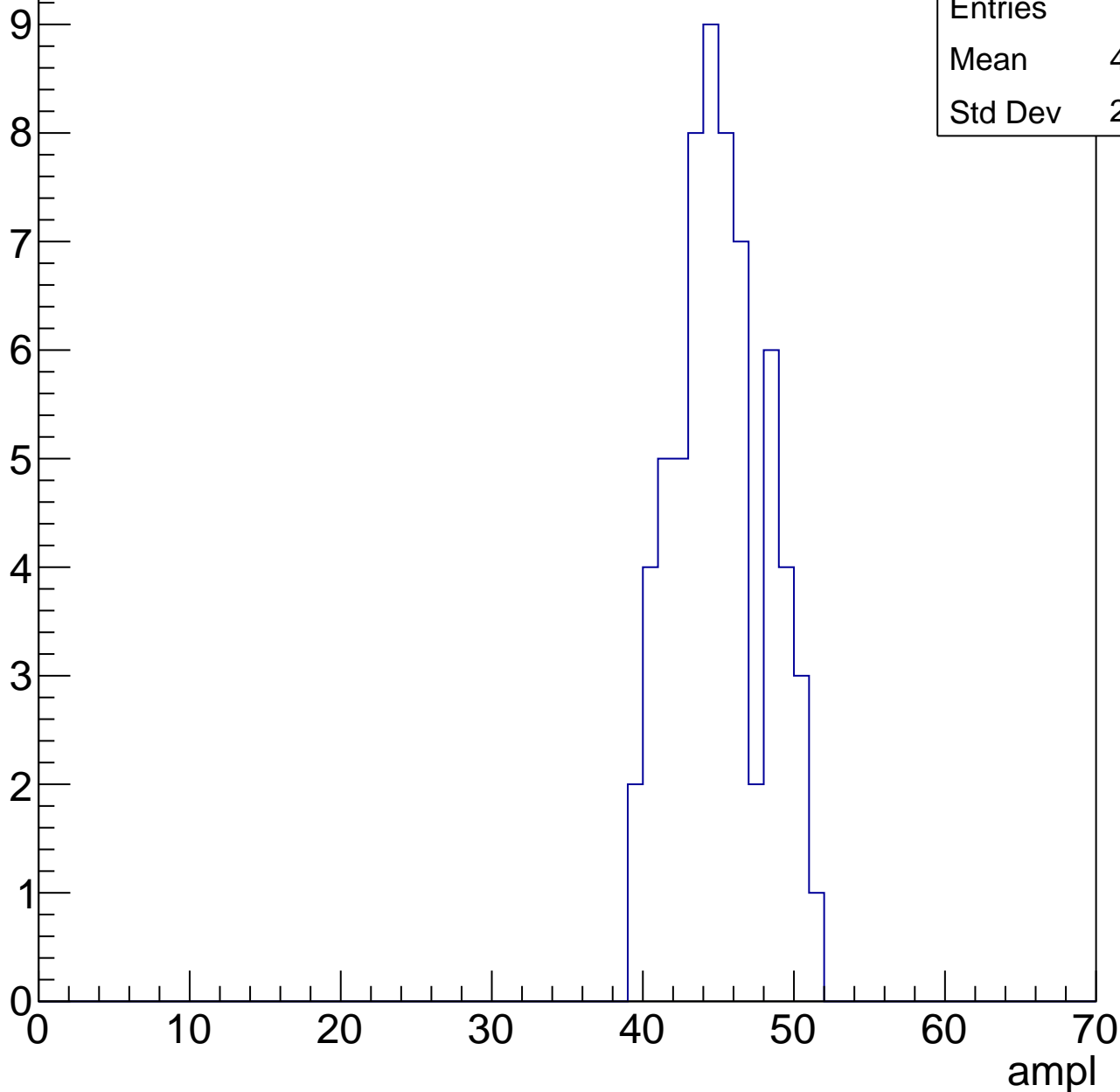
**Gaus mean : 39.3761**  
**Gaus Width: 2.9554**



# B1L002S, U8-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



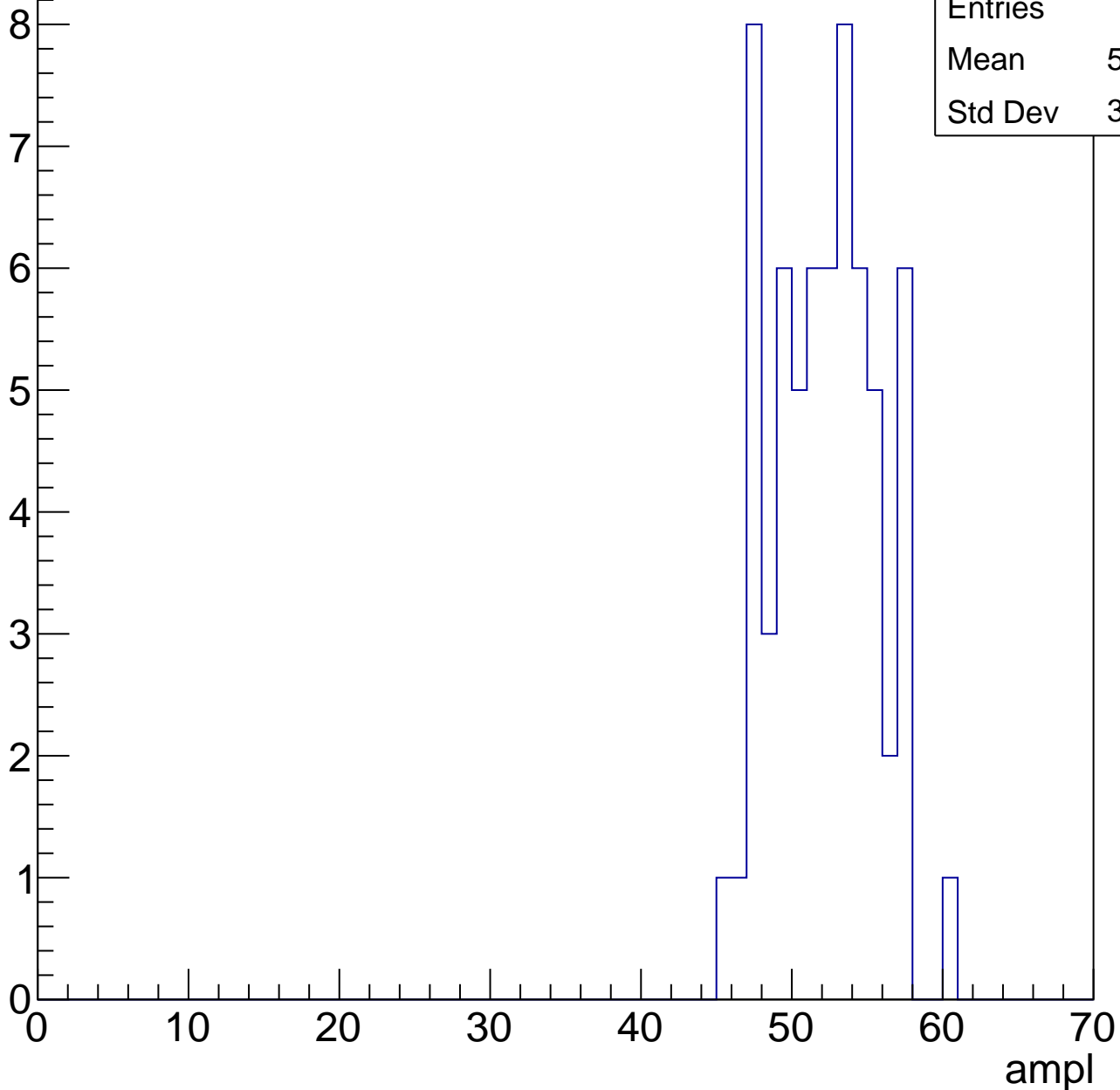
Entries	64
Mean	44.59
Std Dev	2.988

# B1L002S, U8-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

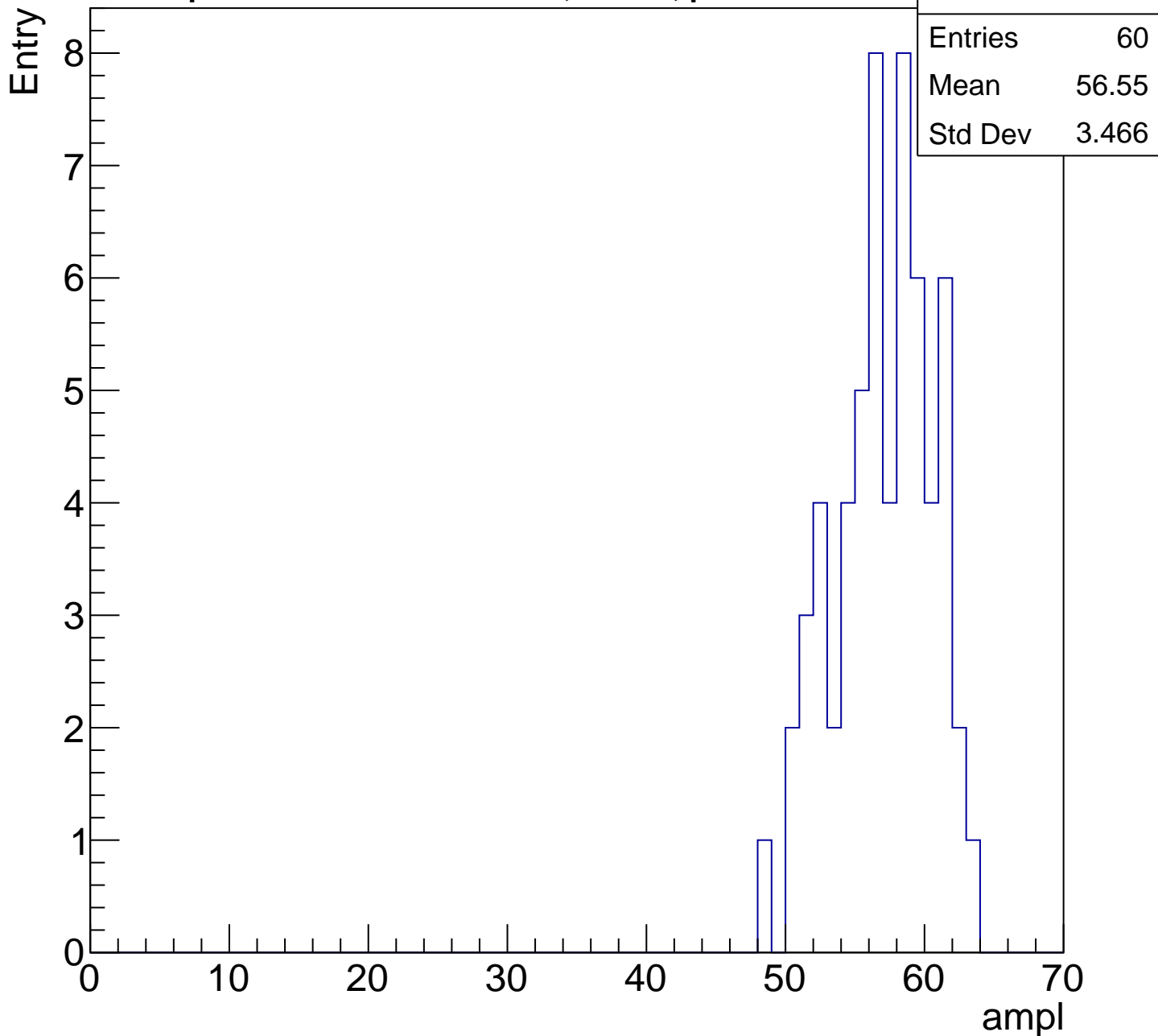
Entry

Entries	64
Mean	51.72
Std Dev	3.379



# B1L002S, U8-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

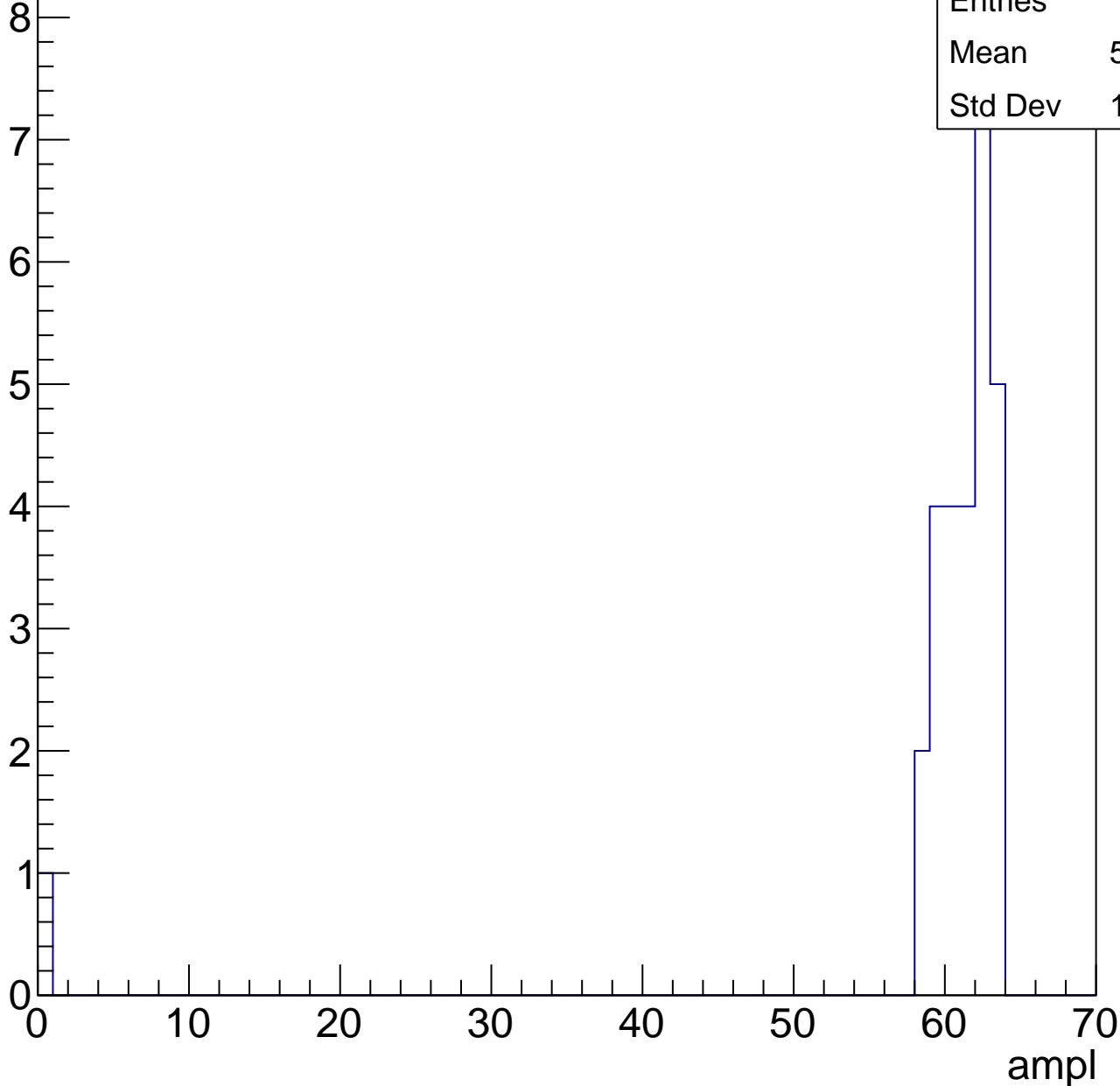


# B1L002S, U8-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.82
Std Dev	11.42

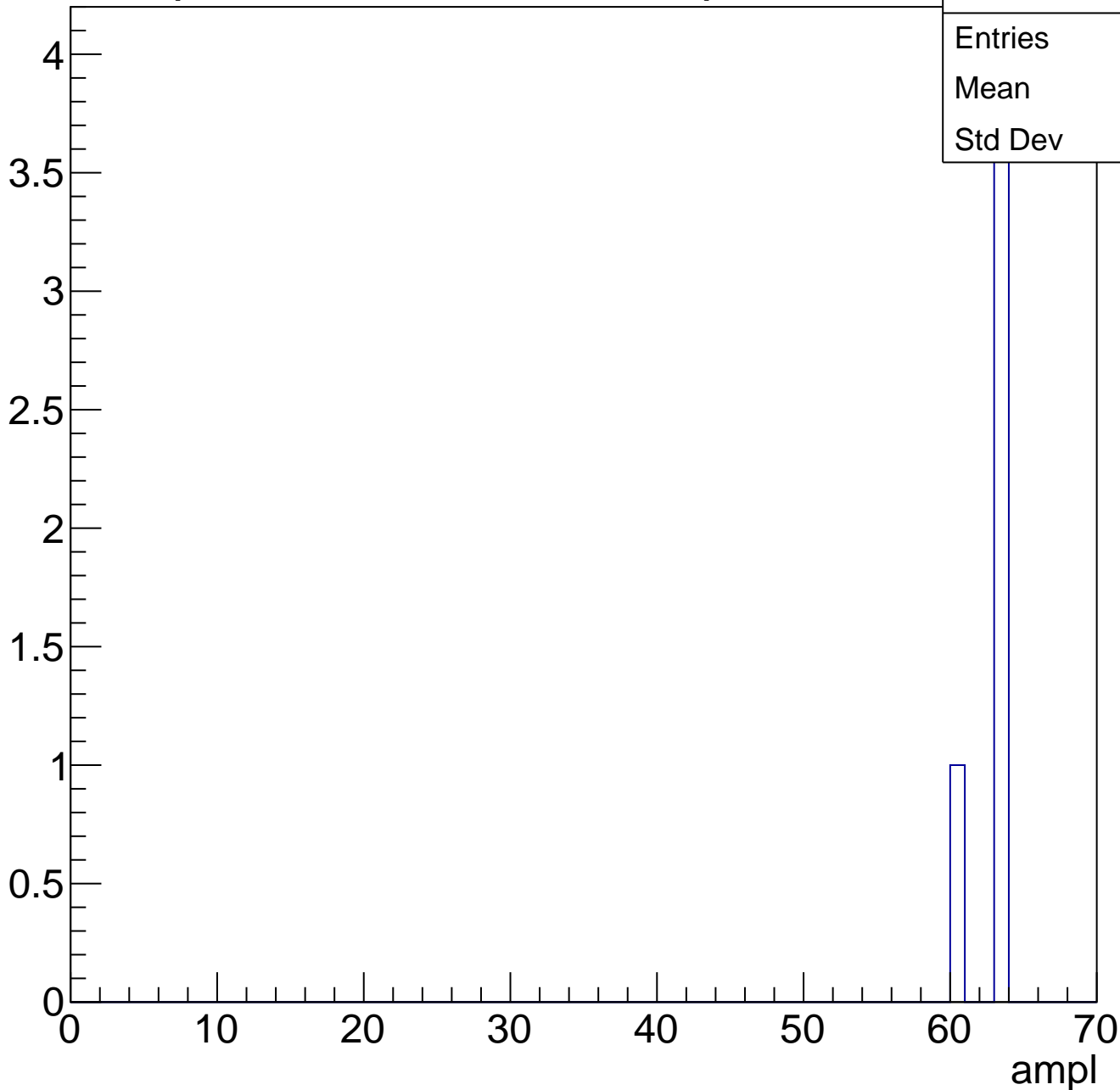




# B1L002S, U8-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch99, adc0

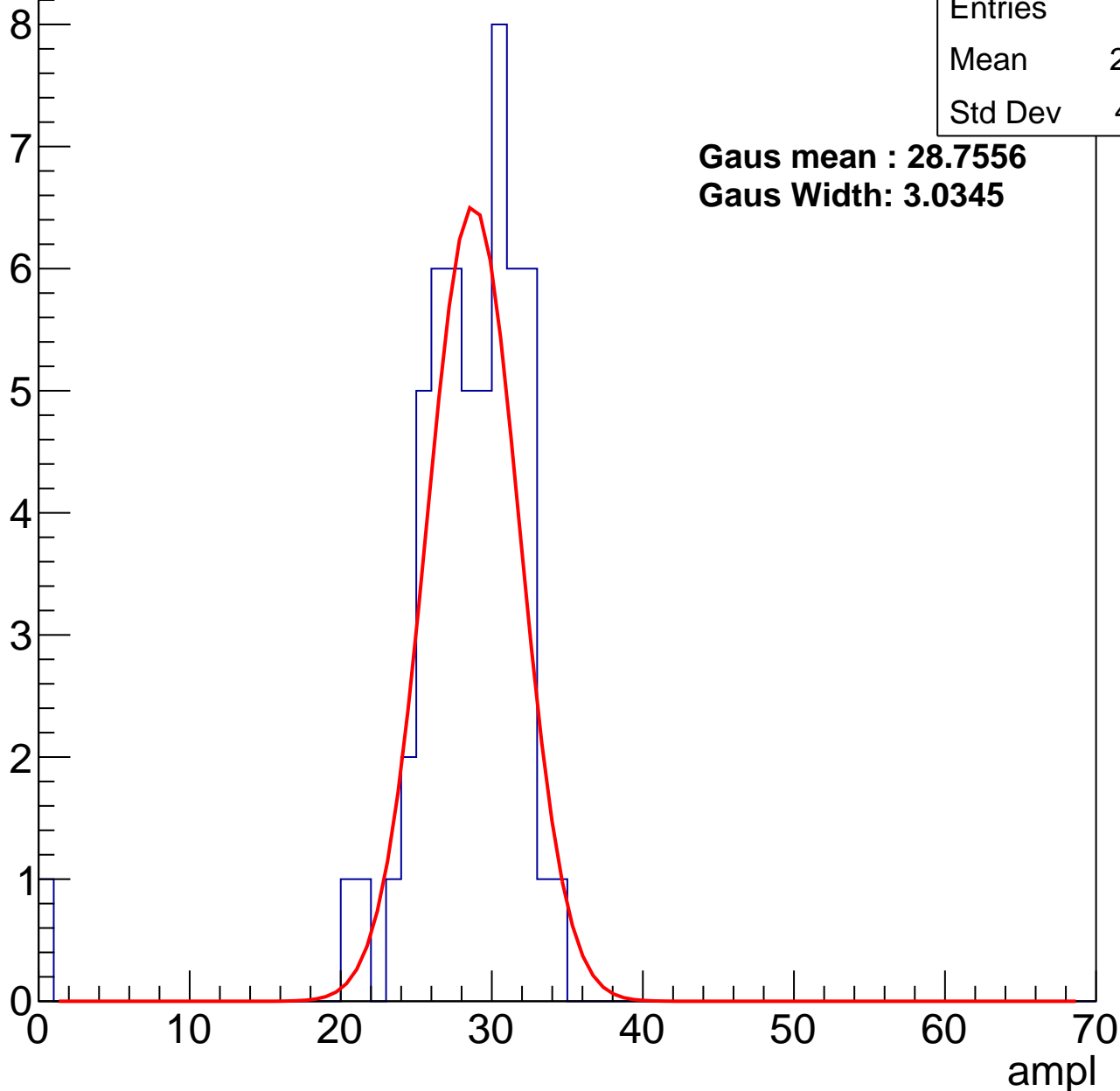
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.73
Std Dev	4.811

**Gaus mean : 28.7556**

**Gaus Width: 3.0345**



# B1L002S, U8-ch99, adc1

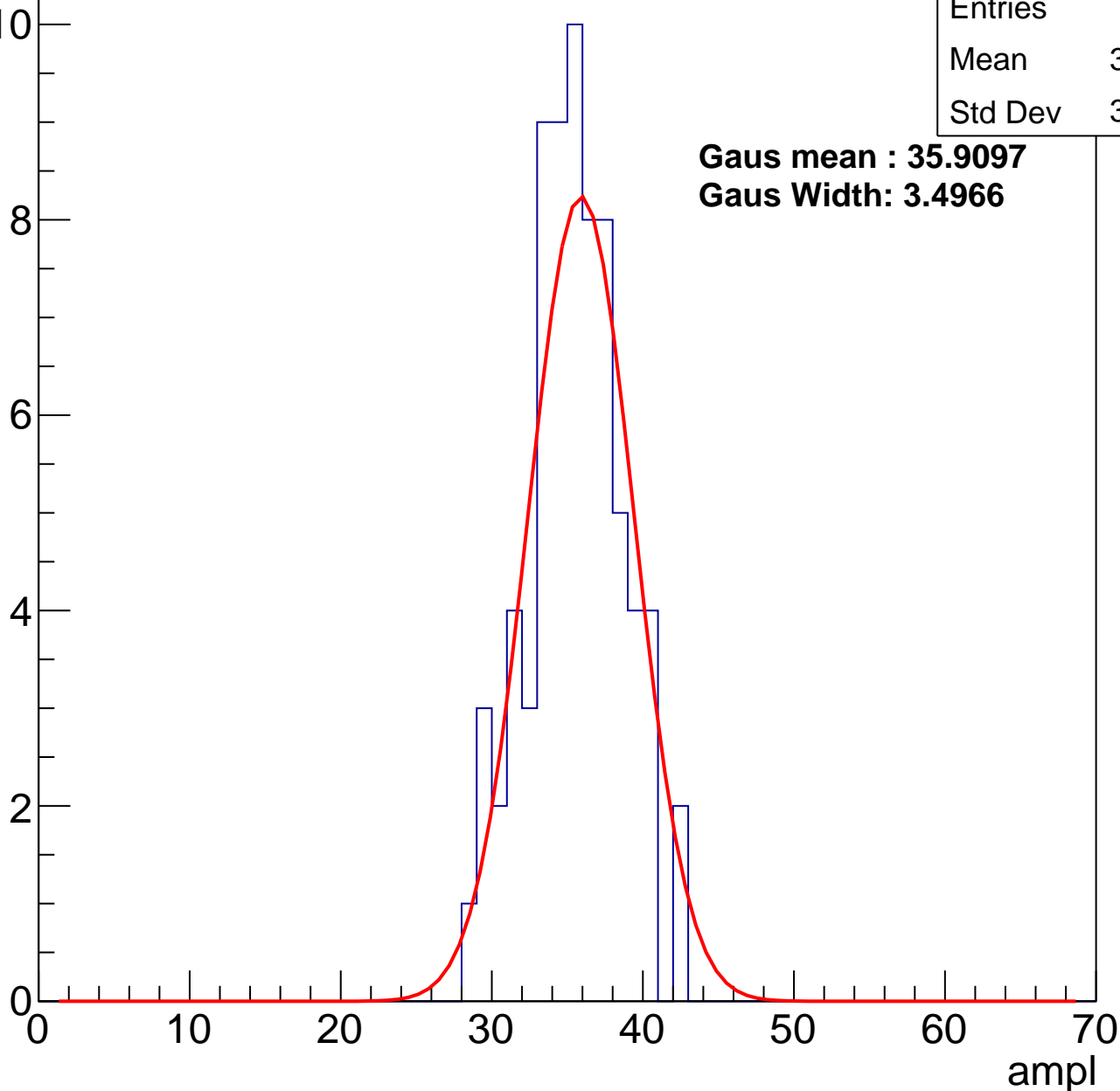
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	35.03
Std Dev	3.096

**Gaus mean : 35.9097**

**Gaus Width: 3.4966**



# B1L002S, U8-ch99, adc2

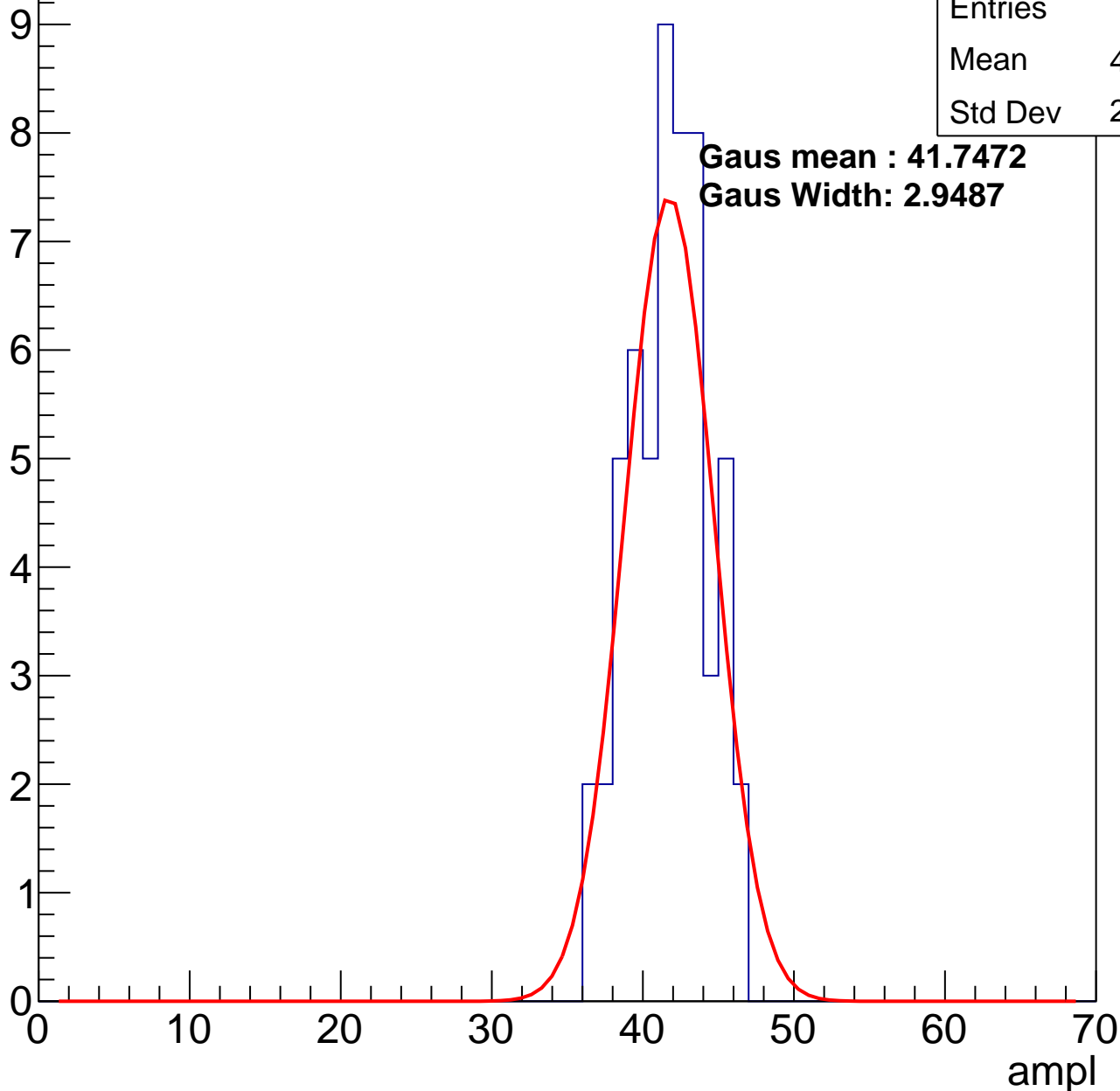
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.24
Std Dev	2.522

**Gaus mean : 41.7472**

**Gaus Width: 2.9487**

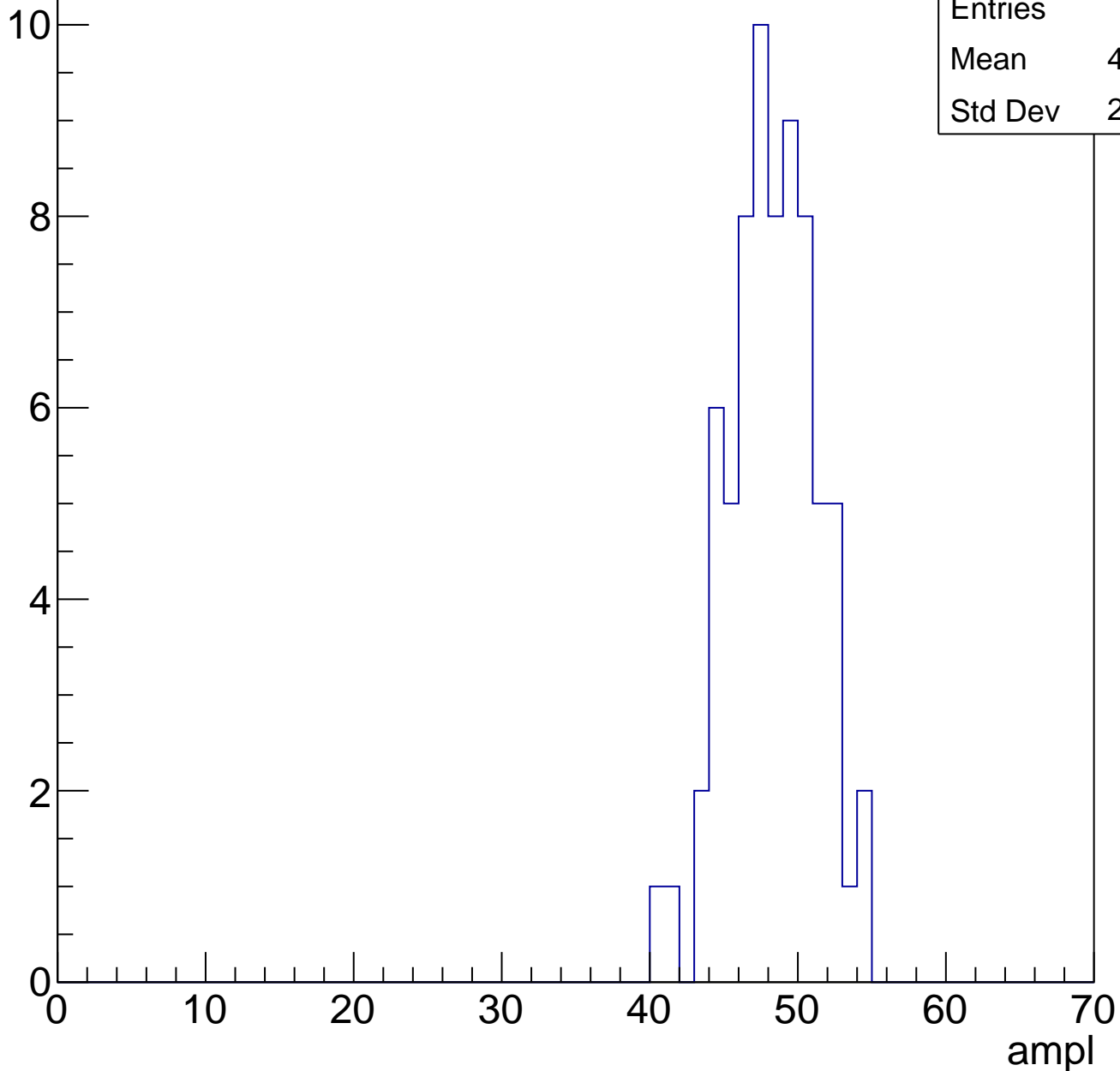


# B1L002S, U8-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	47.82
Std Dev	2.923

Entry

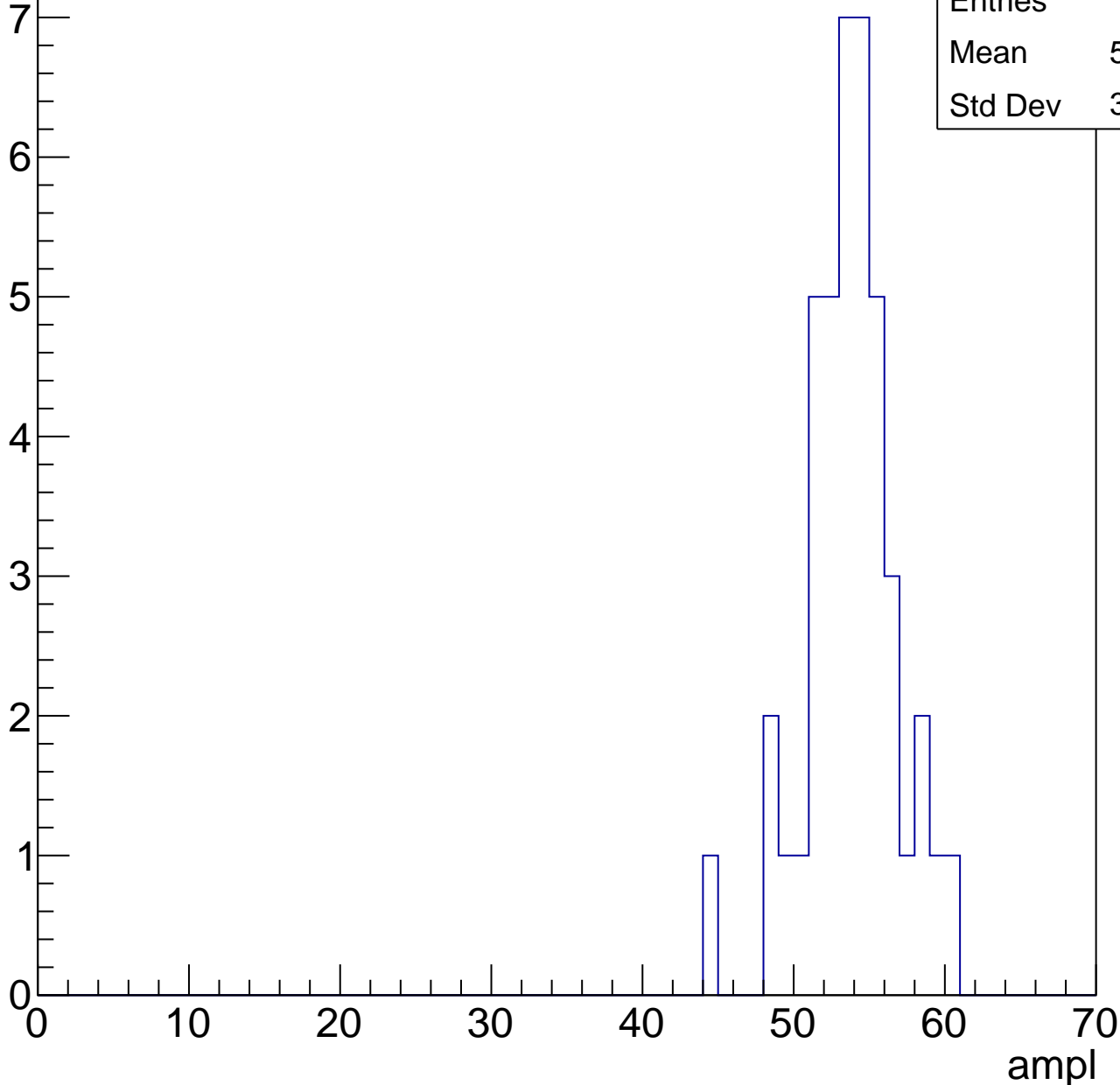


# B1L002S, U8-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

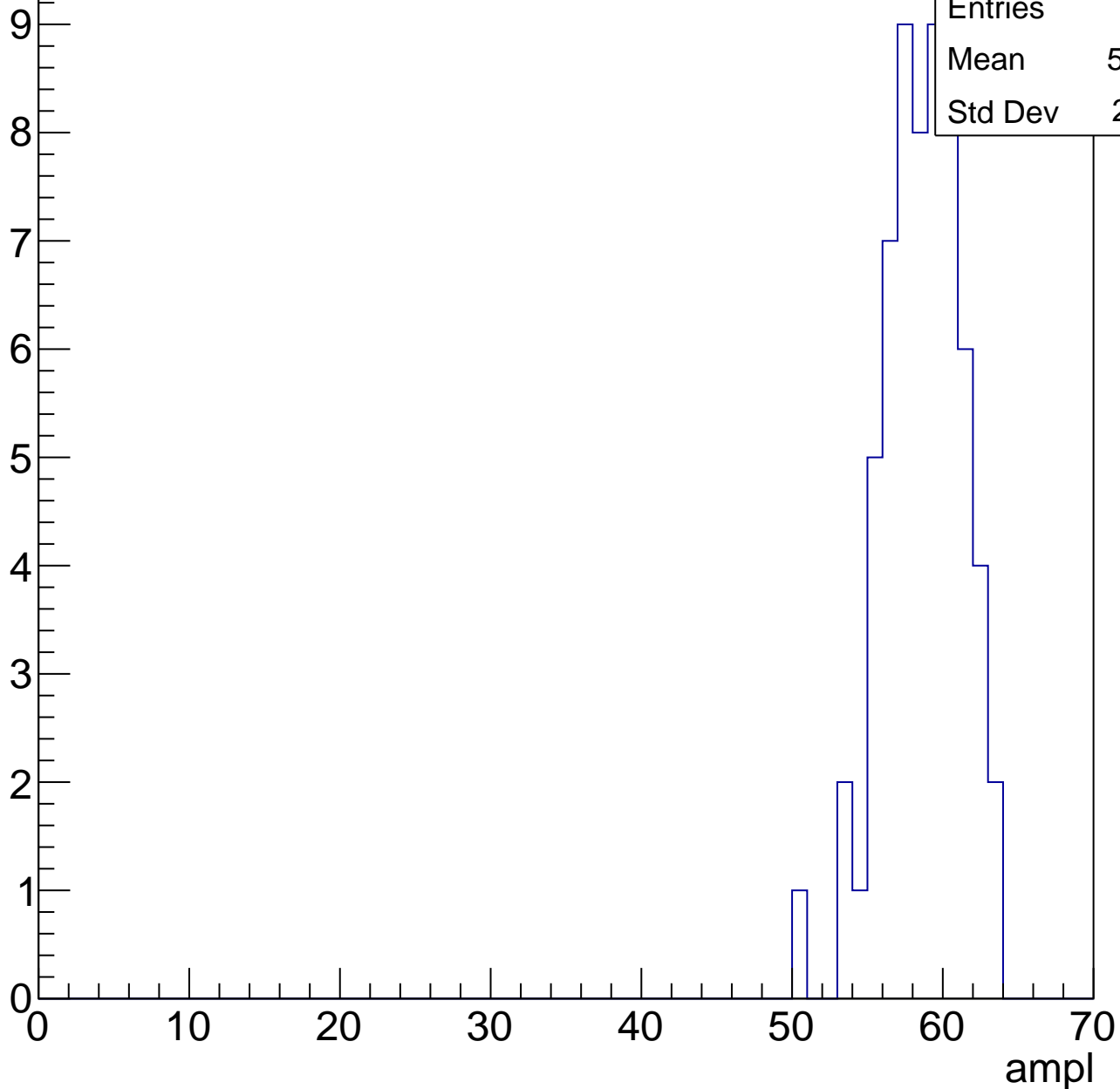
Entries	42
Mean	53.29
Std Dev	3.018



# B1L002S, U8-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

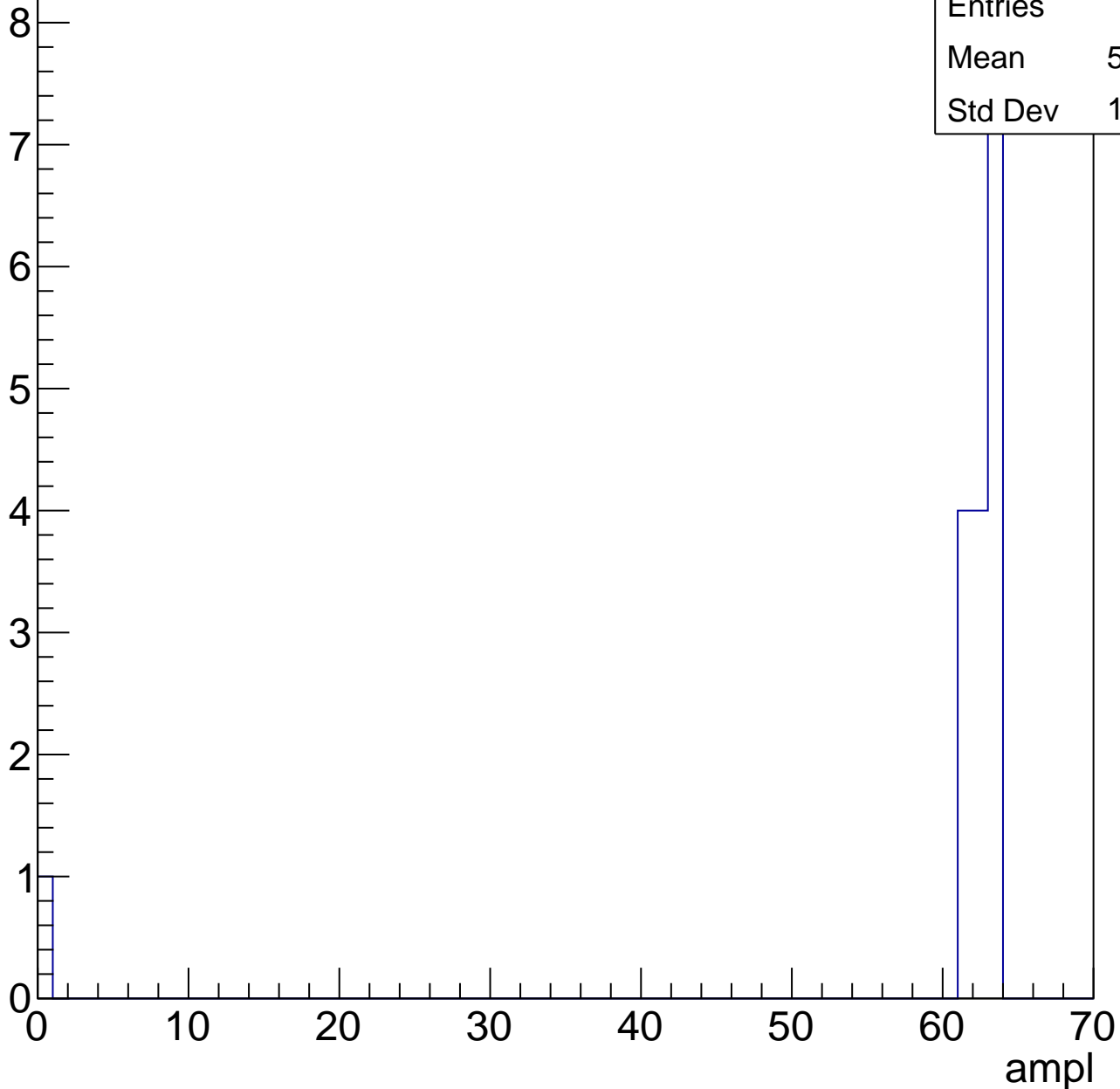


# B1L002S, U8-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58.59
Std Dev	14.67





# B1L002S, U8-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	62
Std Dev	0

0 10 20 30 40 50 60 70

ampl

# B1L002S, U8-ch100, adc0

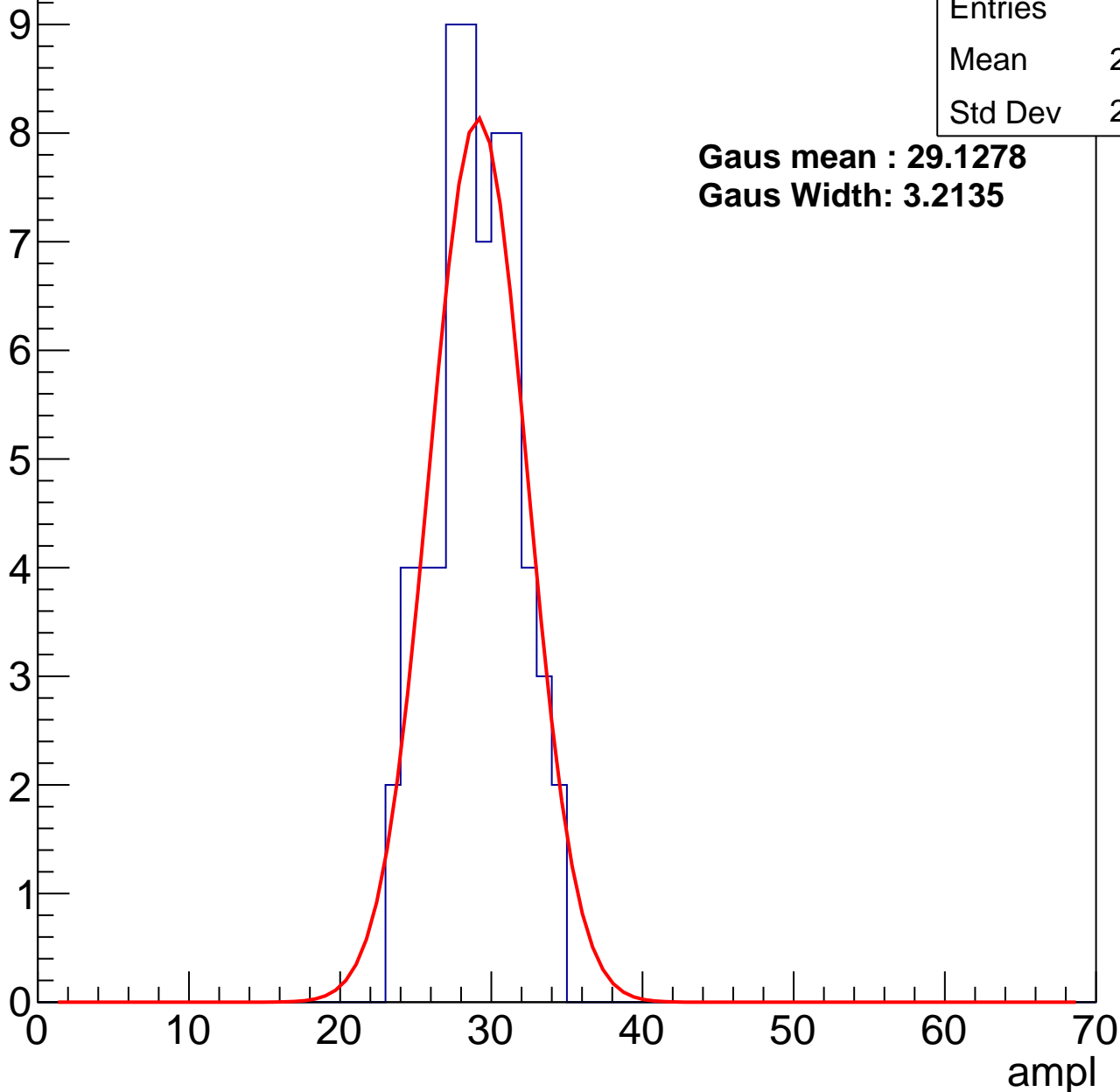
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.55
Std Dev	2.732

**Gaus mean : 29.1278**

**Gaus Width: 3.2135**



# B1L002S, U8-ch100, adc1

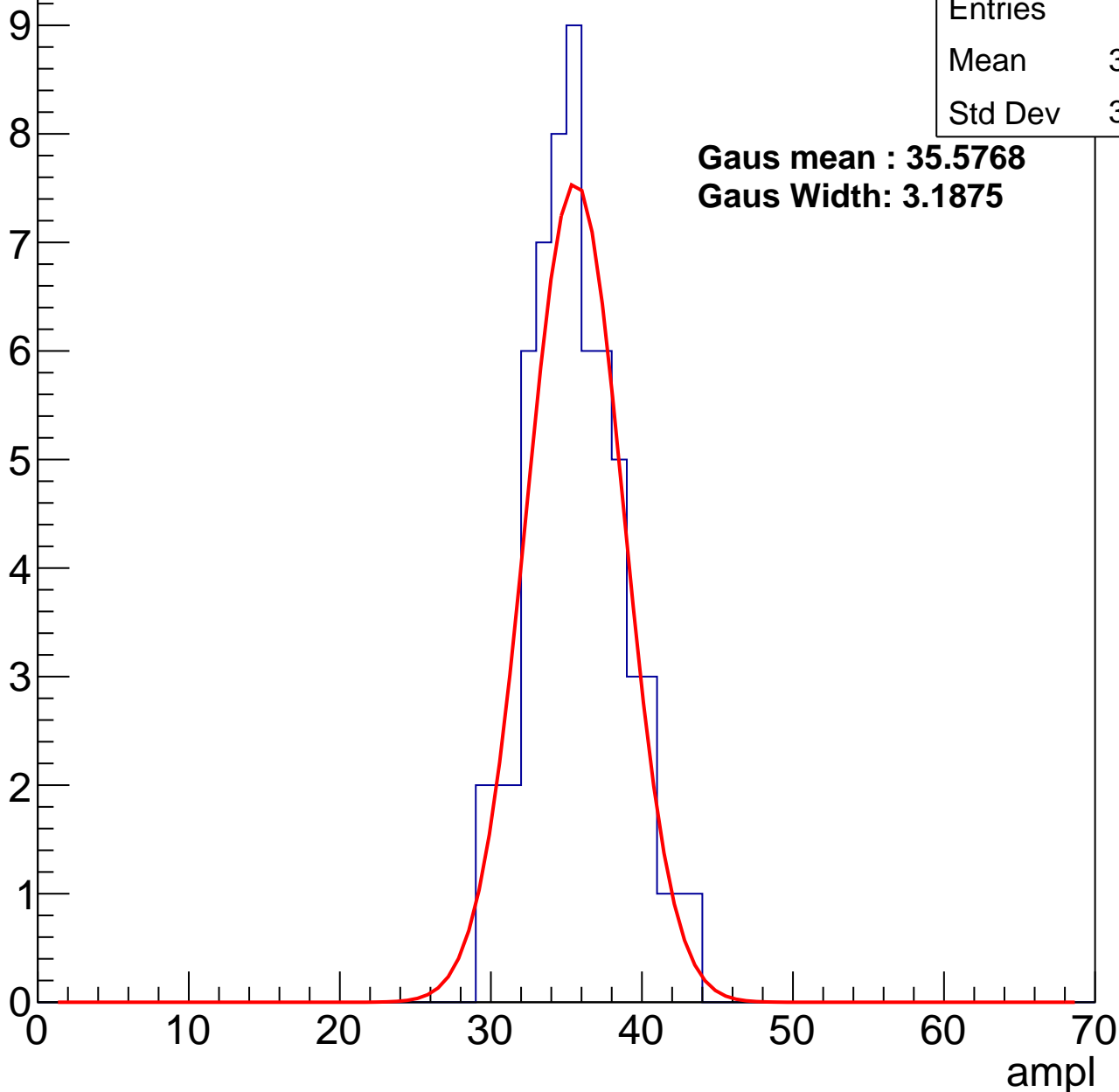
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	35.18
Std Dev	3.082

**Gaus mean : 35.5768**

**Gaus Width: 3.1875**



# B1L002S, U8-ch100, adc2

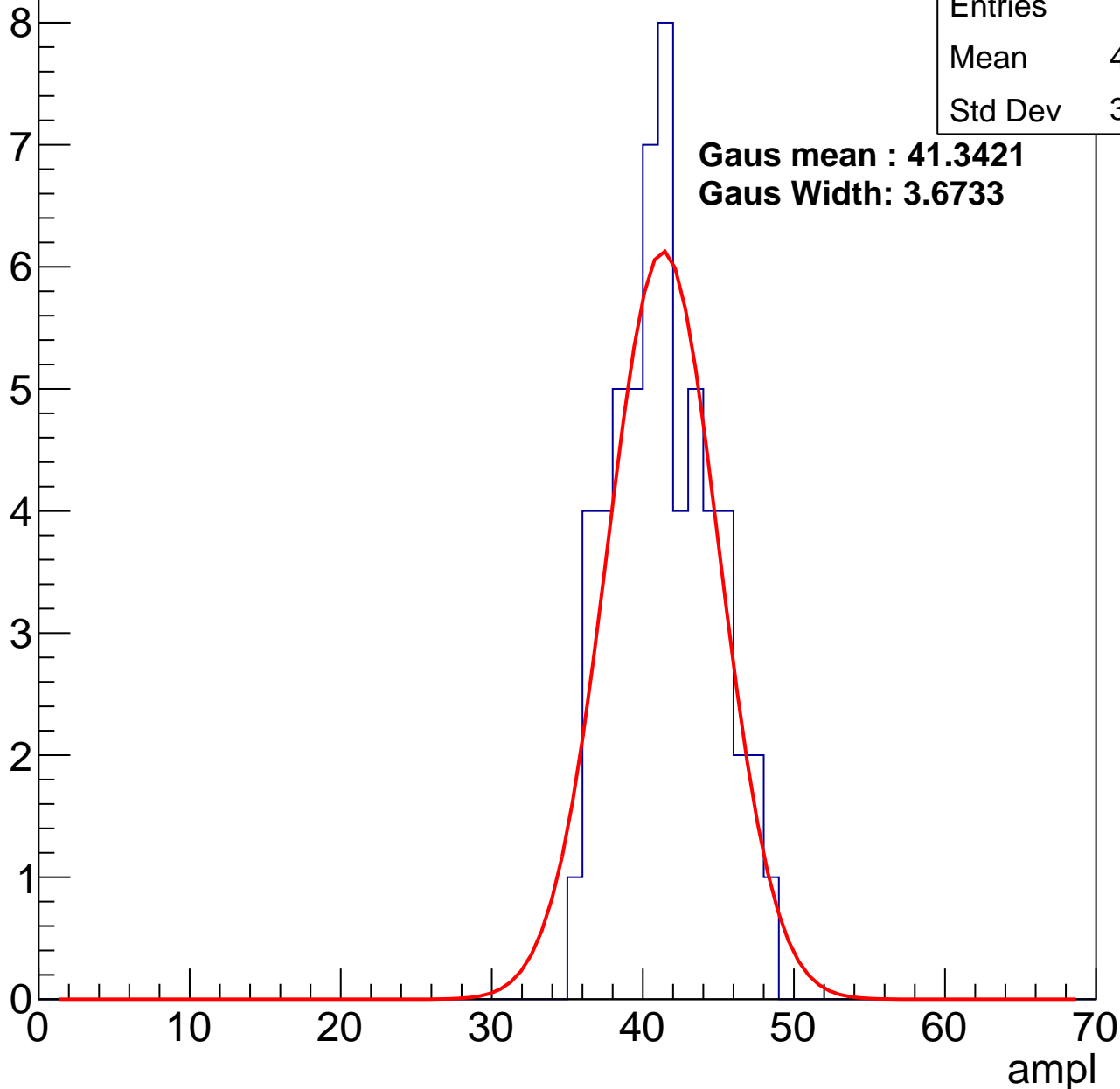
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.95
Std Dev	3.182

**Gaus mean : 41.3421**

**Gaus Width: 3.6733**

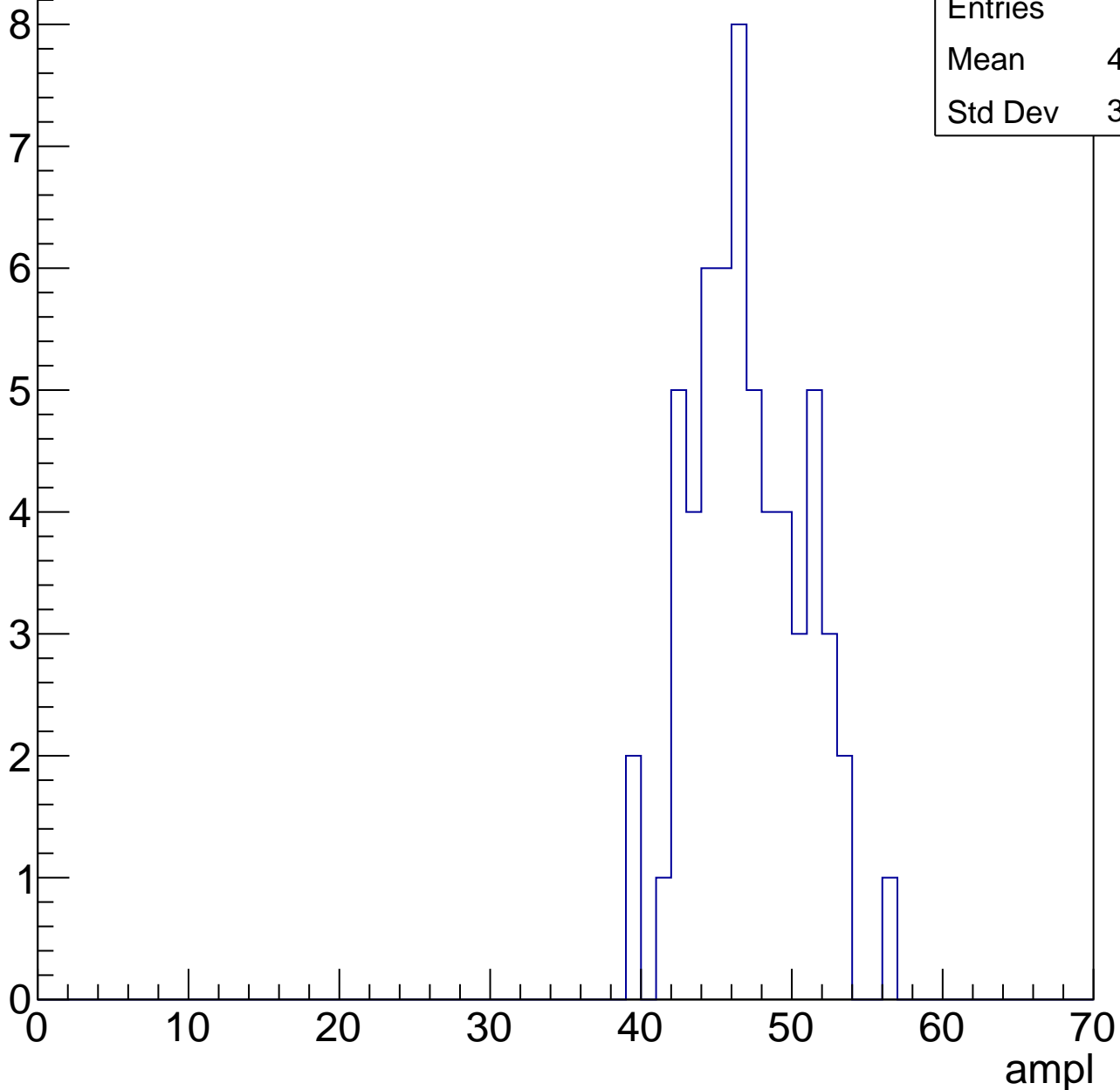


# B1L002S, U8-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

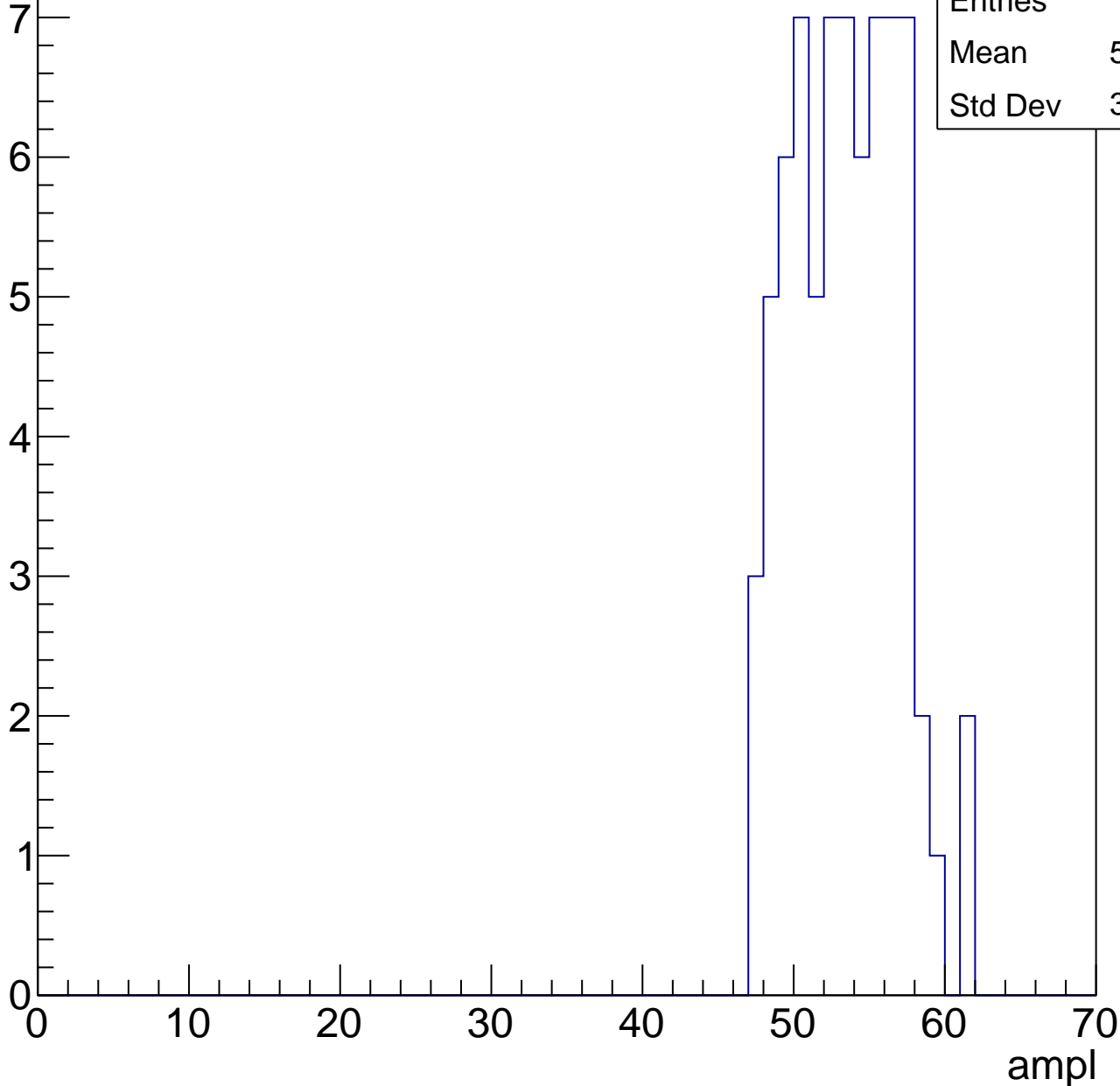
Entries	59
Mean	46.59
Std Dev	3.646



# B1L002S, U8-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



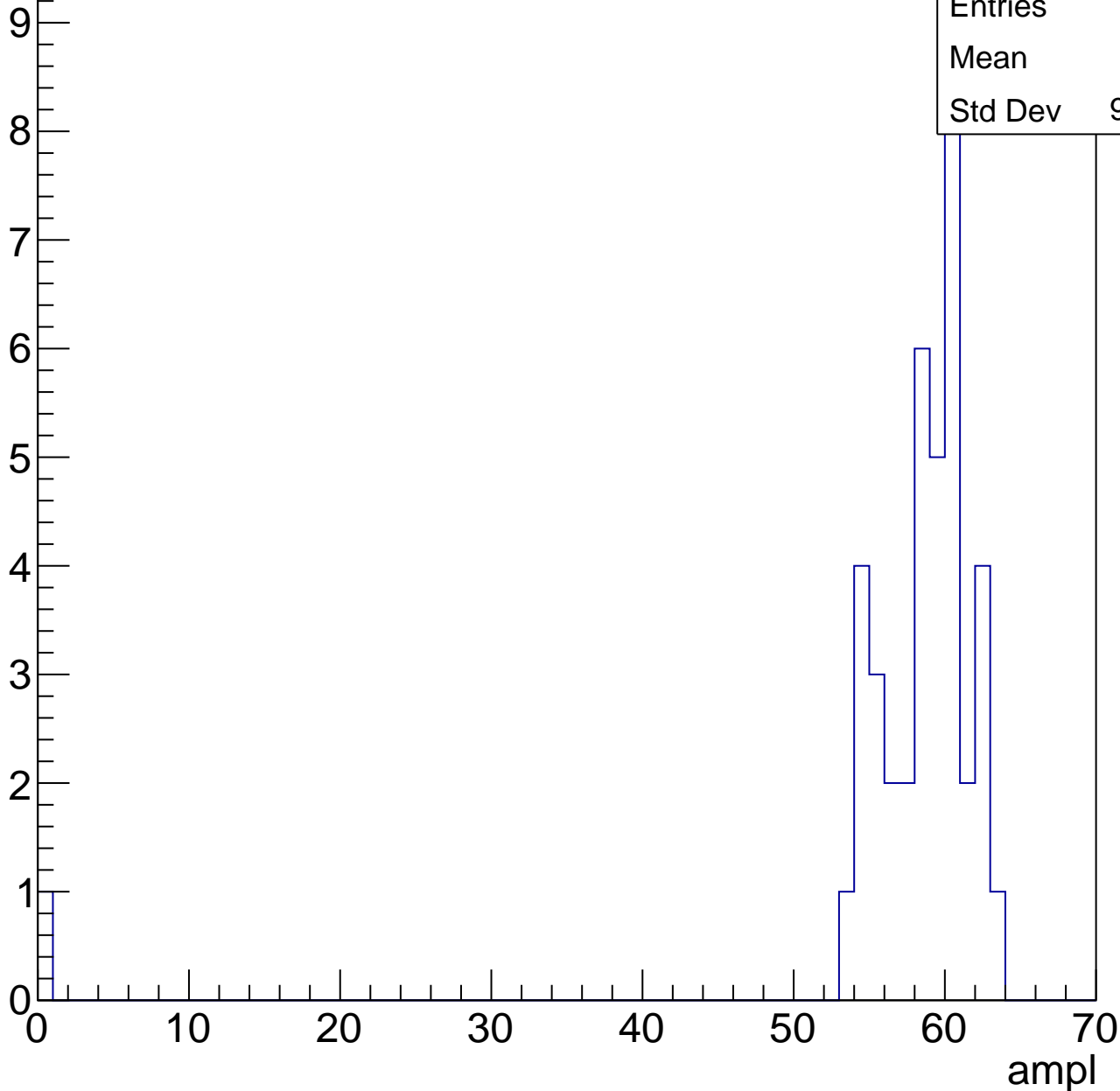
Entries	72
Mean	52.94
Std Dev	3.419

# B1L002S, U8-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	56.9
Std Dev	9.473

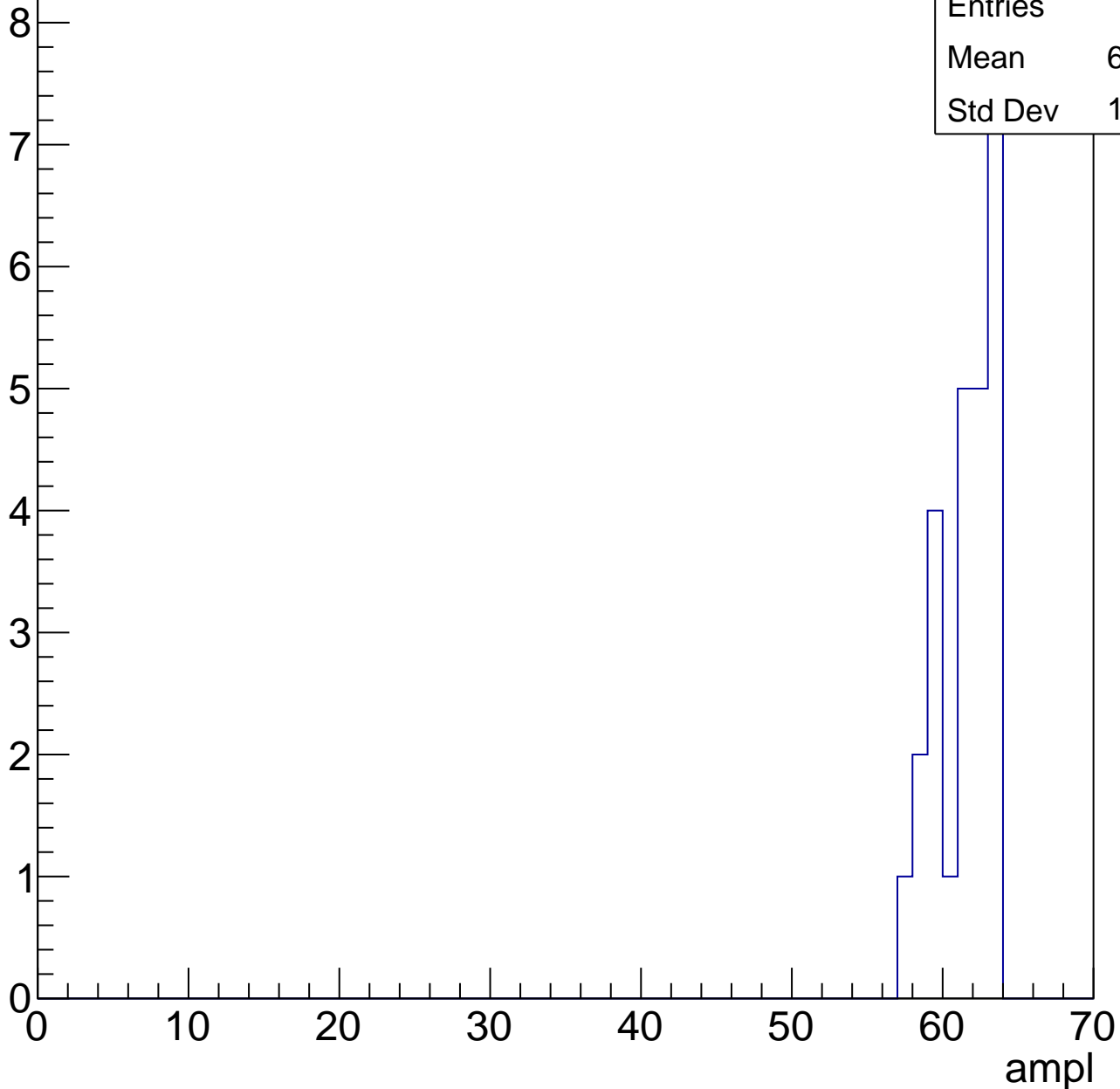


# B1L002S, U8-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.08
Std Dev	1.838

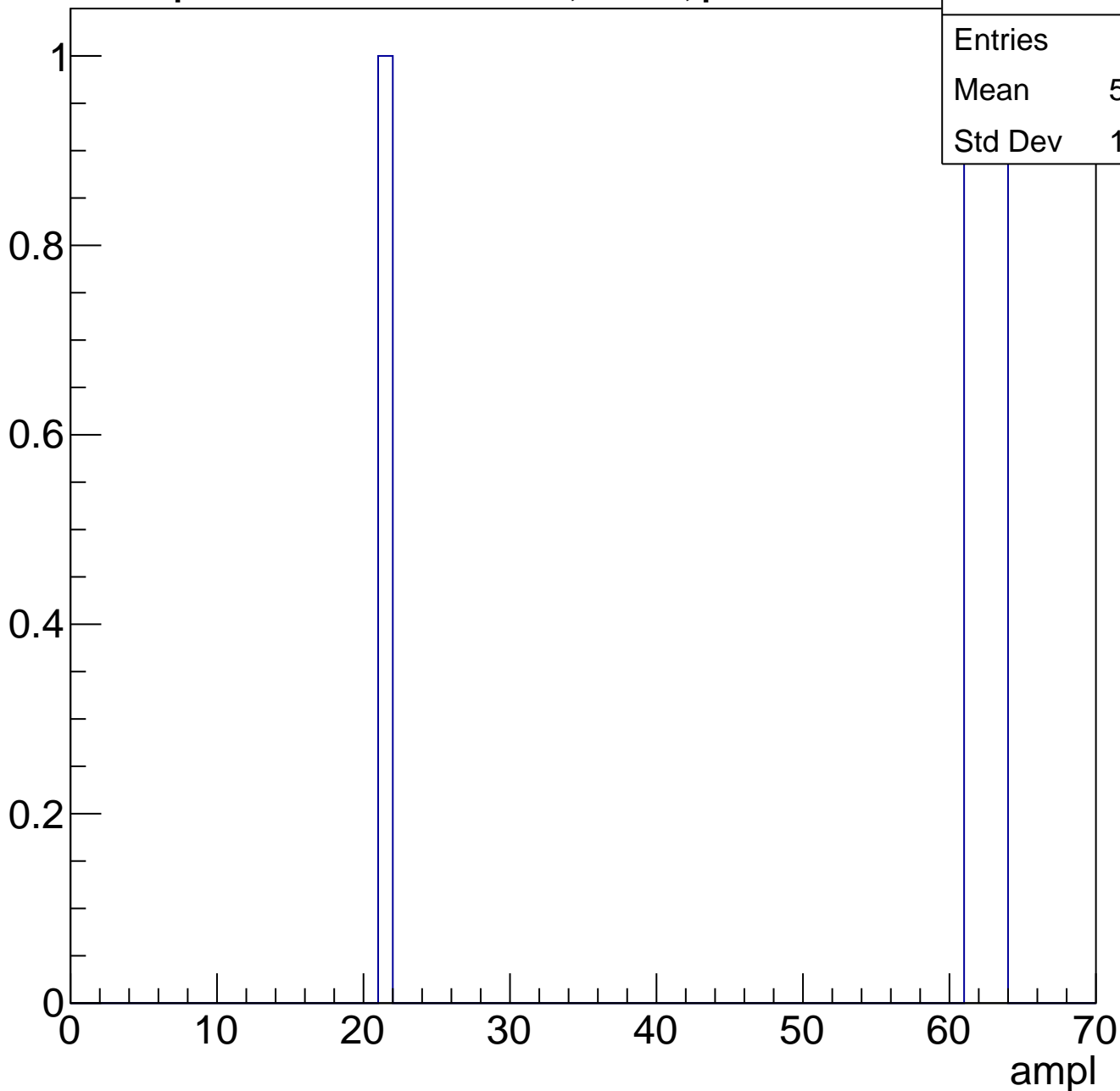




# B1L002S, U8-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch101, adc0

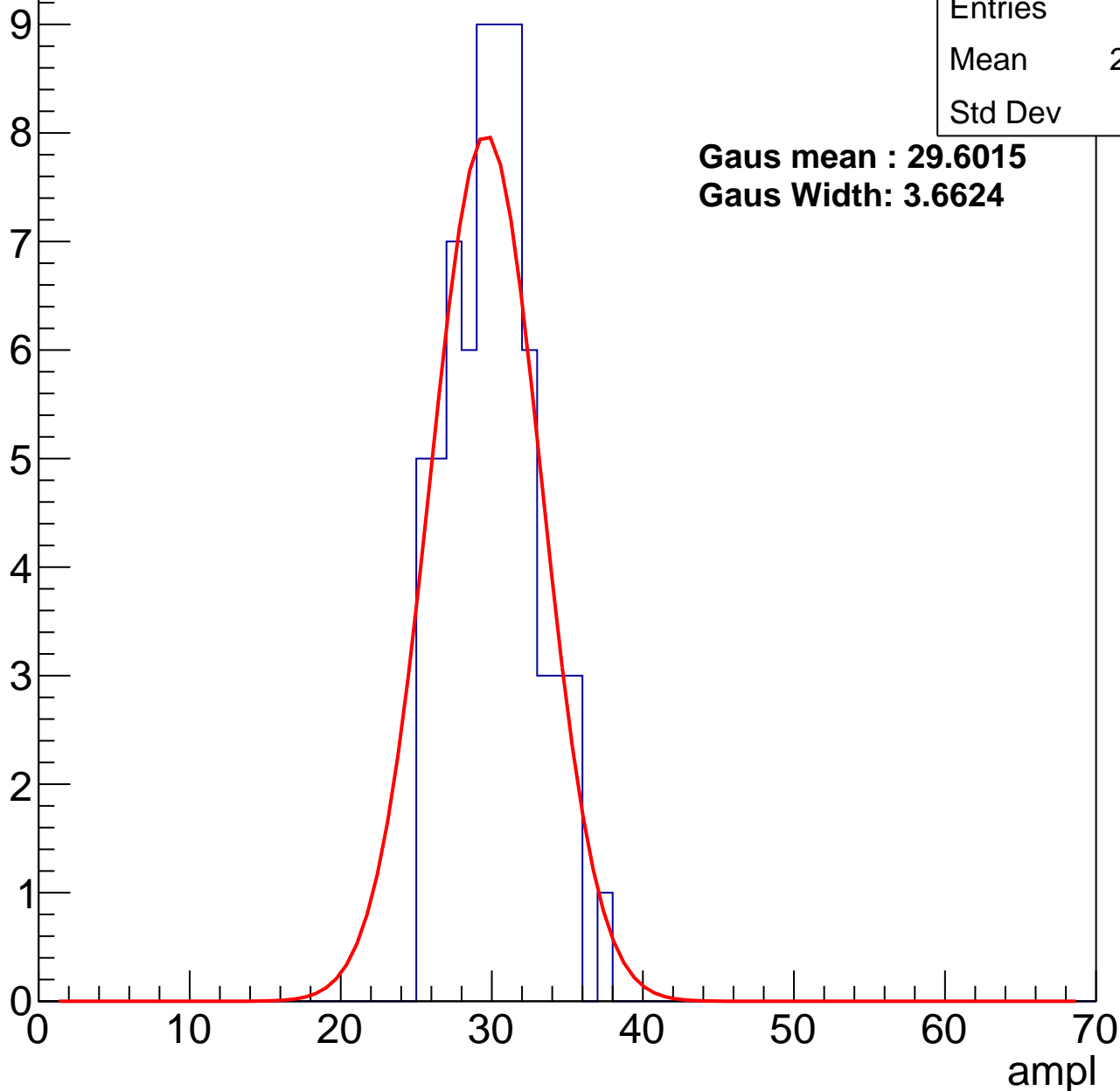
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	29.65
Std Dev	2.82

**Gaus mean : 29.6015**

**Gaus Width: 3.6624**



# B1L002S, U8-ch101, adc1

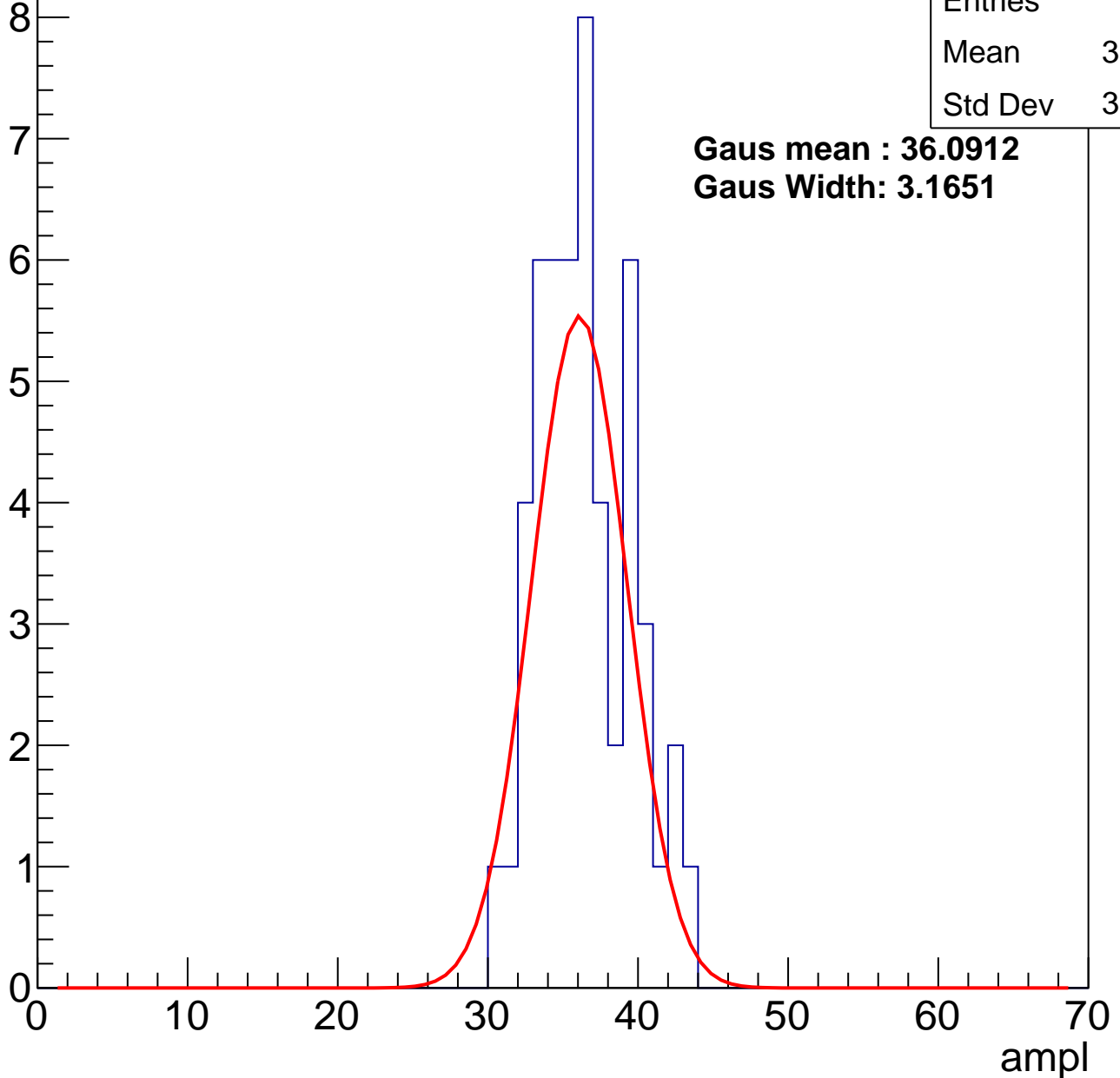
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	35.98
Std Dev	3.032

**Gaus mean : 36.0912**

**Gaus Width: 3.1651**



# B1L002S, U8-ch101, adc2

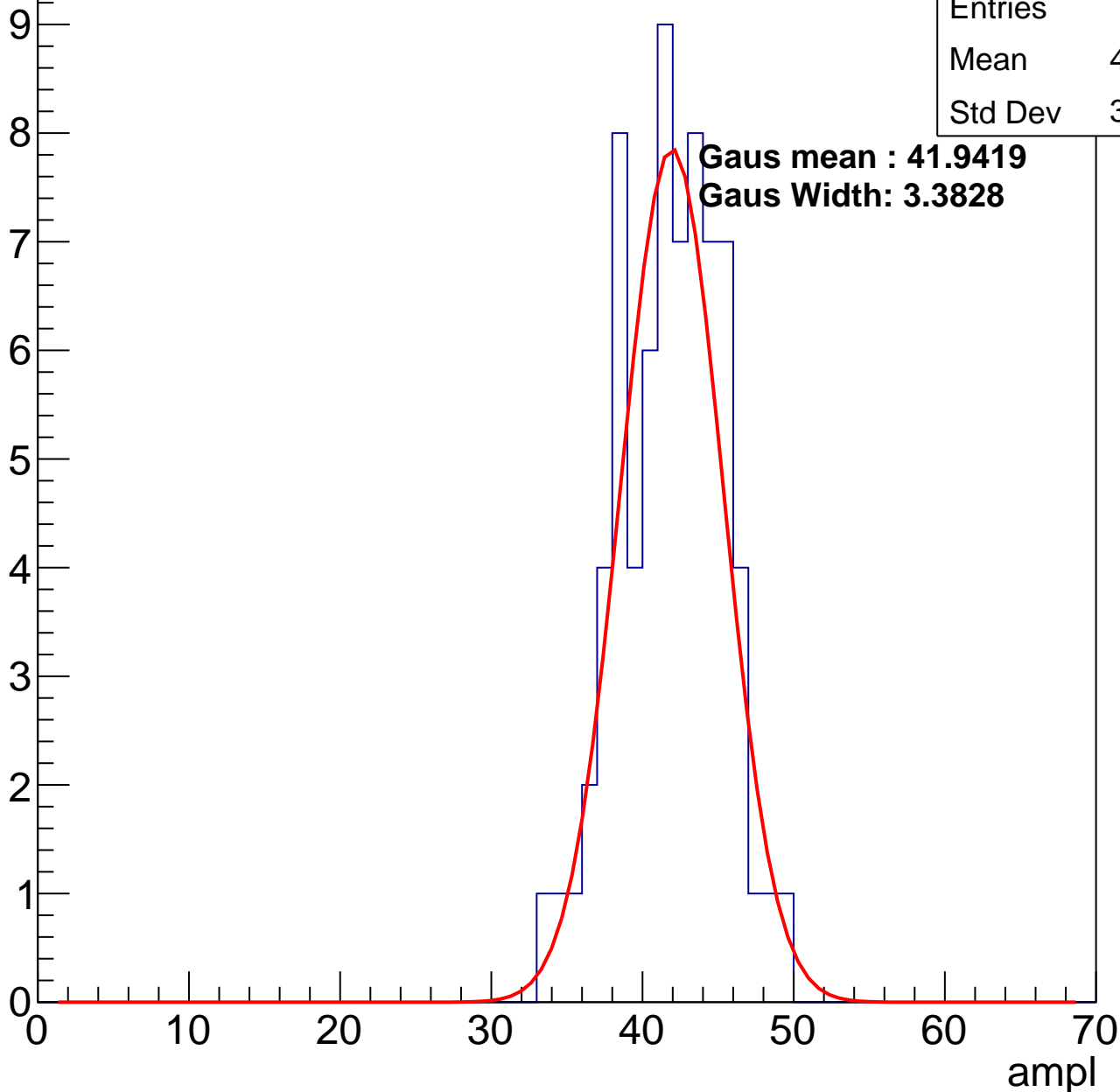
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	41.39
Std Dev	3.352

**Gaus mean : 41.9419**

**Gaus Width: 3.3828**

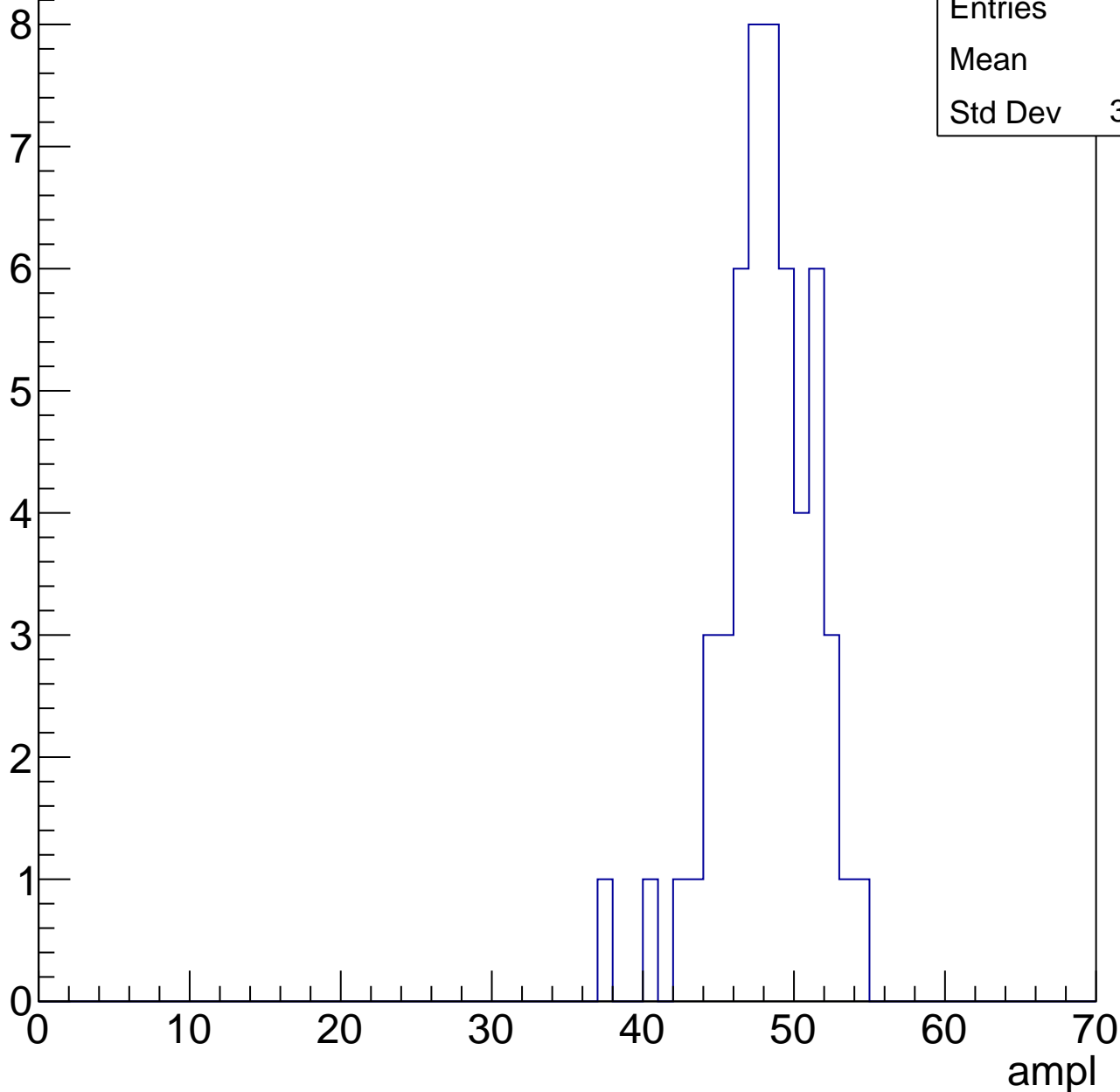


# B1L002S, U8-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

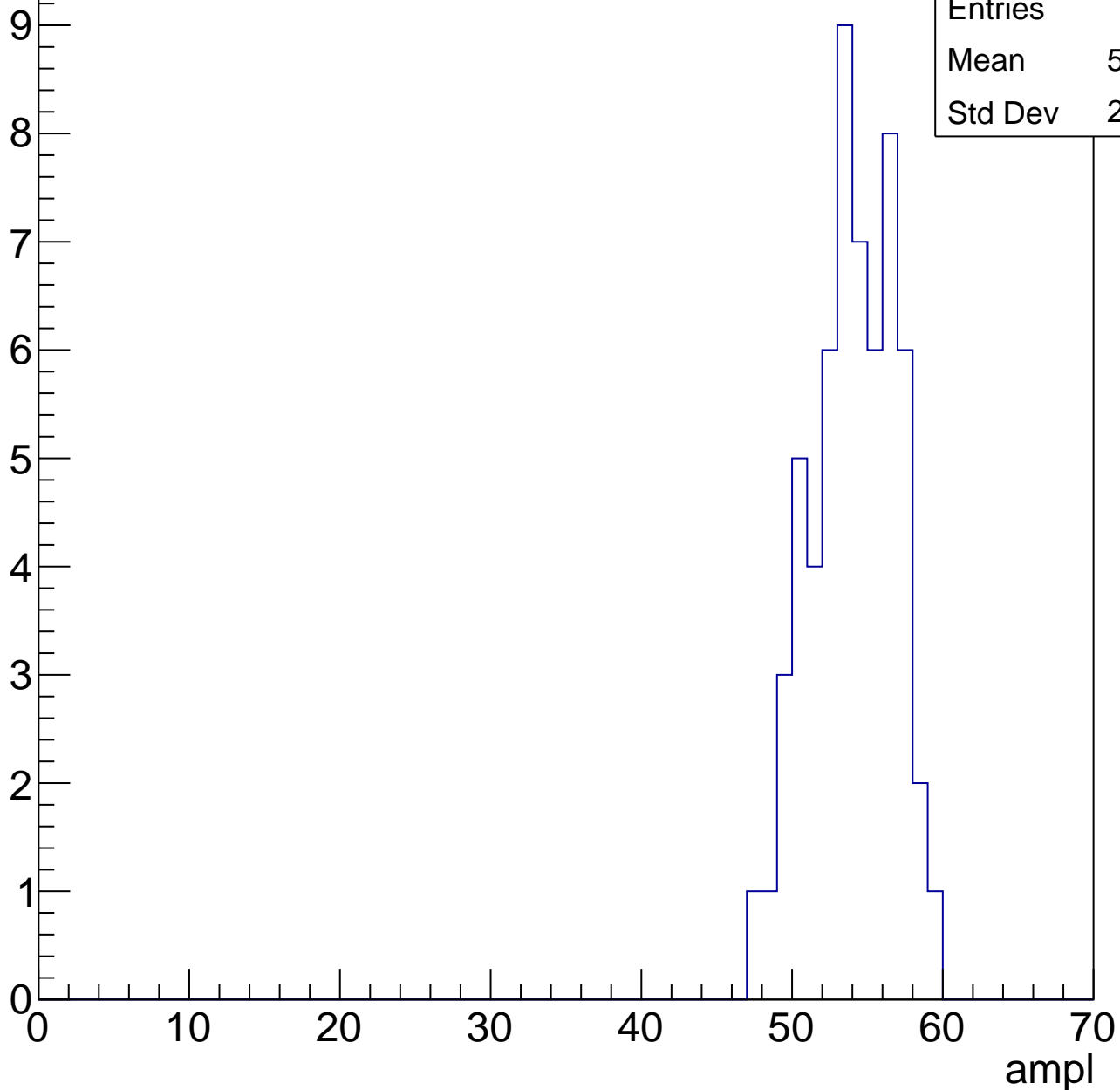
Entries	53
Mean	47.7
Std Dev	3.172



# B1L002S, U8-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



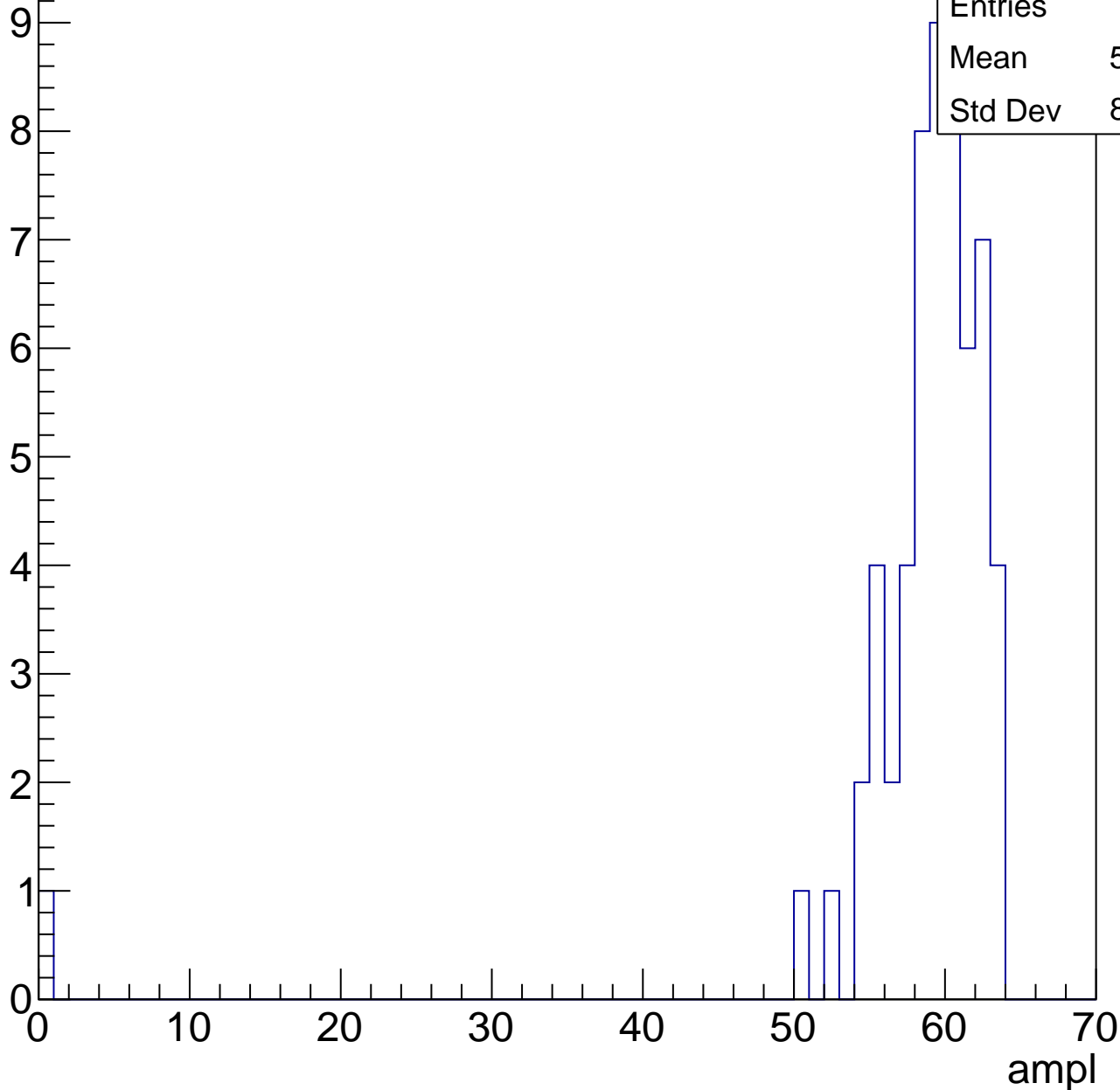
Entries	59
Mean	53.53
Std Dev	2.745

# B1L002S, U8-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.84
Std Dev	8.218

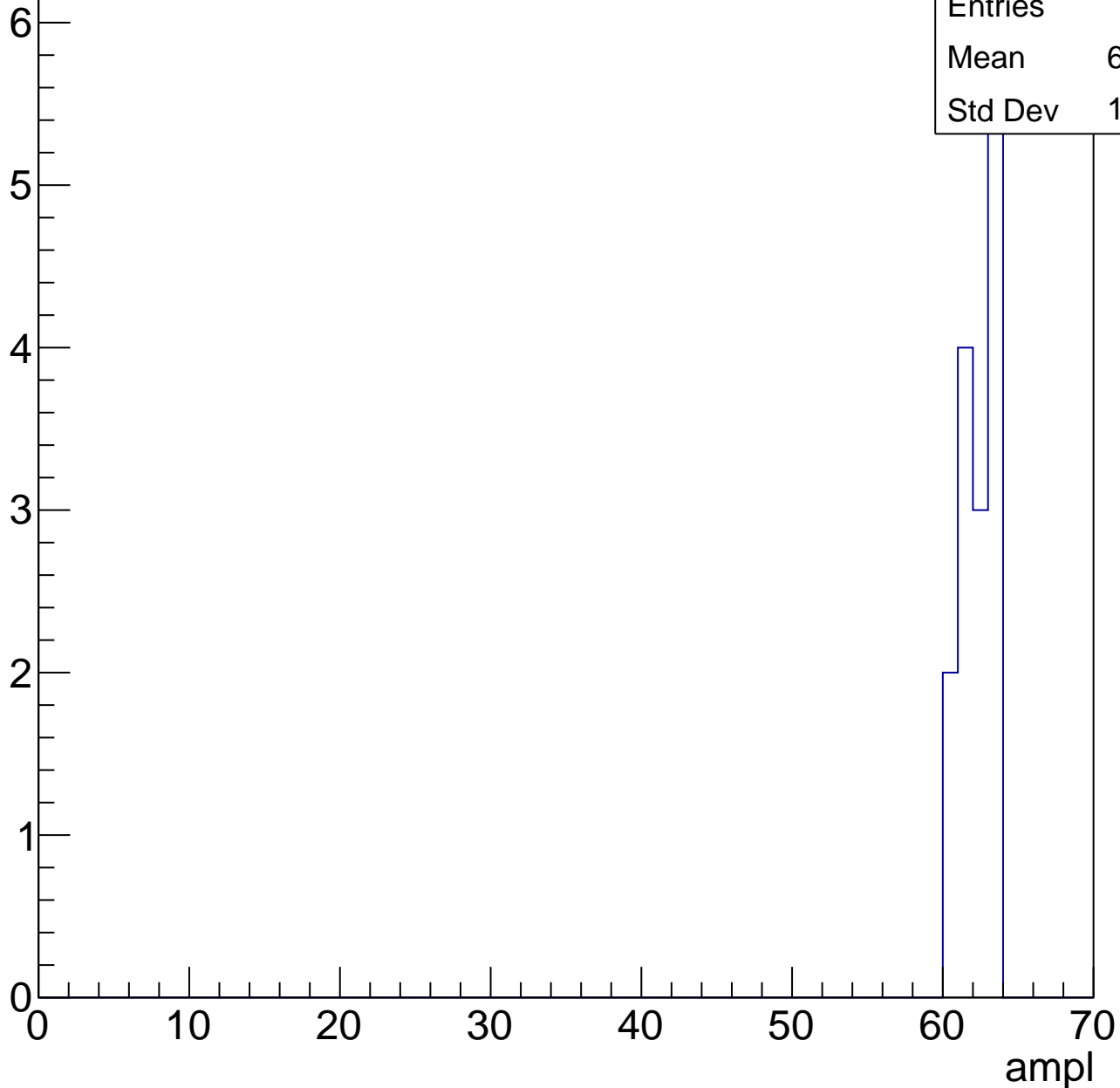


# B1L002S, U8-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	61.87
Std Dev	1.087





# B1L002S, U8-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B1L002S, U8-ch102, adc0

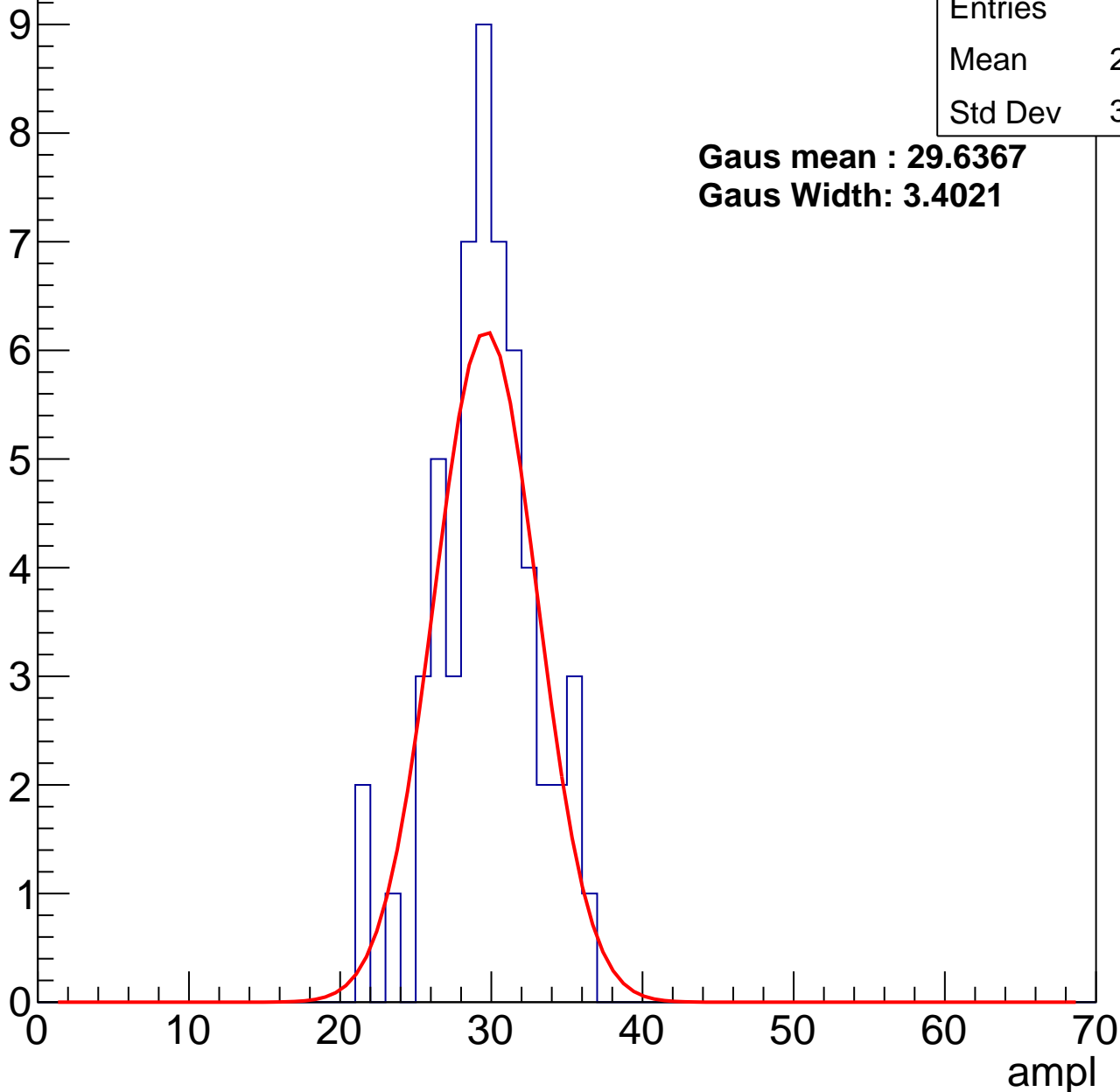
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	29.22
Std Dev	3.246

**Gaus mean : 29.6367**

**Gaus Width: 3.4021**



# B1L002S, U8-ch102, adc1

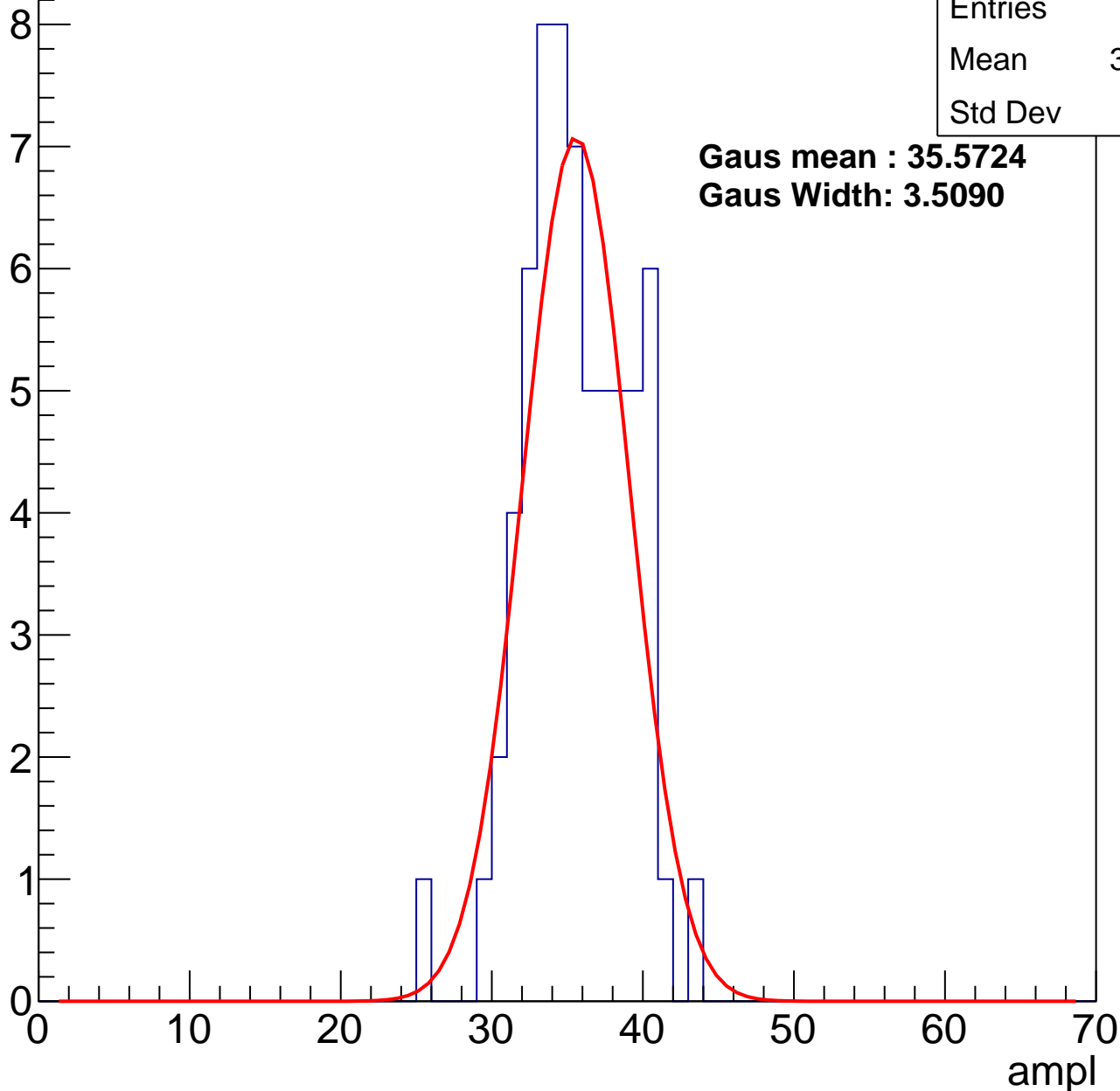
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35.15
Std Dev	3.38

**Gaus mean : 35.5724**

**Gaus Width: 3.5090**



# B1L002S, U8-ch102, adc2

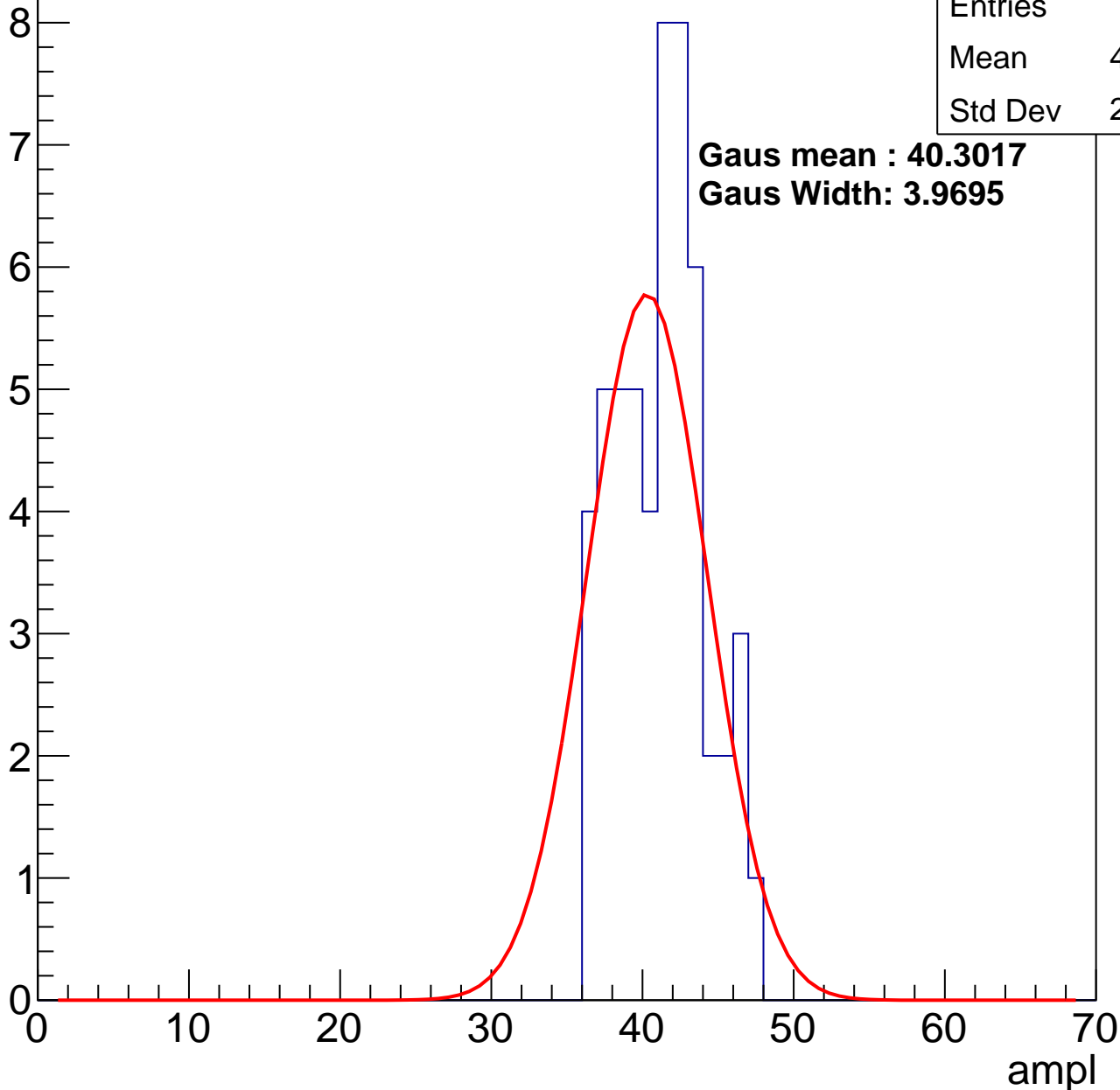
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.74
Std Dev	2.876

**Gaus mean : 40.3017**

**Gaus Width: 3.9695**

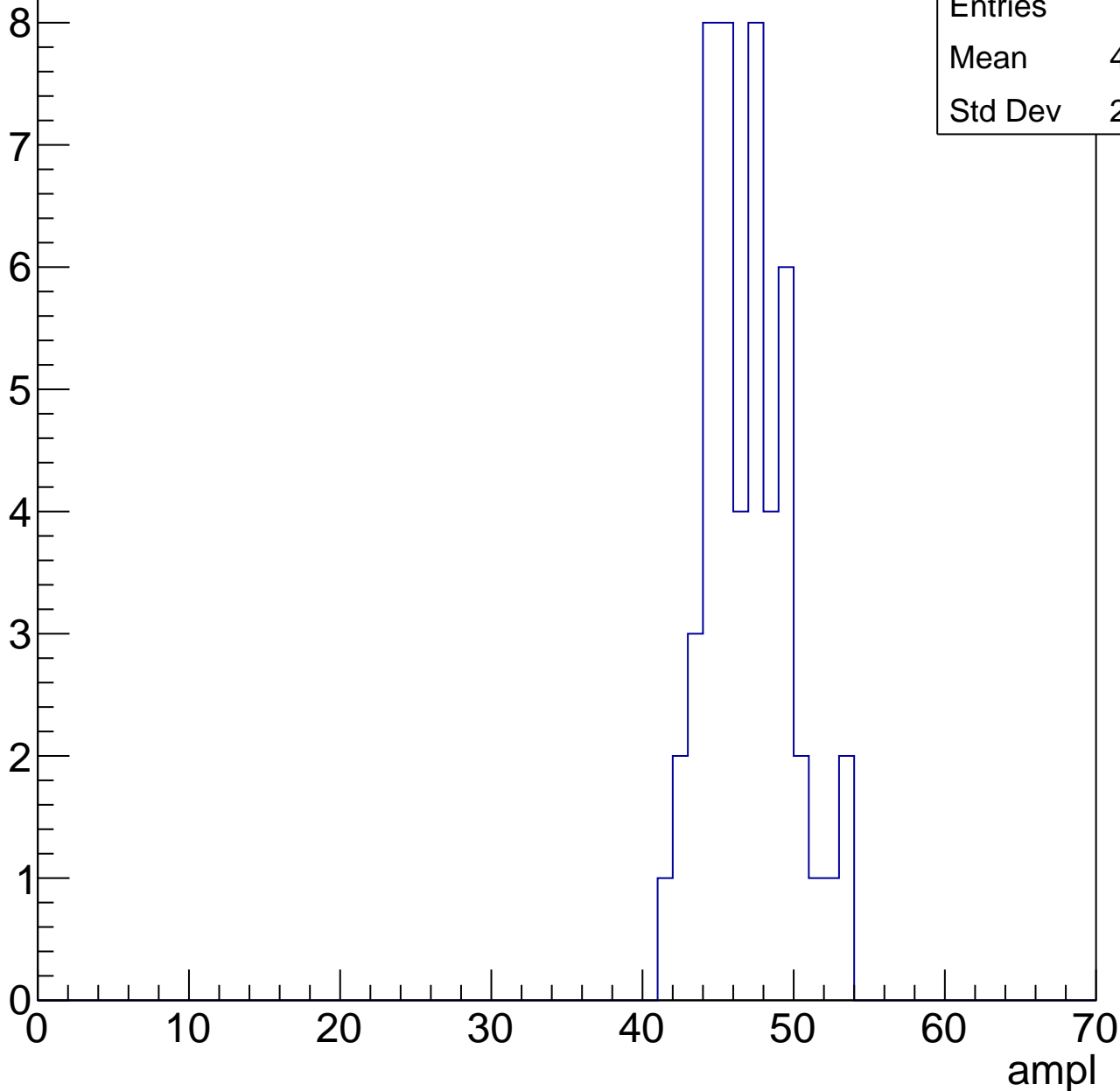


# B1L002S, U8-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	46.42
Std Dev	2.772

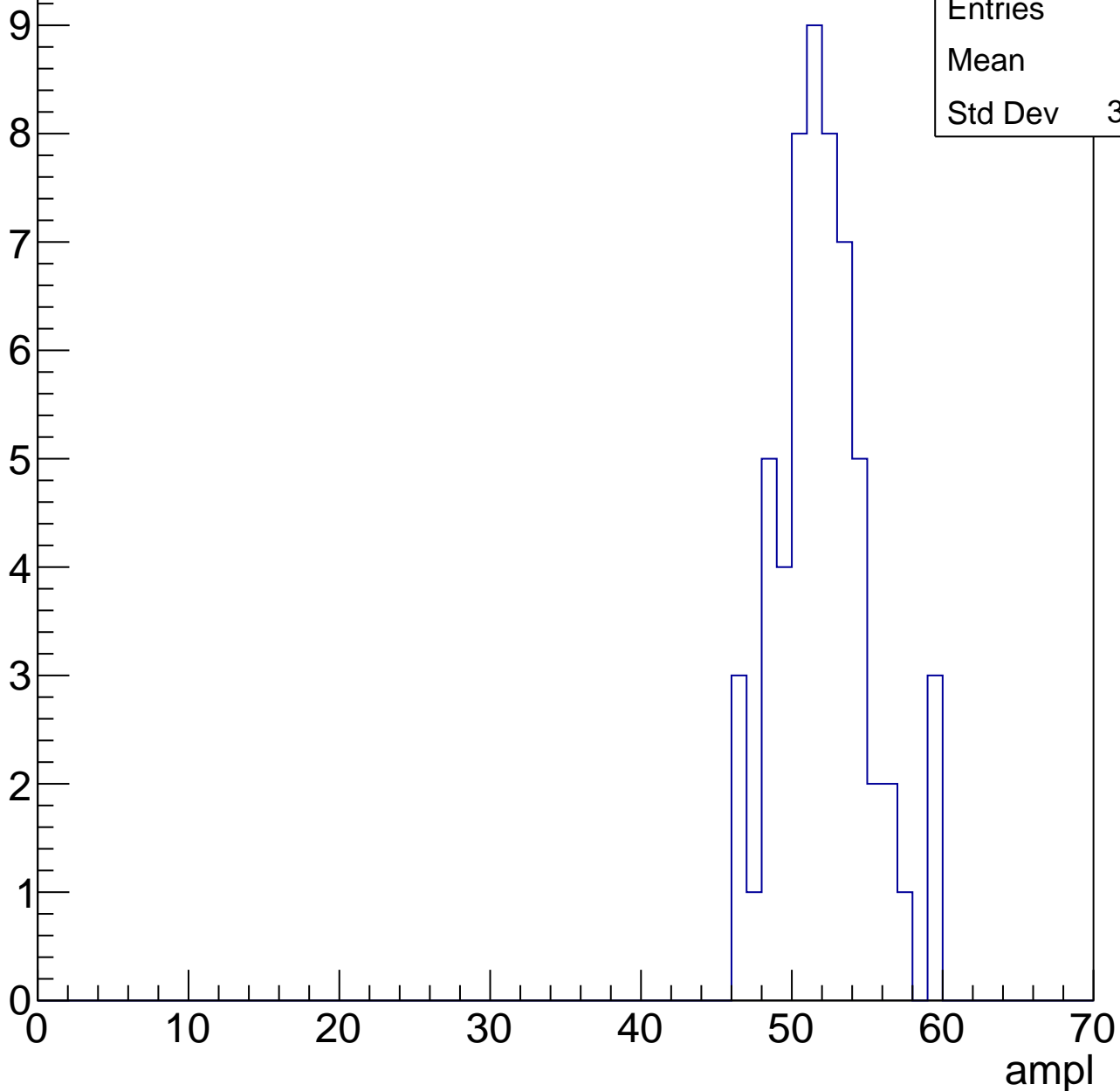


# B1L002S, U8-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

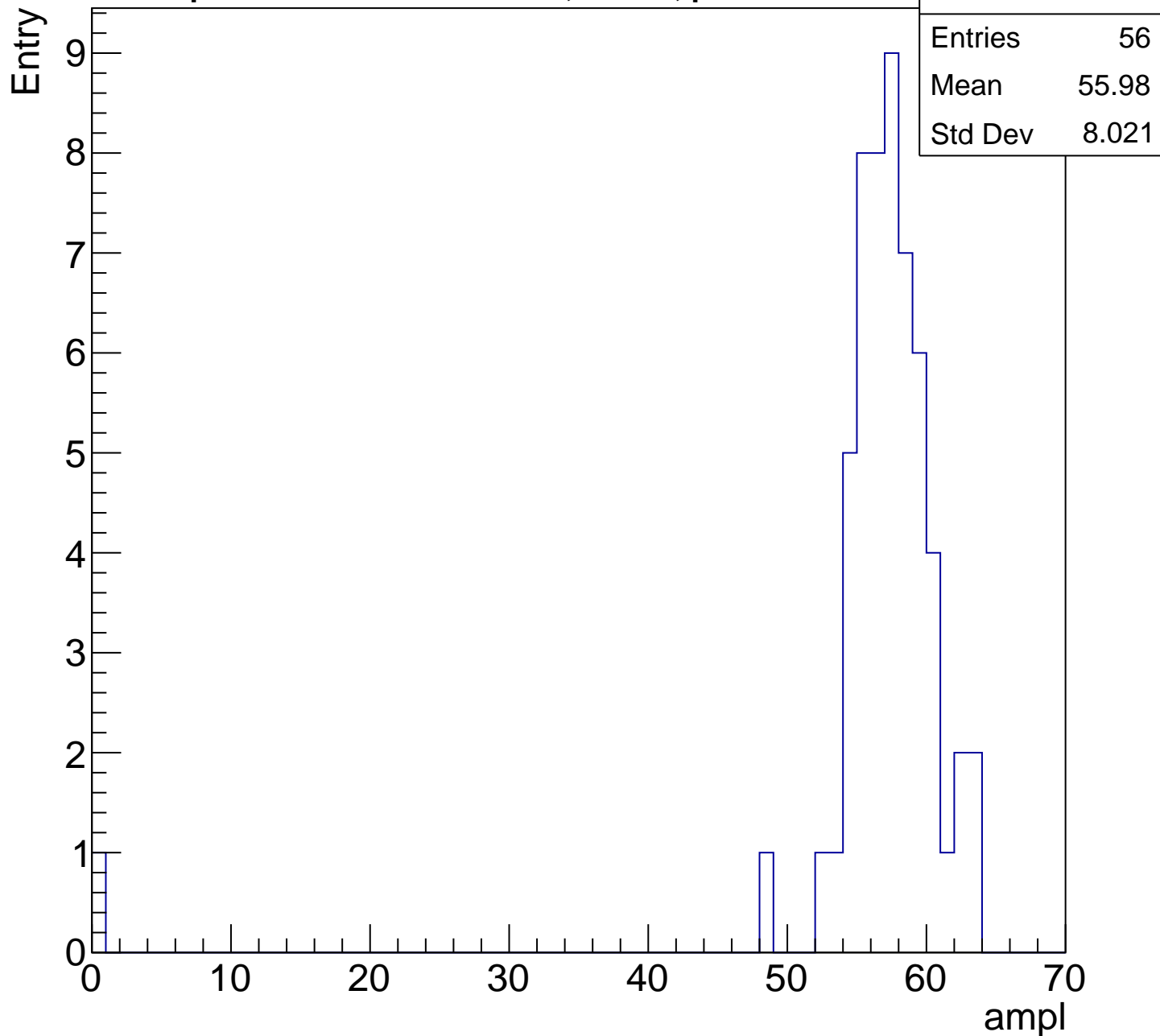
Entry

Entries	58
Mean	51.6
Std Dev	3.023



# B1L002S, U8-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

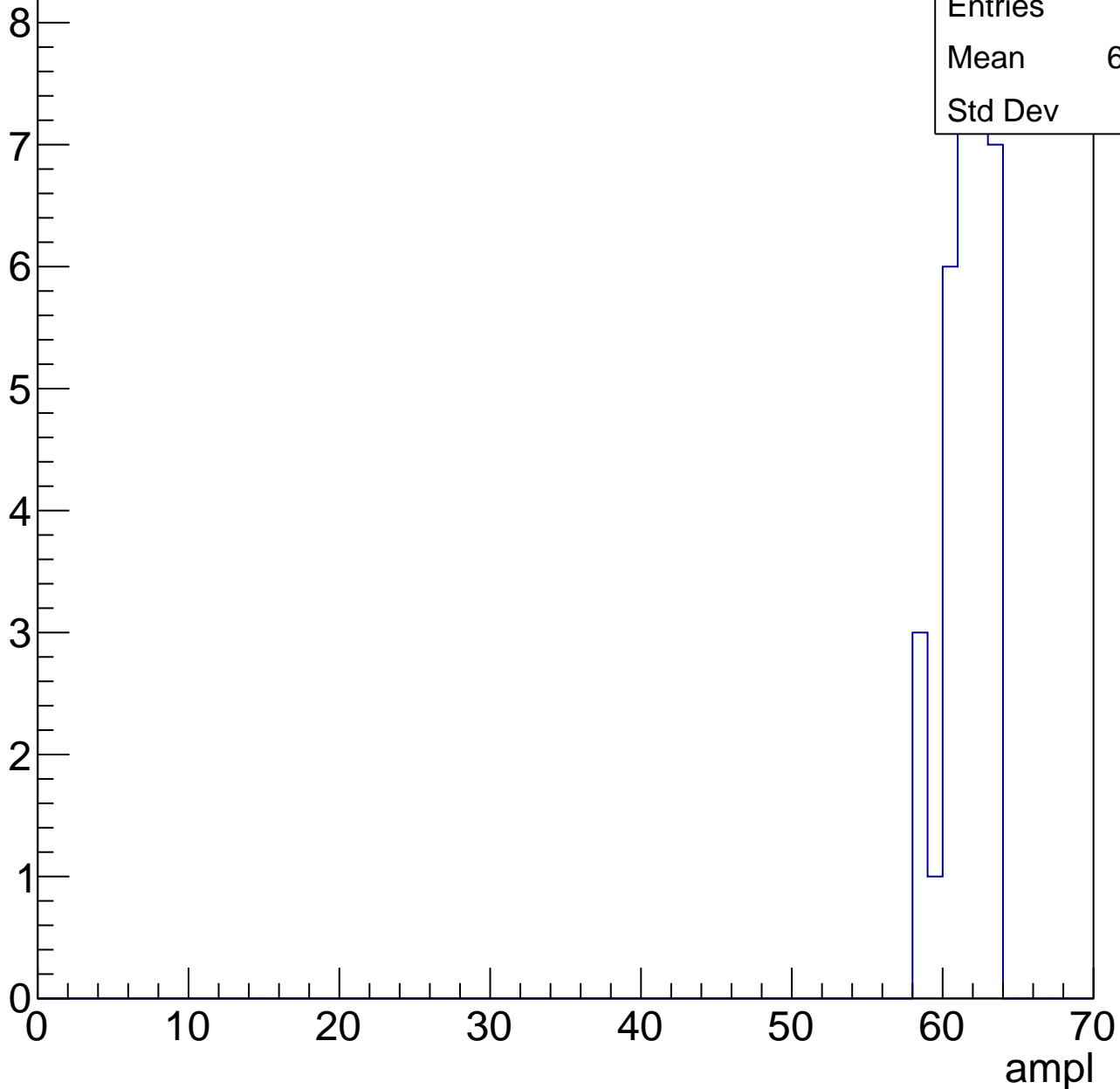


# B1L002S, U8-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	61.15
Std Dev	1.48





# B1L002S, U8-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch103, adc0

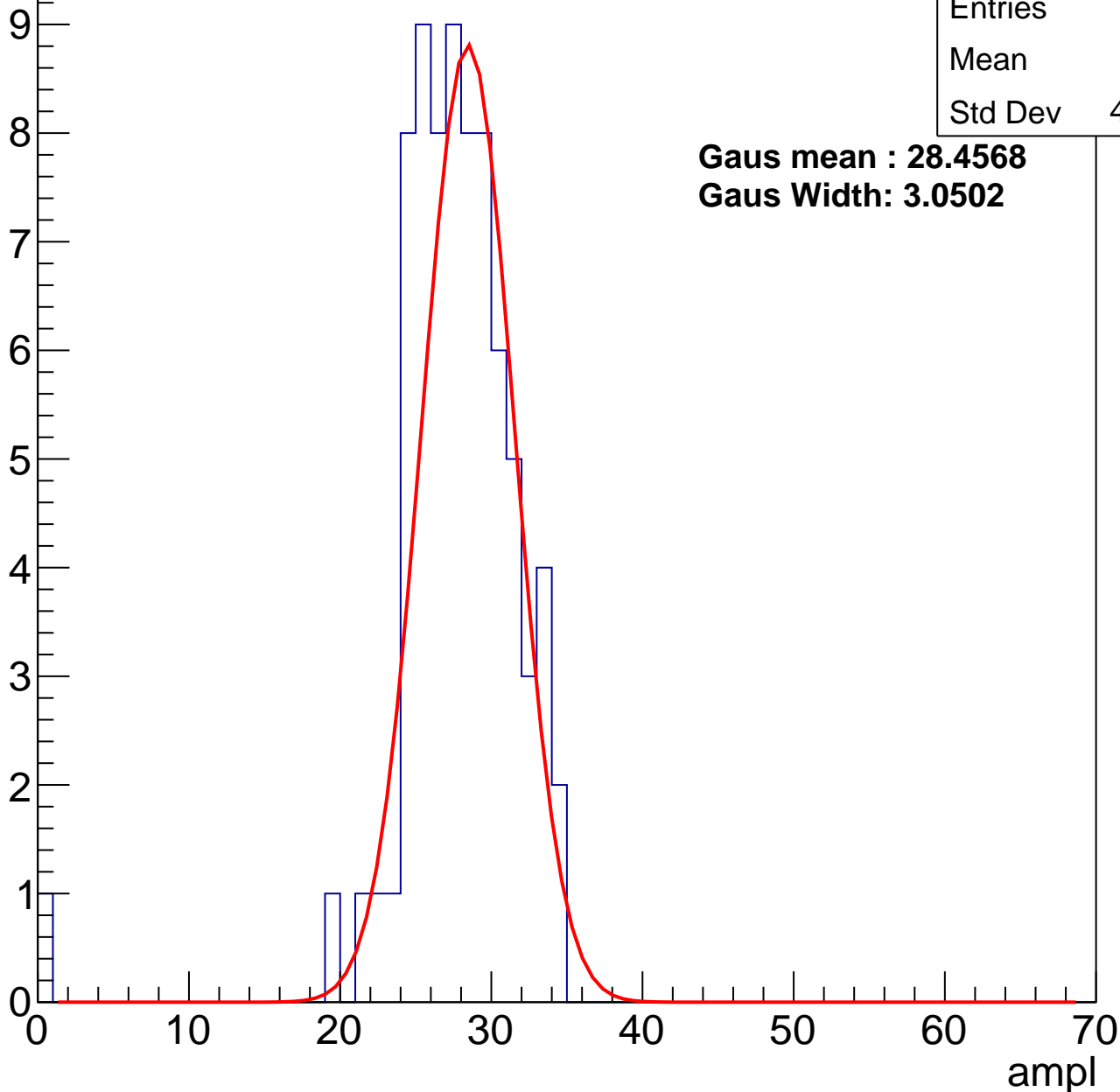
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	27.2
Std Dev	4.433

**Gaus mean : 28.4568**

**Gaus Width: 3.0502**



# B1L002S, U8-ch103, adc1

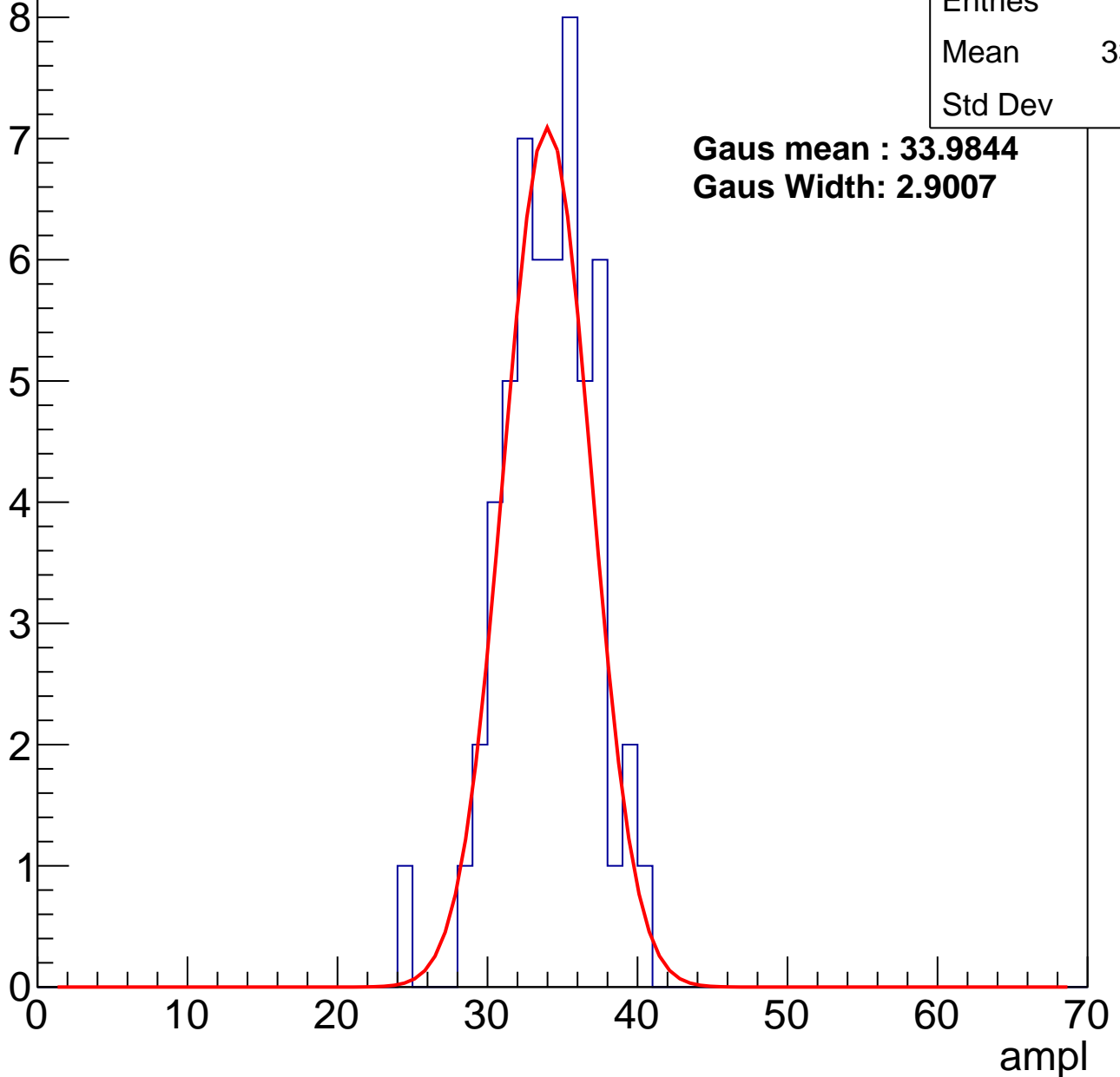
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	33.62
Std Dev	3.03

**Gaus mean : 33.9844**

**Gaus Width: 2.9007**



# B1L002S, U8-ch103, adc2

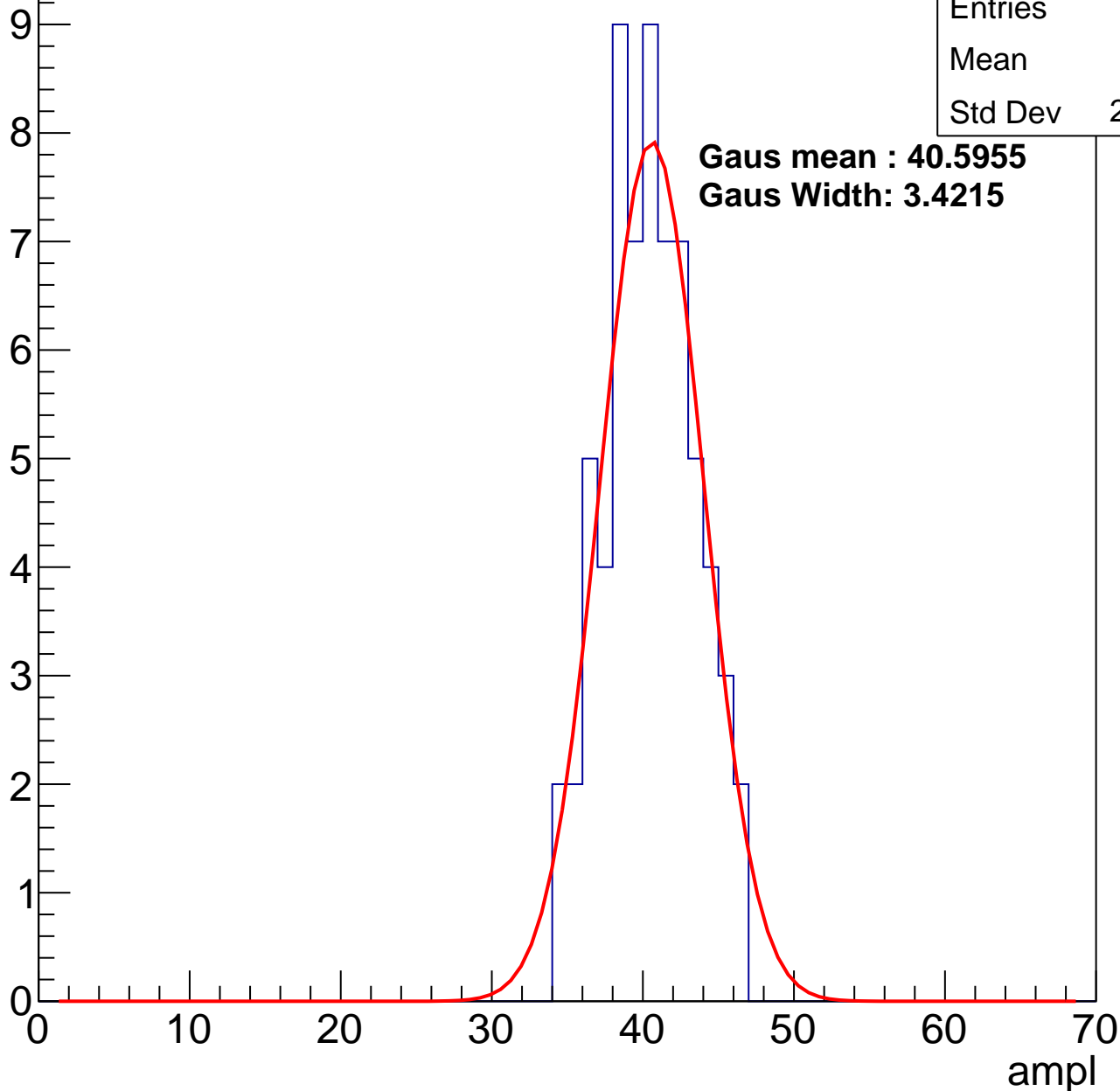
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40
Std Dev	2.944

**Gaus mean : 40.5955**

**Gaus Width: 3.4215**

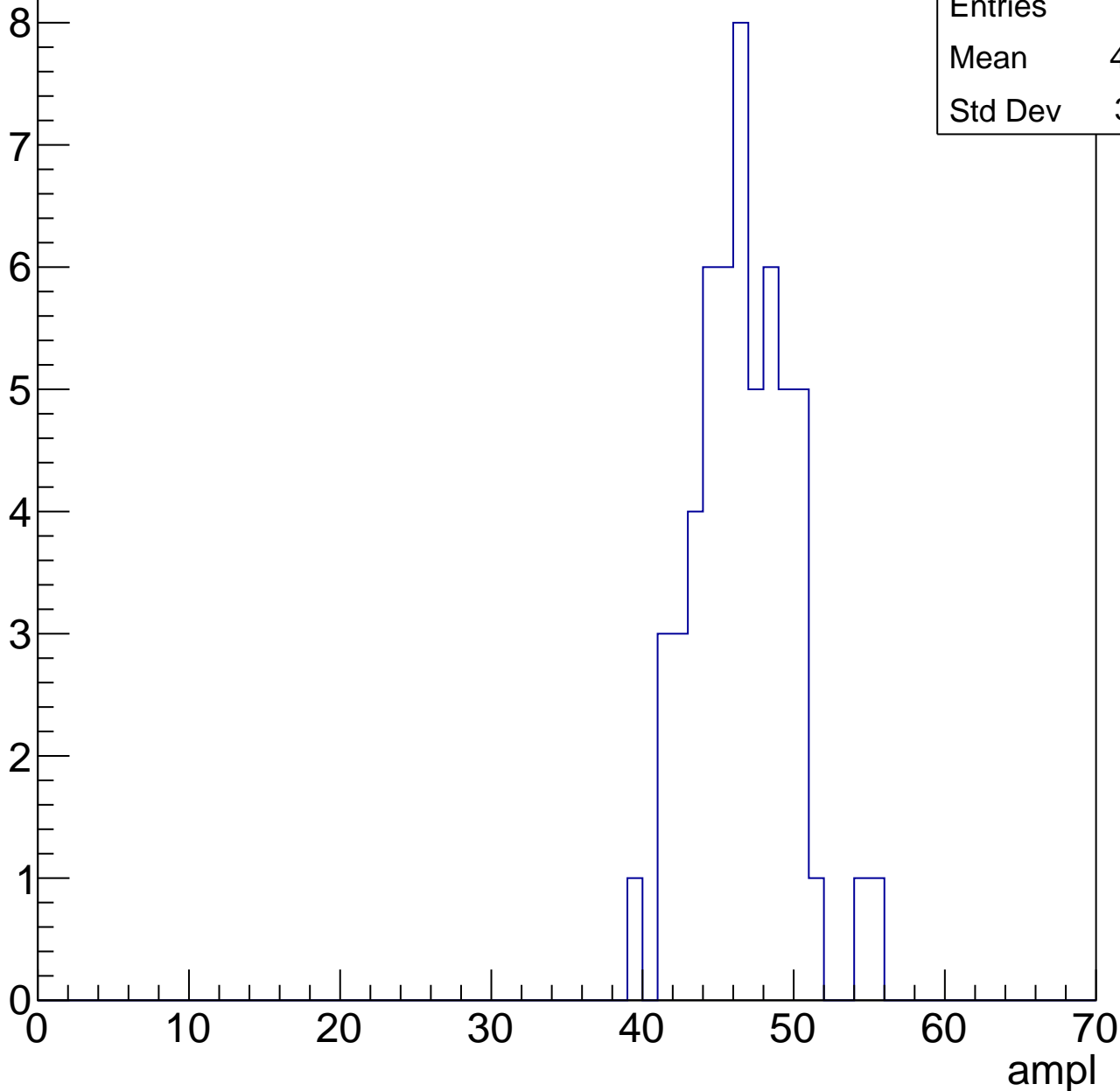


# B1L002S, U8-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.18
Std Dev	3.191

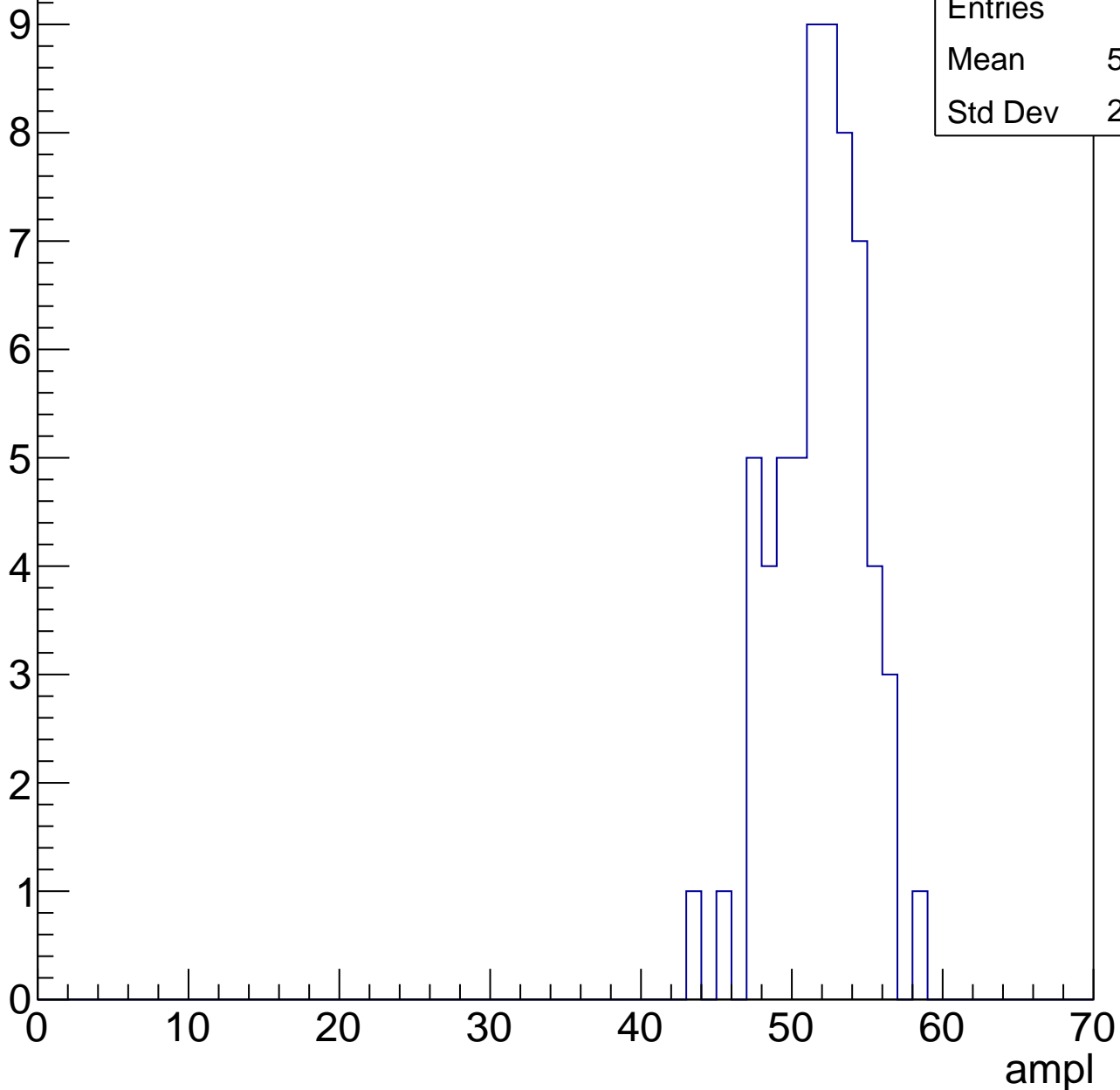


# B1L002S, U8-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

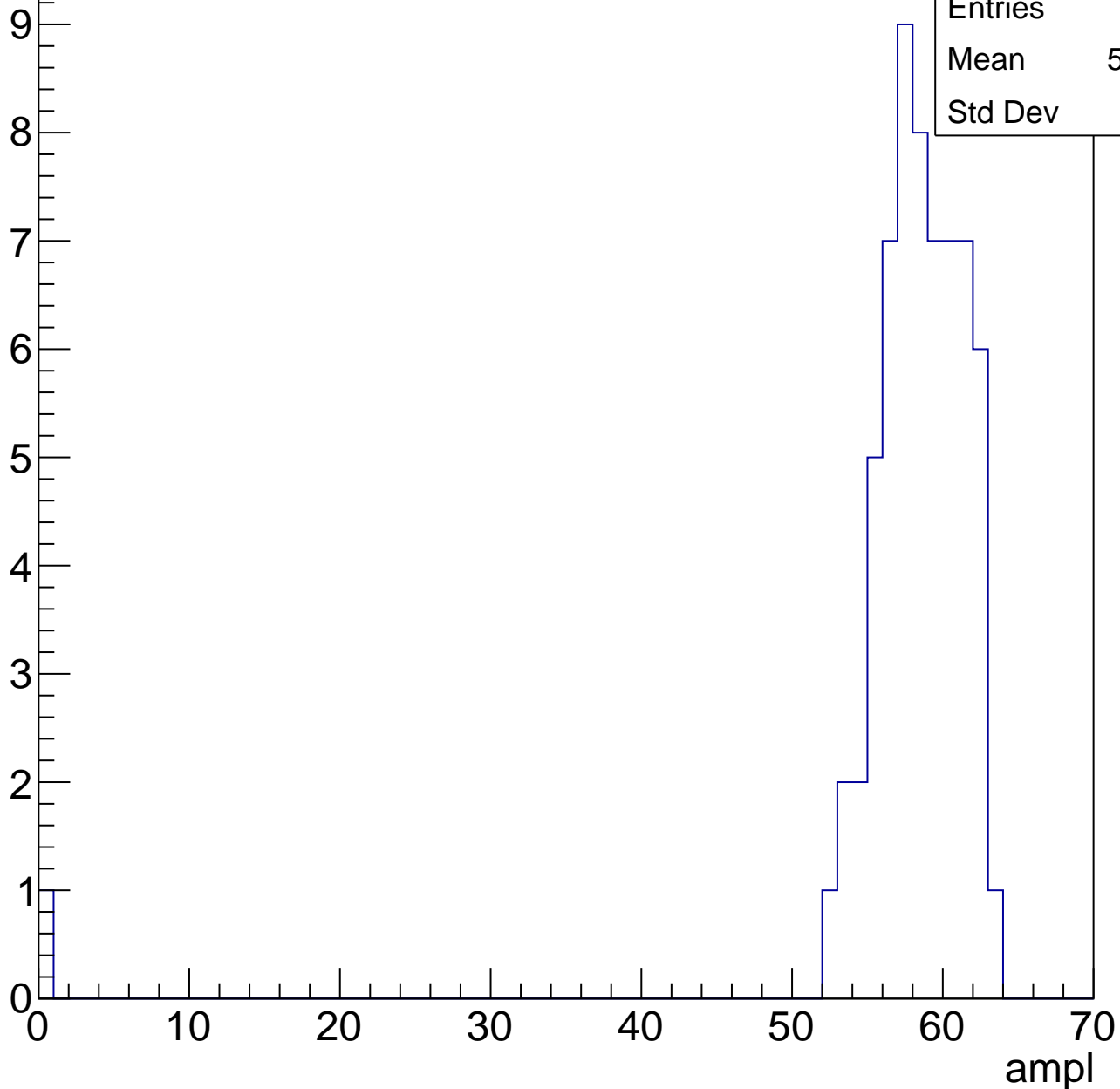
Entries	62
Mean	51.37
Std Dev	2.908



# B1L002S, U8-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

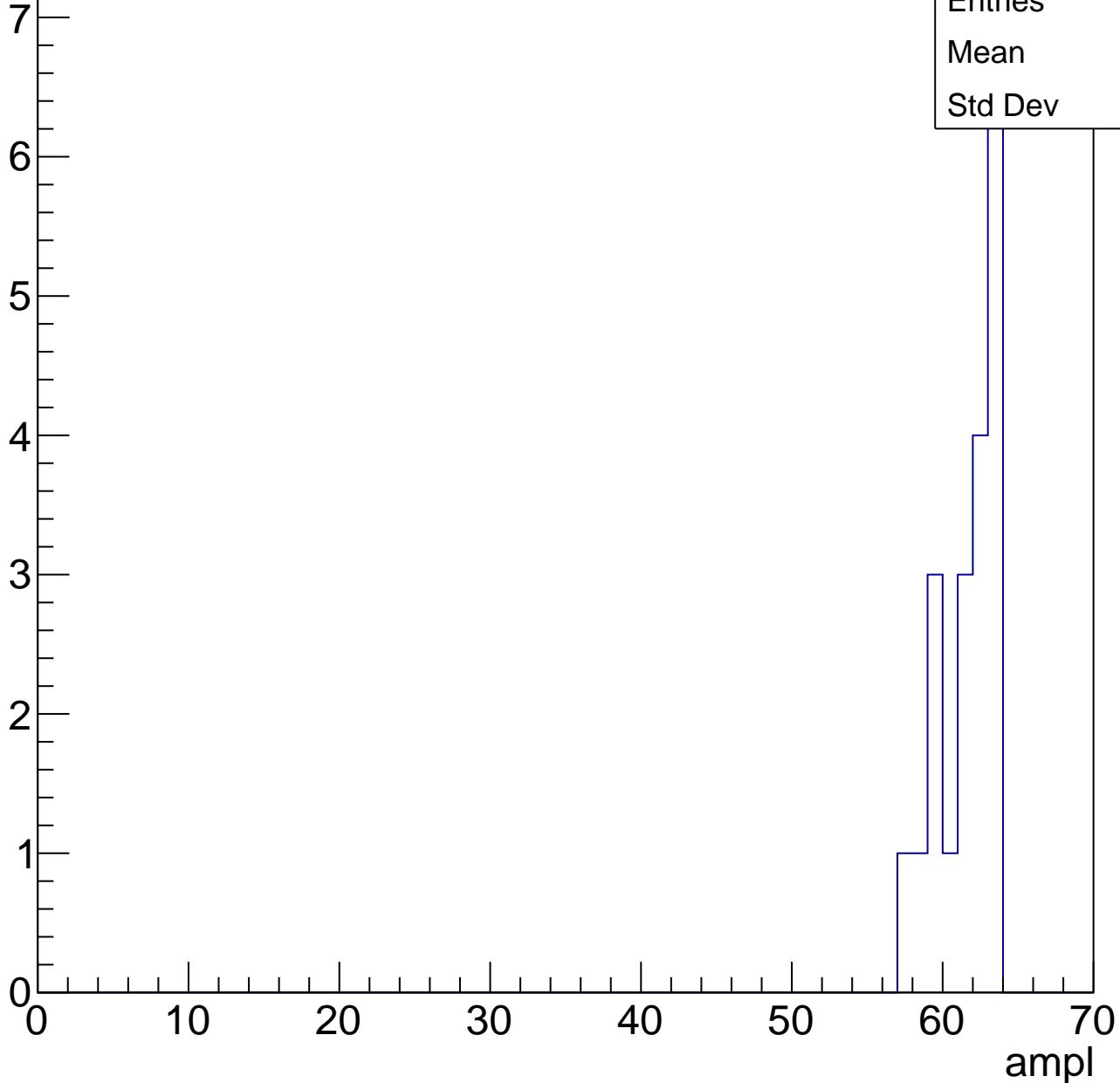


# B1L002S, U8-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.2
Std Dev	1.86

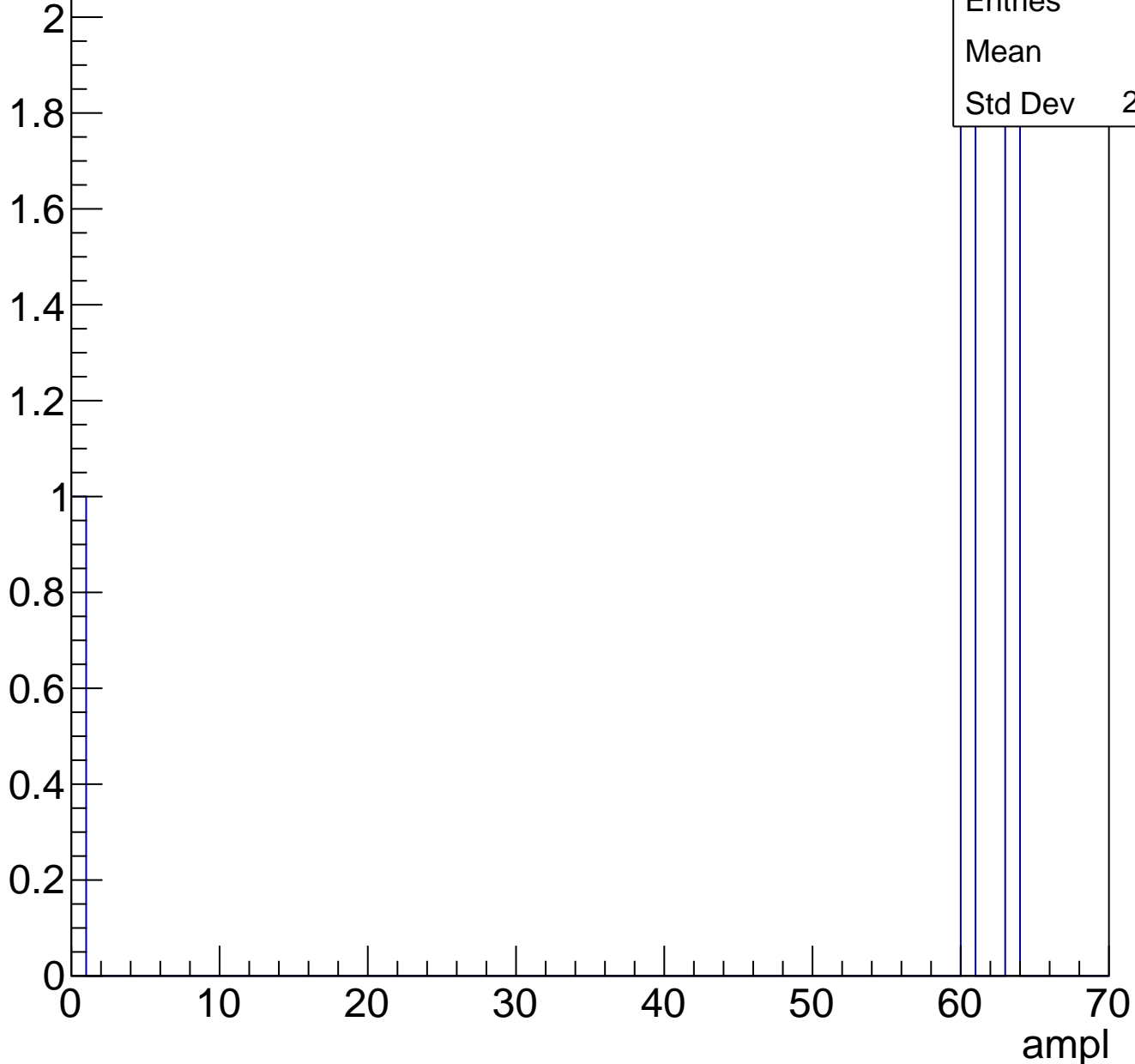




# B1L002S, U8-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch104, adc0

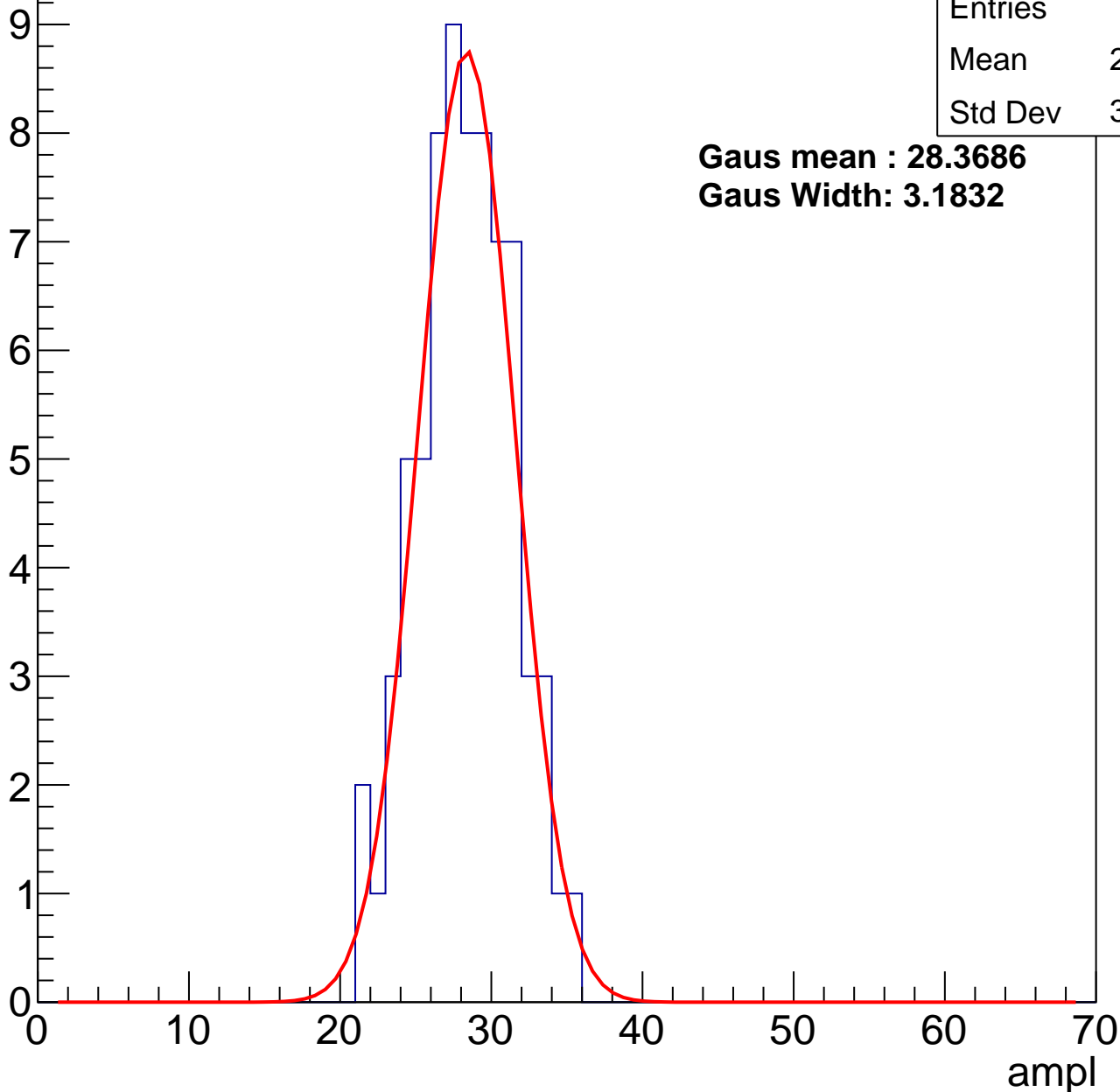
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	27.83
Std Dev	3.095

**Gaus mean : 28.3686**

**Gaus Width: 3.1832**



# B1L002S, U8-ch104, adc1

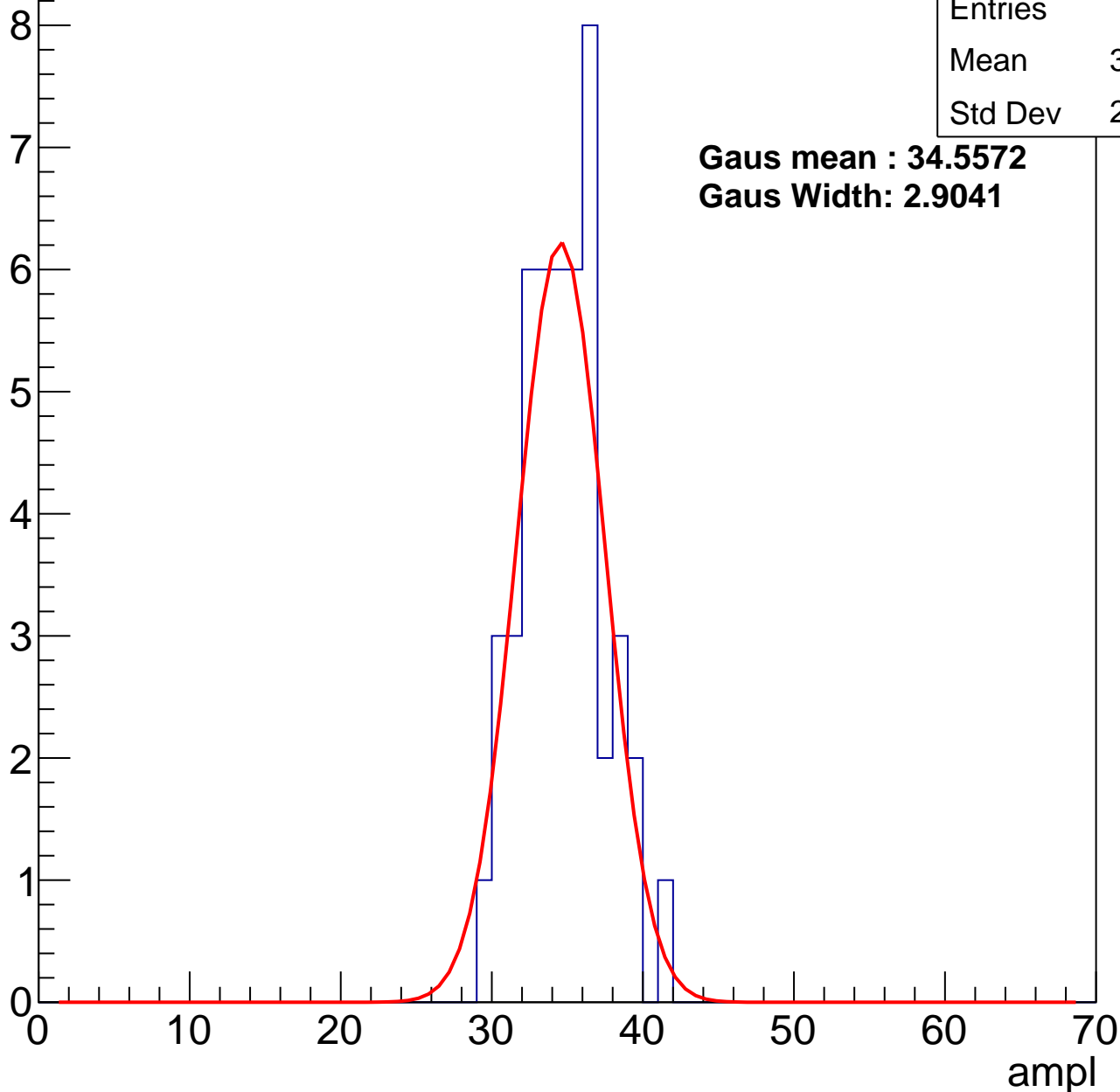
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	34.28
Std Dev	2.647

**Gaus mean : 34.5572**

**Gaus Width: 2.9041**



# B1L002S, U8-ch104, adc2

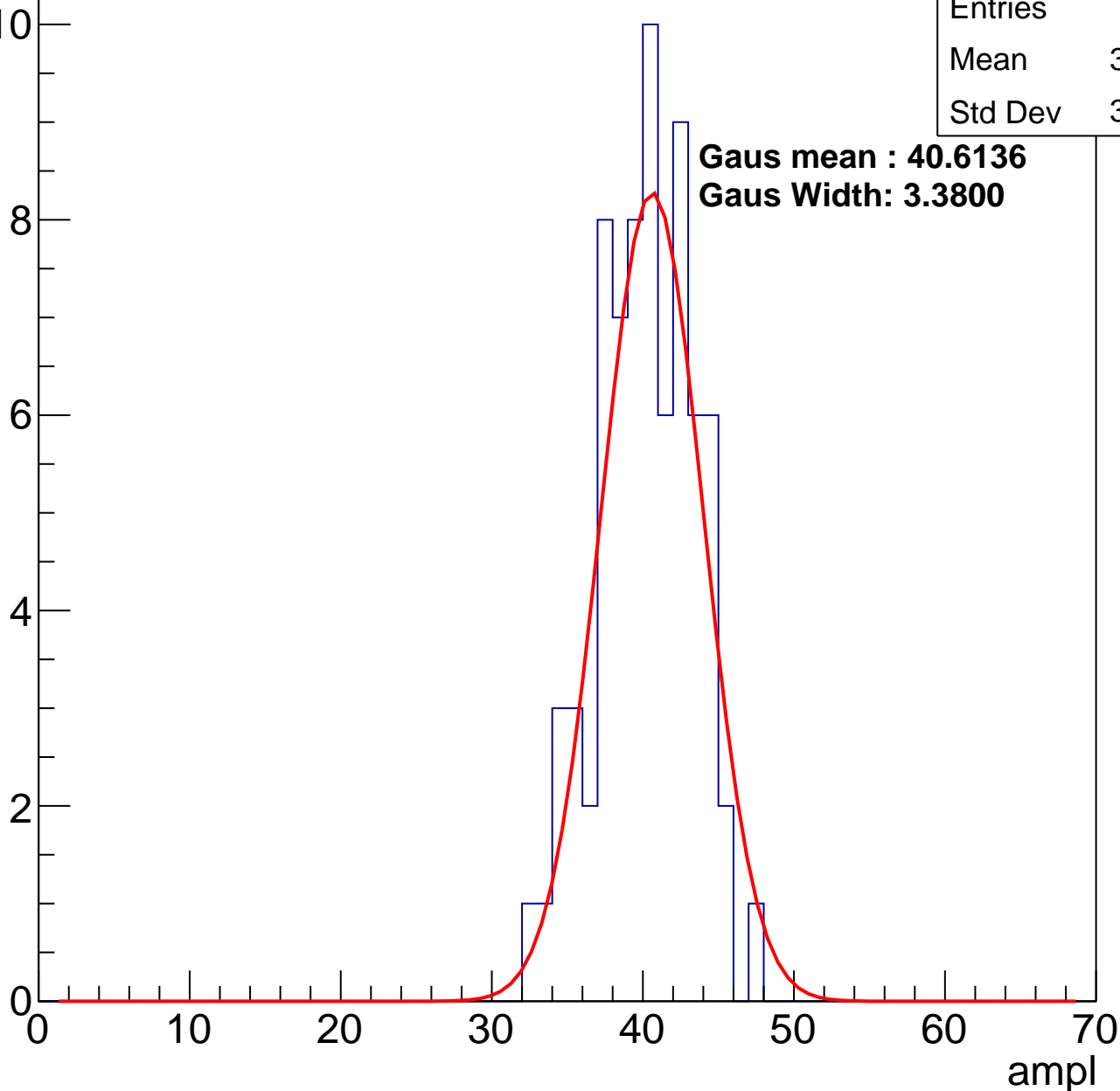
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	39.74
Std Dev	3.145

**Gaus mean : 40.6136**

**Gaus Width: 3.3800**

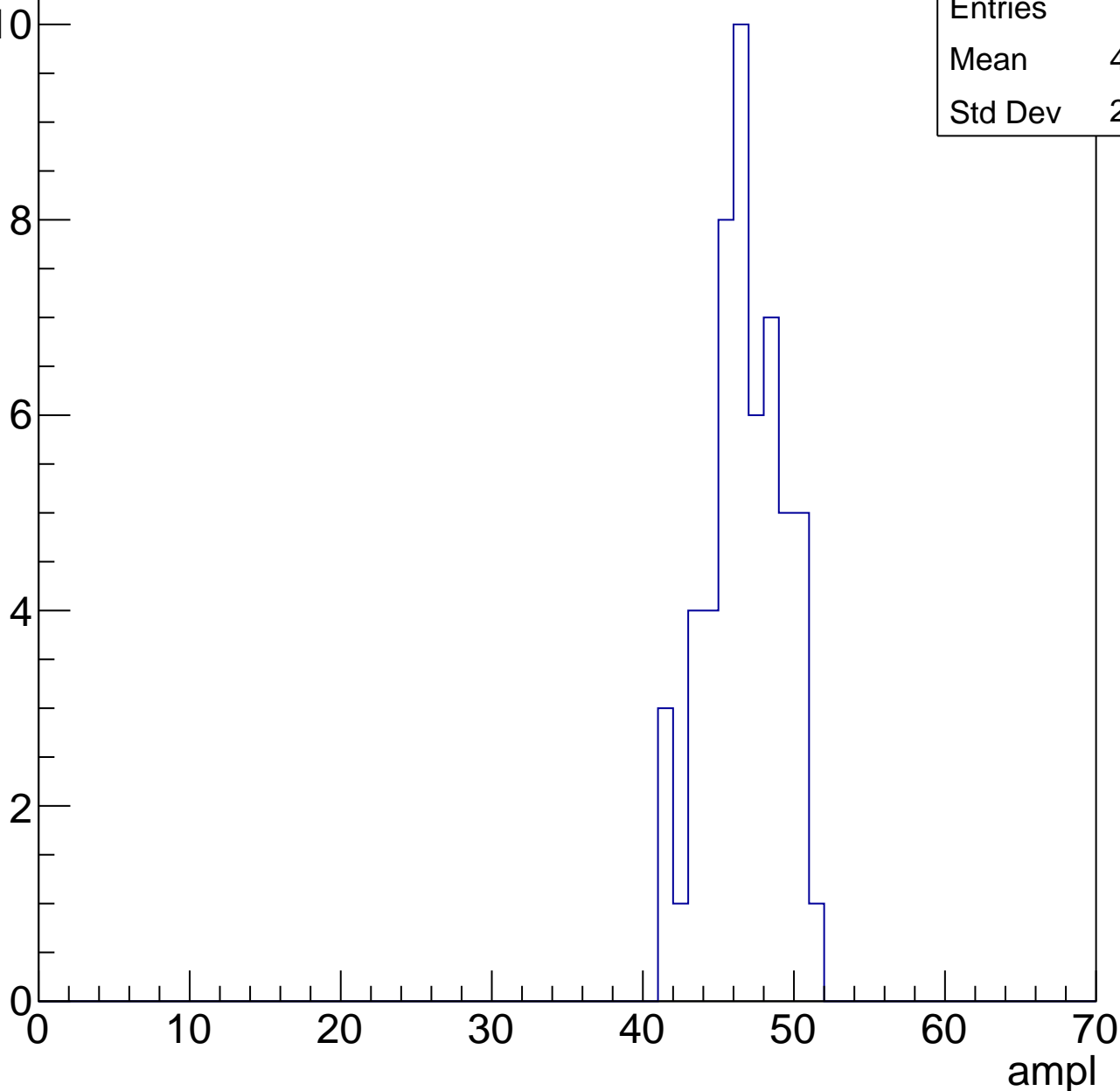


# B1L002S, U8-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

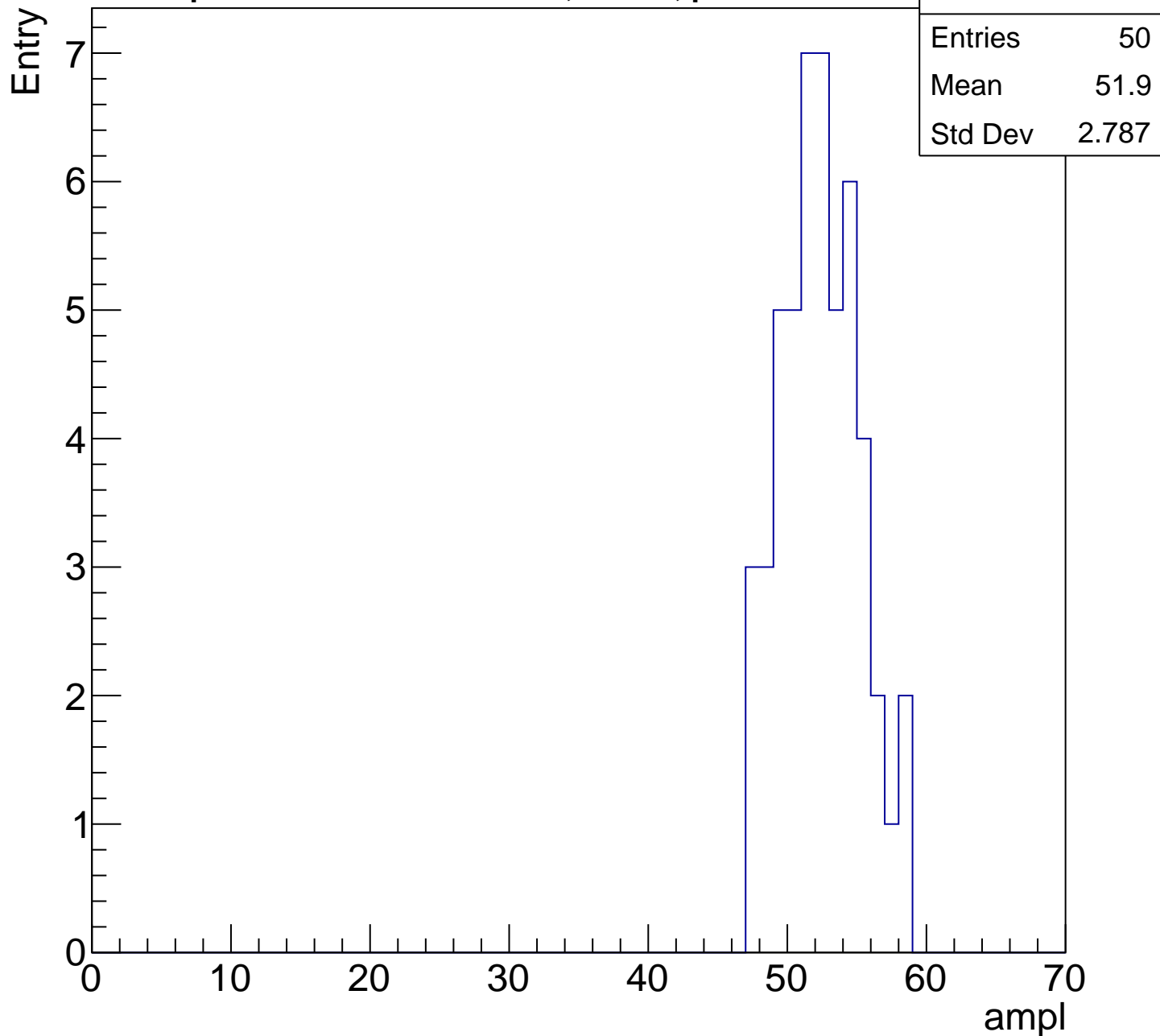
Entry

Entries	54
Mean	46.24
Std Dev	2.479



# B1L002S, U8-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

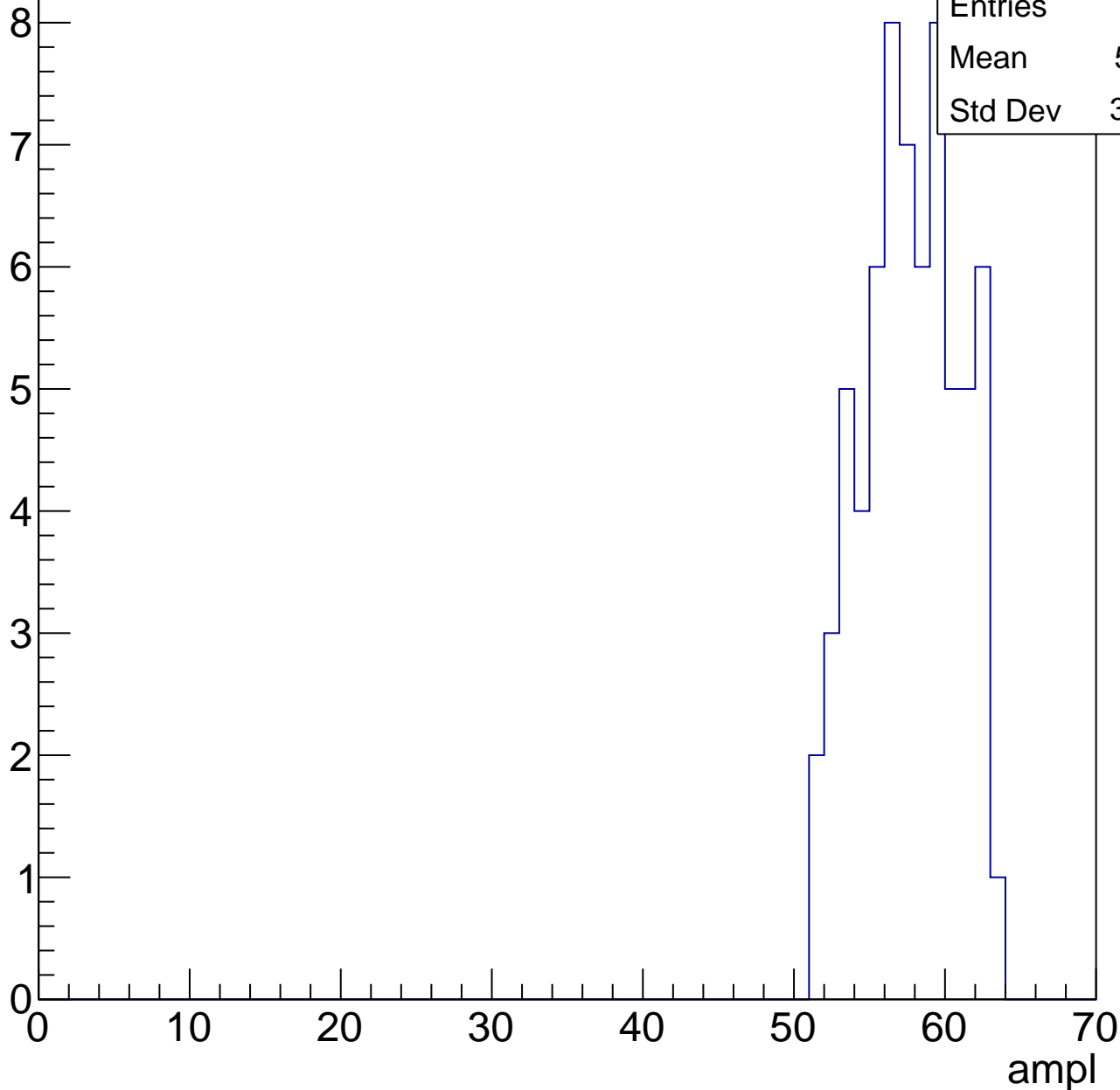


# B1L002S, U8-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.21
Std Dev	3.117

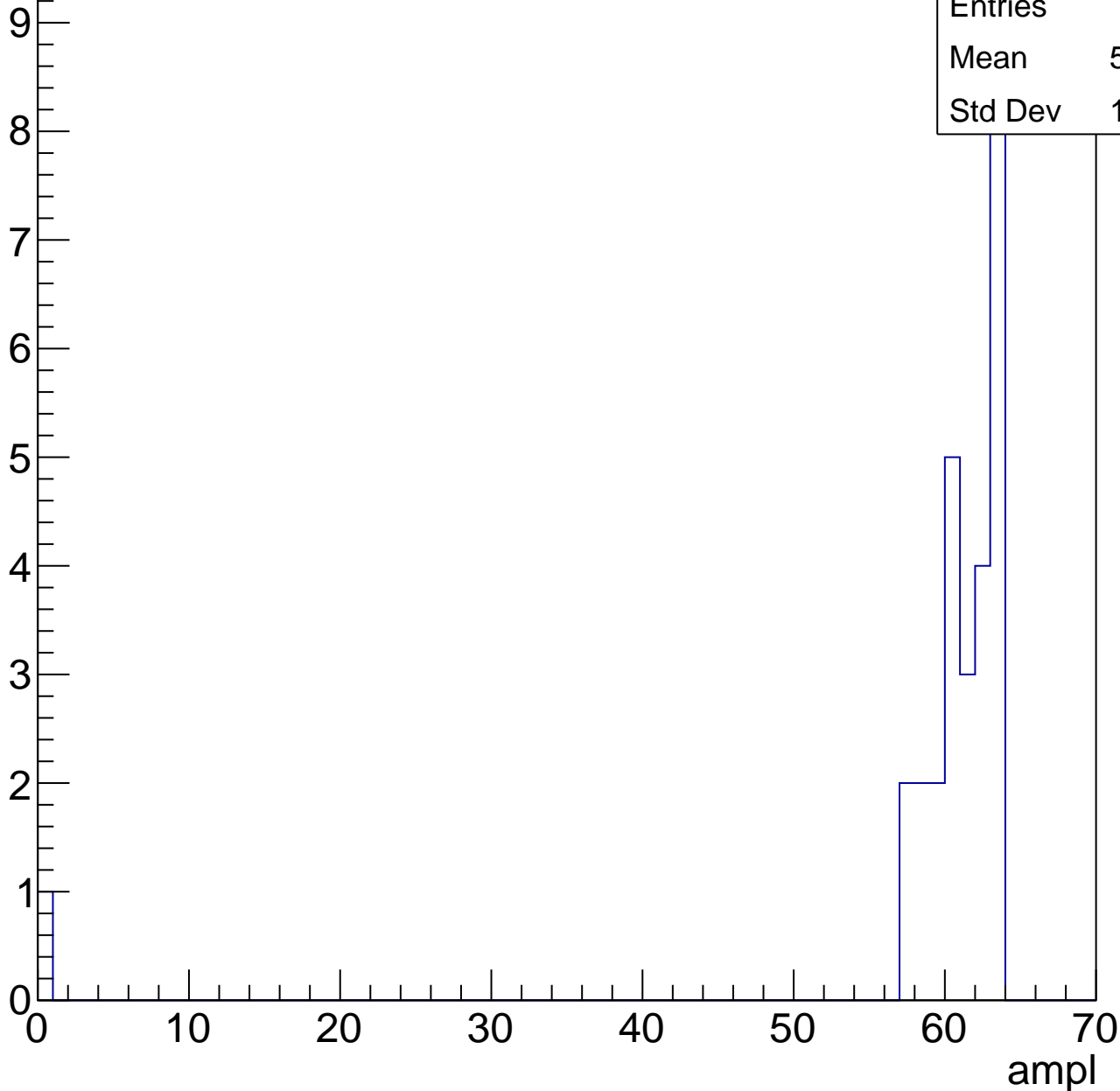


# B1L002S, U8-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.79
Std Dev	11.47

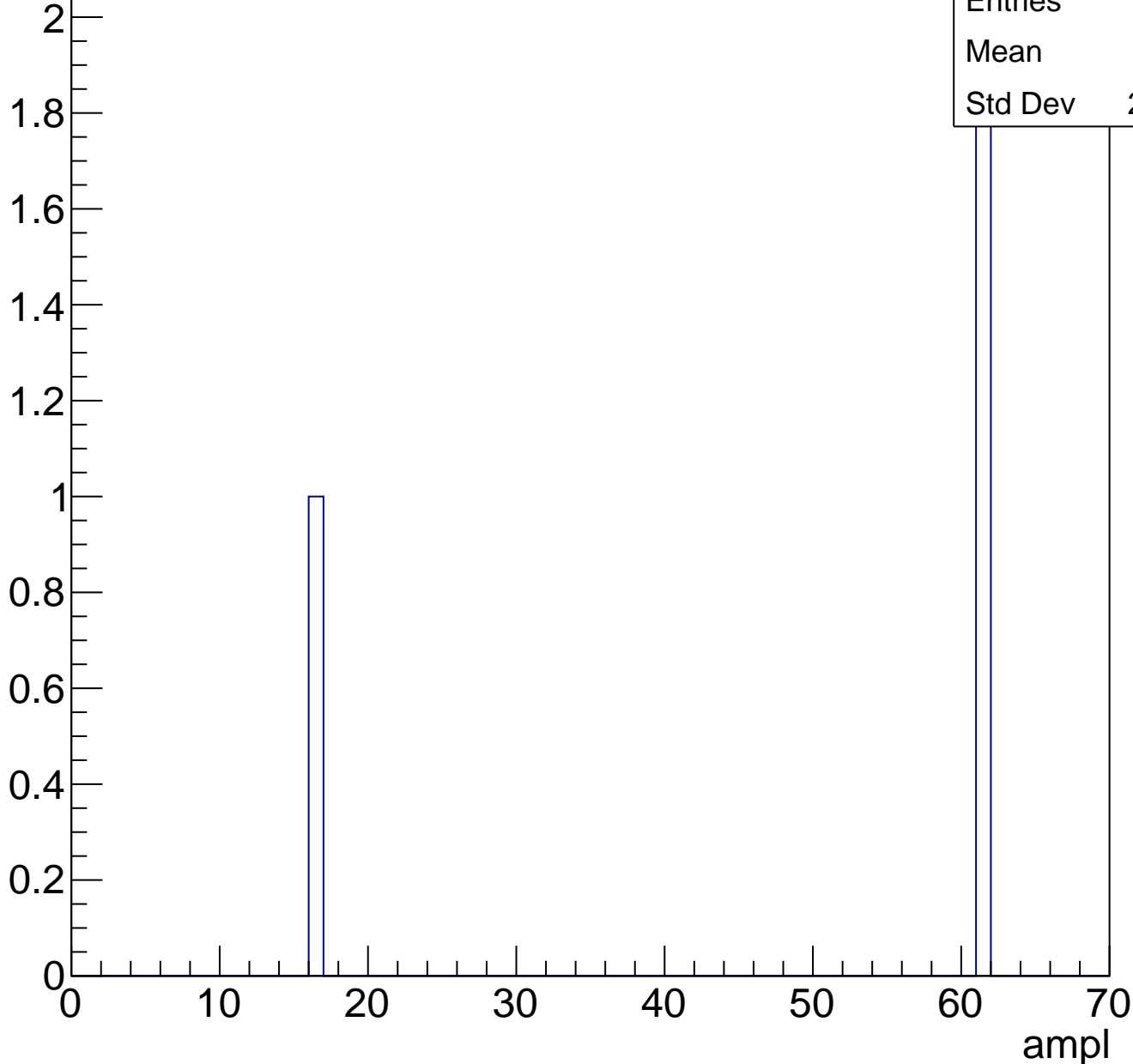




# B1L002S, U8-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch105, adc0

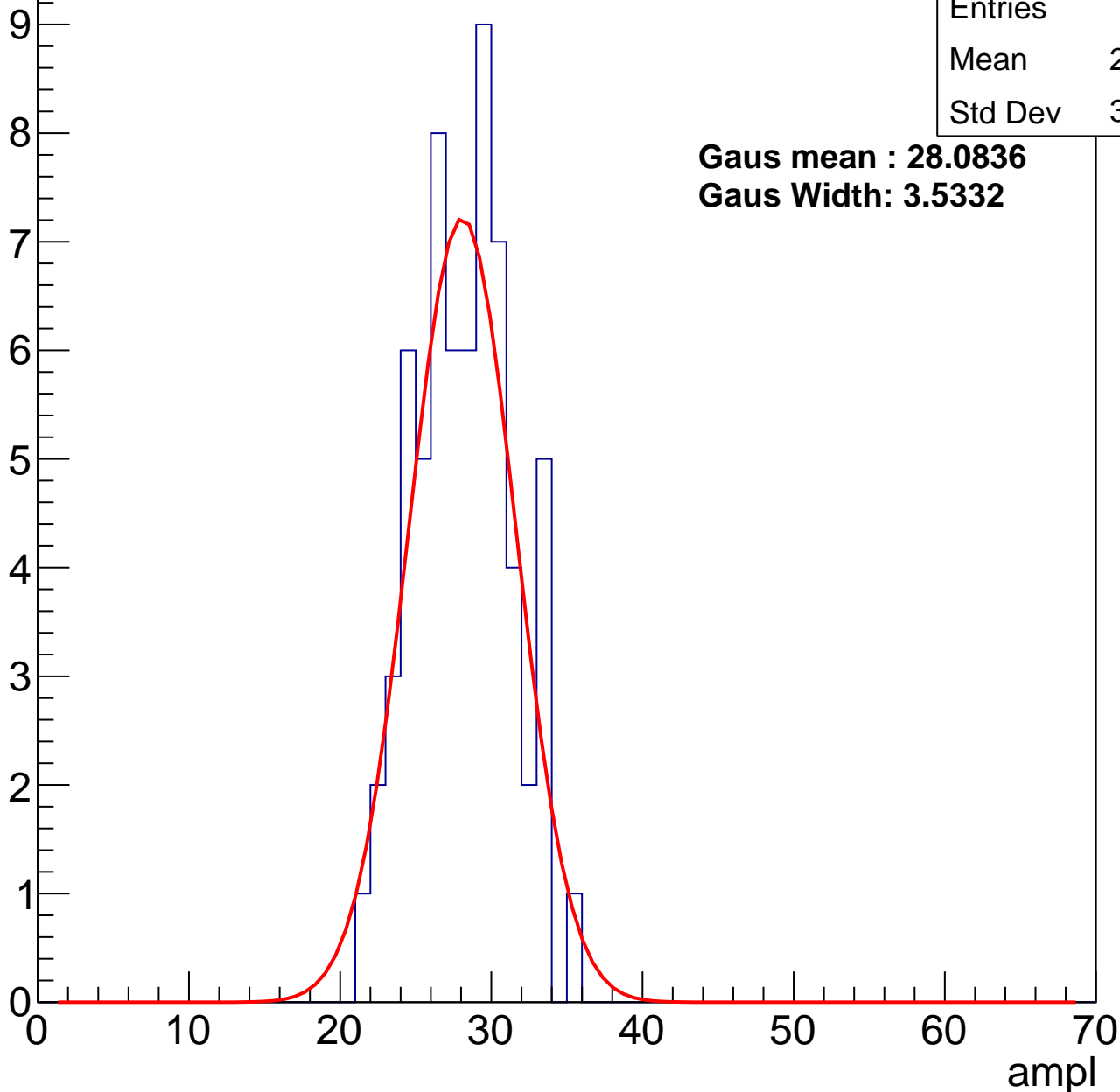
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.69
Std Dev	3.157

**Gaus mean : 28.0836**

**Gaus Width: 3.5332**



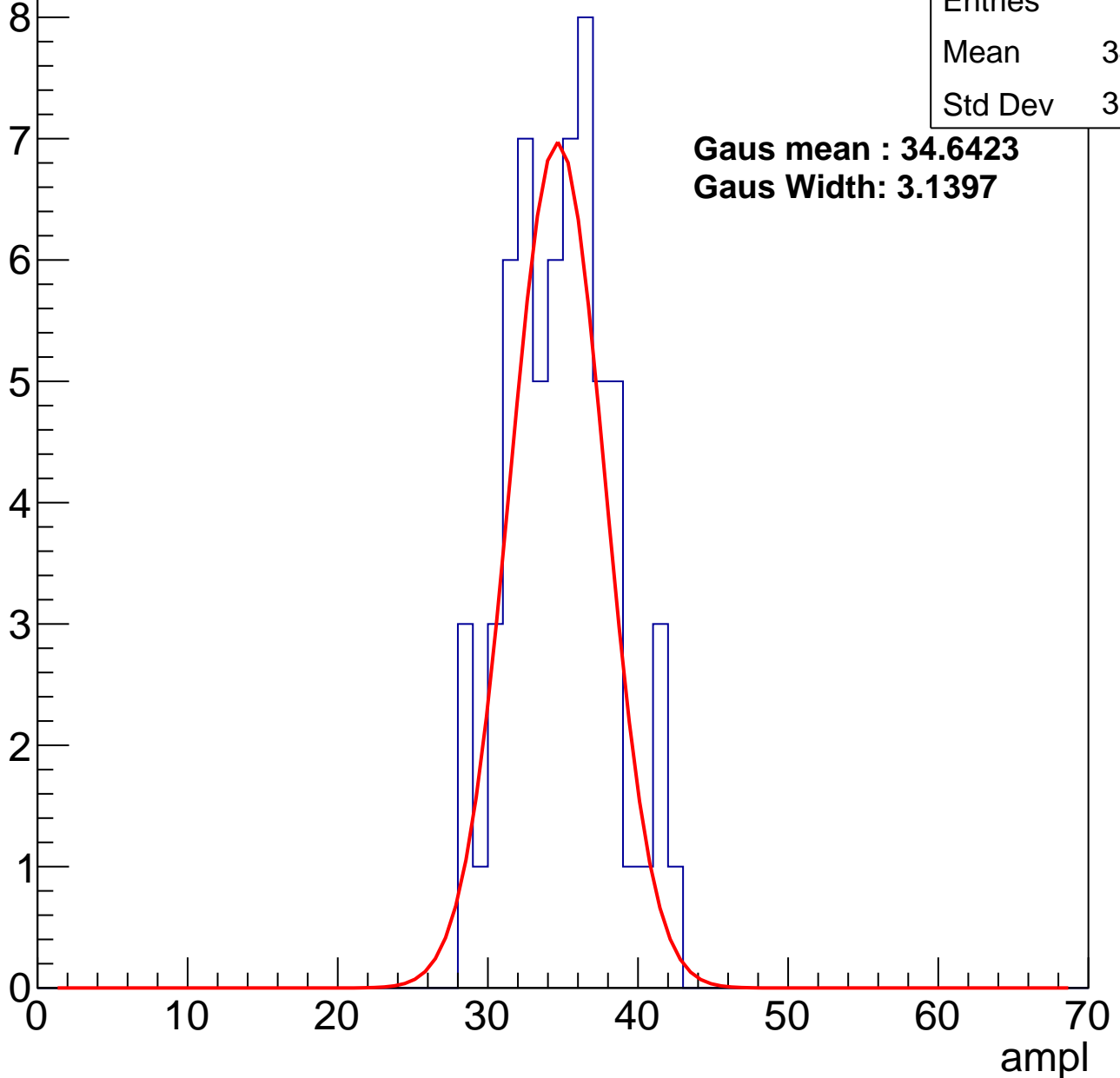
# B1L002S, U8-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.42
Std Dev	3.343

**Gaus mean : 34.6423**  
**Gaus Width: 3.1397**



# B1L002S, U8-ch105, adc2

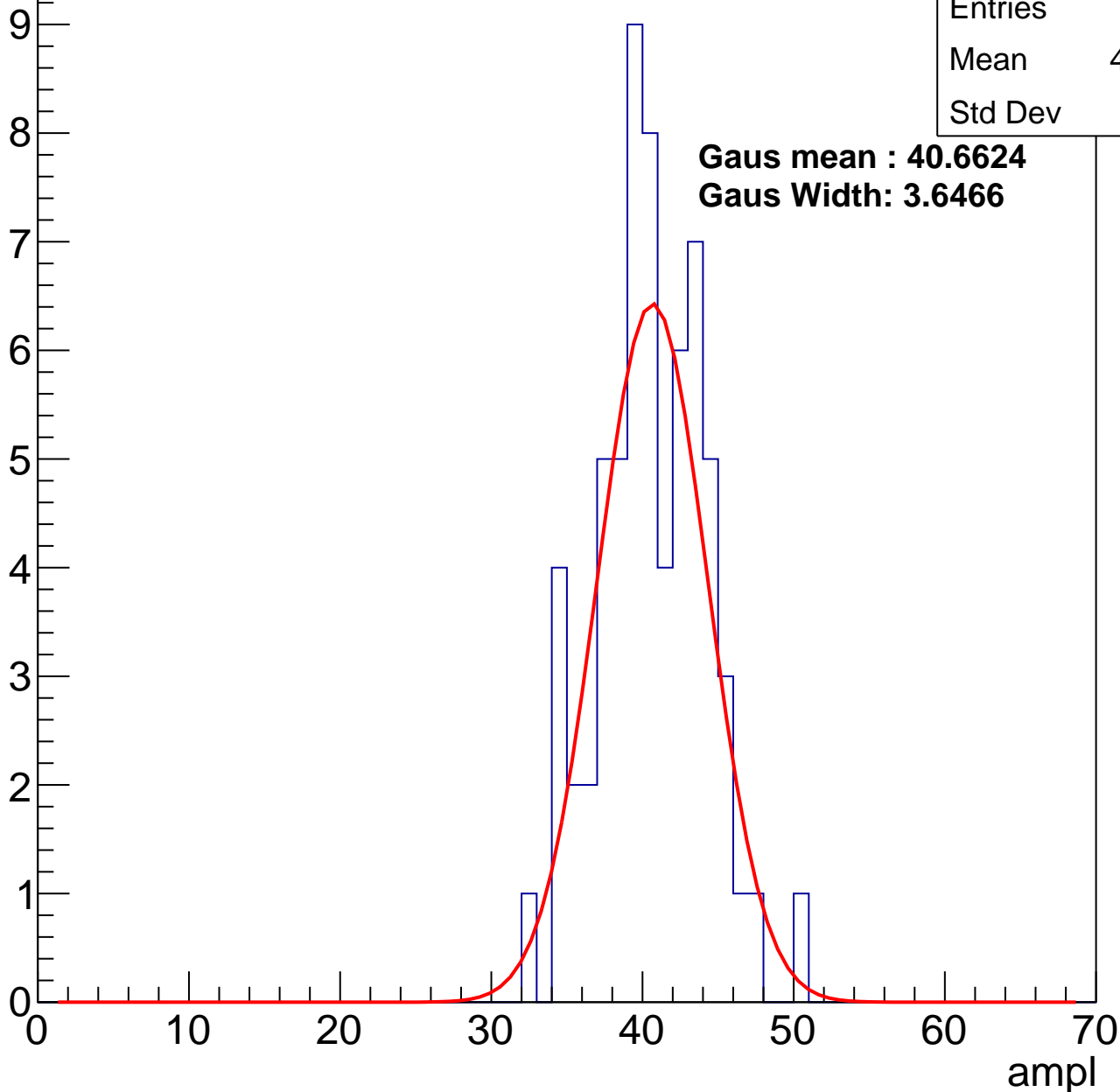
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.17
Std Dev	3.52

**Gaus mean : 40.6624**

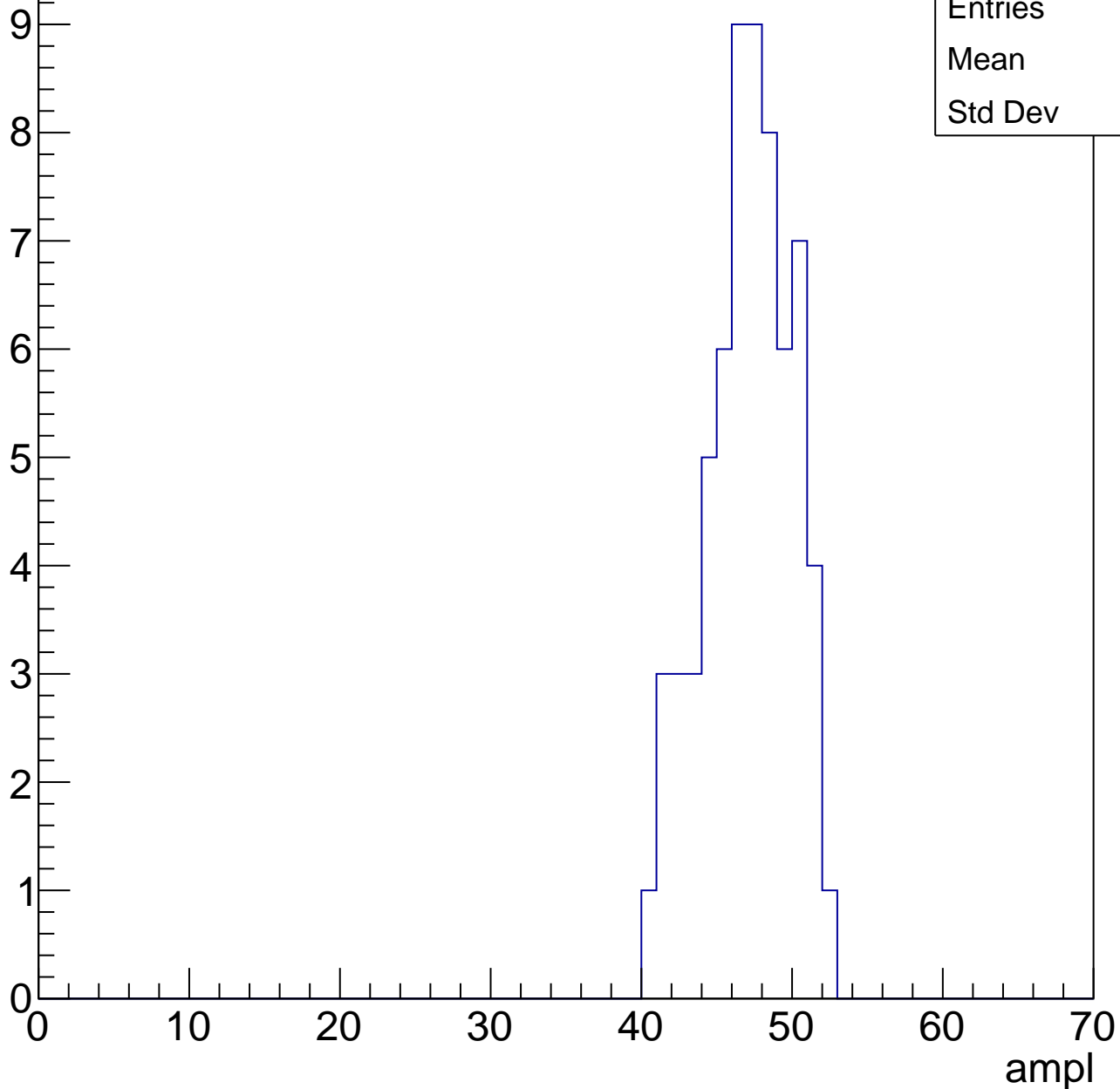
**Gaus Width: 3.6466**



# B1L002S, U8-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

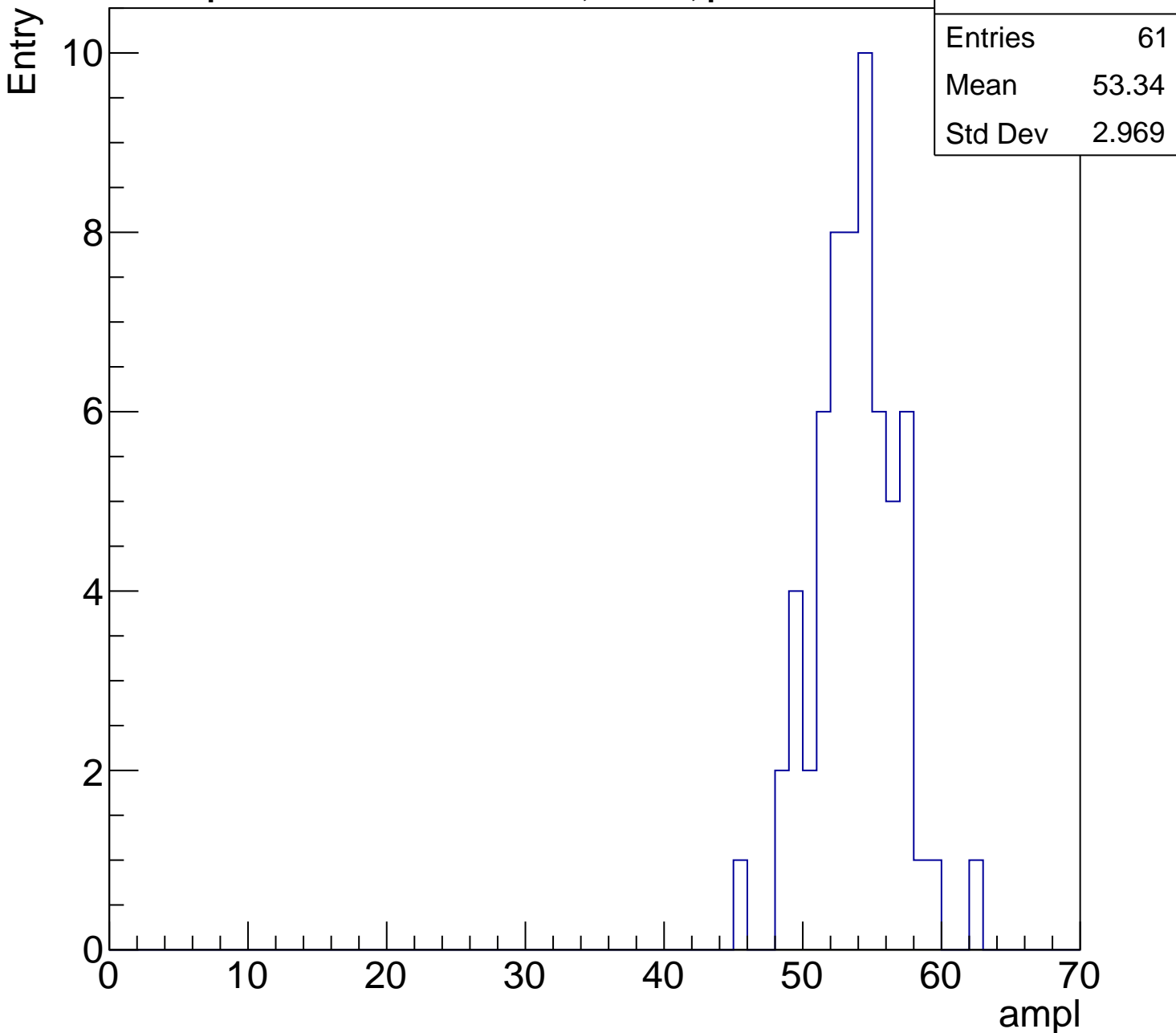
Entries	61
Mean	53.34
Std Dev	2.969

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

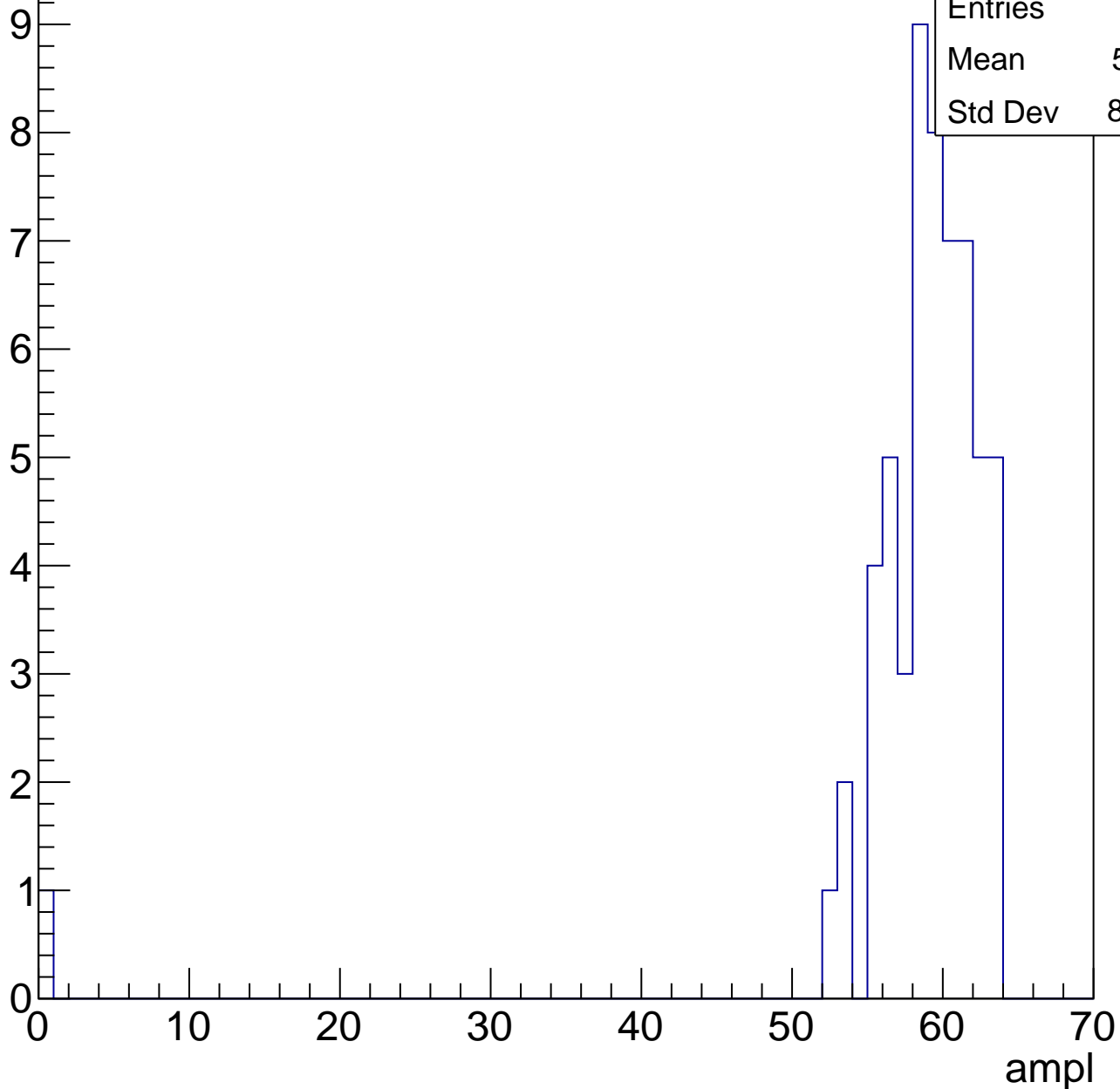
ampl



# B1L002S, U8-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

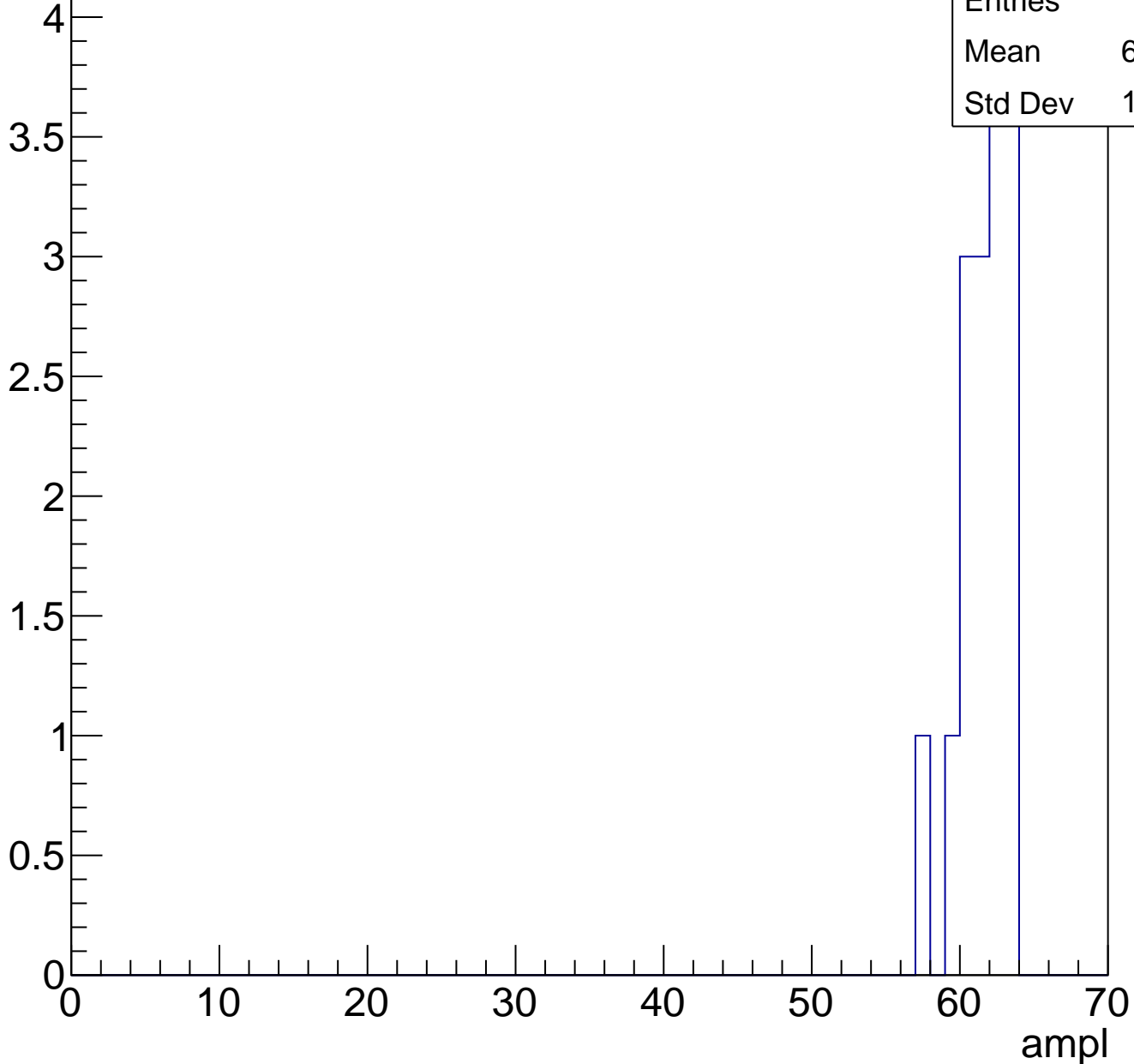
Entry



# B1L002S, U8-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch105, adc7

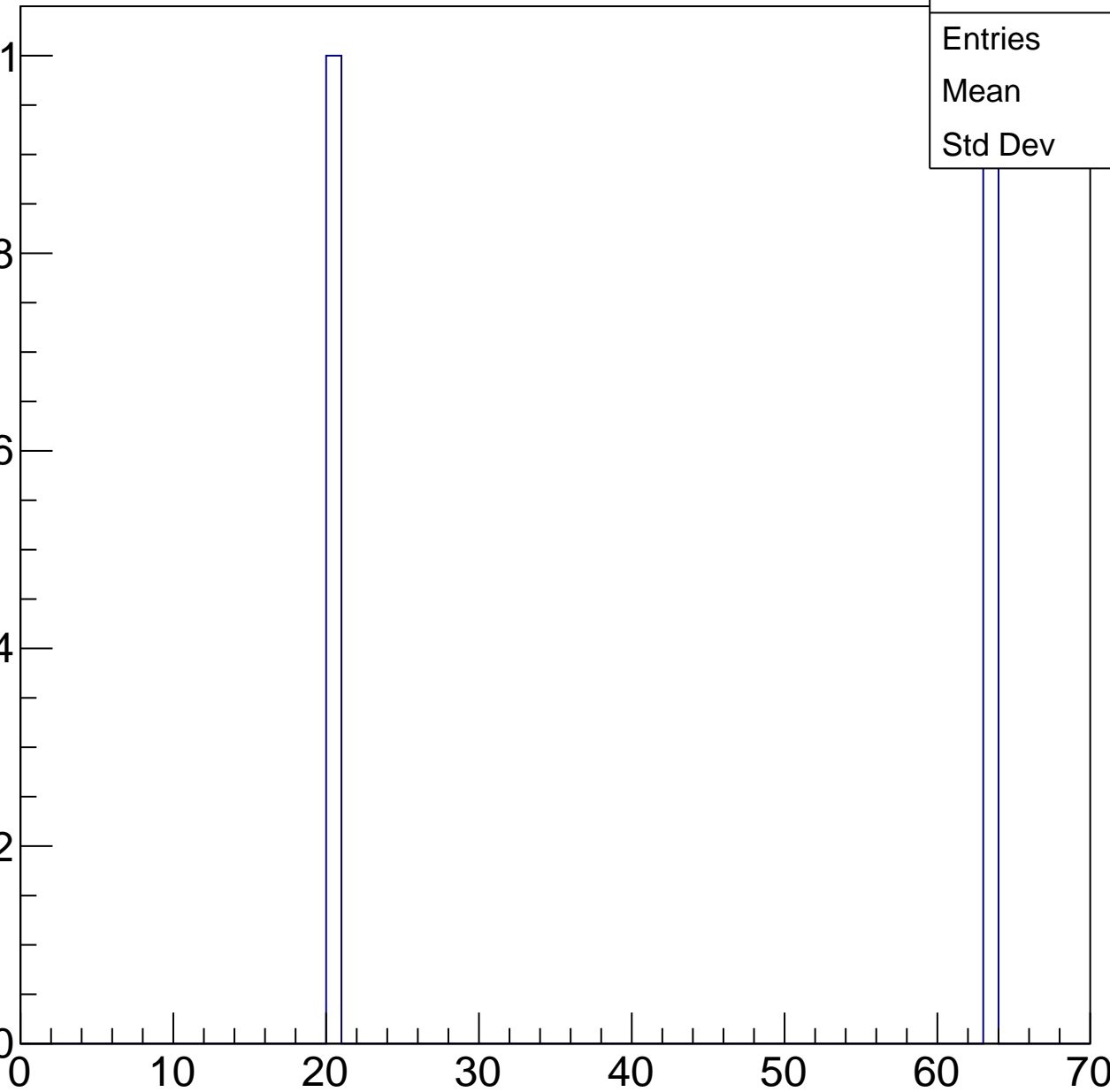
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl



# B1L002S, U8-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	26.44
Std Dev	4.404

**Gaus mean : 27.0635**

**Gaus Width: 2.6047**

10

8

6

4

2

0

0

10

20

30

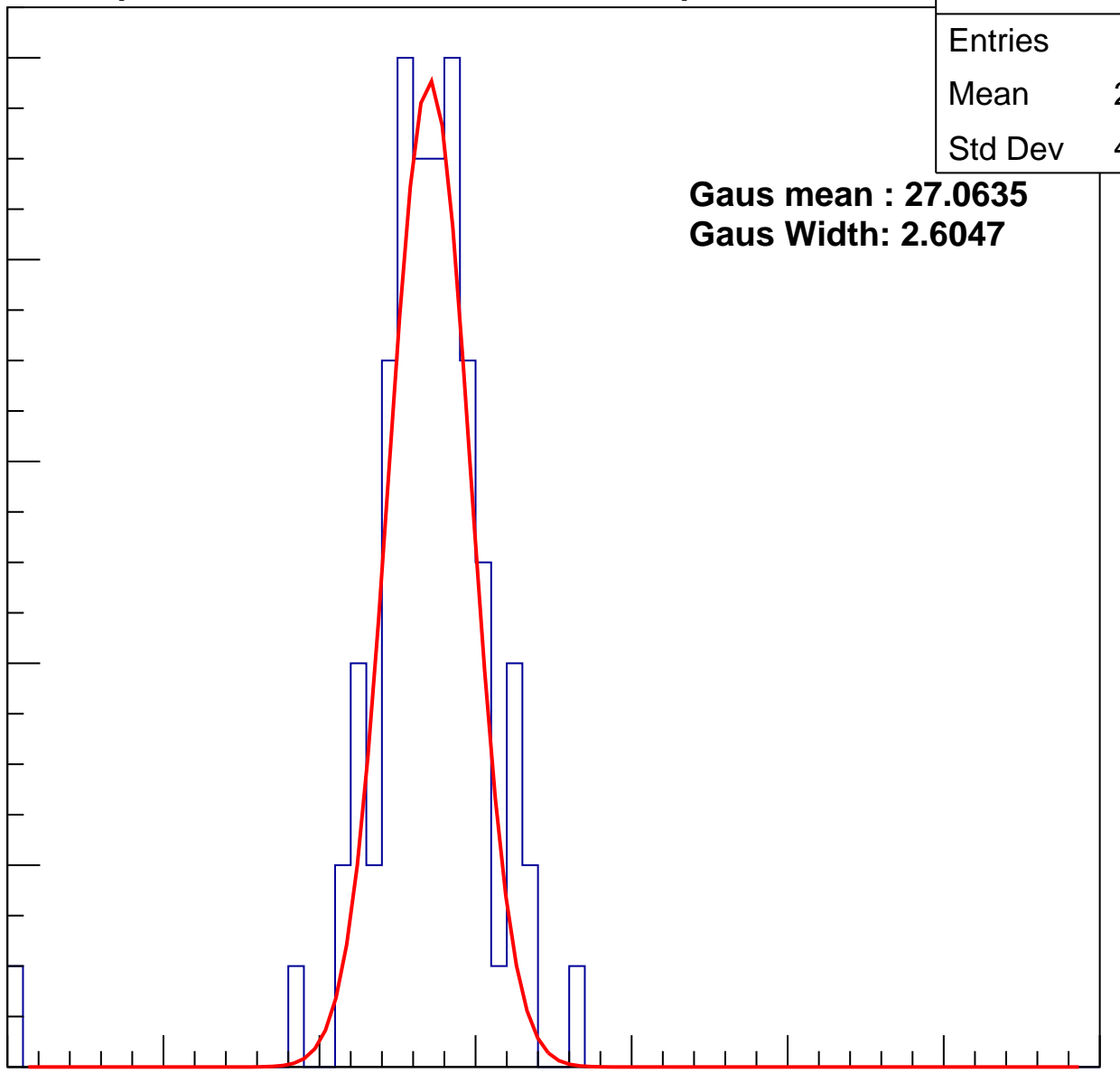
40

50

60

70

ampl



# B1L002S, U8-ch106, adc1

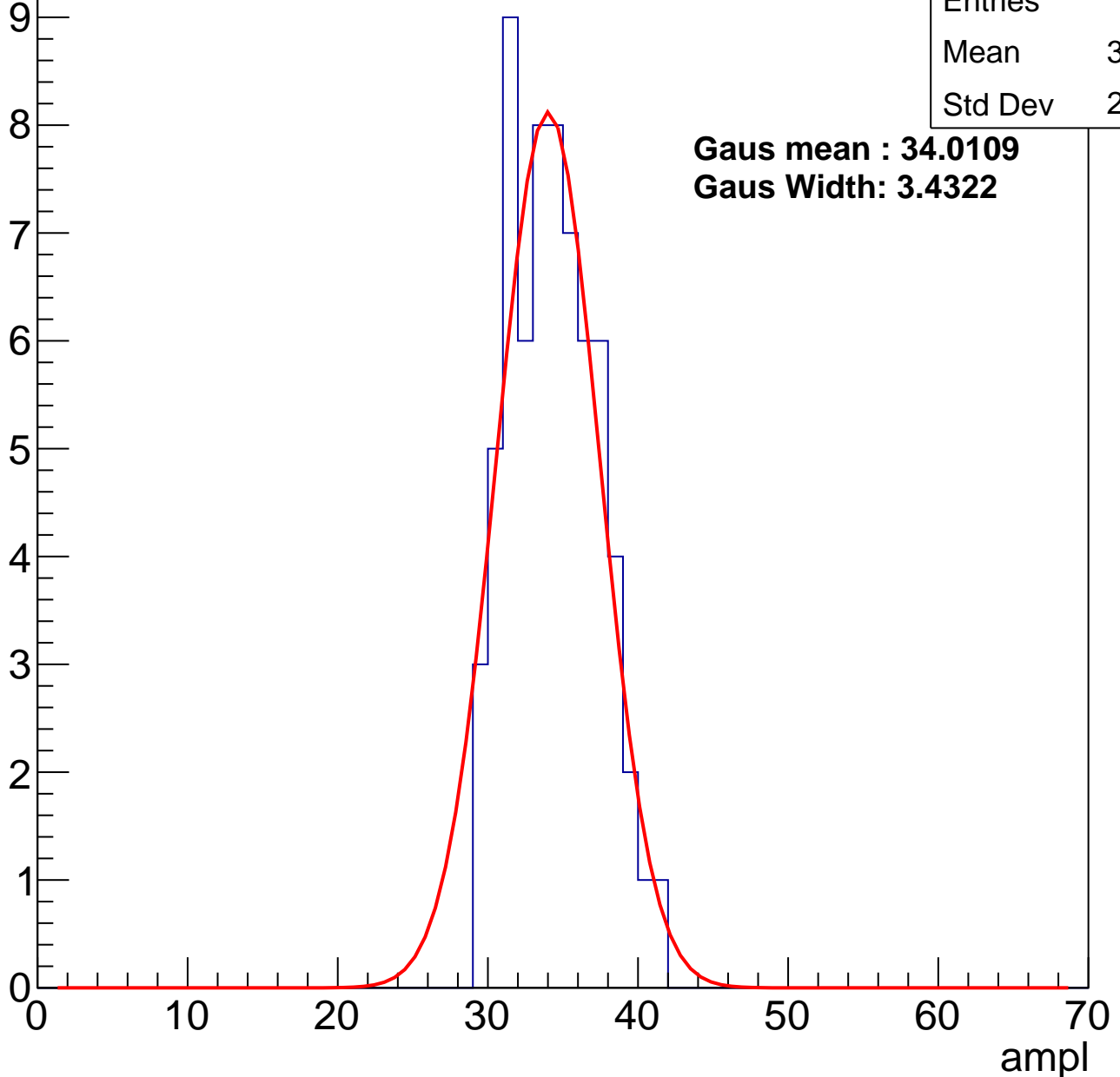
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.91
Std Dev	2.891

**Gaus mean : 34.0109**

**Gaus Width: 3.4322**



# B1L002S, U8-ch106, adc2

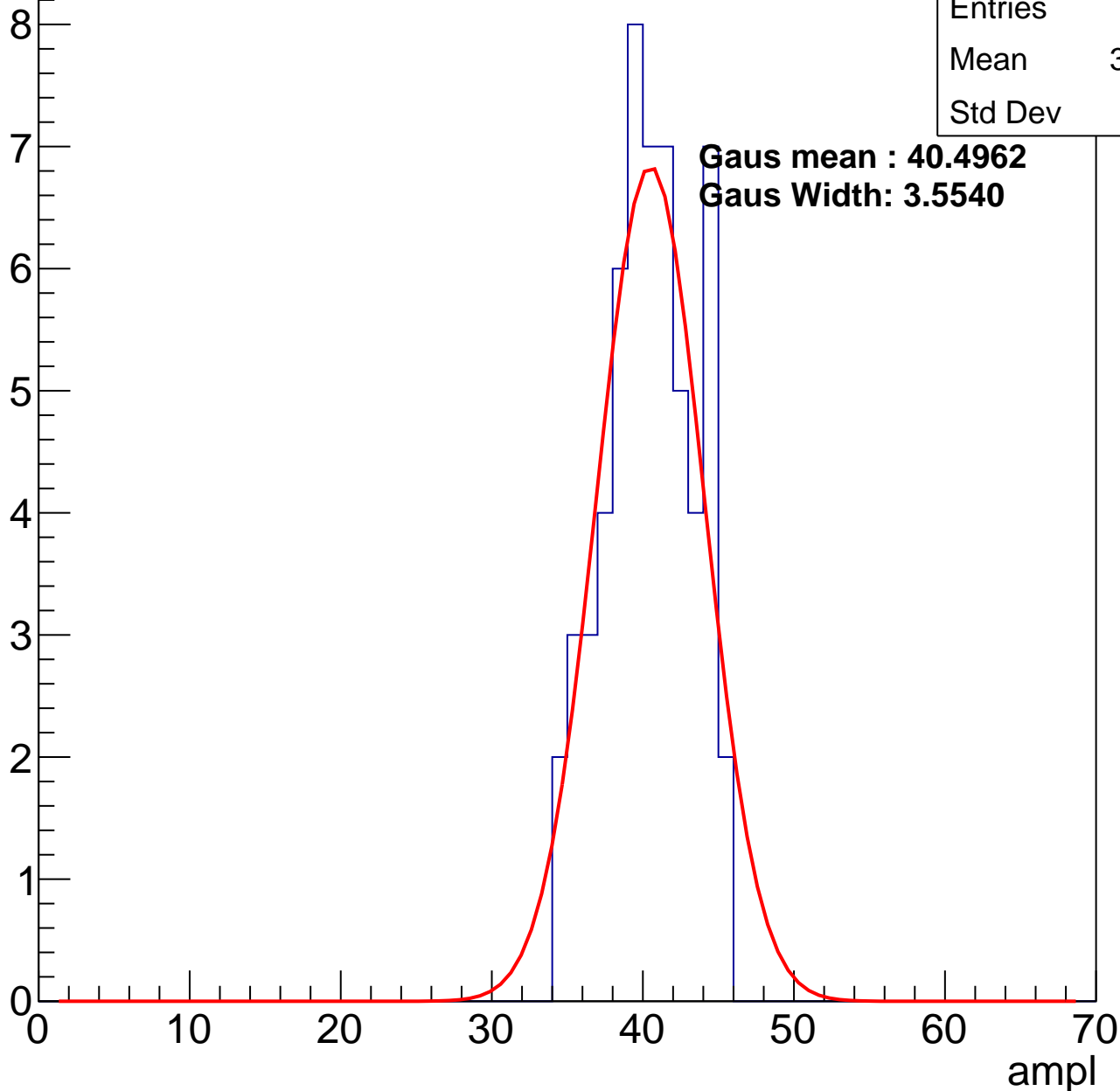
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.93
Std Dev	2.9

**Gaus mean : 40.4962**

**Gaus Width: 3.5540**

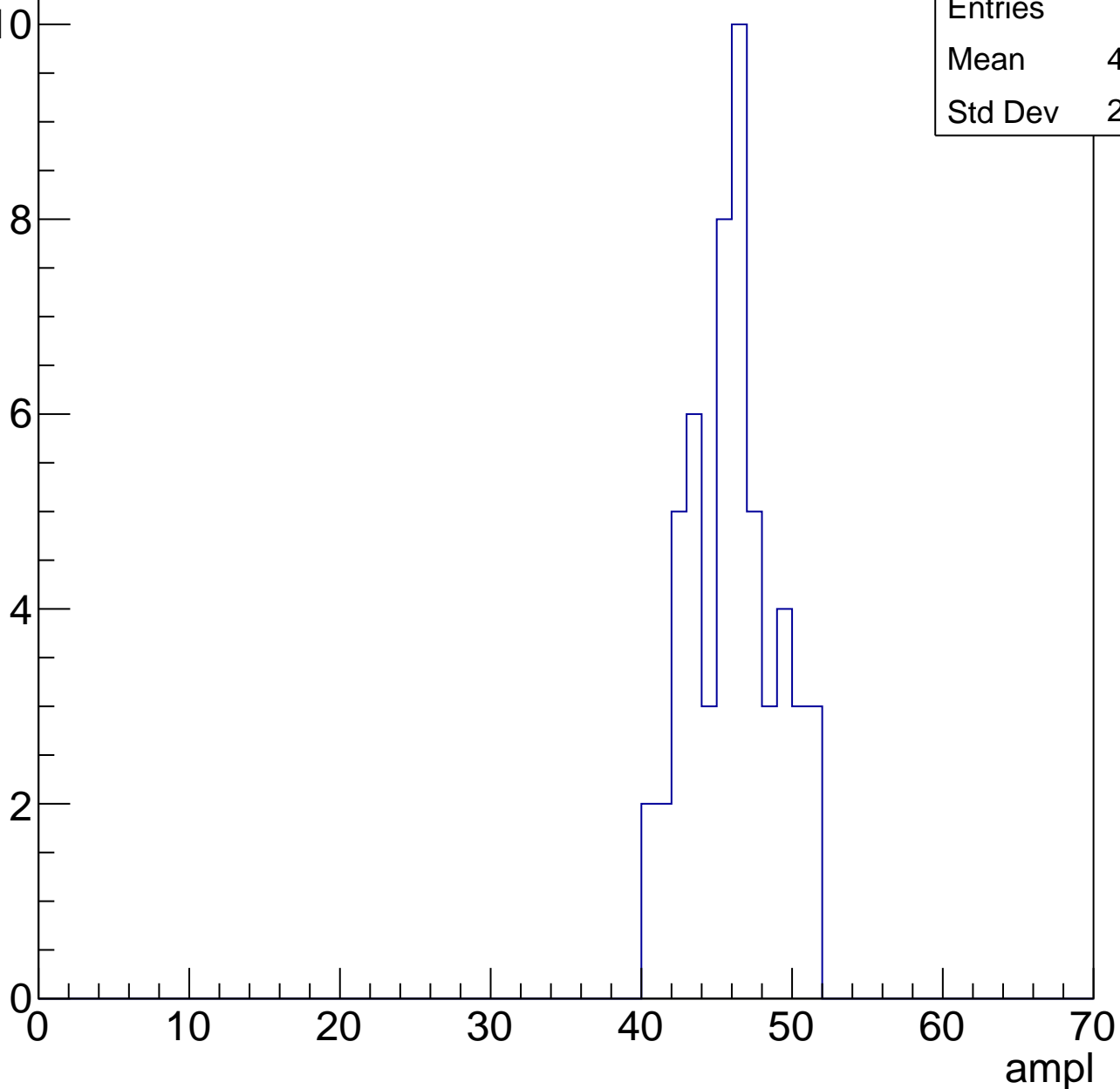


# B1L002S, U8-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	45.56
Std Dev	2.859

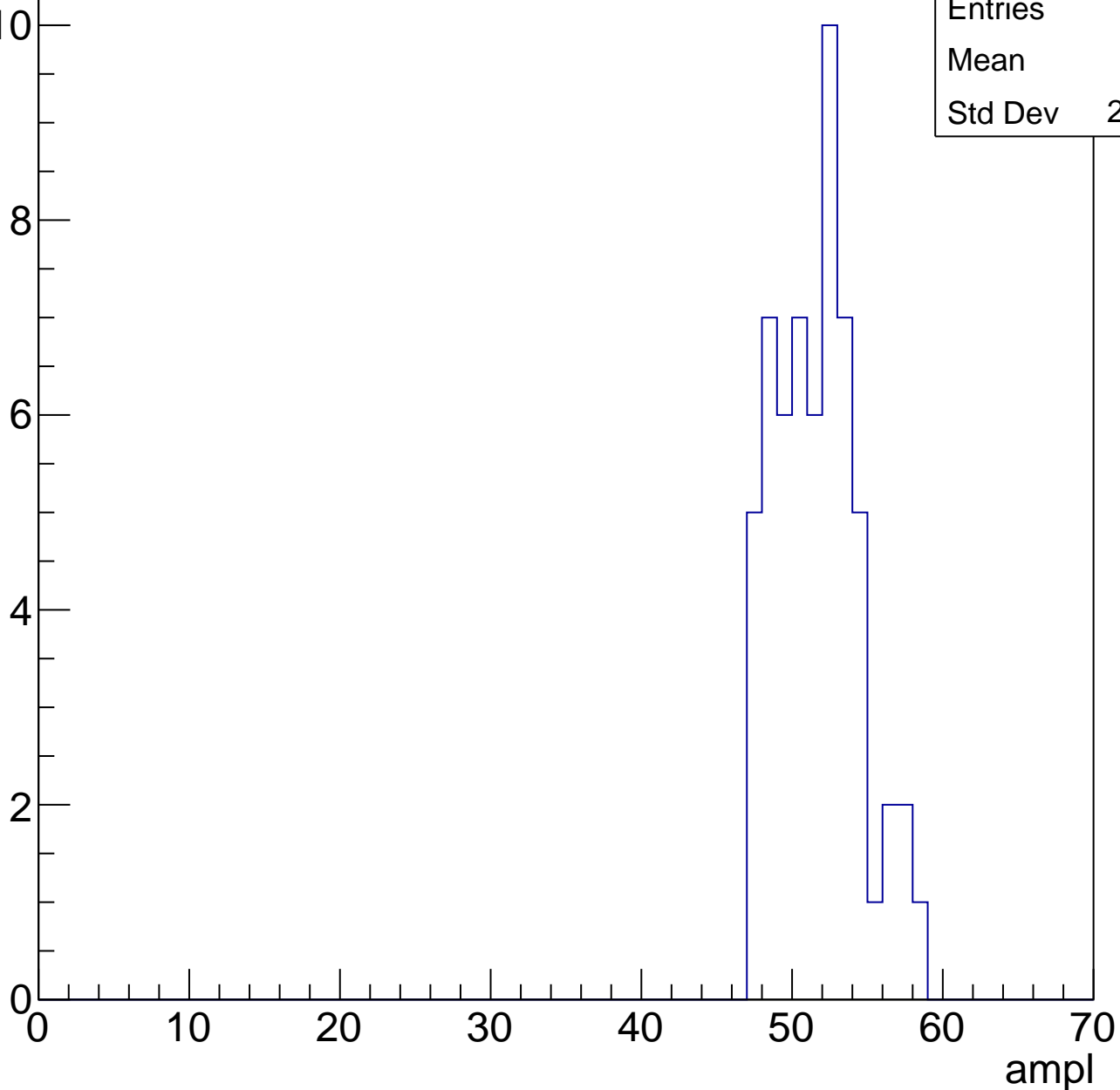


# B1L002S, U8-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	51.2
Std Dev	2.736

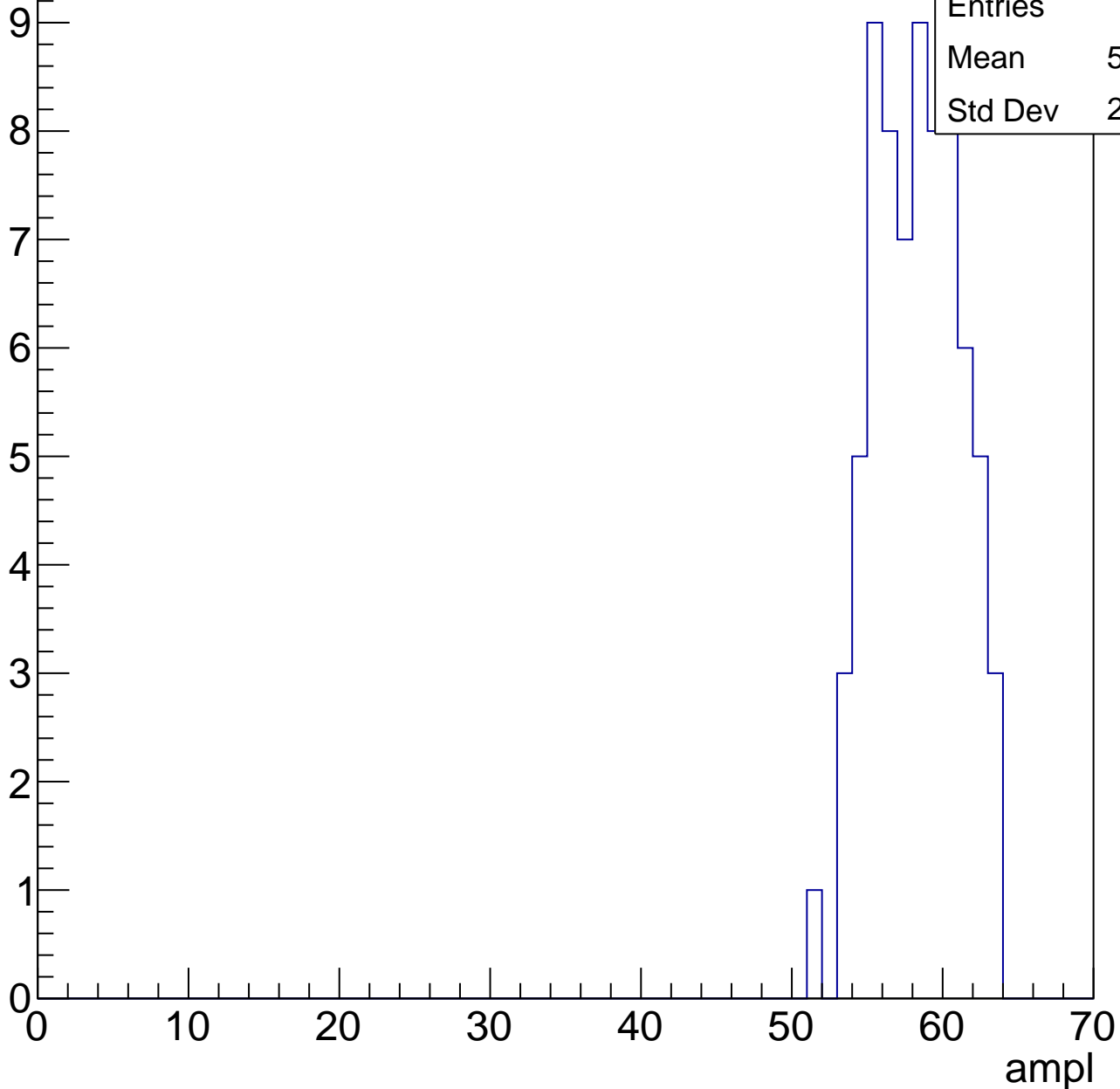


# B1L002S, U8-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	57.82
Std Dev	2.806

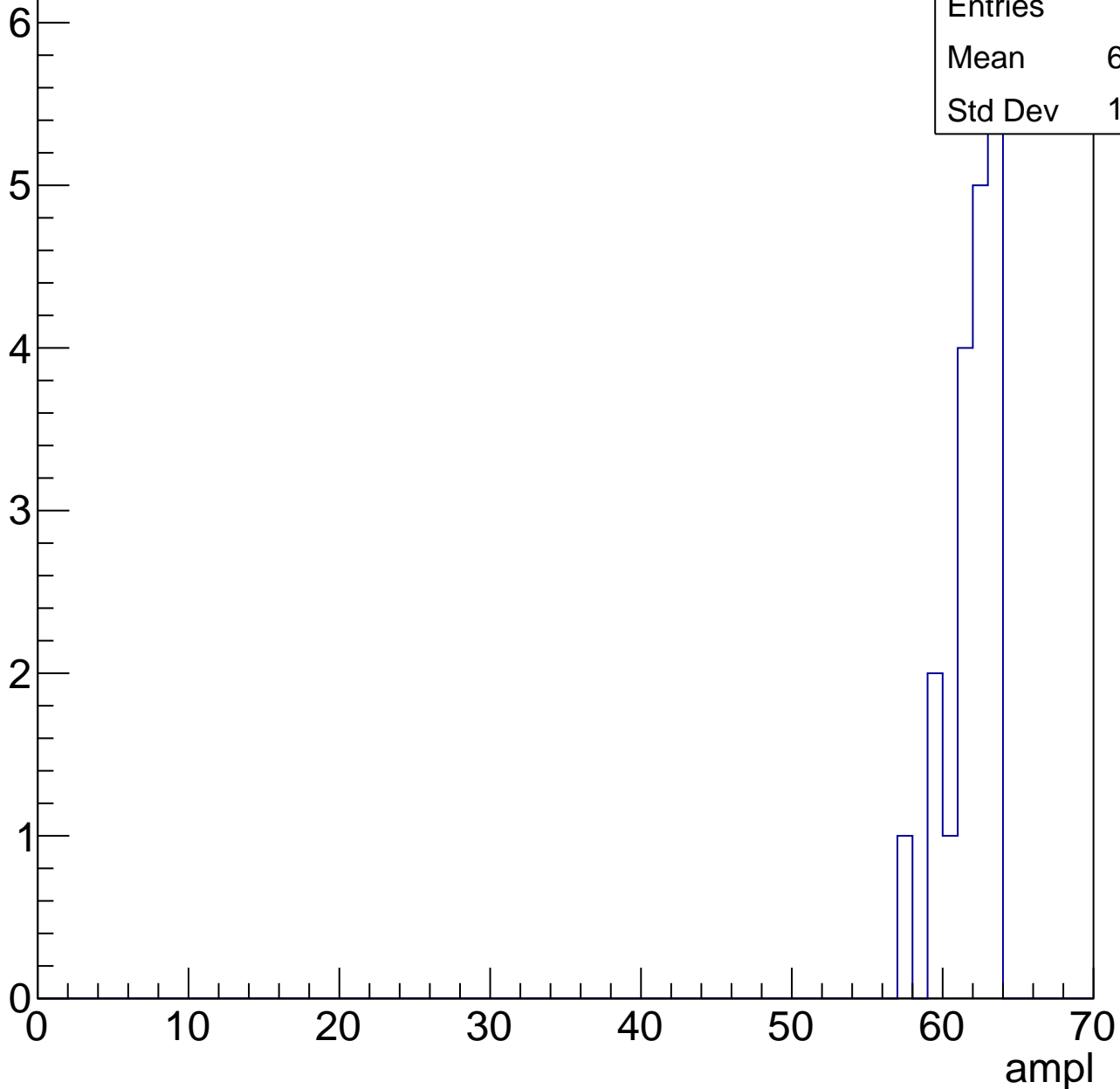


# B1L002S, U8-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.42
Std Dev	1.632

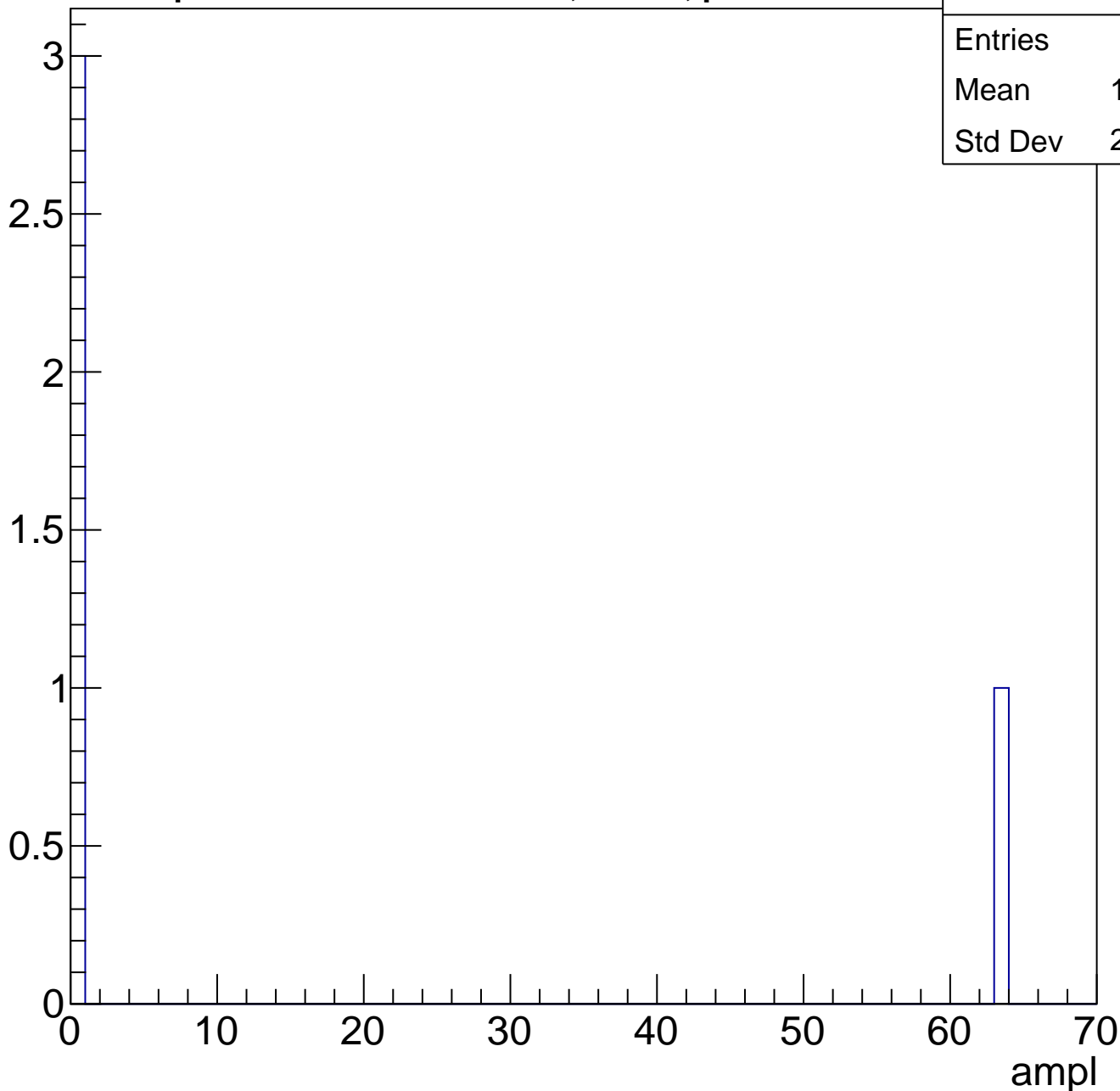




# B1L002S, U8-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	15.75
Std Dev	27.28

# B1L002S, U8-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	28.66
Std Dev	3.05

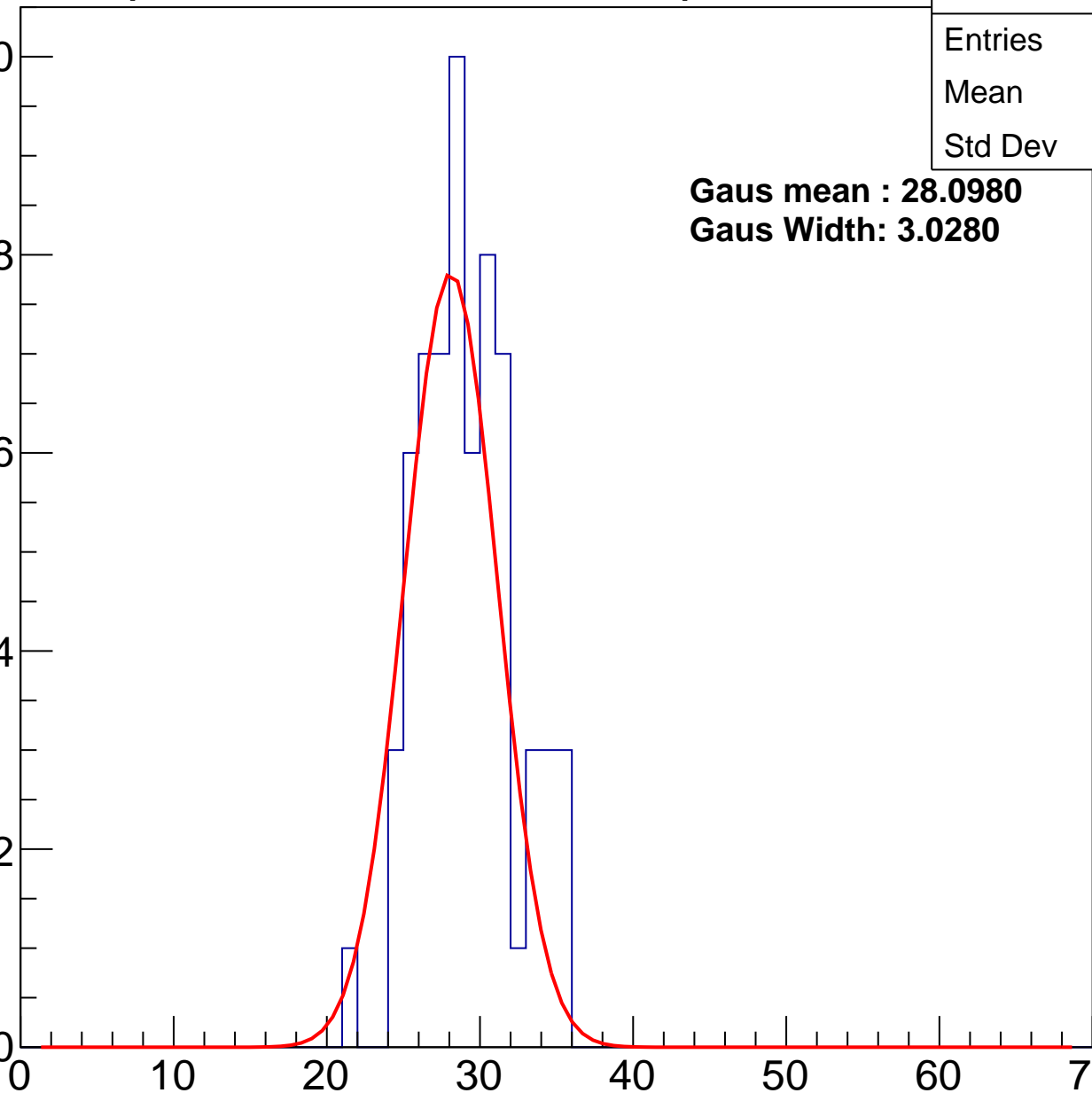
**Gaus mean : 28.0980**

**Gaus Width: 3.0280**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U8-ch107, adc1

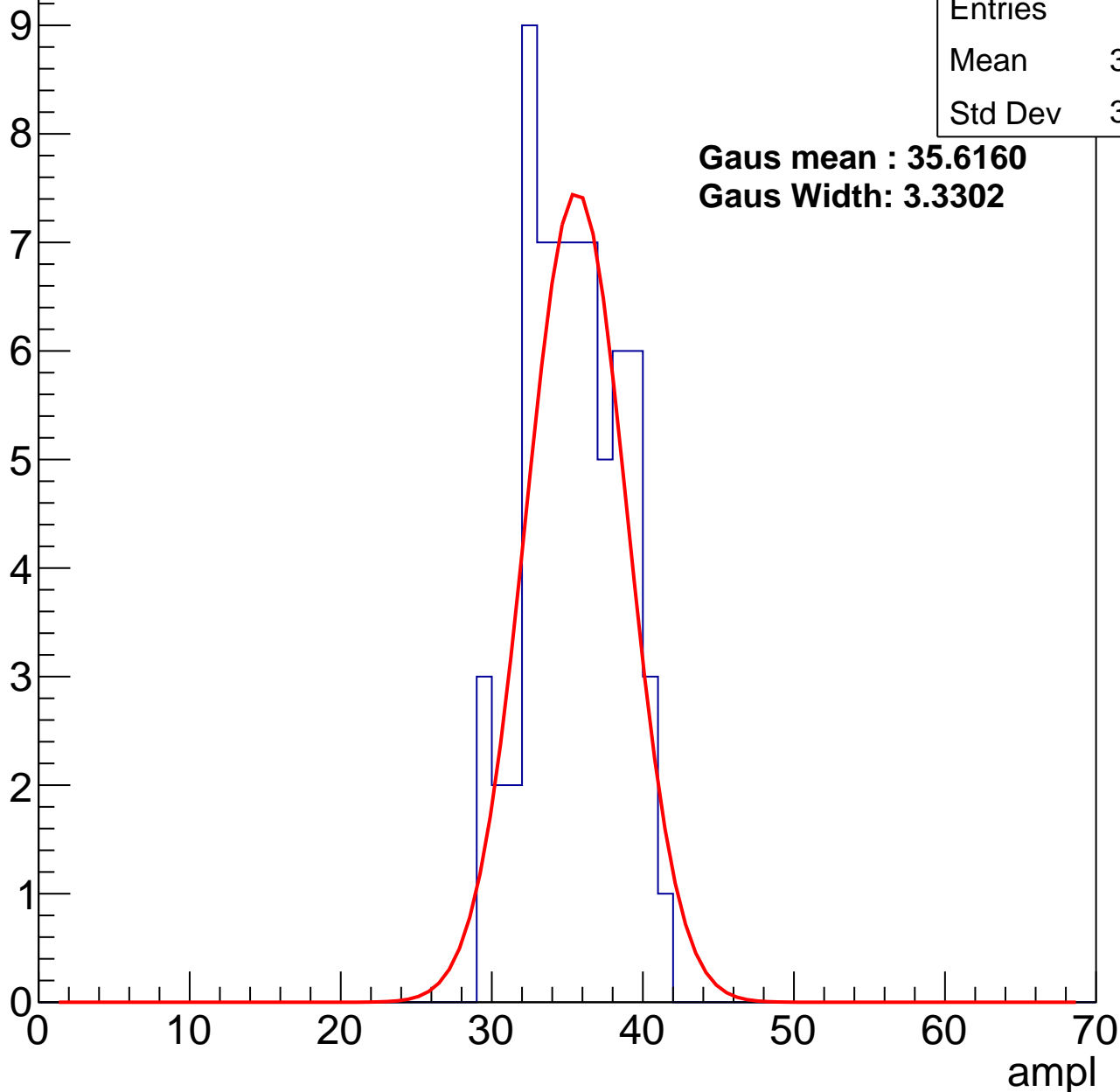
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.94
Std Dev	3.022

**Gaus mean : 35.6160**

**Gaus Width: 3.3302**



# B1L002S, U8-ch107, adc2

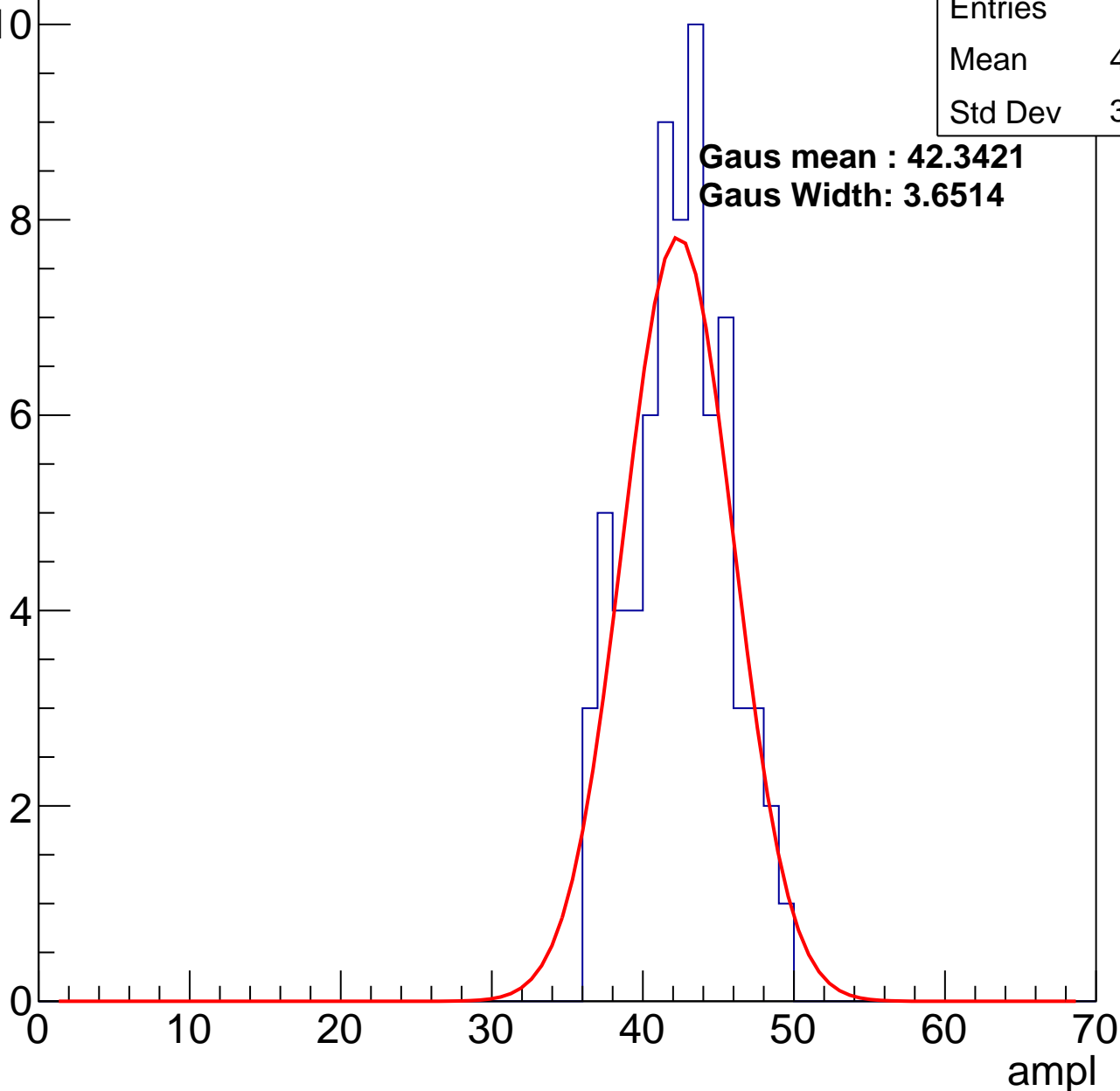
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	41.96
Std Dev	3.155

**Gaus mean : 42.3421**

**Gaus Width: 3.6514**

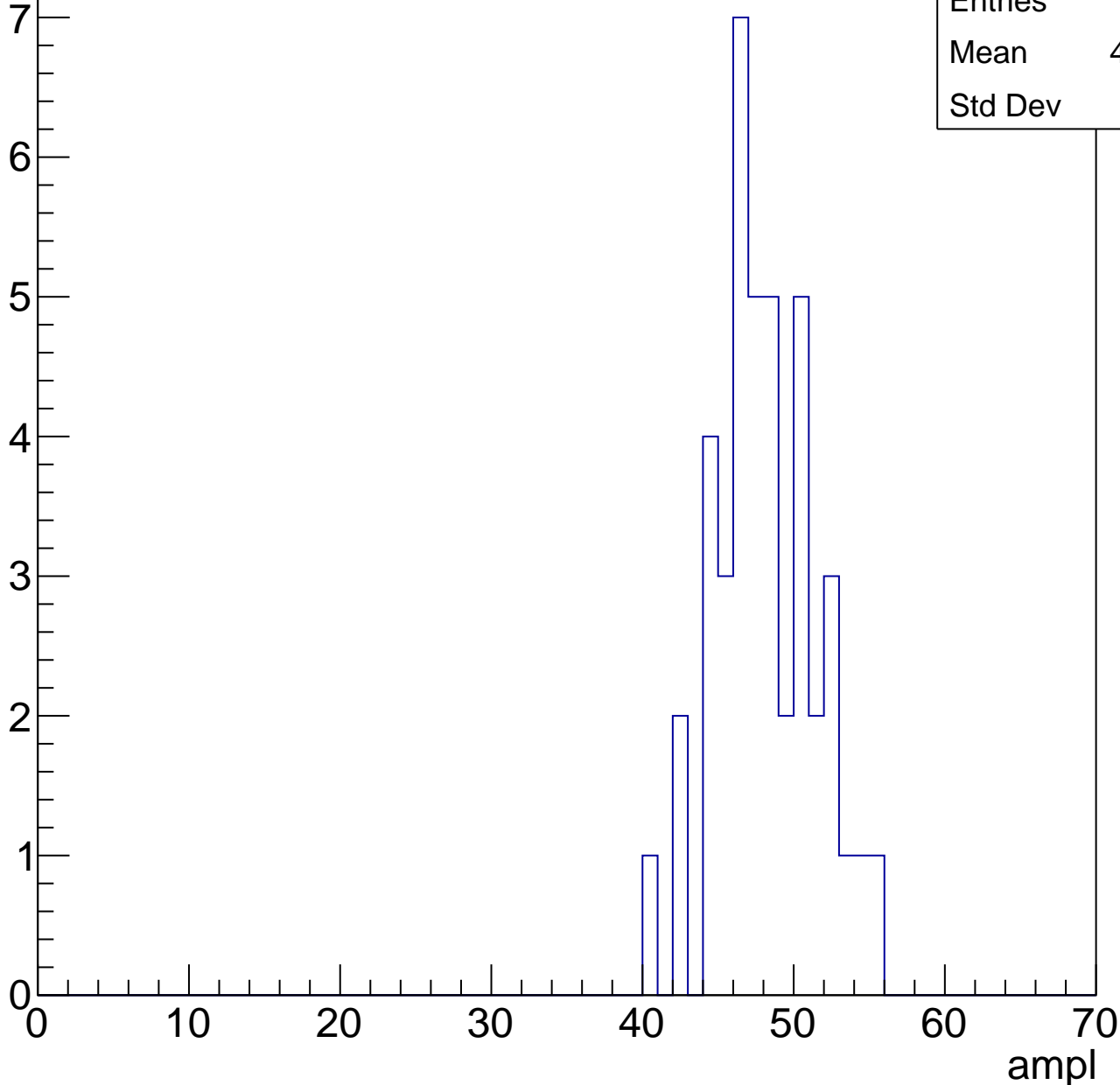


# B1L002S, U8-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	47.62
Std Dev	3.28

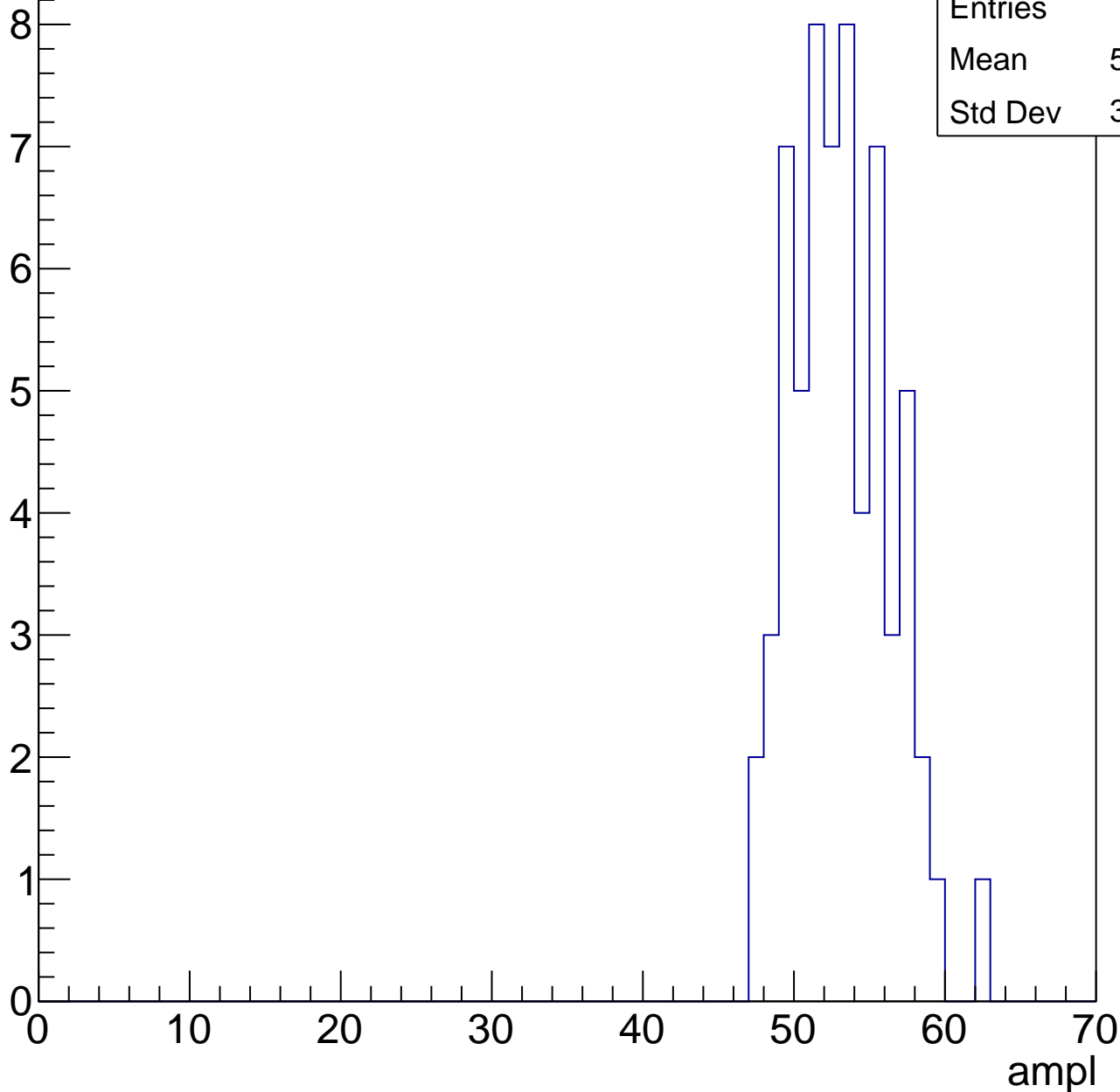


# B1L002S, U8-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	52.67
Std Dev	3.192

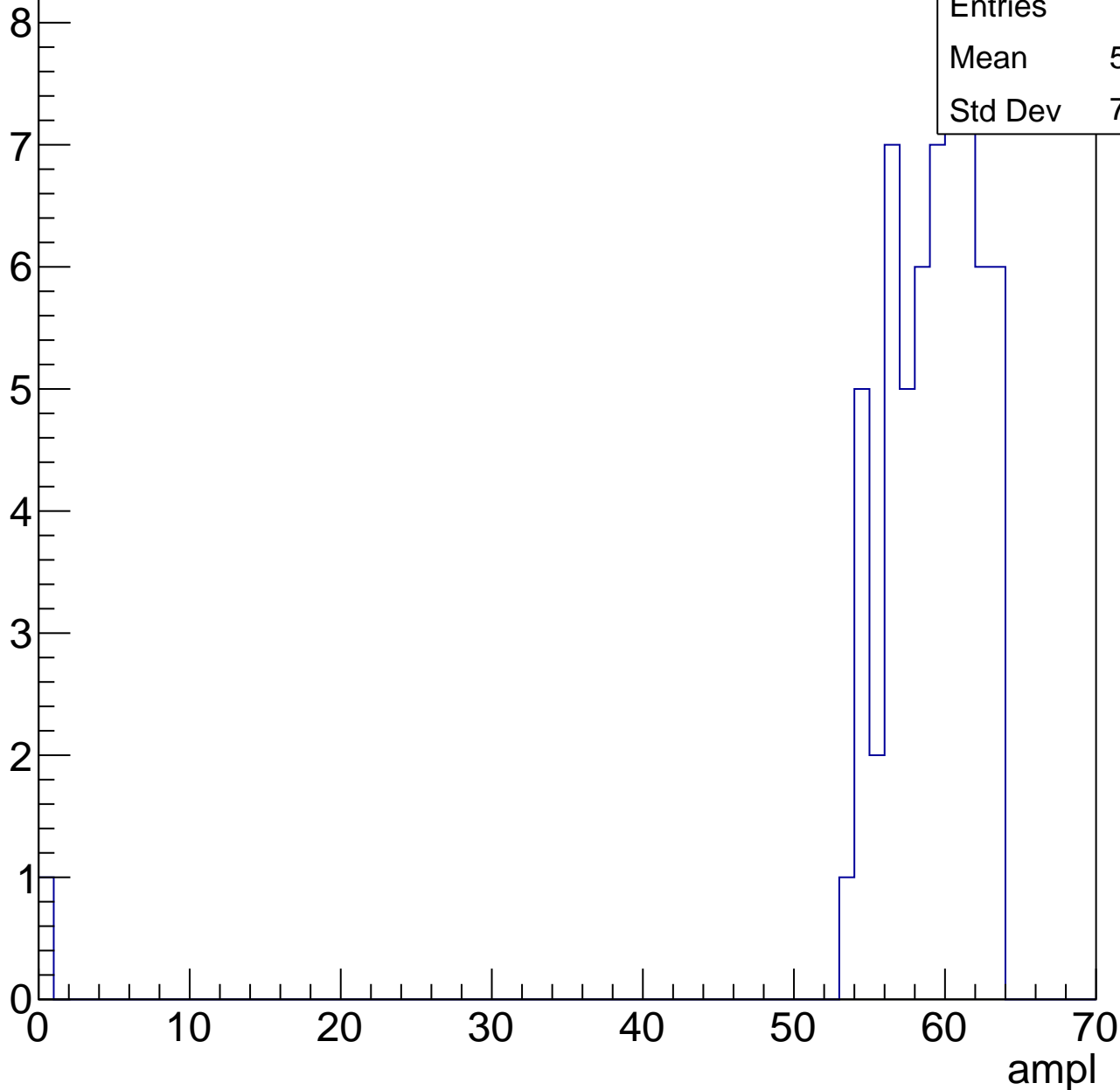


# B1L002S, U8-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

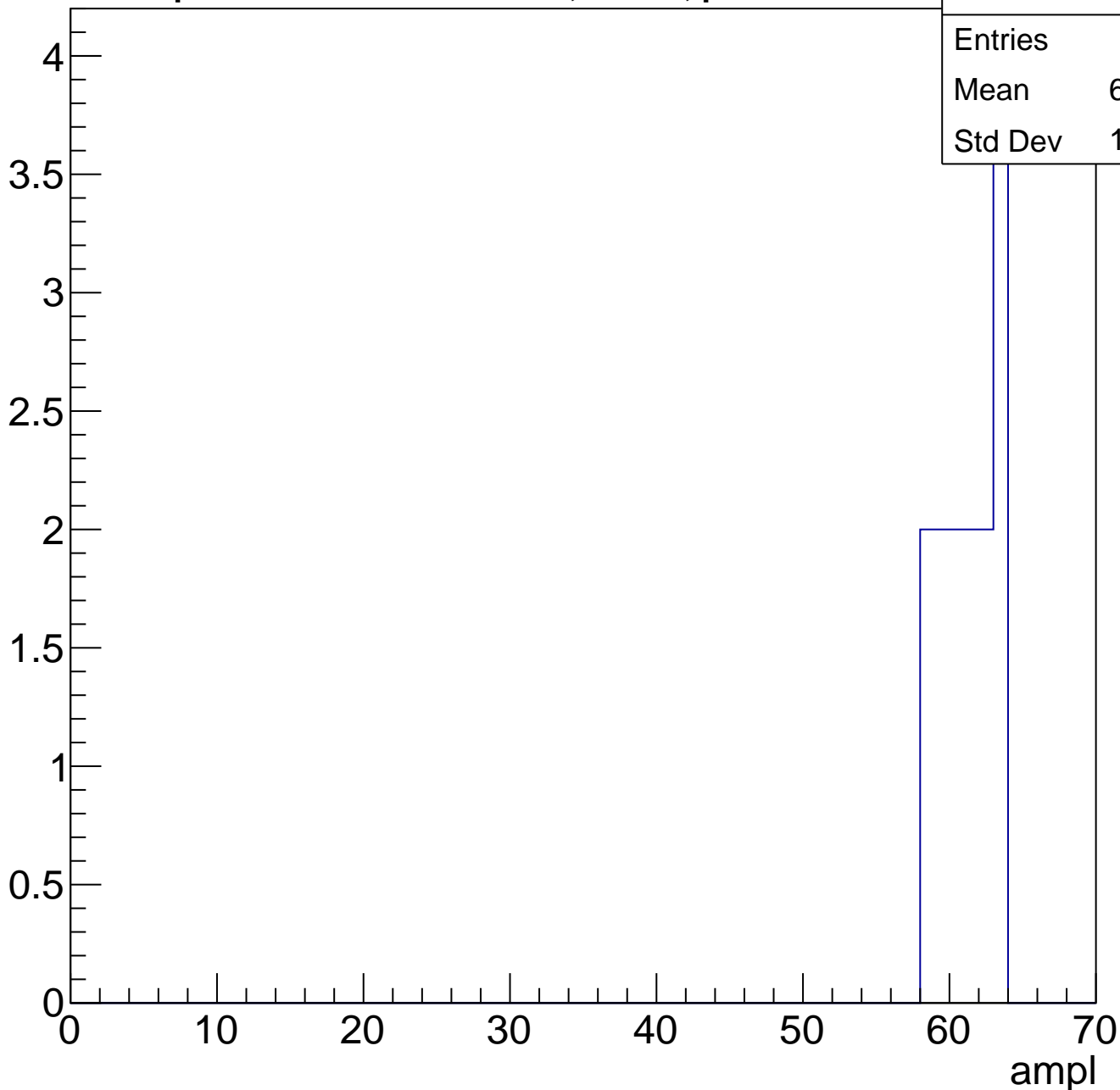
Entries	62
Mean	57.89
Std Dev	7.907



# B1L002S, U8-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

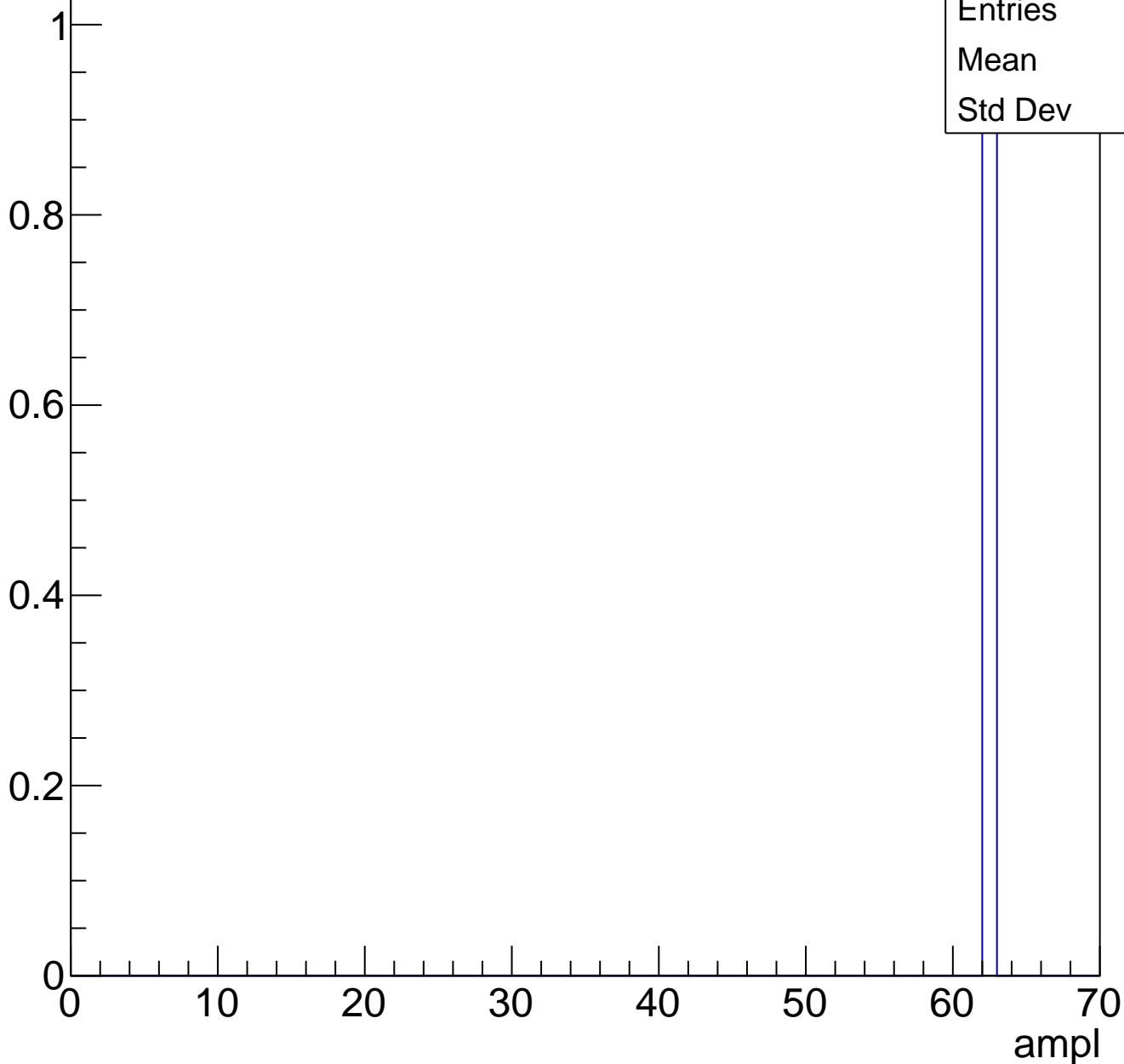




# B1L002S, U8-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch108, adc0

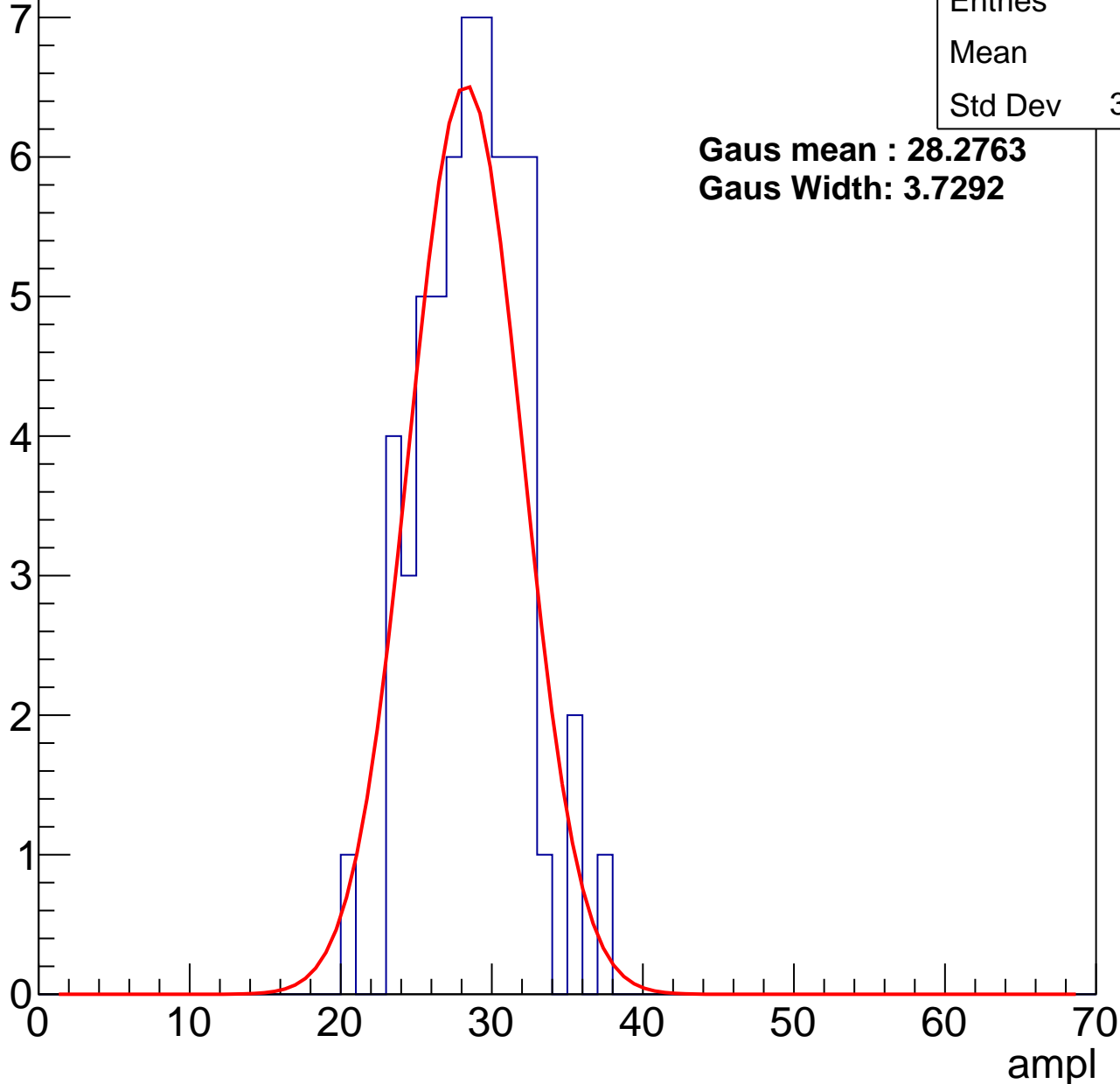
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.3
Std Dev	3.323

**Gaus mean : 28.2763**

**Gaus Width: 3.7292**



# B1L002S, U8-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	34.33
Std Dev	2.773

**Gaus mean : 34.9463**

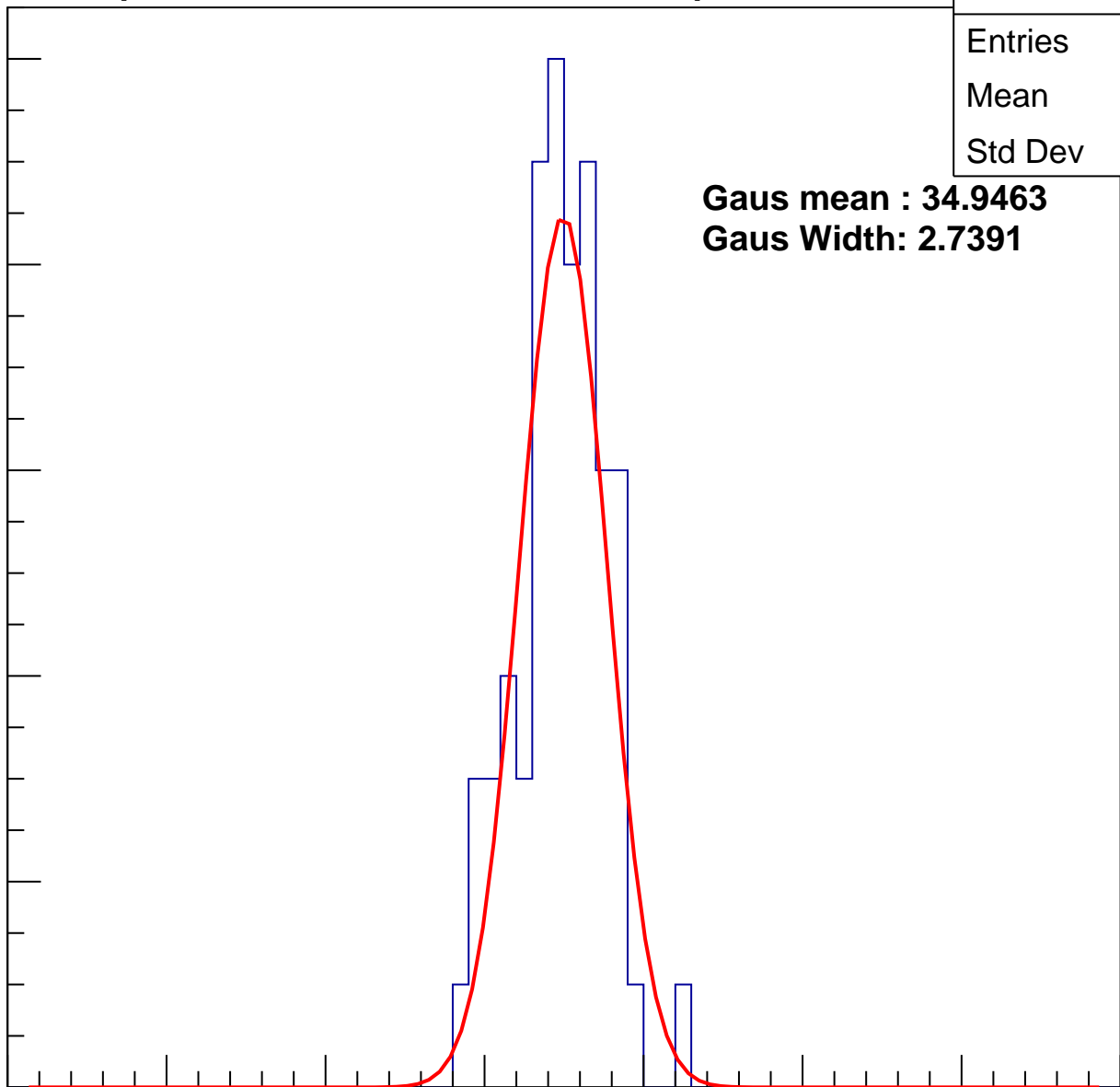
**Gaus Width: 2.7391**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch108, adc2

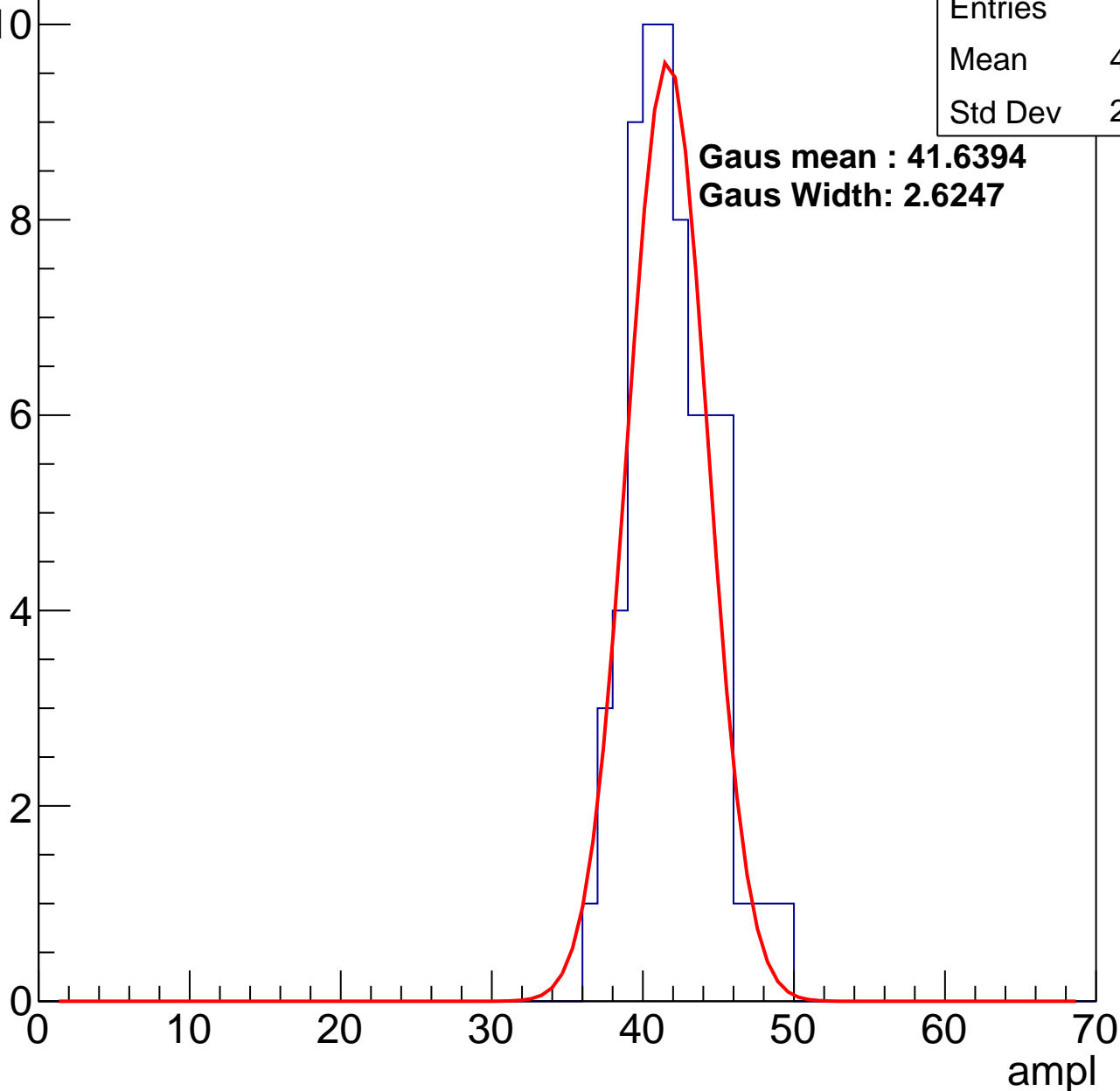
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	41.46
Std Dev	2.723

**Gaus mean : 41.6394**

**Gaus Width: 2.6247**

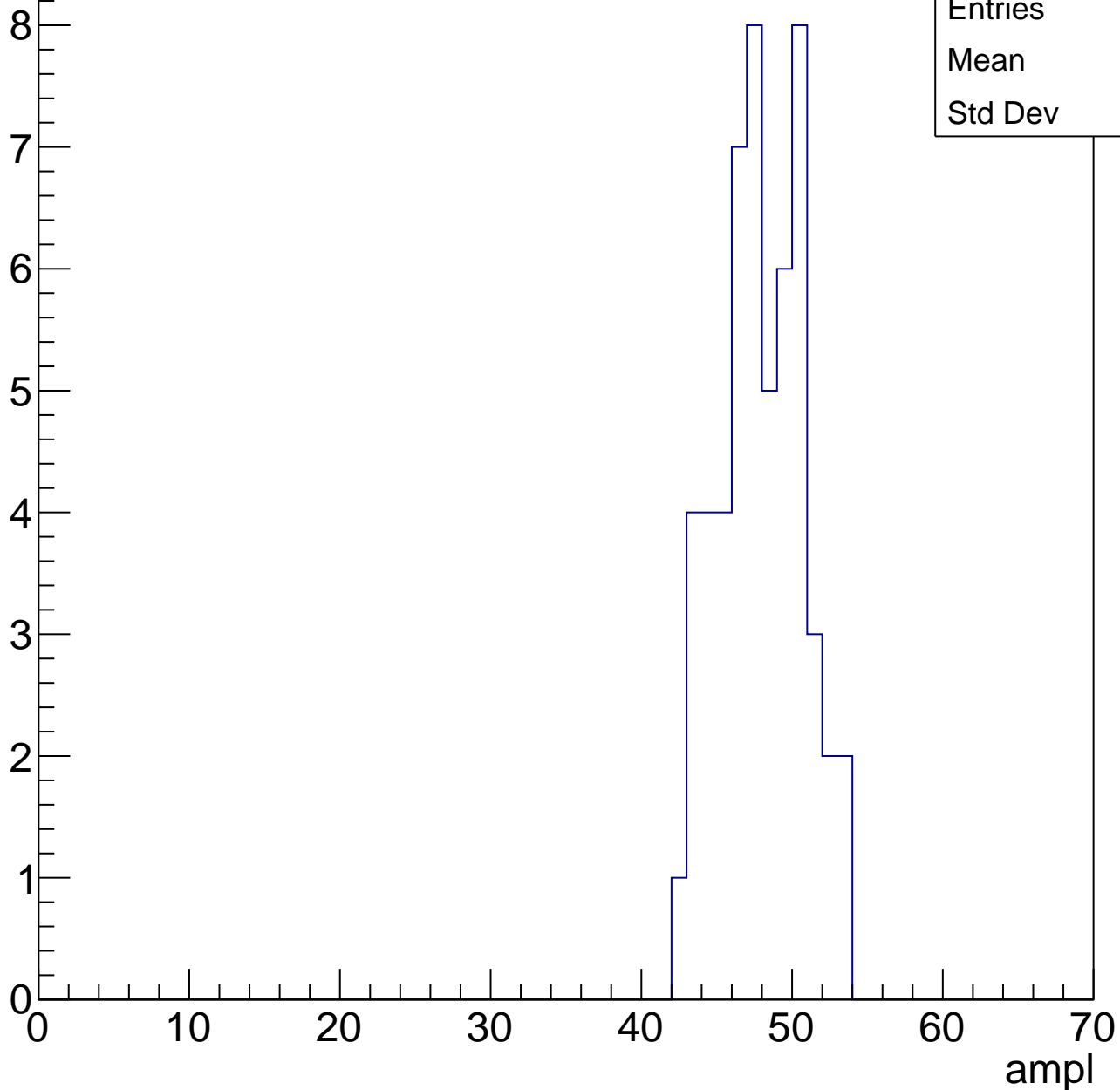


# B1L002S, U8-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	47.5
Std Dev	2.74



# B1L002S, U8-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

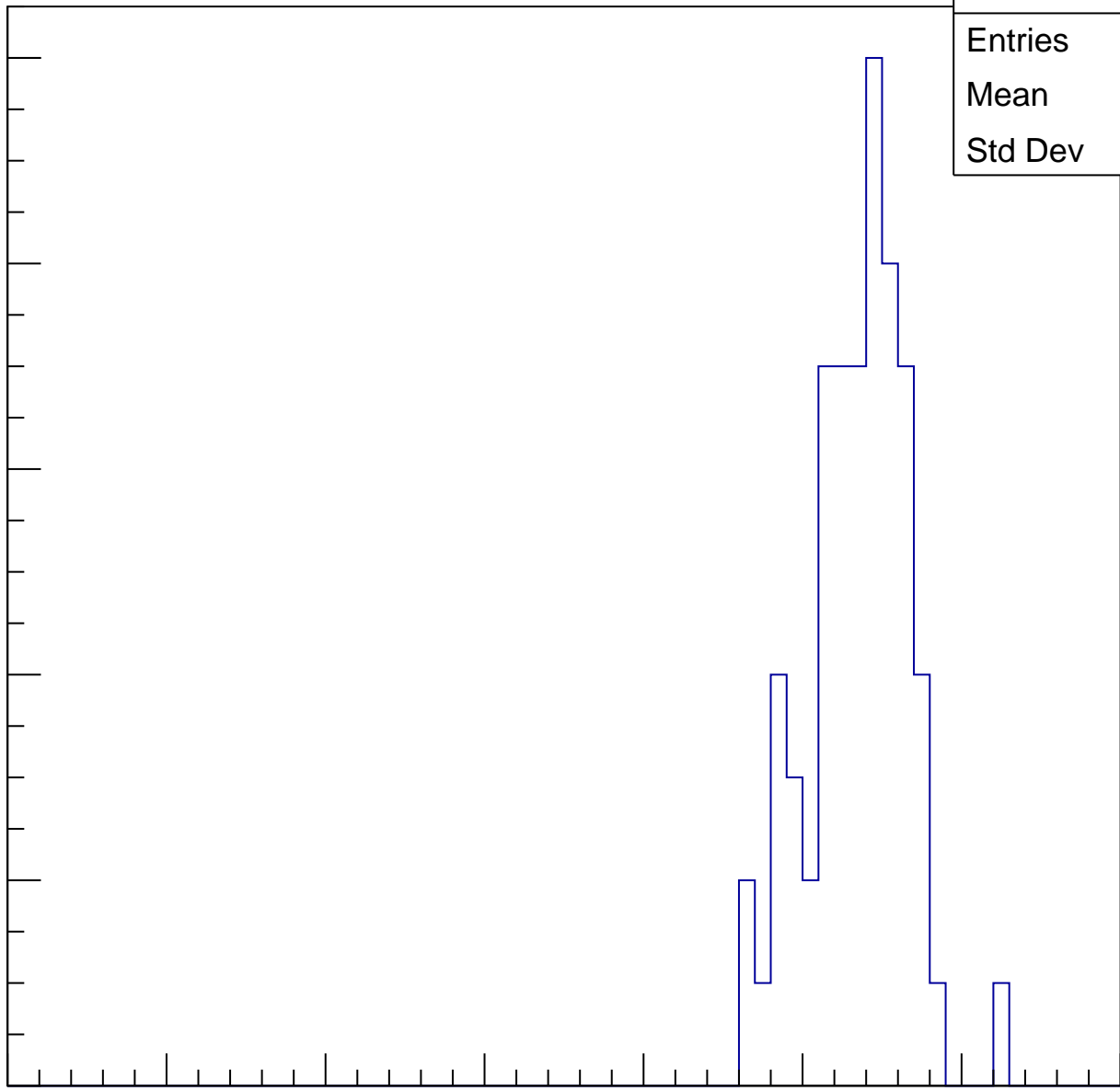
Entries	64
Mean	52.97
Std Dev	3.087

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

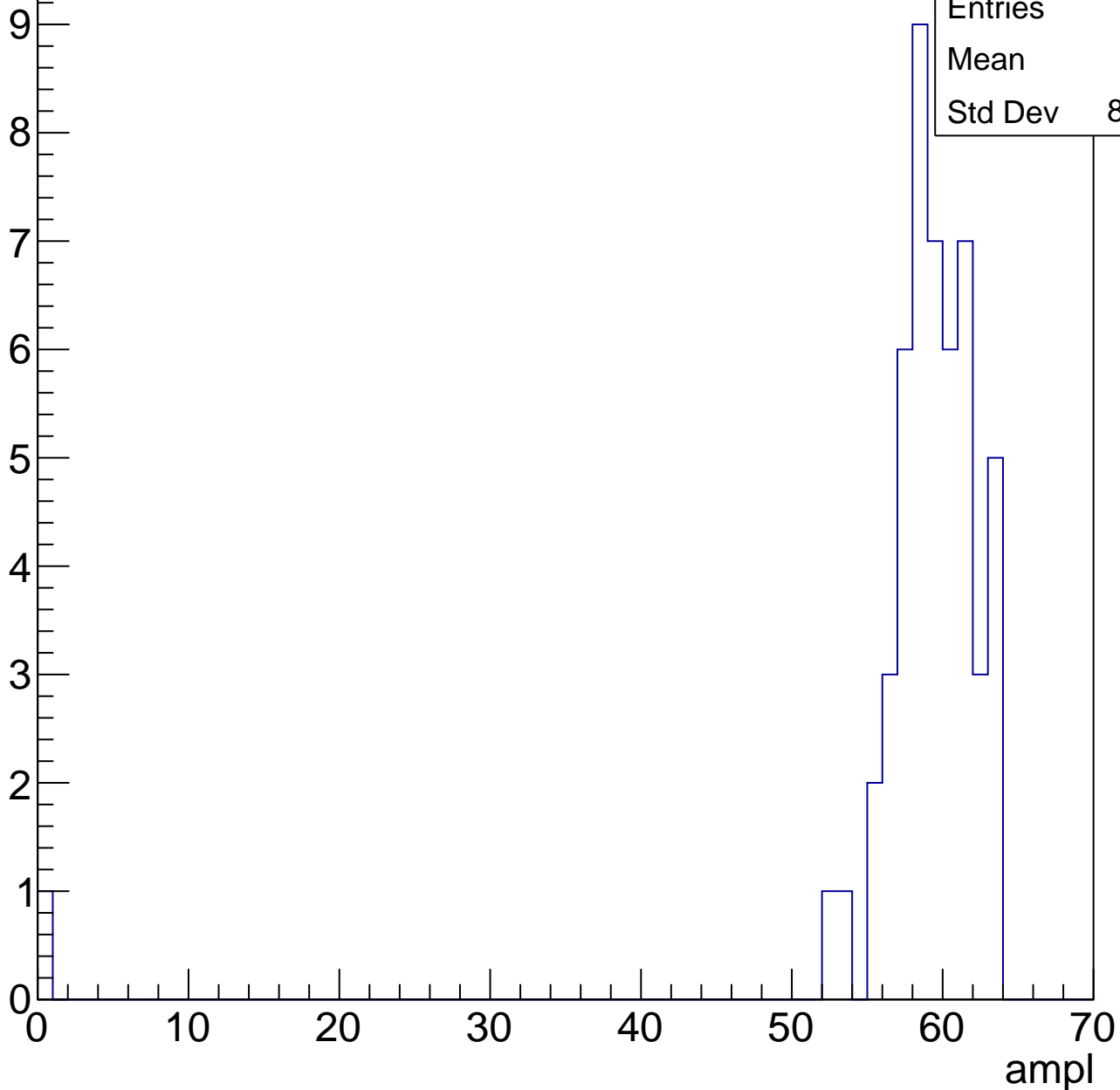


# B1L002S, U8-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.8
Std Dev	8.547

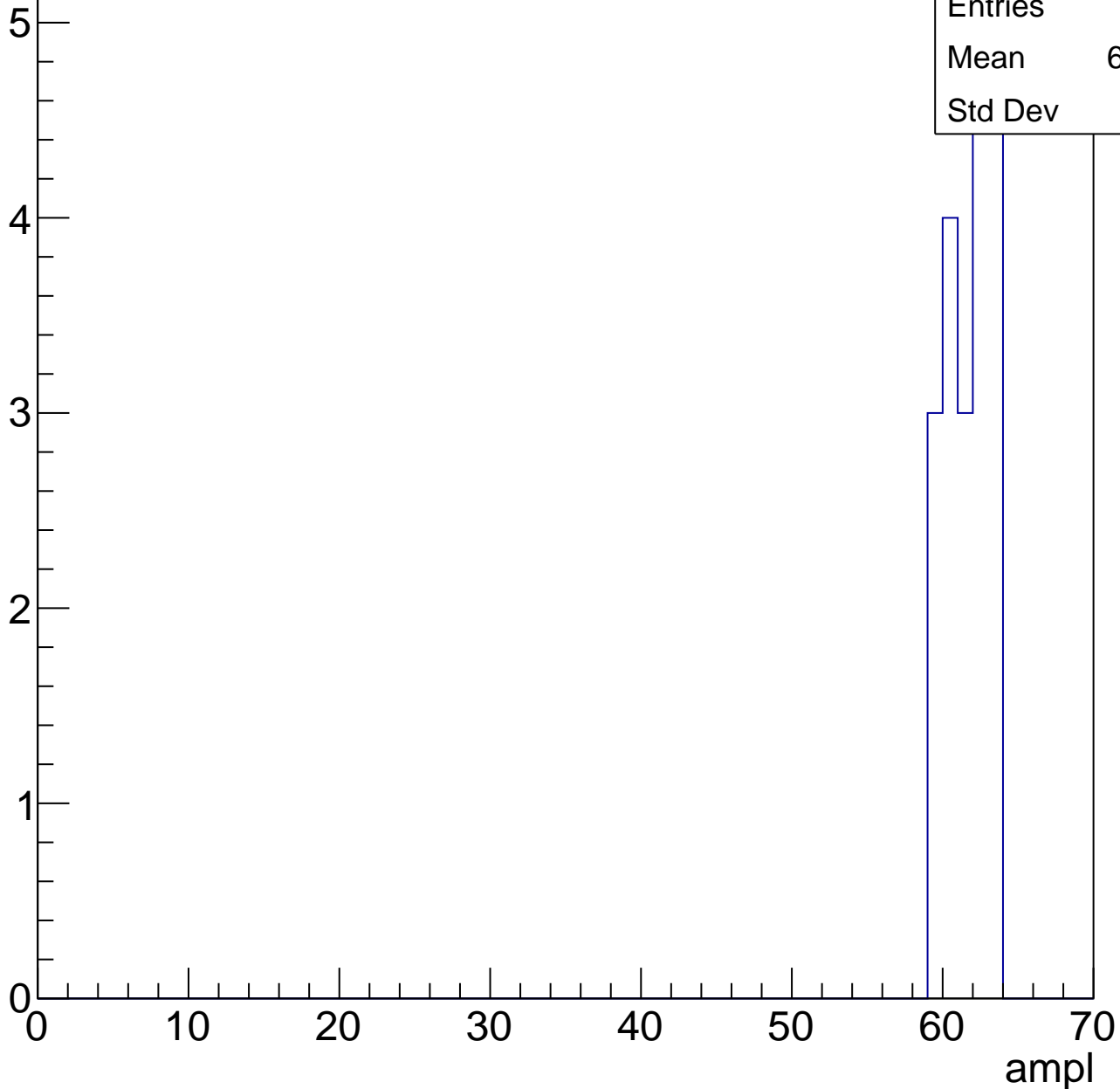


# B1L002S, U8-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.25
Std Dev	1.41

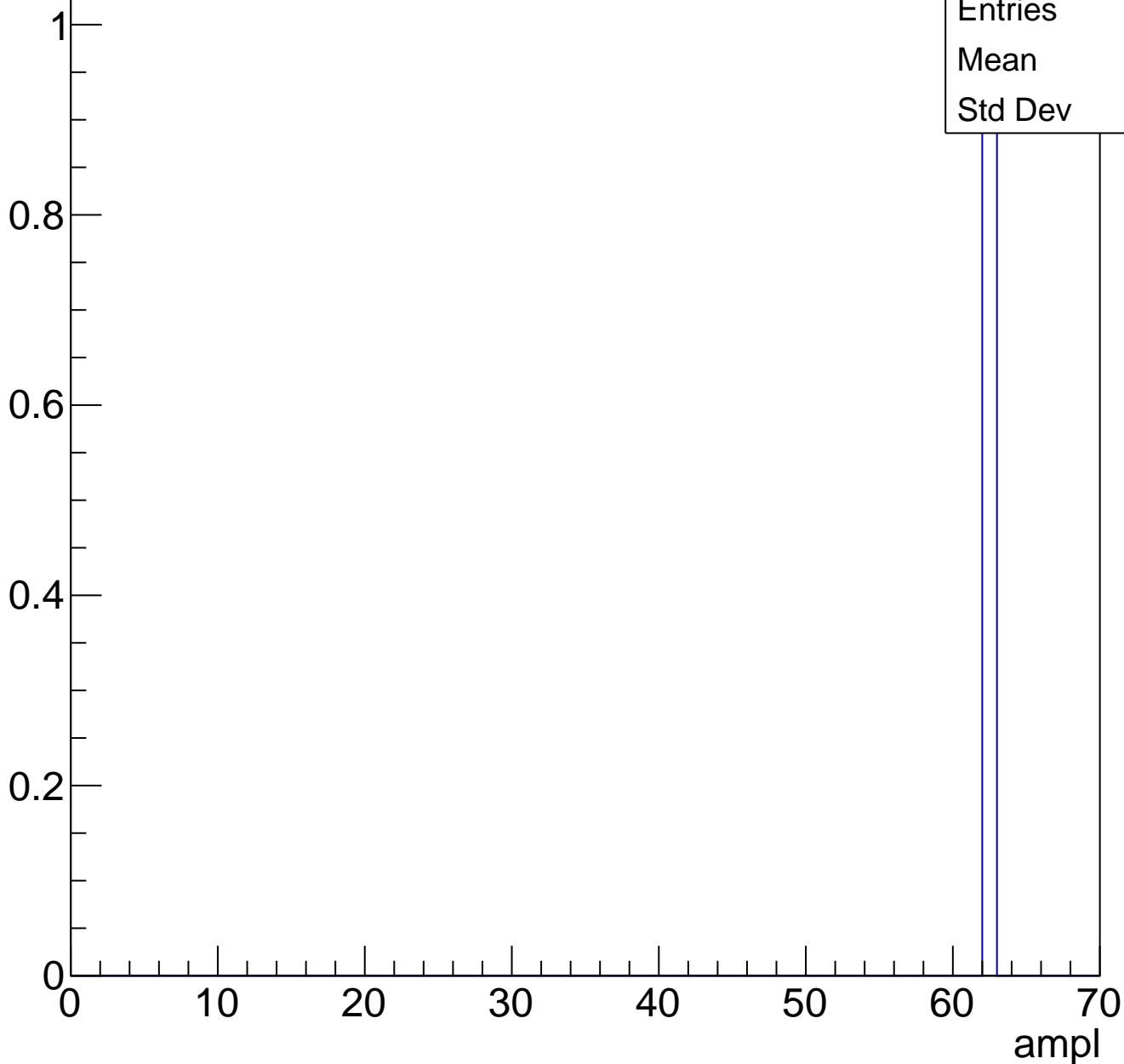




# B1L002S, U8-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch109, adc0

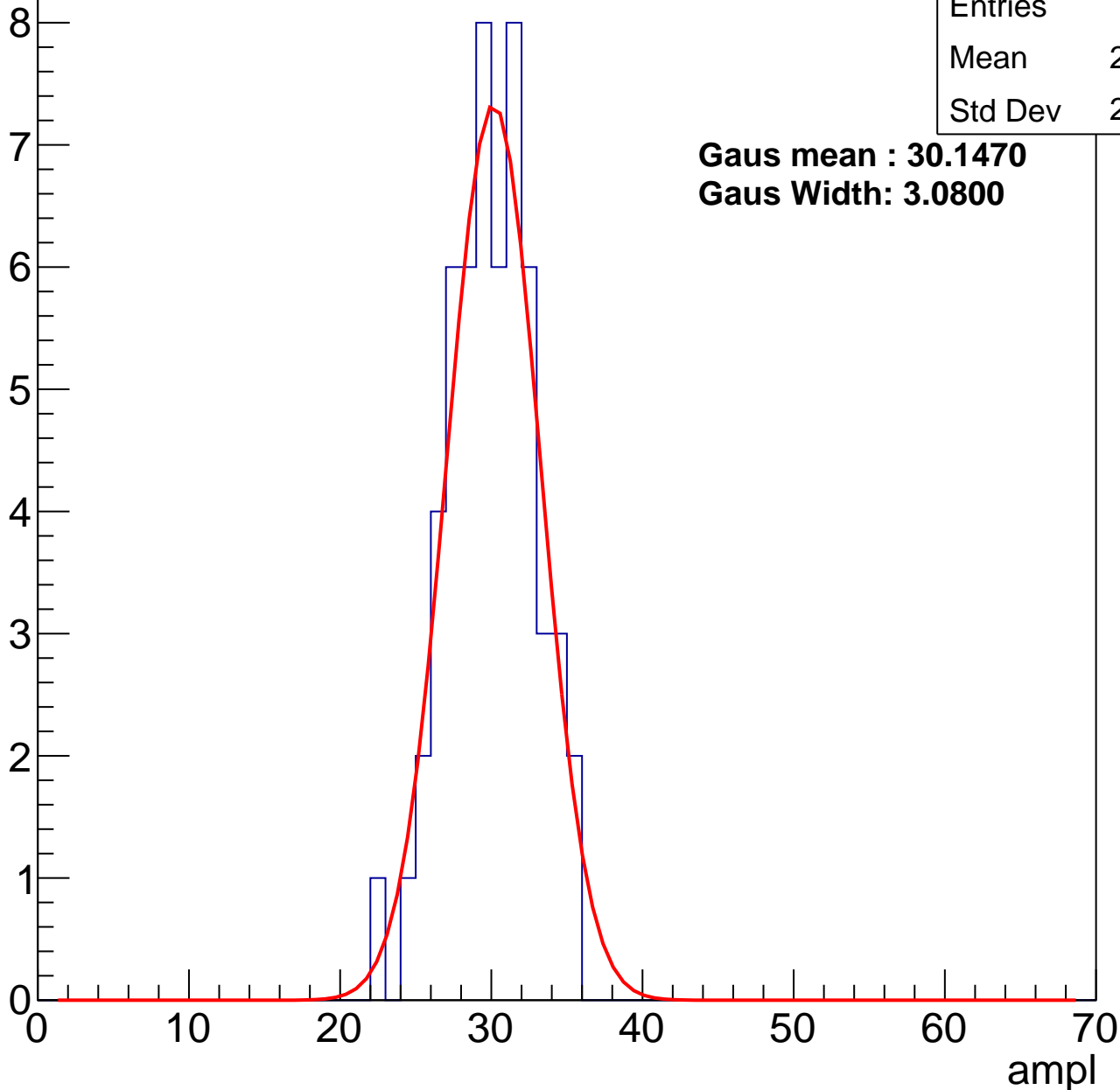
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	29.52
Std Dev	2.816

**Gaus mean : 30.1470**

**Gaus Width: 3.0800**



# B1L002S, U8-ch109, adc1

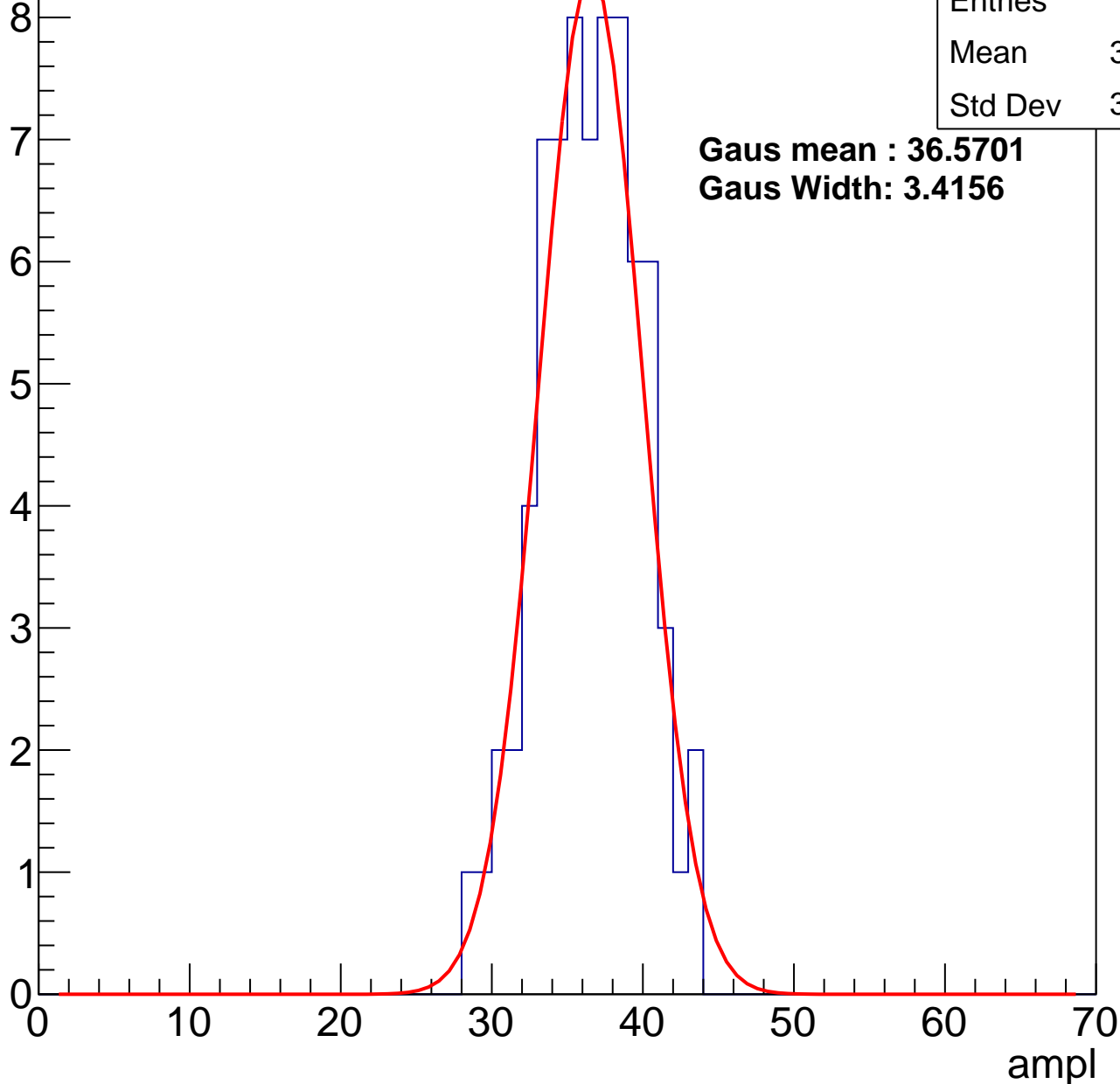
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.07
Std Dev	3.304

**Gaus mean : 36.5701**

**Gaus Width: 3.4156**



# B1L002S, U8-ch109, adc2

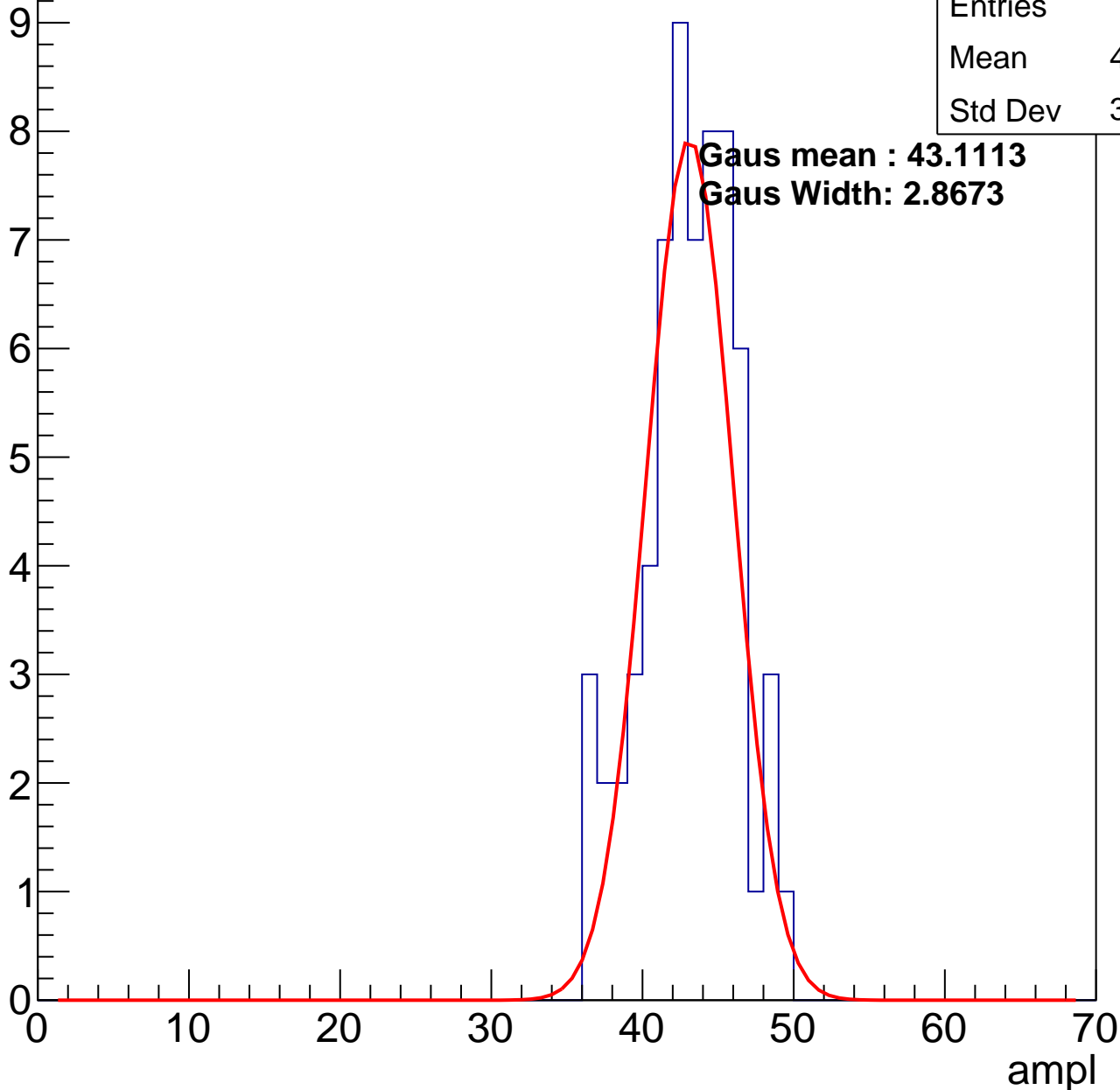
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	42.64
Std Dev	3.069

**Gaus mean : 43.1113**

**Gaus Width: 2.8673**

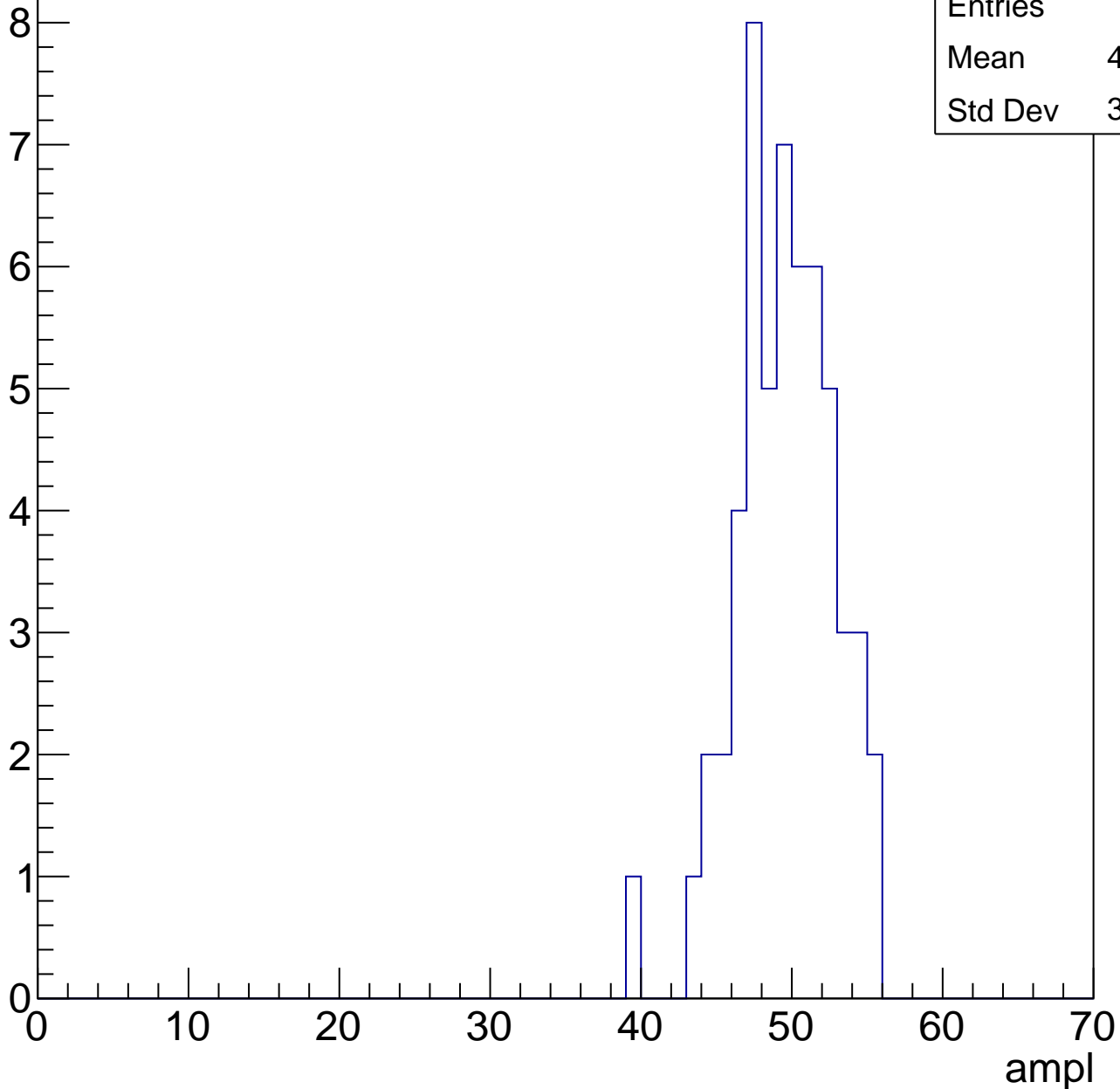


# B1L002S, U8-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	49.09
Std Dev	3.192



# B1L002S, U8-ch109, adc4

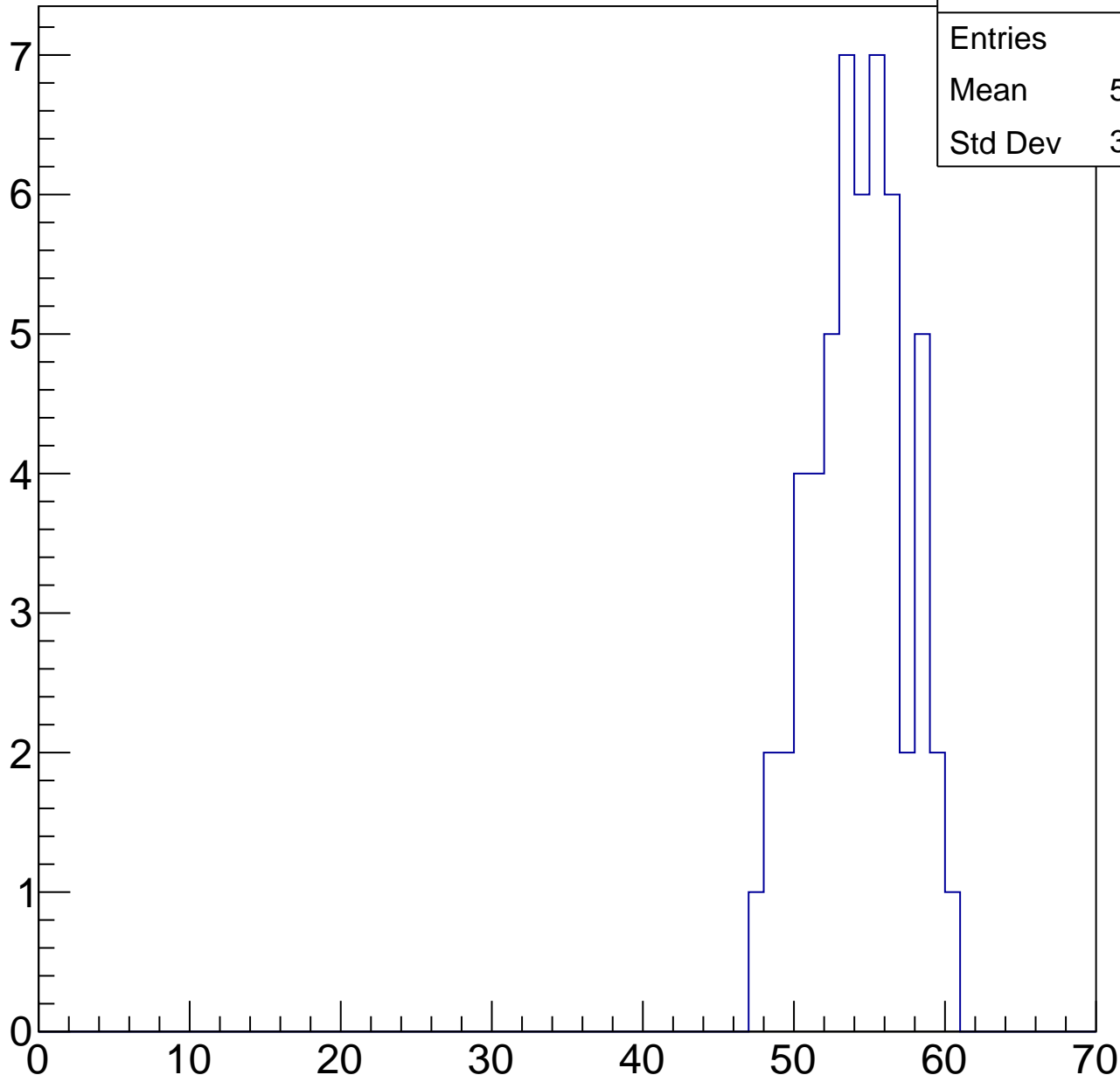
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	53.76
Std Dev	3.073

ampl

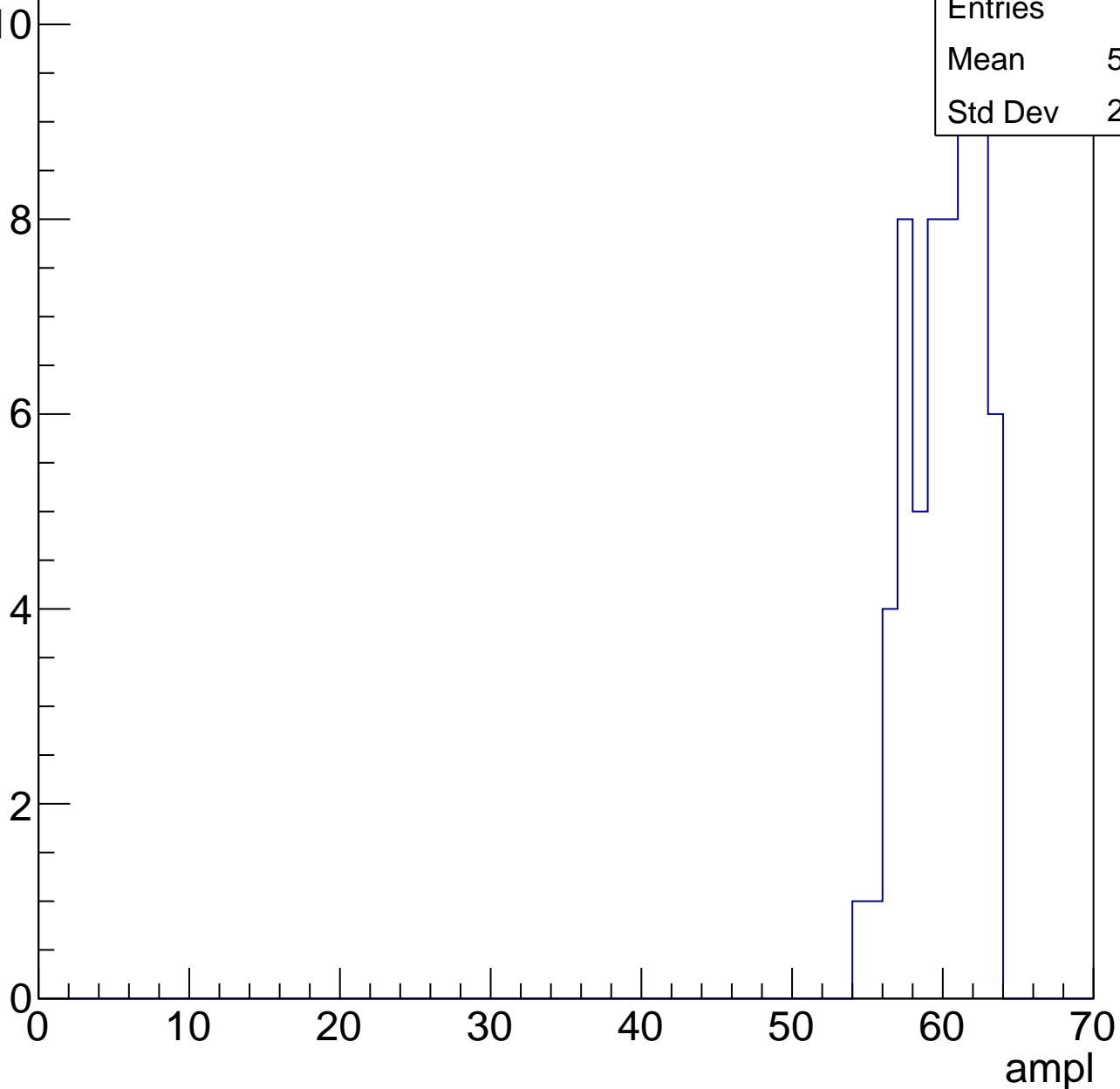


# B1L002S, U8-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

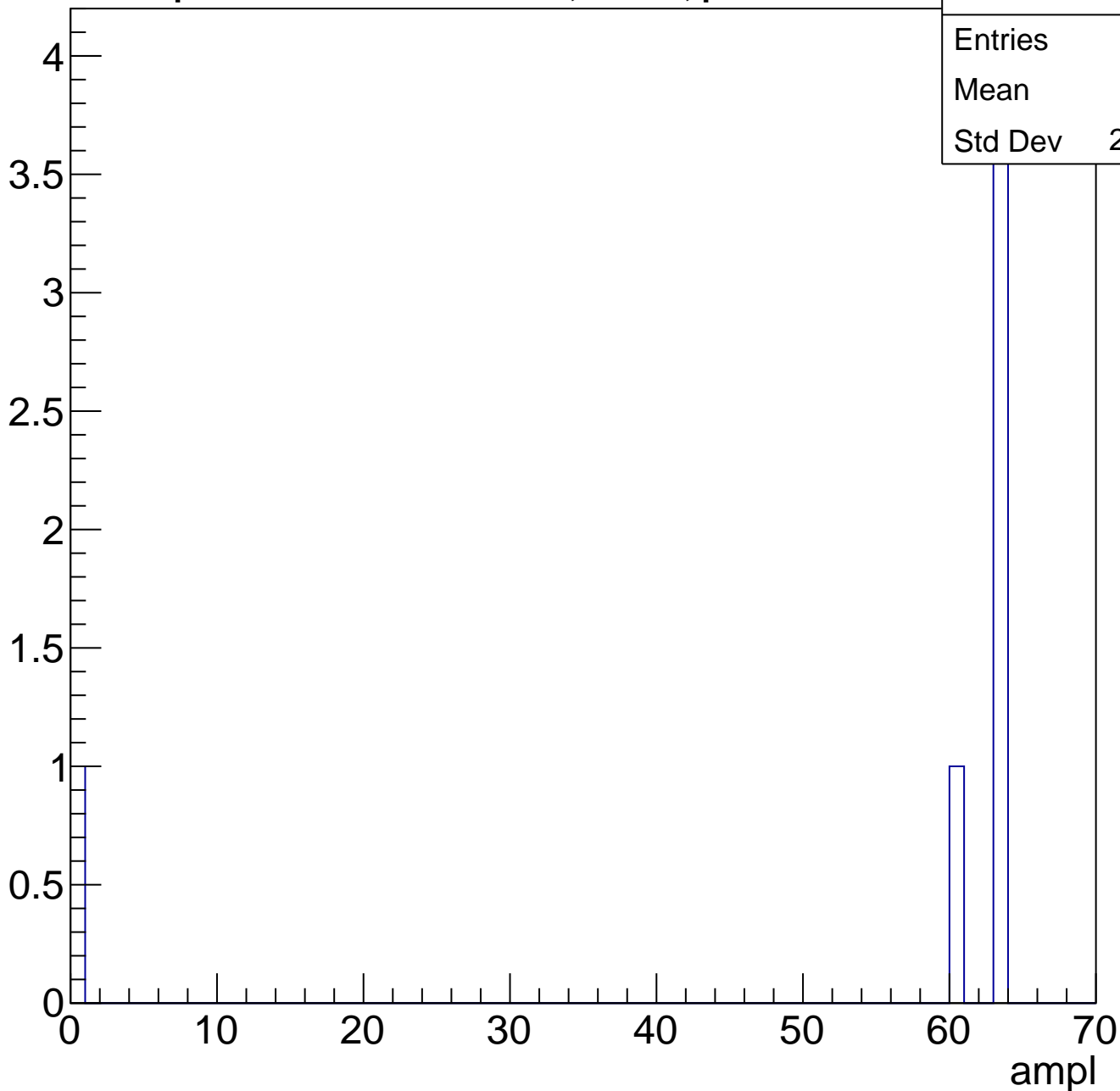
Entries	61
Mean	59.66
Std Dev	2.297



# B1L002S, U8-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	52
Std Dev	23.28



# B1L002S, U8-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch110, adc0

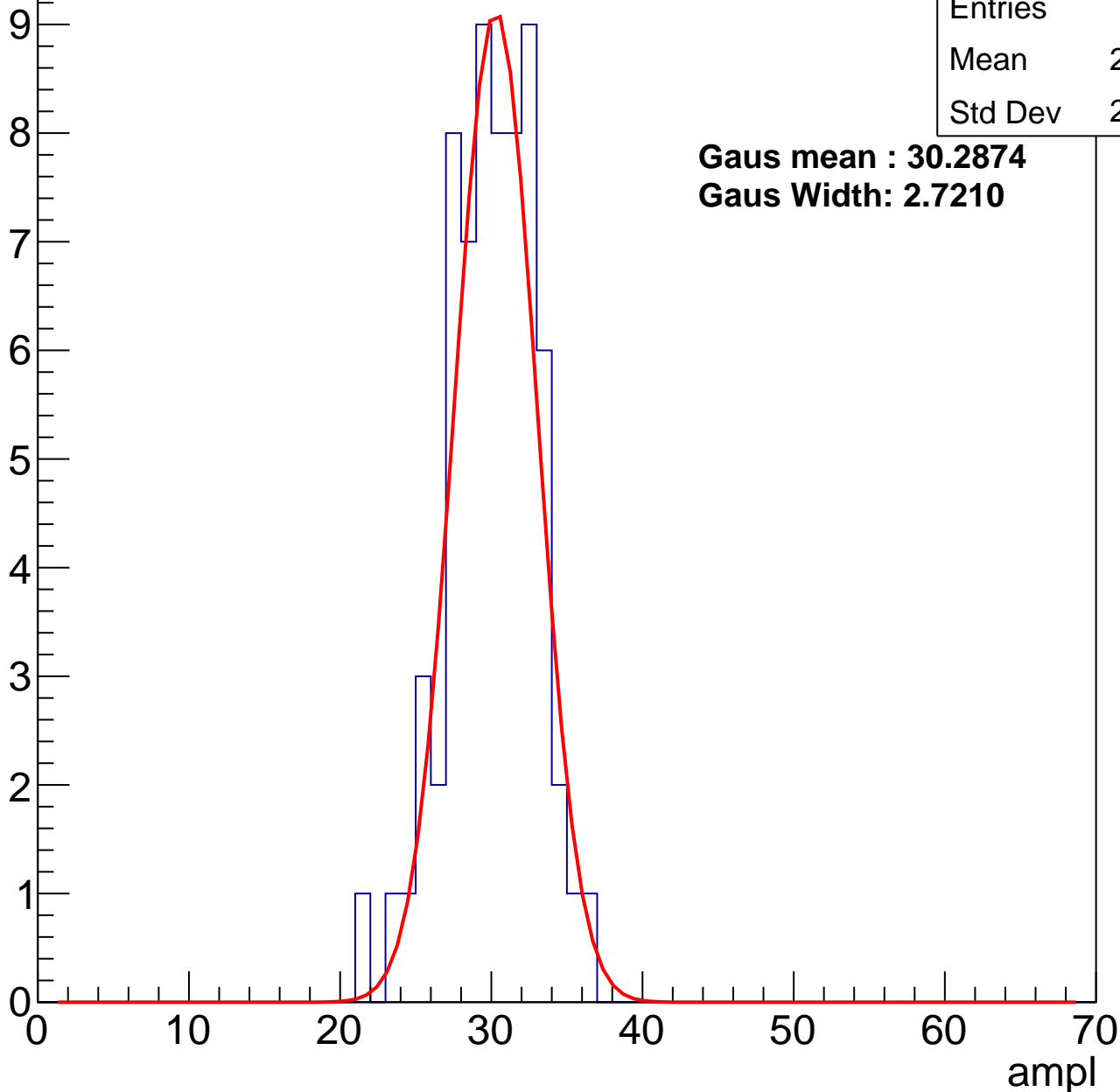
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.57
Std Dev	2.892

**Gaus mean : 30.2874**

**Gaus Width: 2.7210**



# B1L002S, U8-ch110, adc1

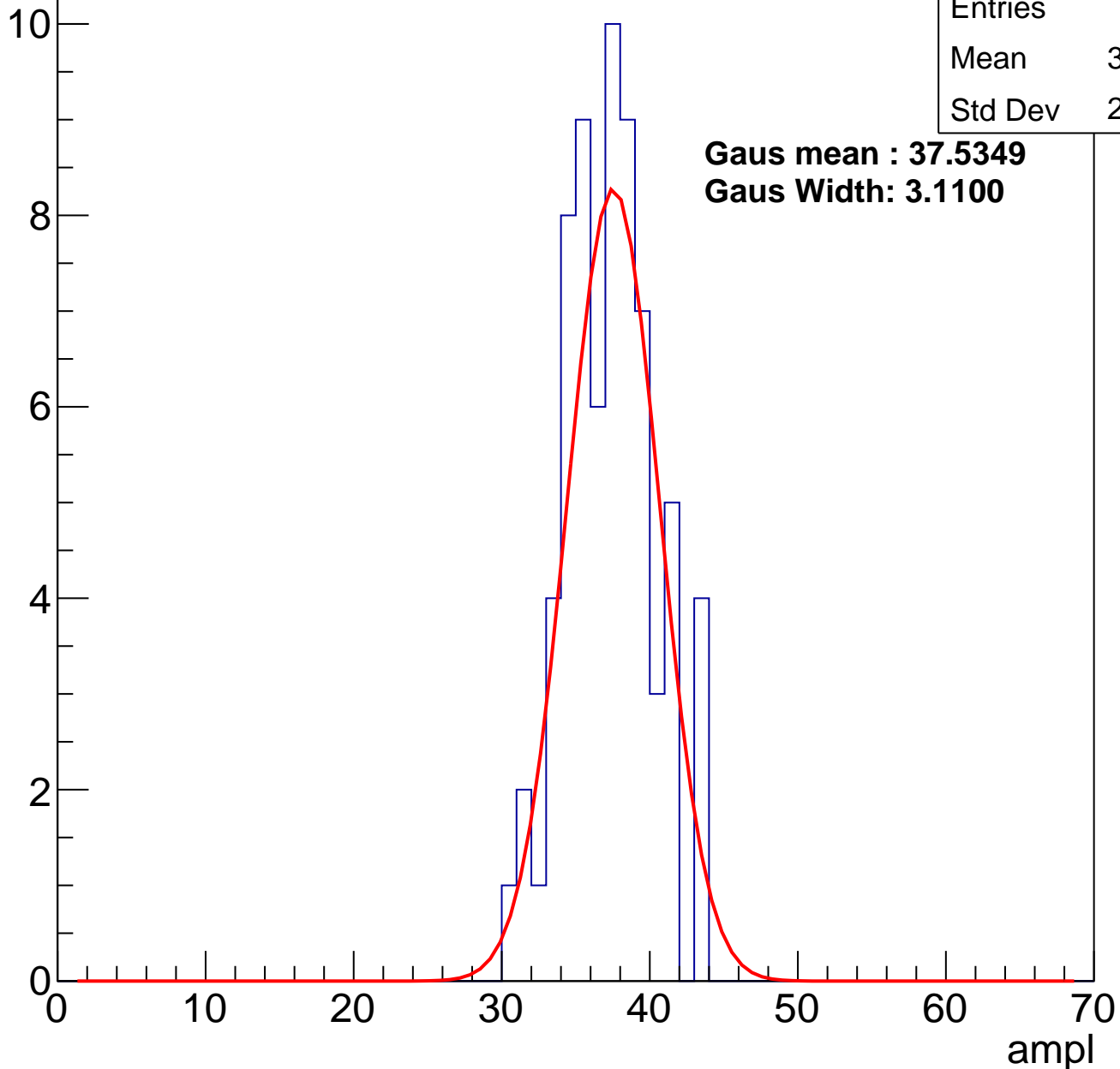
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	36.83
Std Dev	2.973

**Gaus mean : 37.5349**

**Gaus Width: 3.1100**

Entry



# B1L002S, U8-ch110, adc2

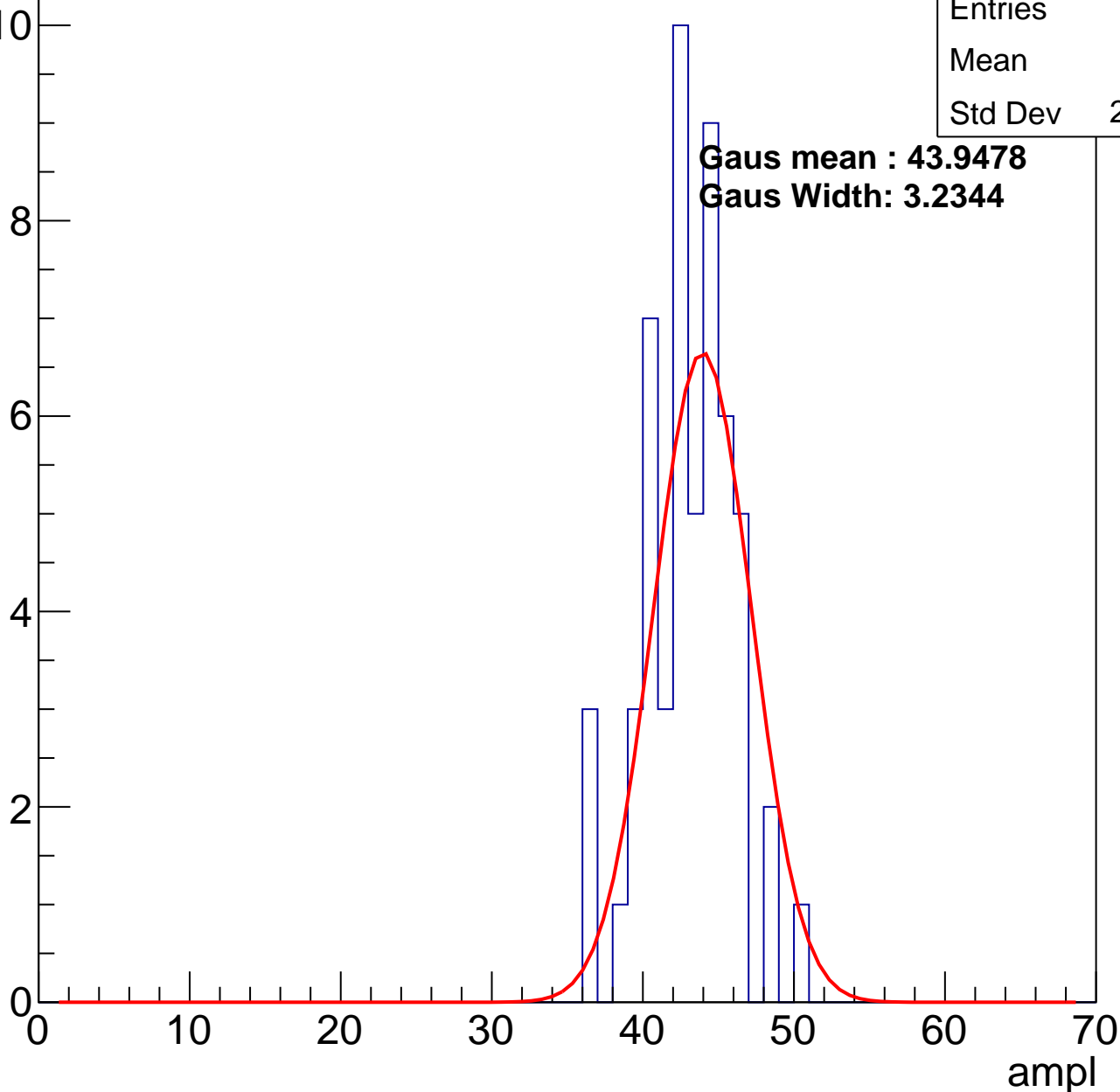
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	42.6
Std Dev	2.933

**Gaus mean : 43.9478**

**Gaus Width: 3.2344**

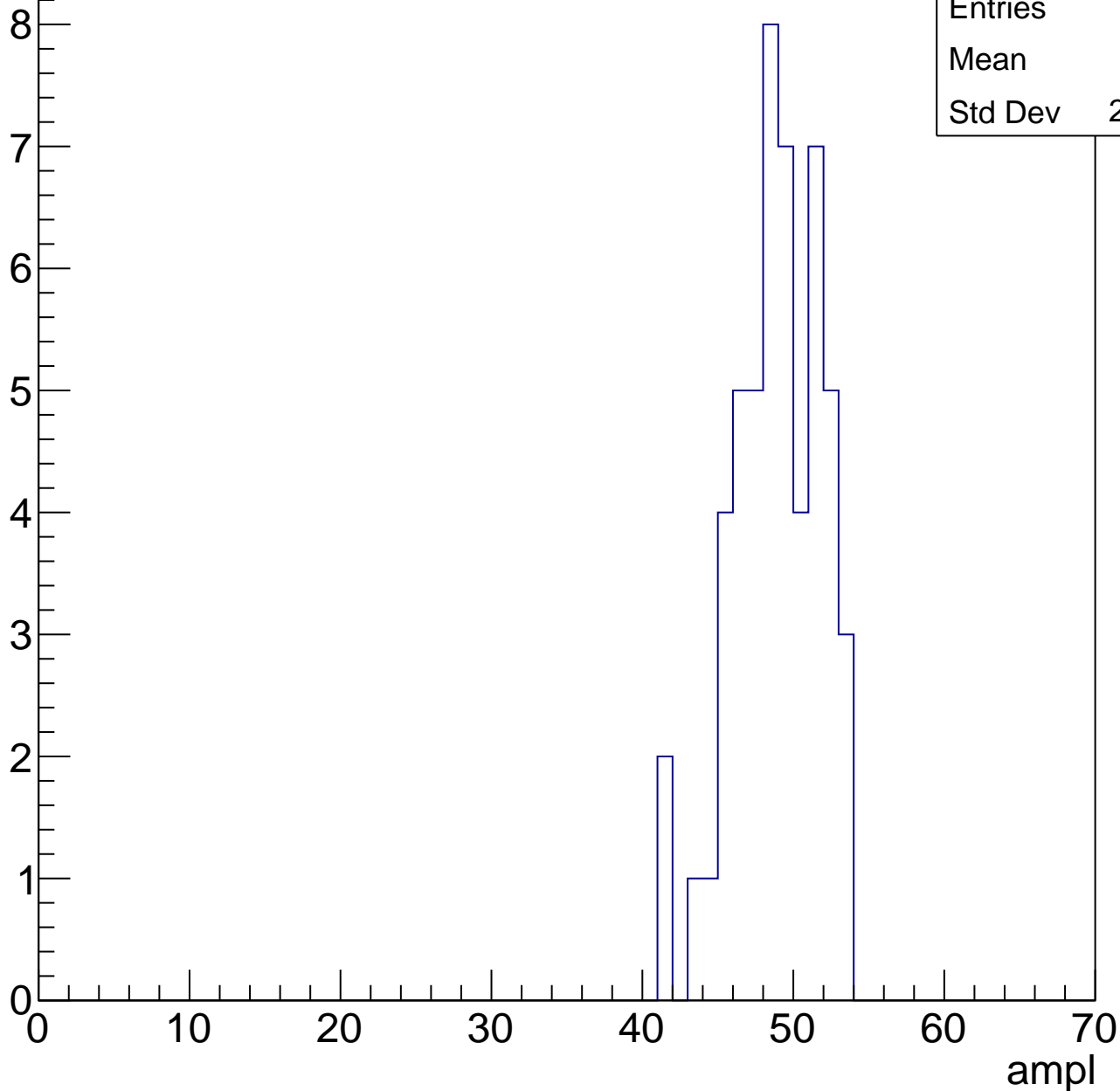


# B1L002S, U8-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	48.4
Std Dev	2.884

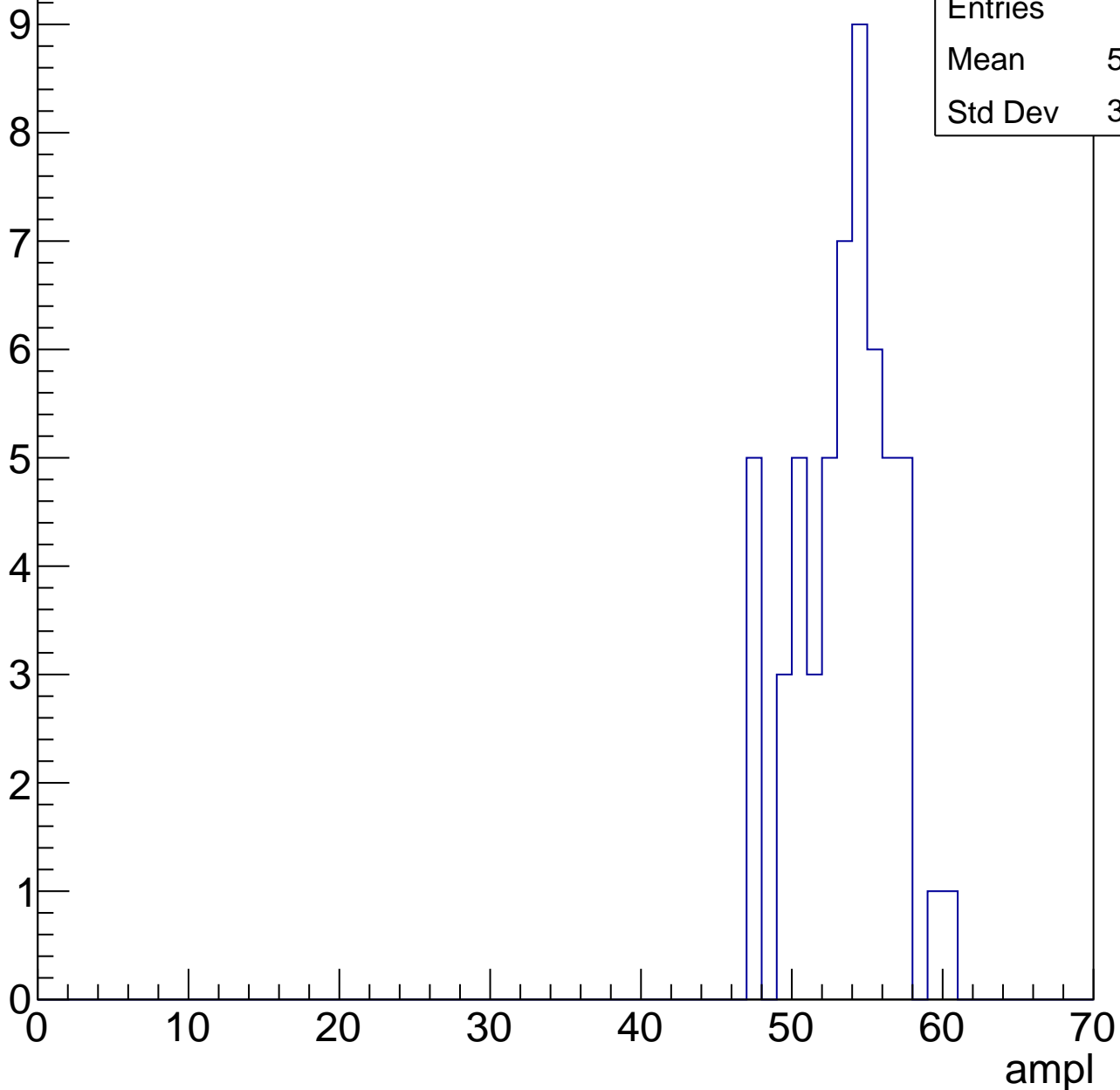


# B1L002S, U8-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.02
Std Dev	3.113



# B1L002S, U8-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

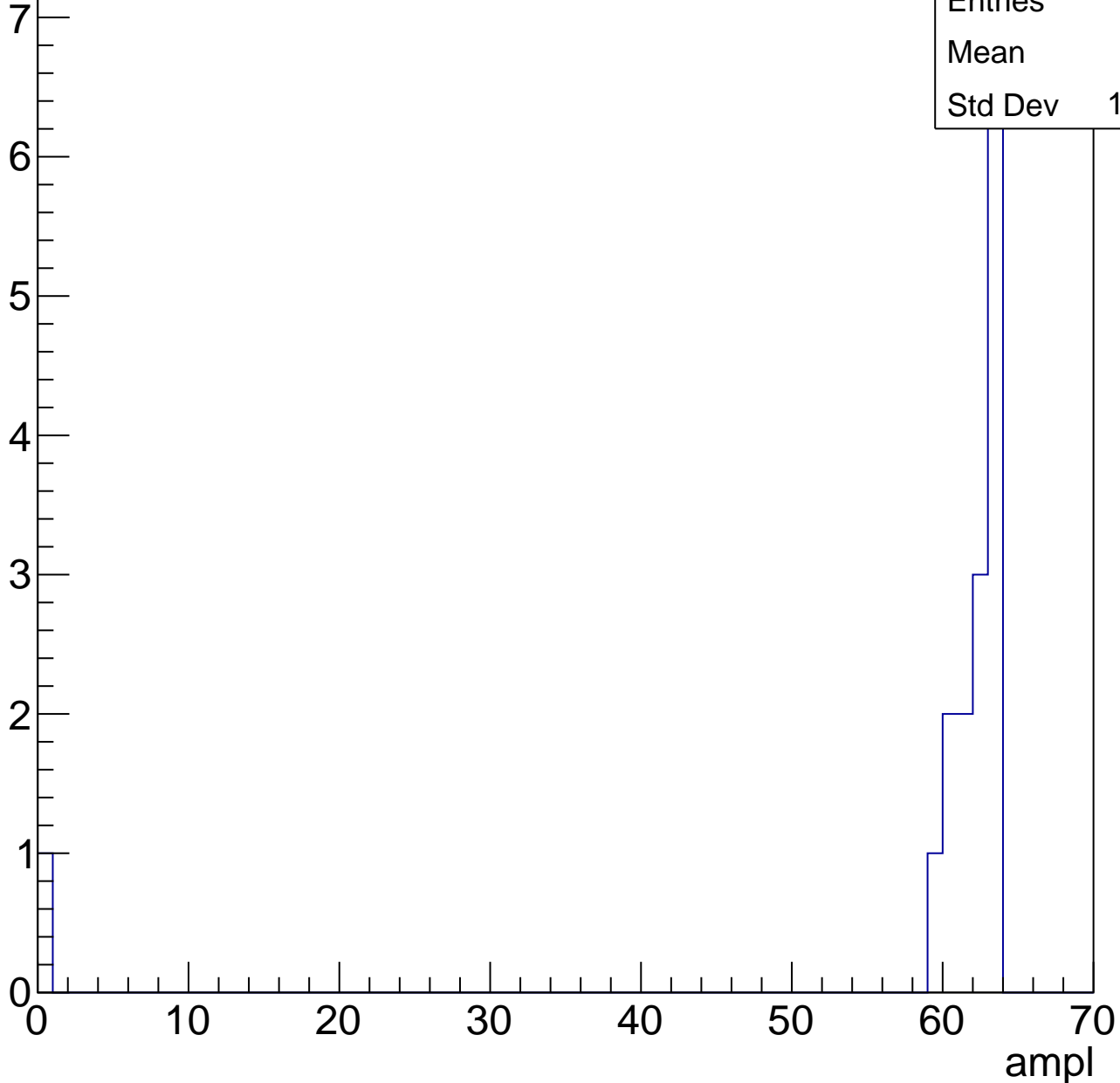
Entries	58
Mean	59
Std Dev	2.327

# B1L002S, U8-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	58
Std Dev	15.03





# B1L002S, U8-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch111, adc0

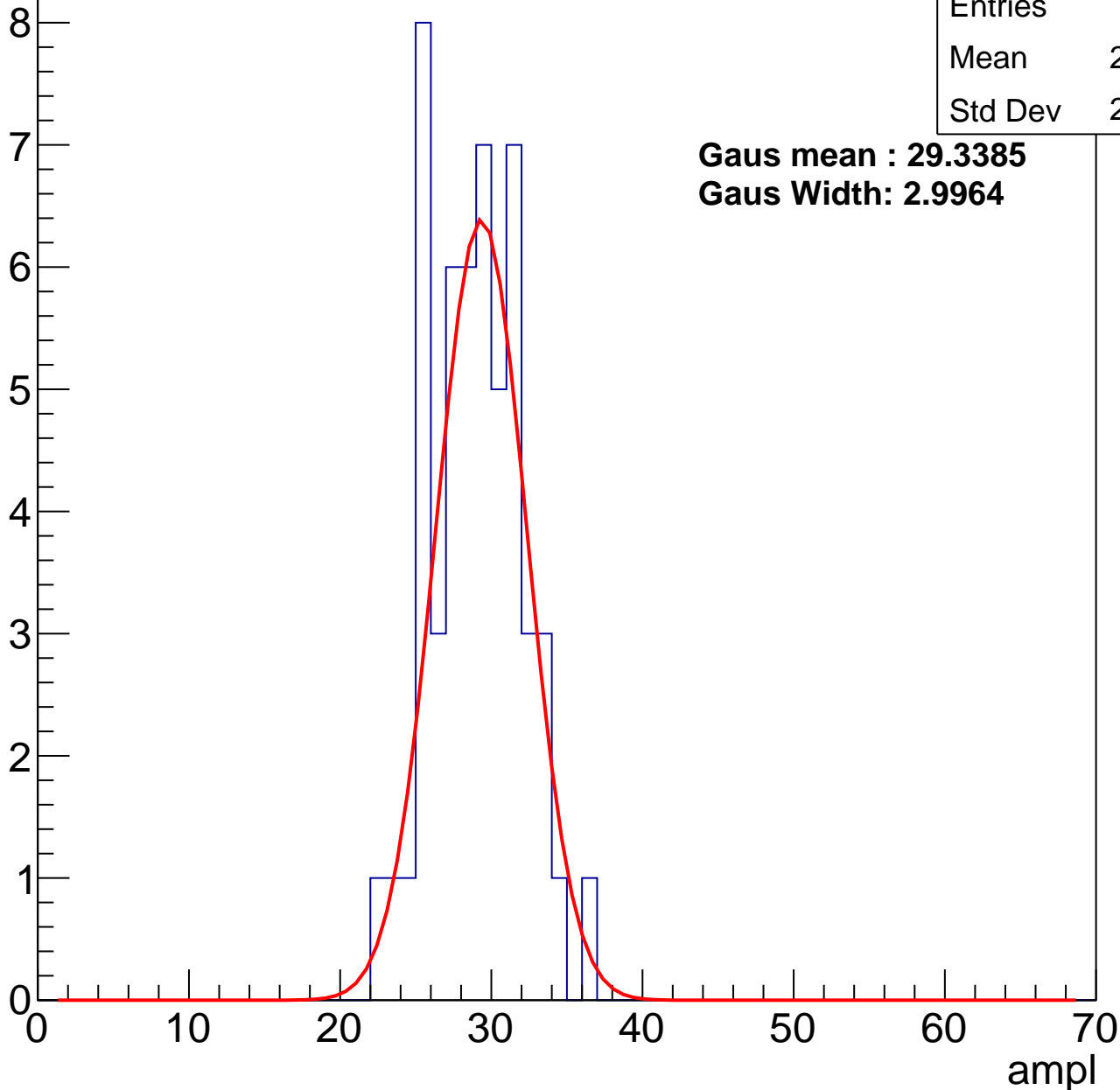
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.53
Std Dev	2.963

**Gaus mean : 29.3385**

**Gaus Width: 2.9964**



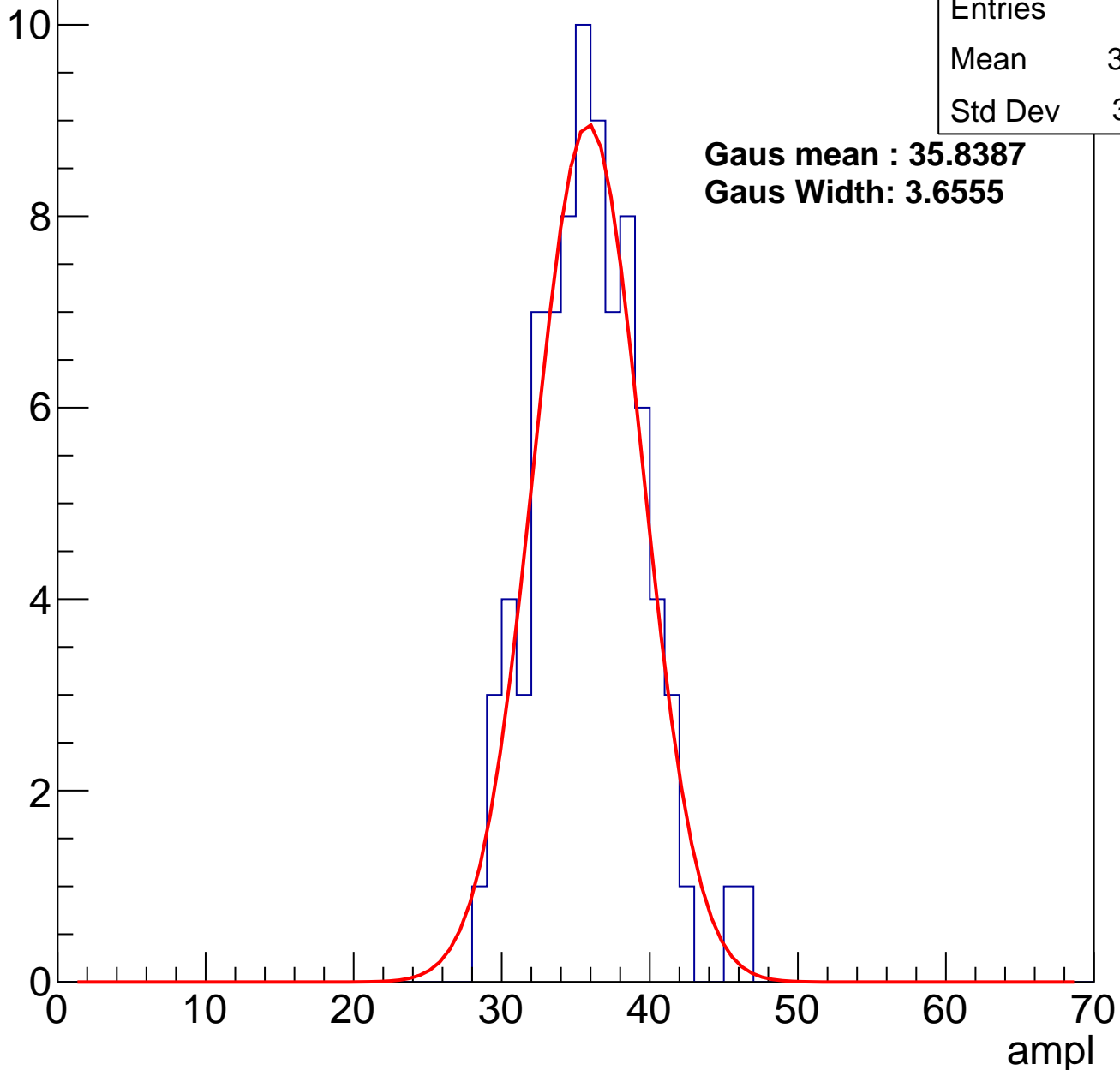
# B1L002S, U8-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	35.45
Std Dev	3.591

**Gaus mean : 35.8387**  
**Gaus Width: 3.6555**

Entry



# B1L002S, U8-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	42.22
Std Dev	2.956

**Gaus mean : 42.6967**

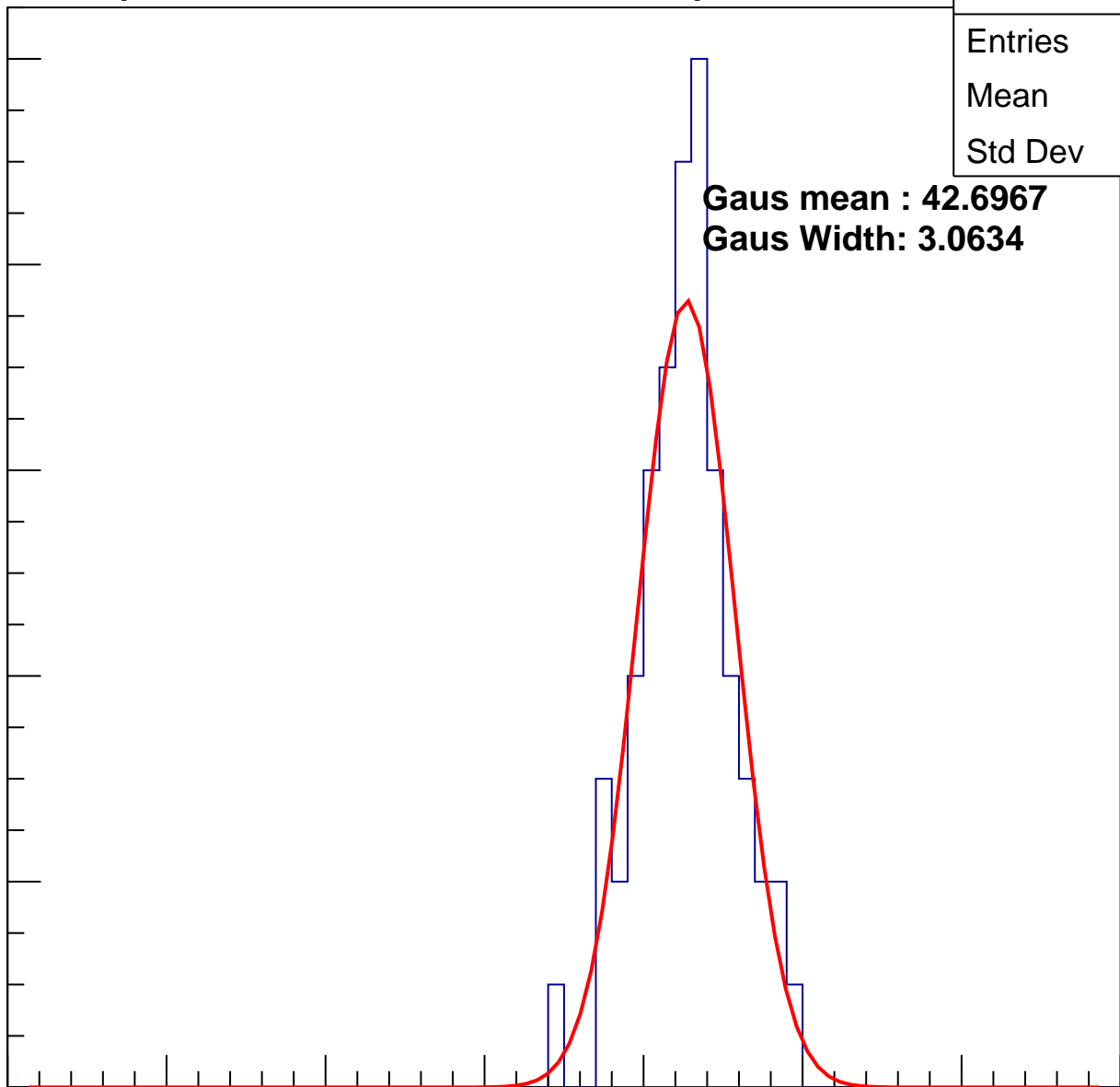
**Gaus Width: 3.0634**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

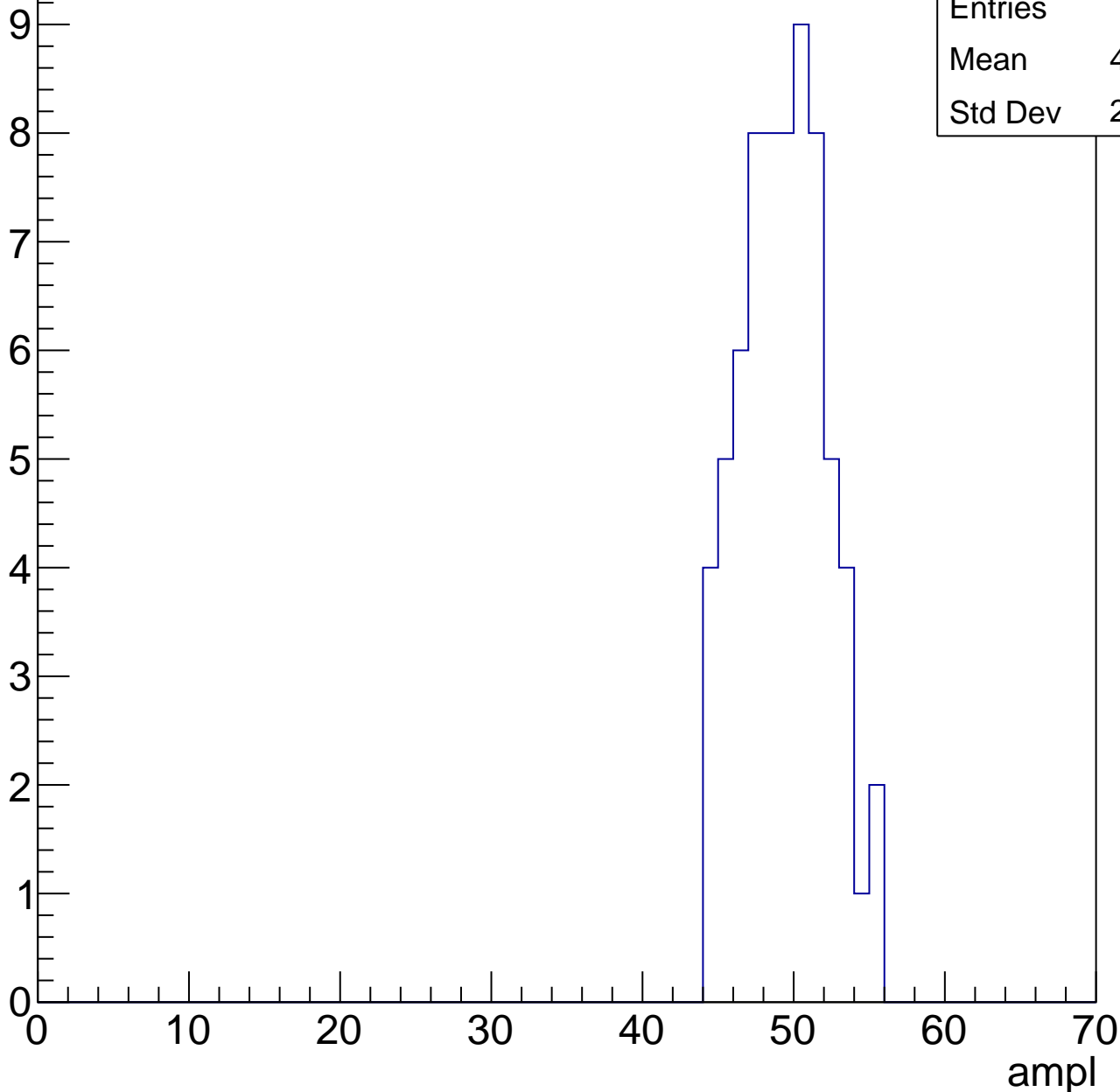


# B1L002S, U8-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	48.87
Std Dev	2.765

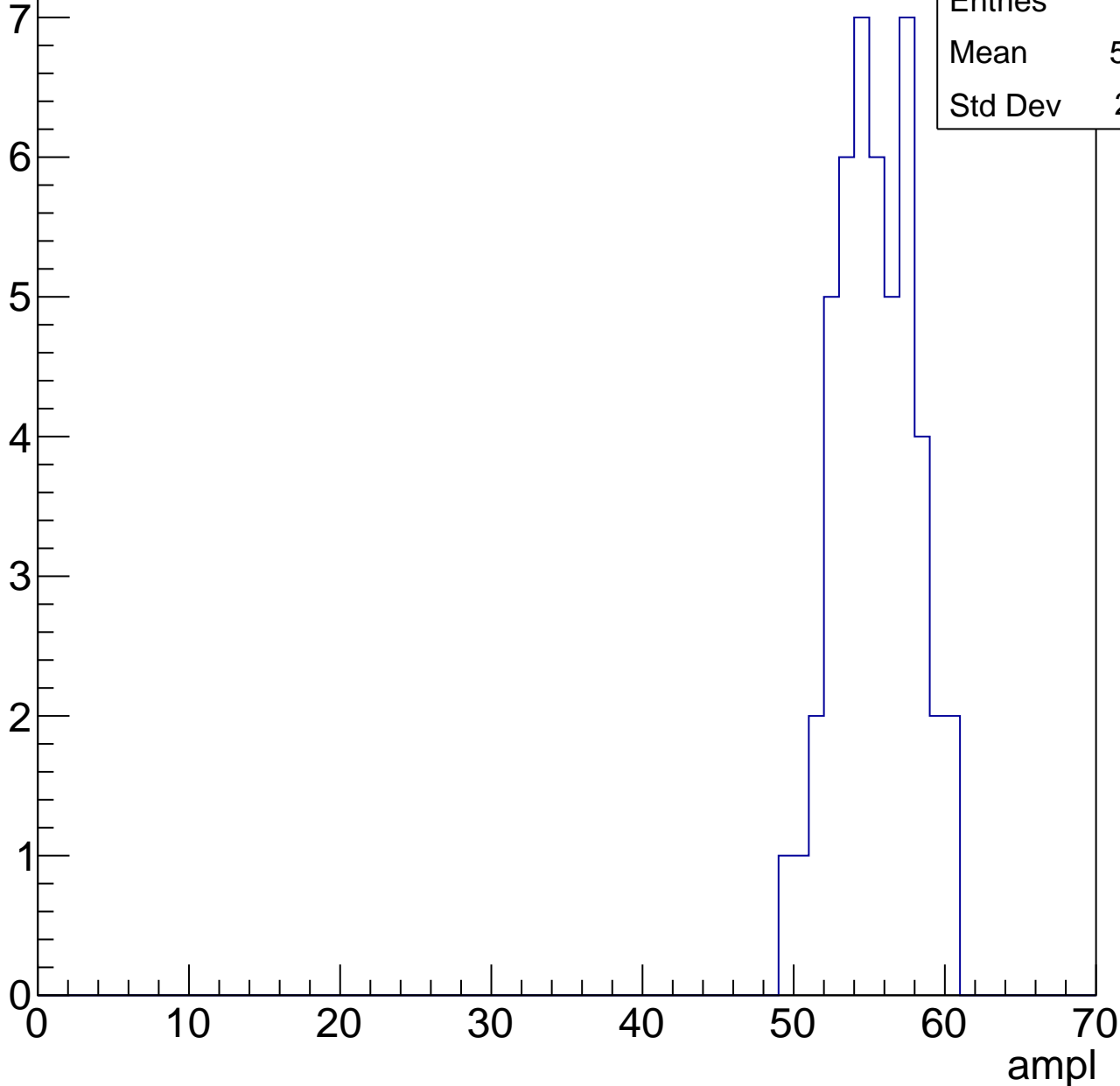


# B1L002S, U8-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

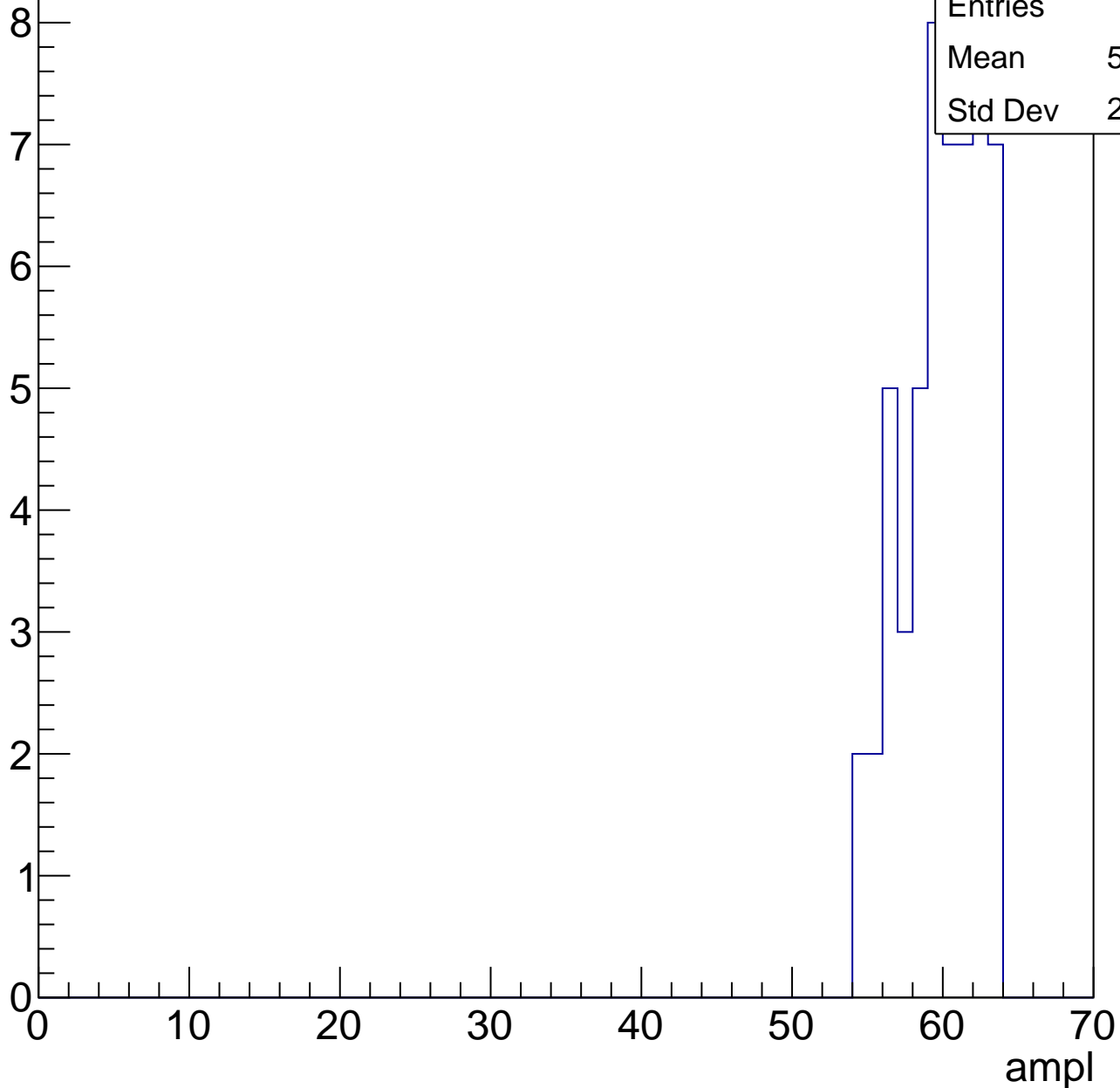
Entries	48
Mean	54.92
Std Dev	2.581



# B1L002S, U8-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

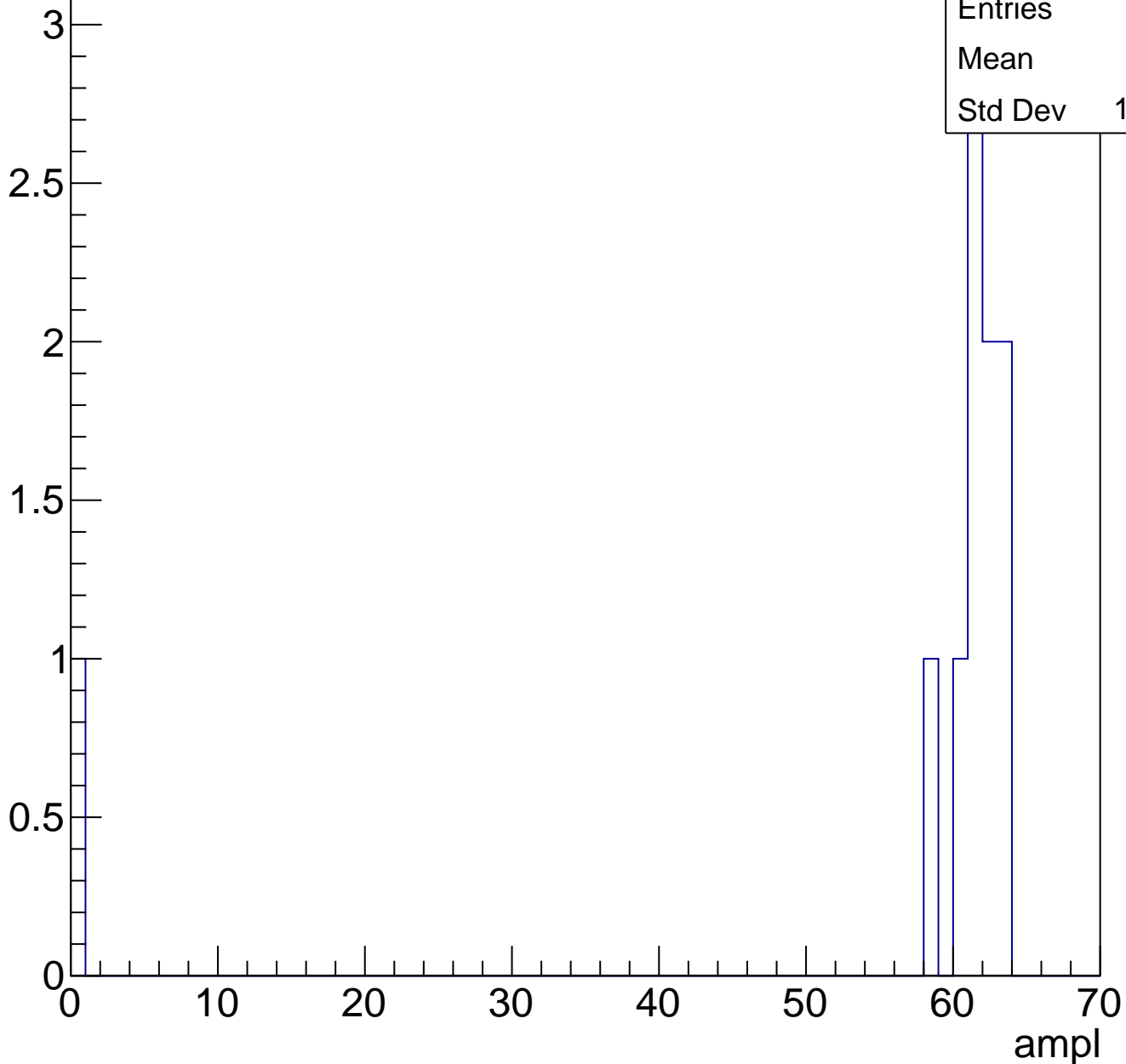


Entries	54
Mean	59.54
Std Dev	2.536

# B1L002S, U8-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch112, adc0

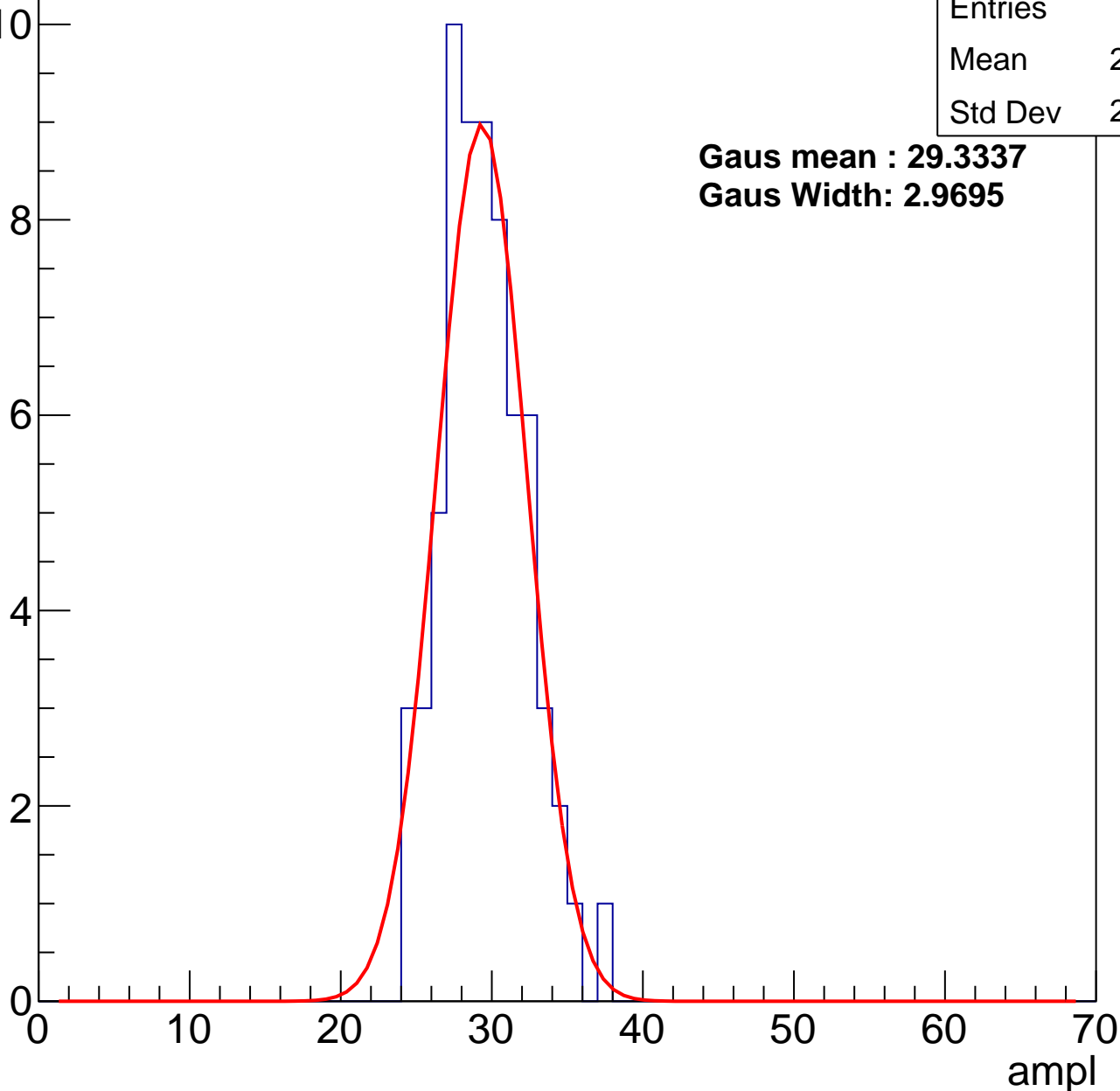
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	29.05
Std Dev	2.755

**Gaus mean : 29.3337**

**Gaus Width: 2.9695**



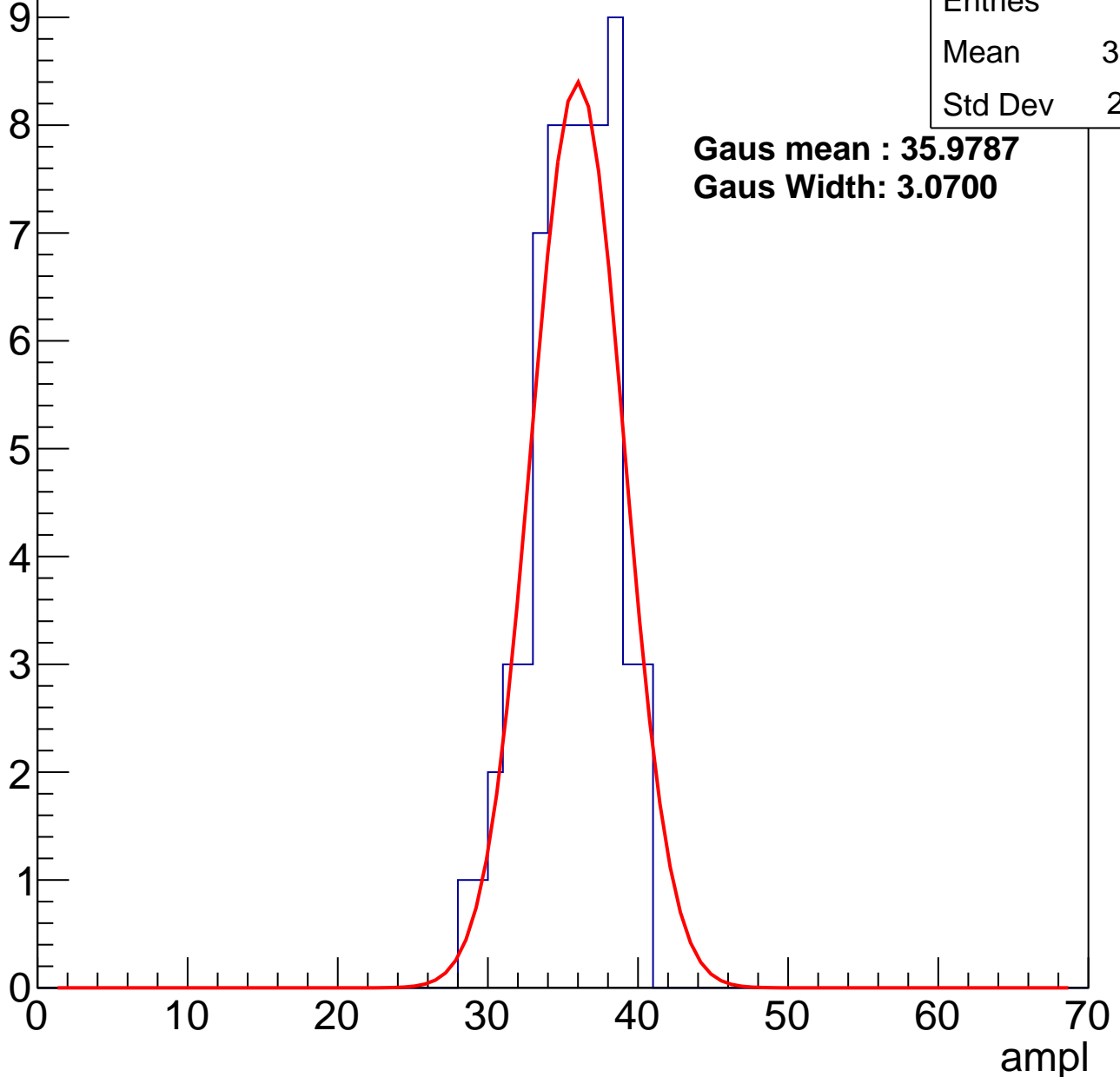
# B1L002S, U8-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.19
Std Dev	2.761

**Gaus mean : 35.9787**  
**Gaus Width: 3.0700**



# B1L002S, U8-ch112, adc2

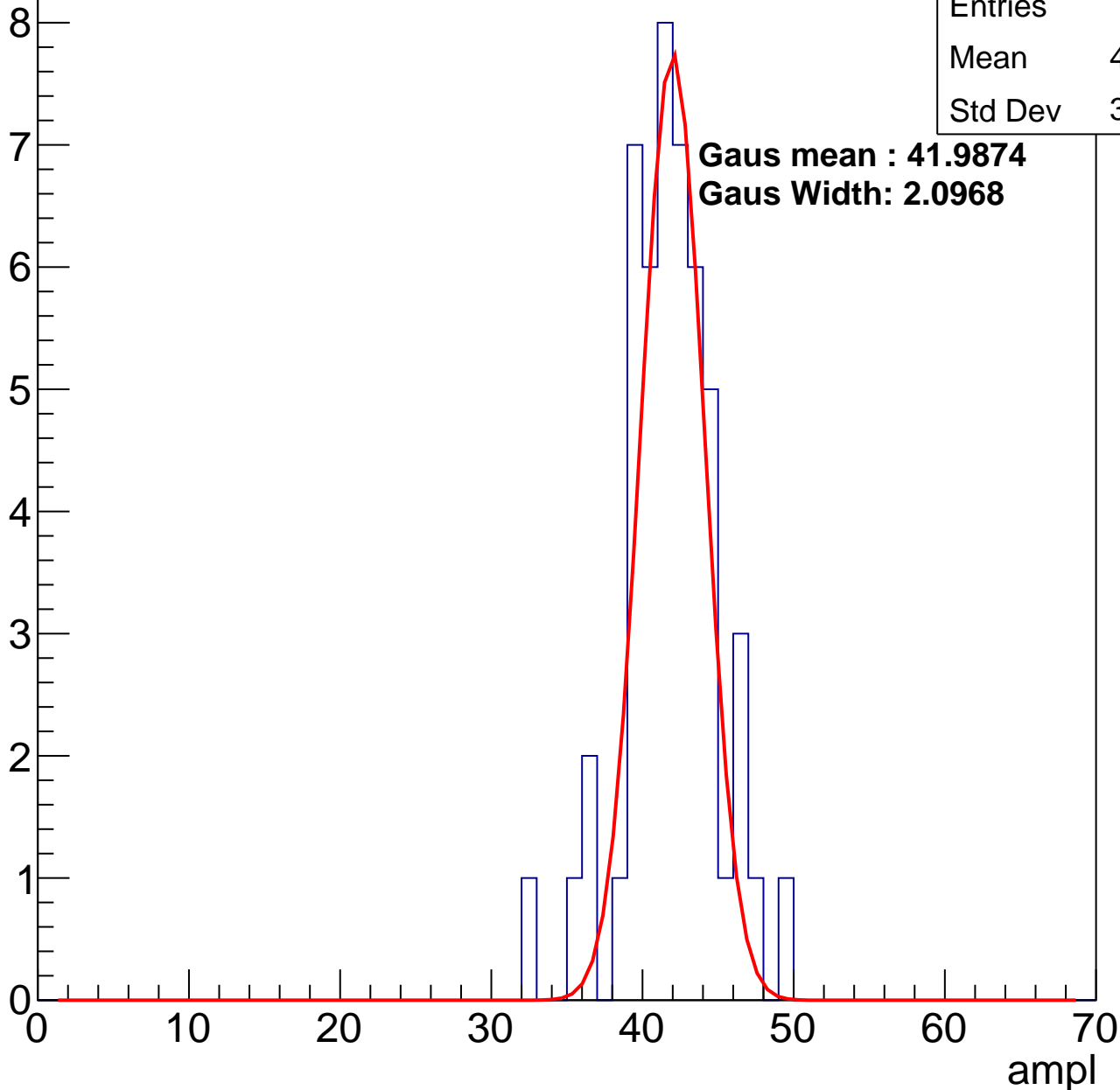
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	41.38
Std Dev	3.065

**Gaus mean : 41.9874**

**Gaus Width: 2.0968**

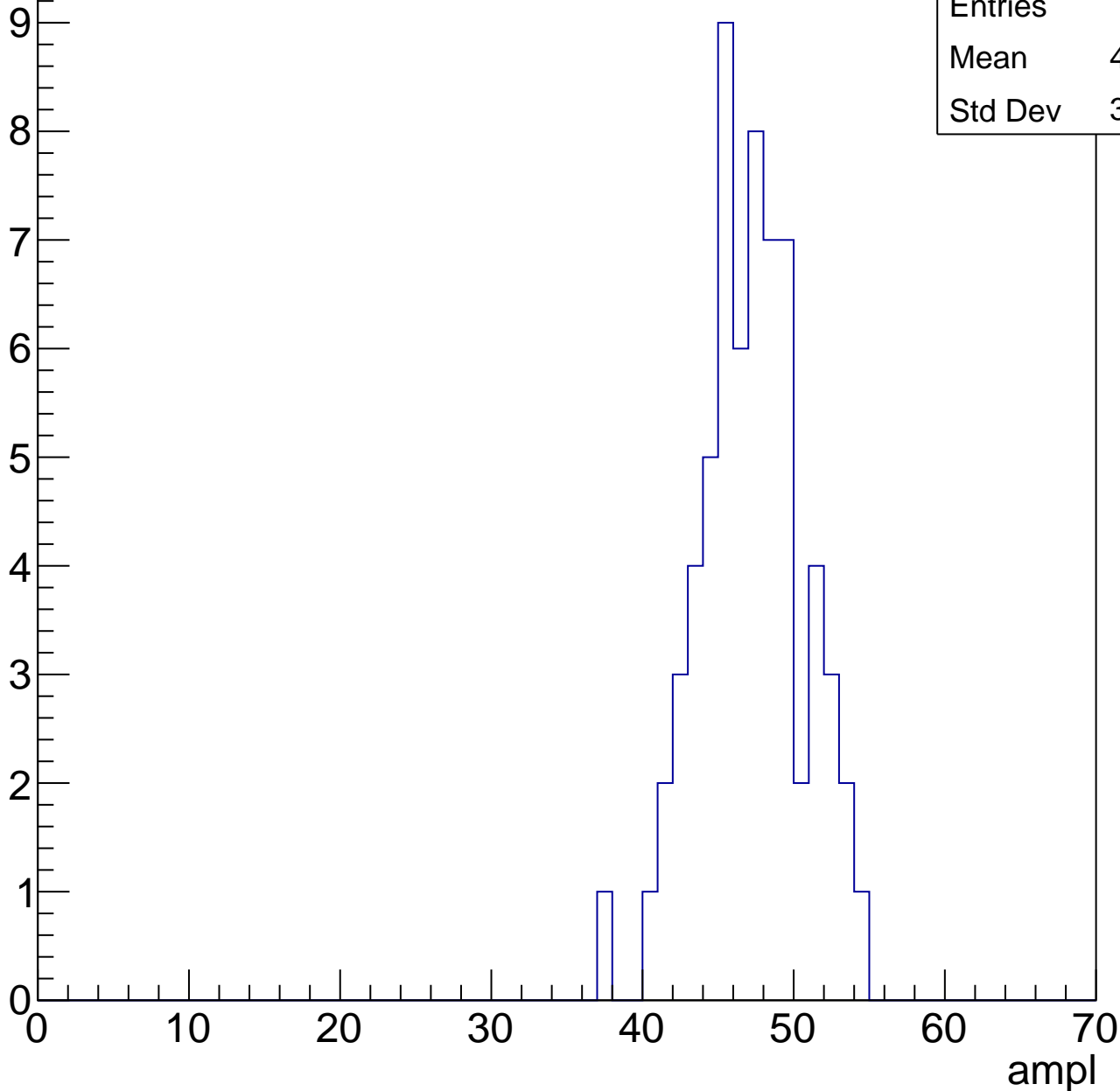


# B1L002S, U8-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.66
Std Dev	3.403

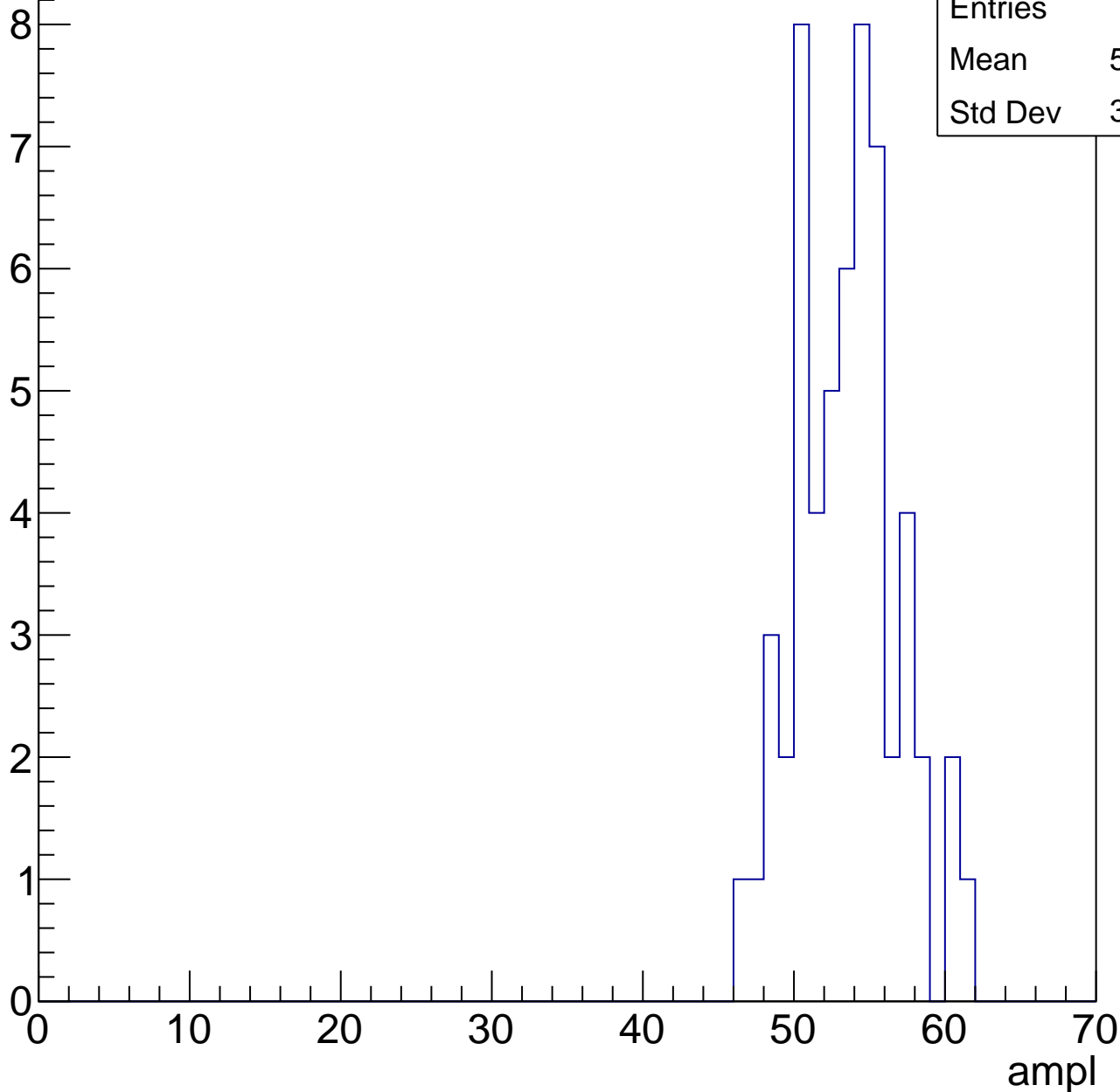


# B1L002S, U8-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.05
Std Dev	3.313

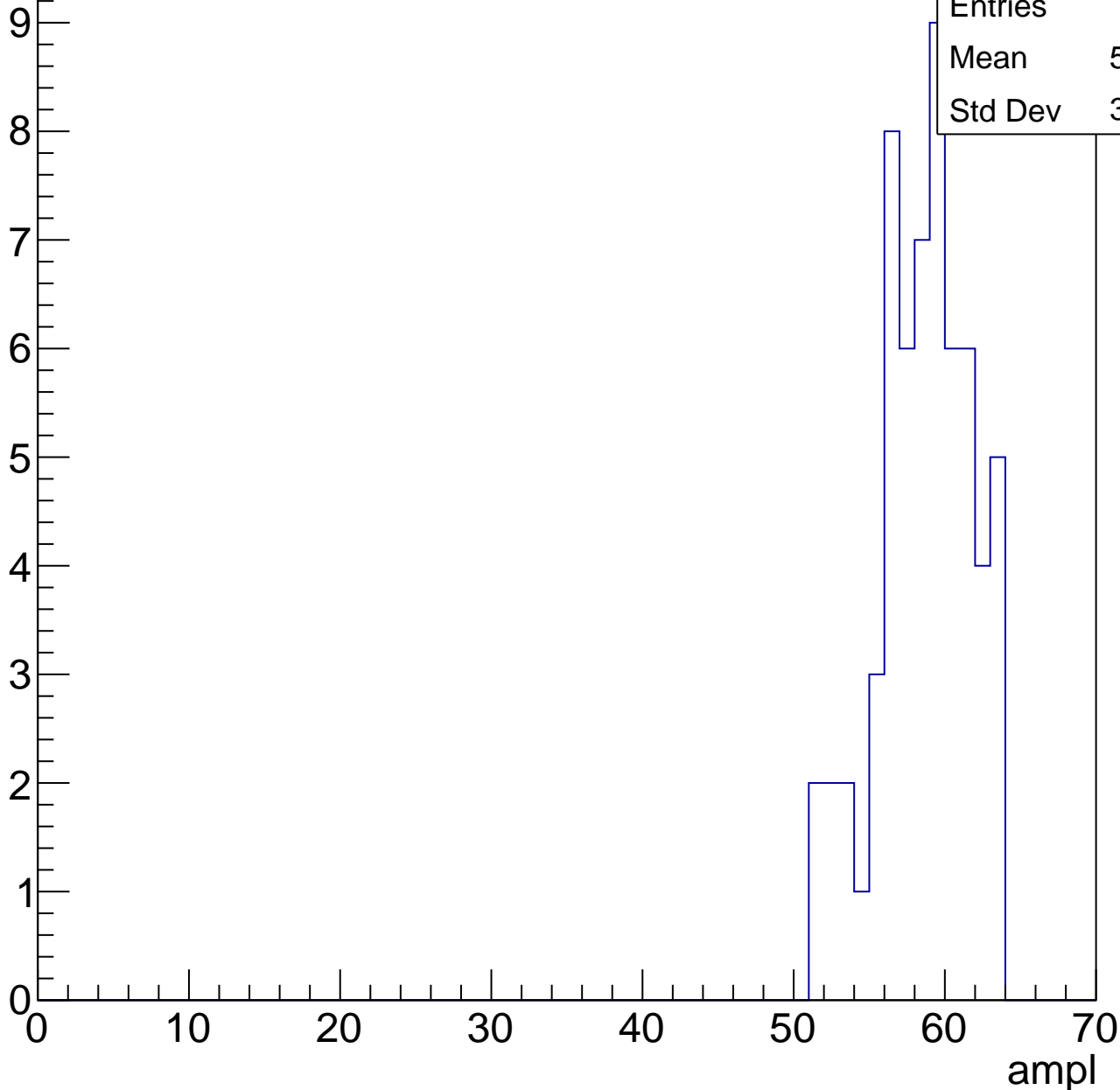


# B1L002S, U8-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	58.15
Std Dev	3.072

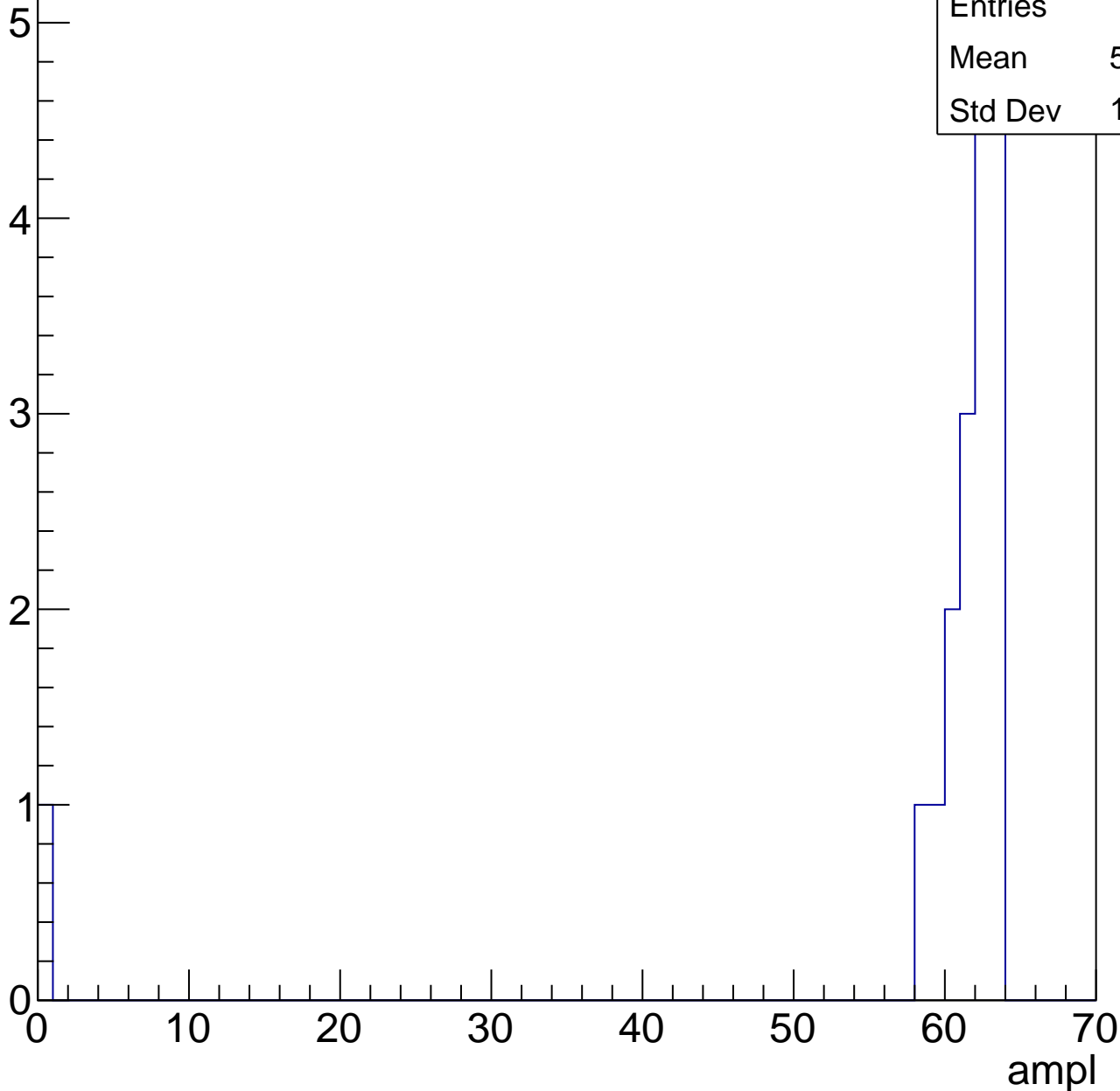


# B1L002S, U8-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.06
Std Dev	14.15

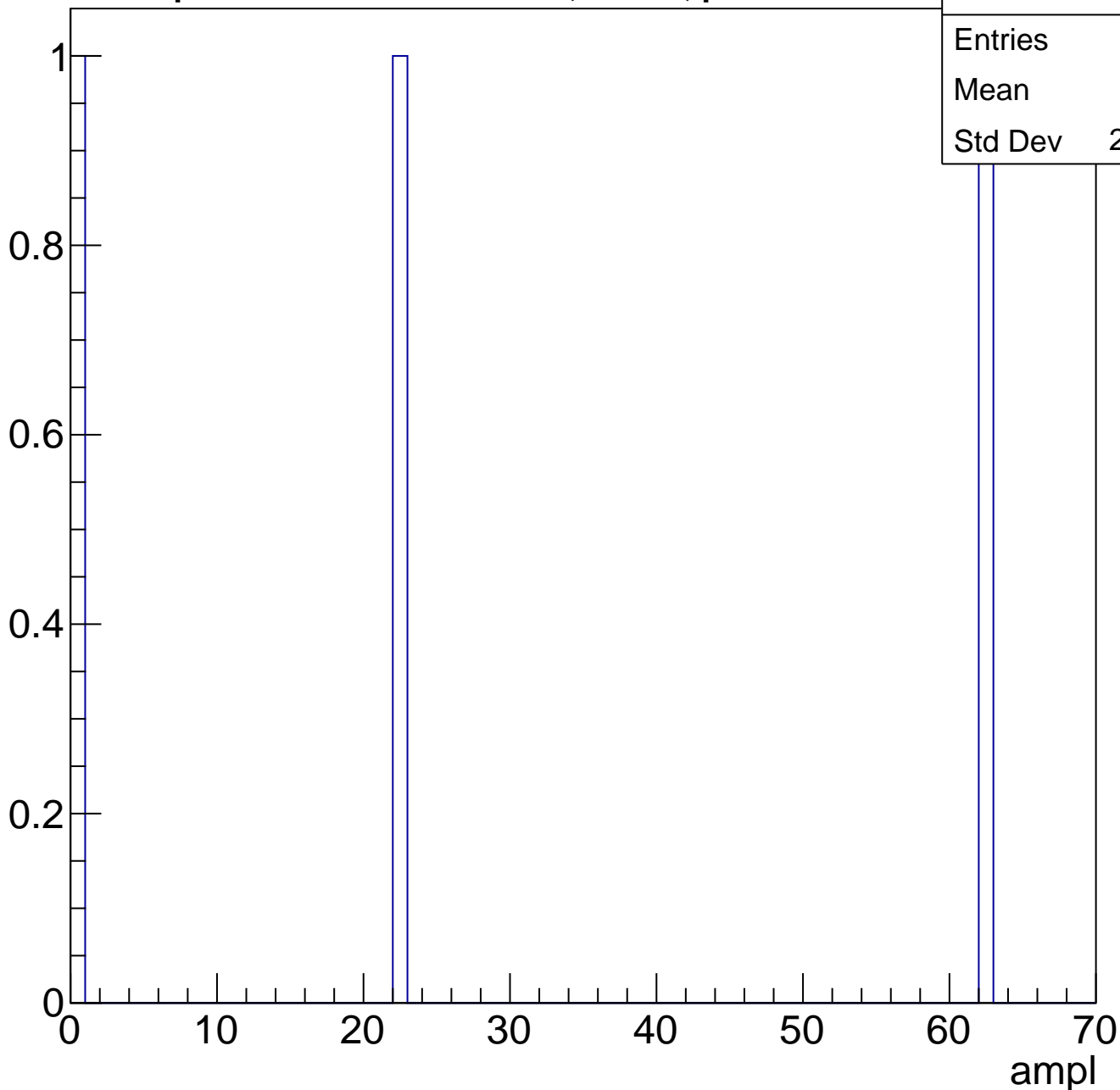




# B1L002S, U8-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	25.66

# B1L002S, U8-ch113, adc0

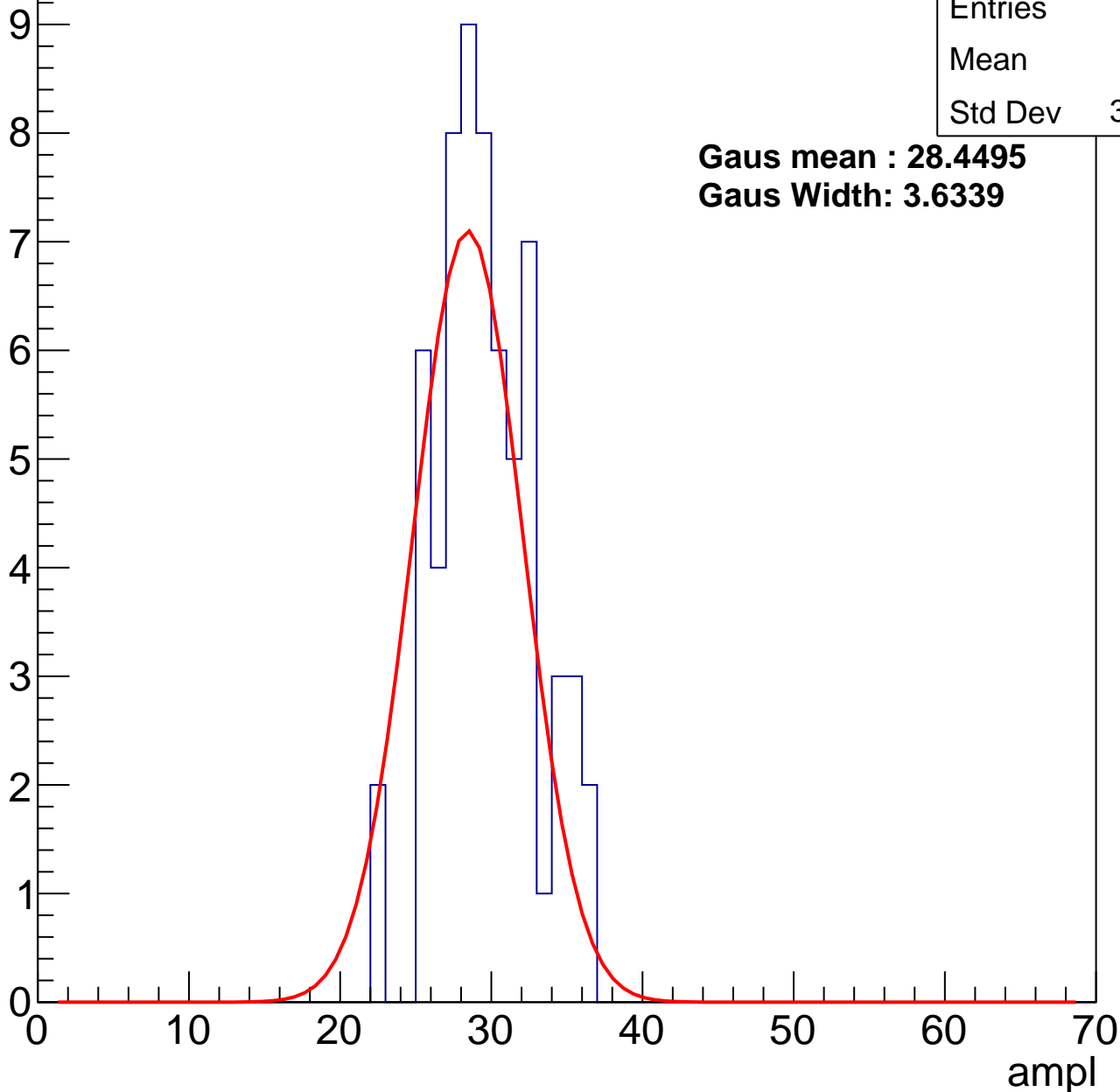
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	29.2
Std Dev	3.198

**Gaus mean : 28.4495**

**Gaus Width: 3.6339**



# B1L002S, U8-ch113, adc1

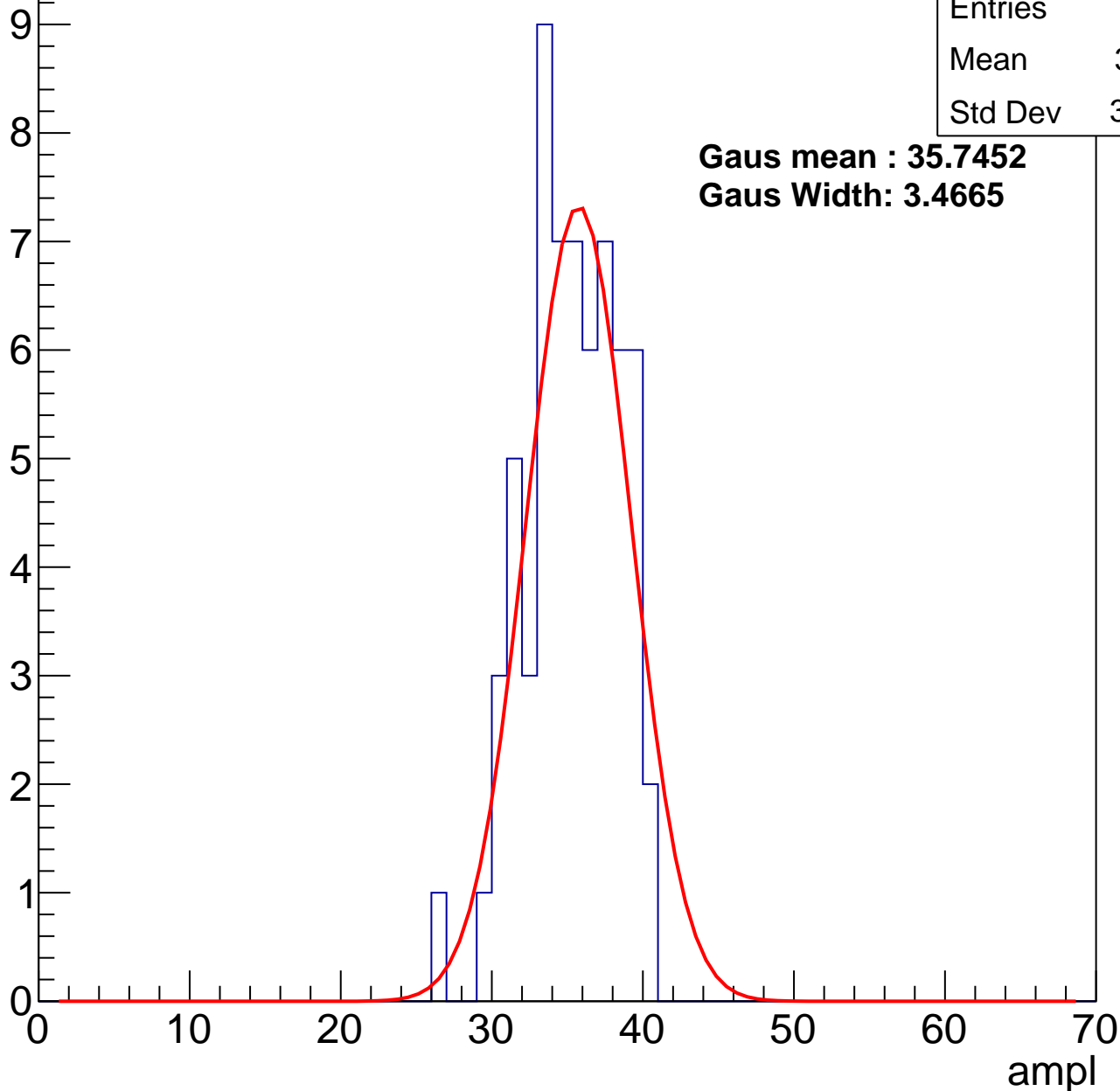
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.81
Std Dev	3.018

**Gaus mean : 35.7452**

**Gaus Width: 3.4665**



# B1L002S, U8-ch113, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	41.71
Std Dev	2.887

**Gaus mean : 42.0682**

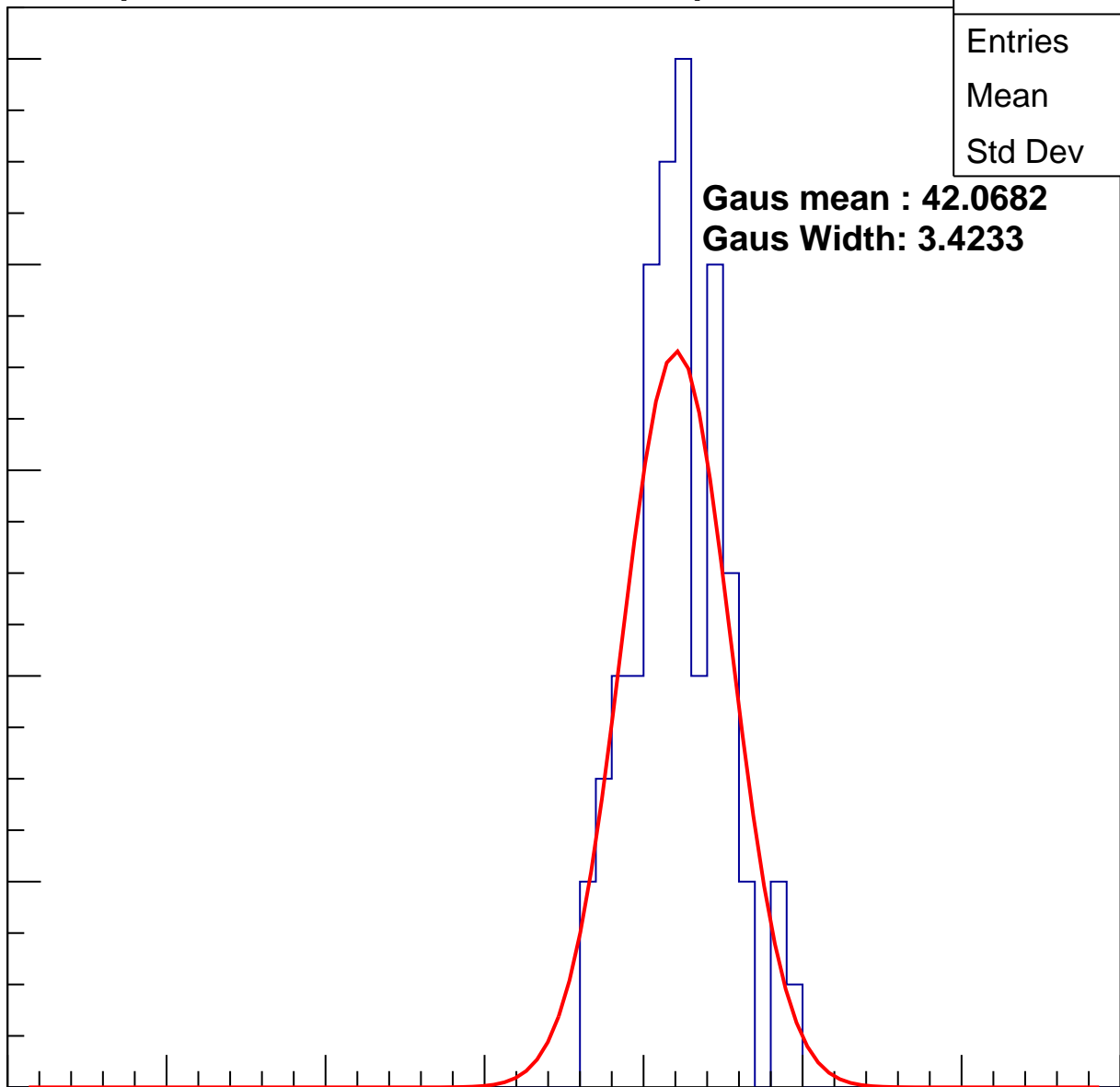
**Gaus Width: 3.4233**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

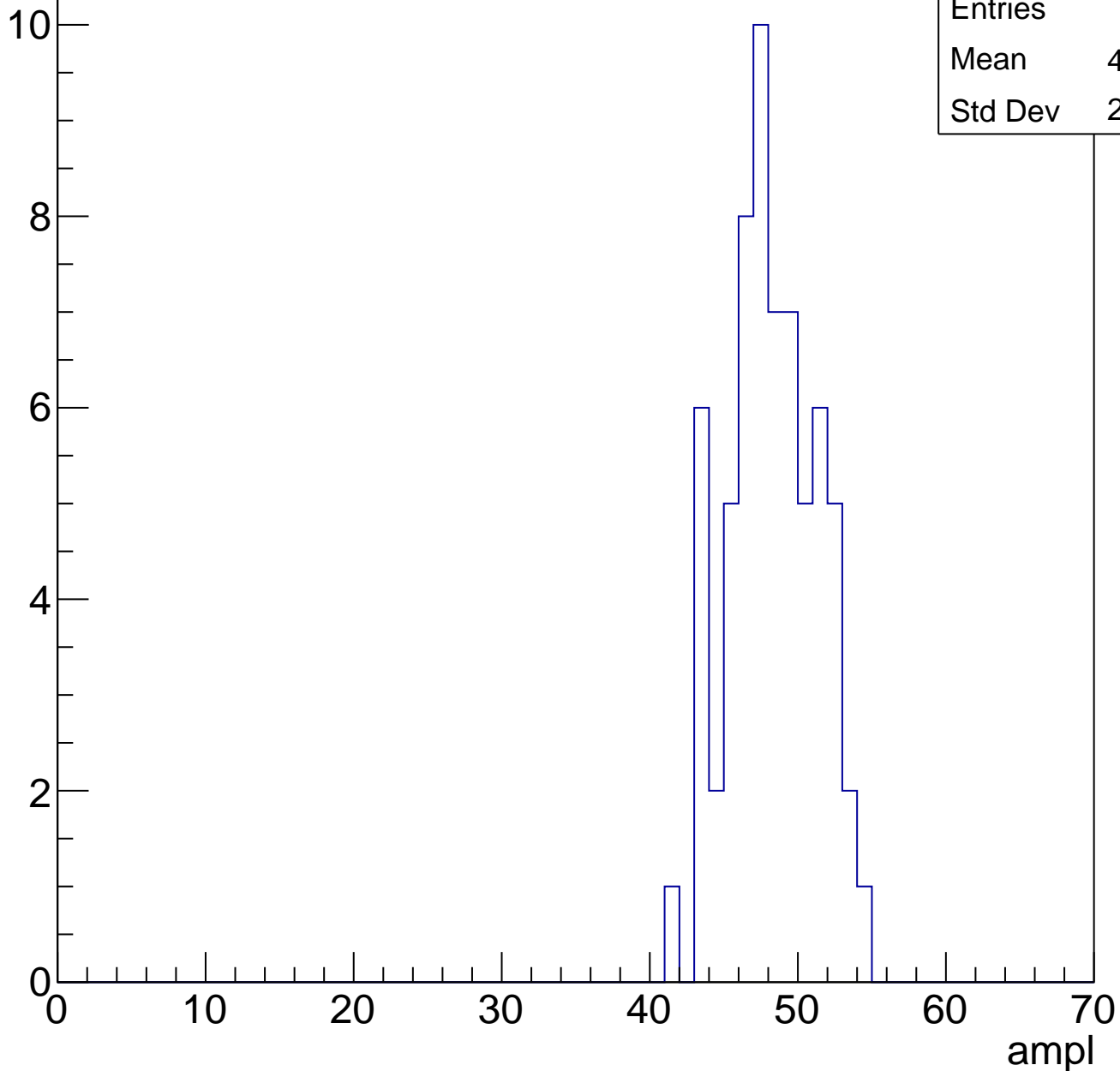


# B1L002S, U8-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	47.77
Std Dev	2.939

Entry

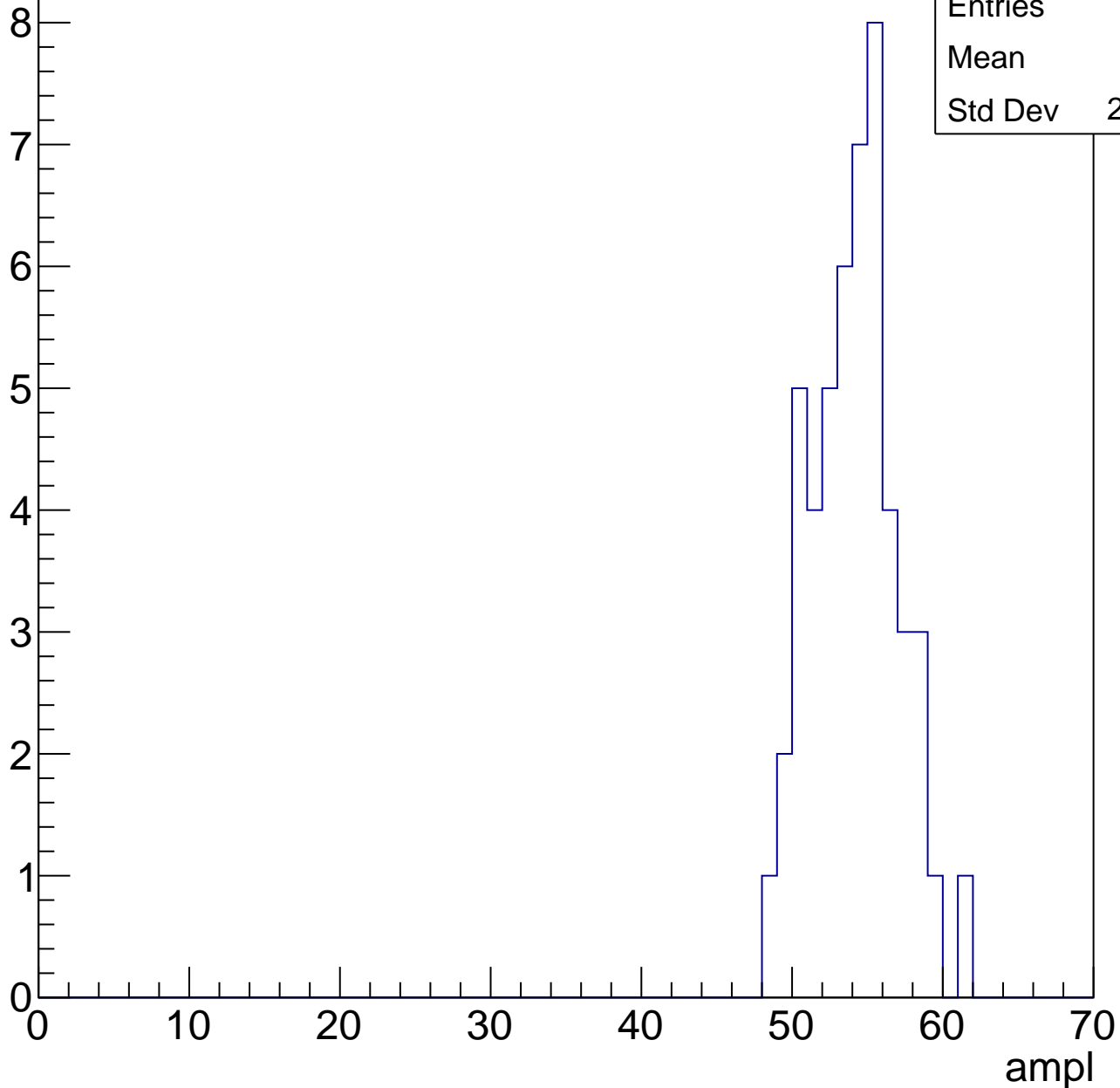


# B1L002S, U8-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

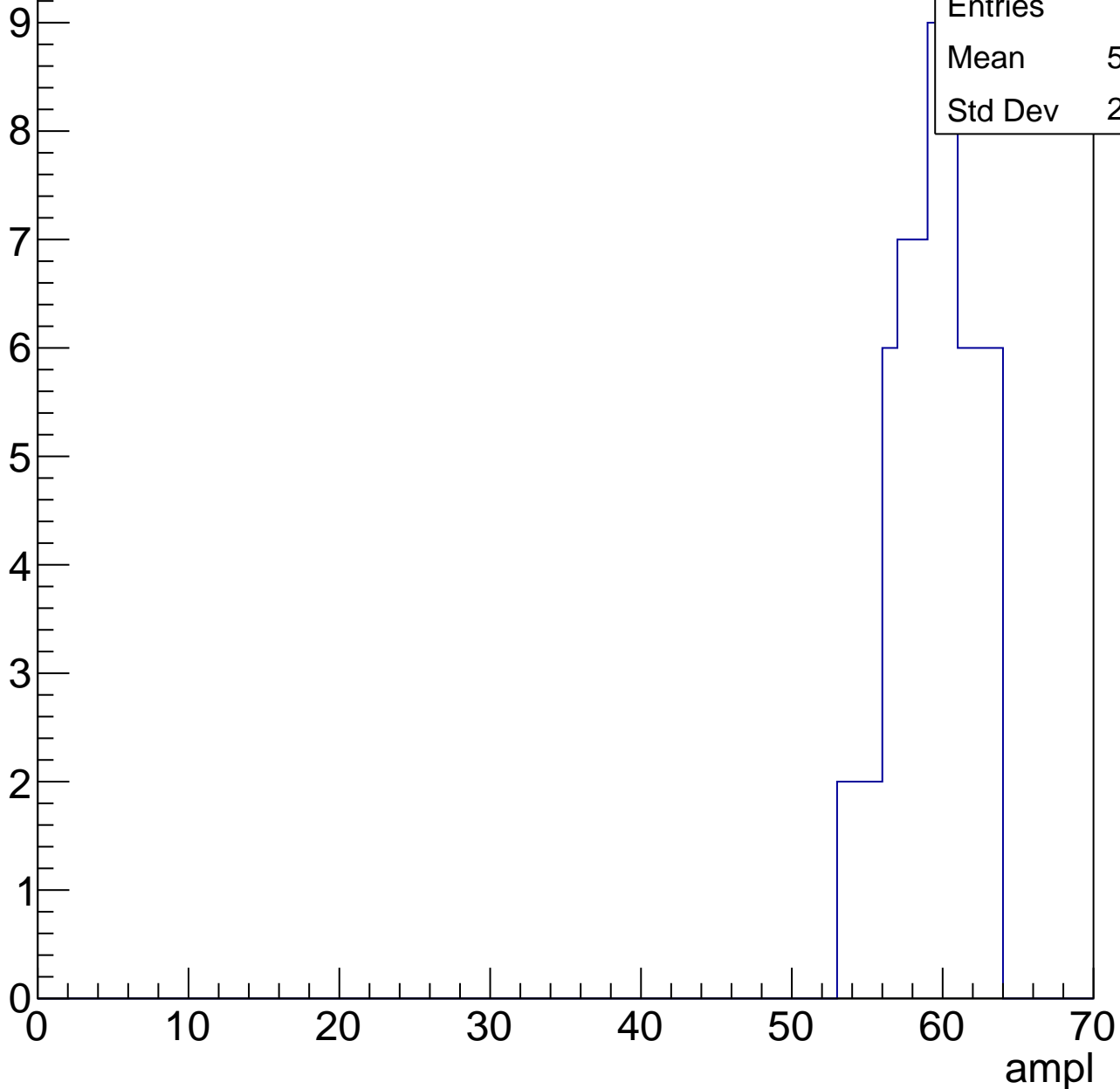
Entries	50
Mean	53.7
Std Dev	2.816



# B1L002S, U8-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

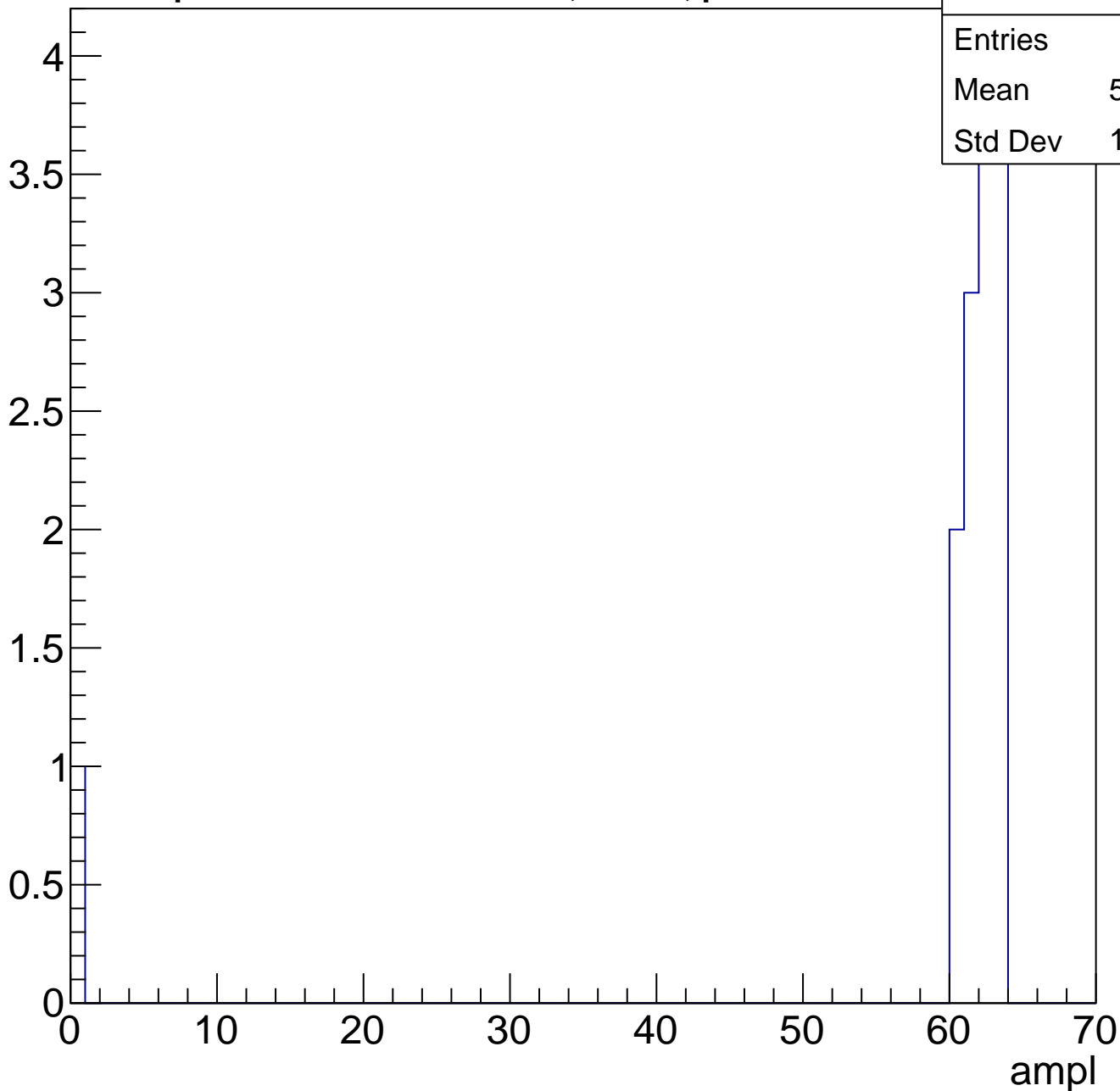


Entries	61
Mean	58.89
Std Dev	2.637

# B1L002S, U8-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch113, adc7

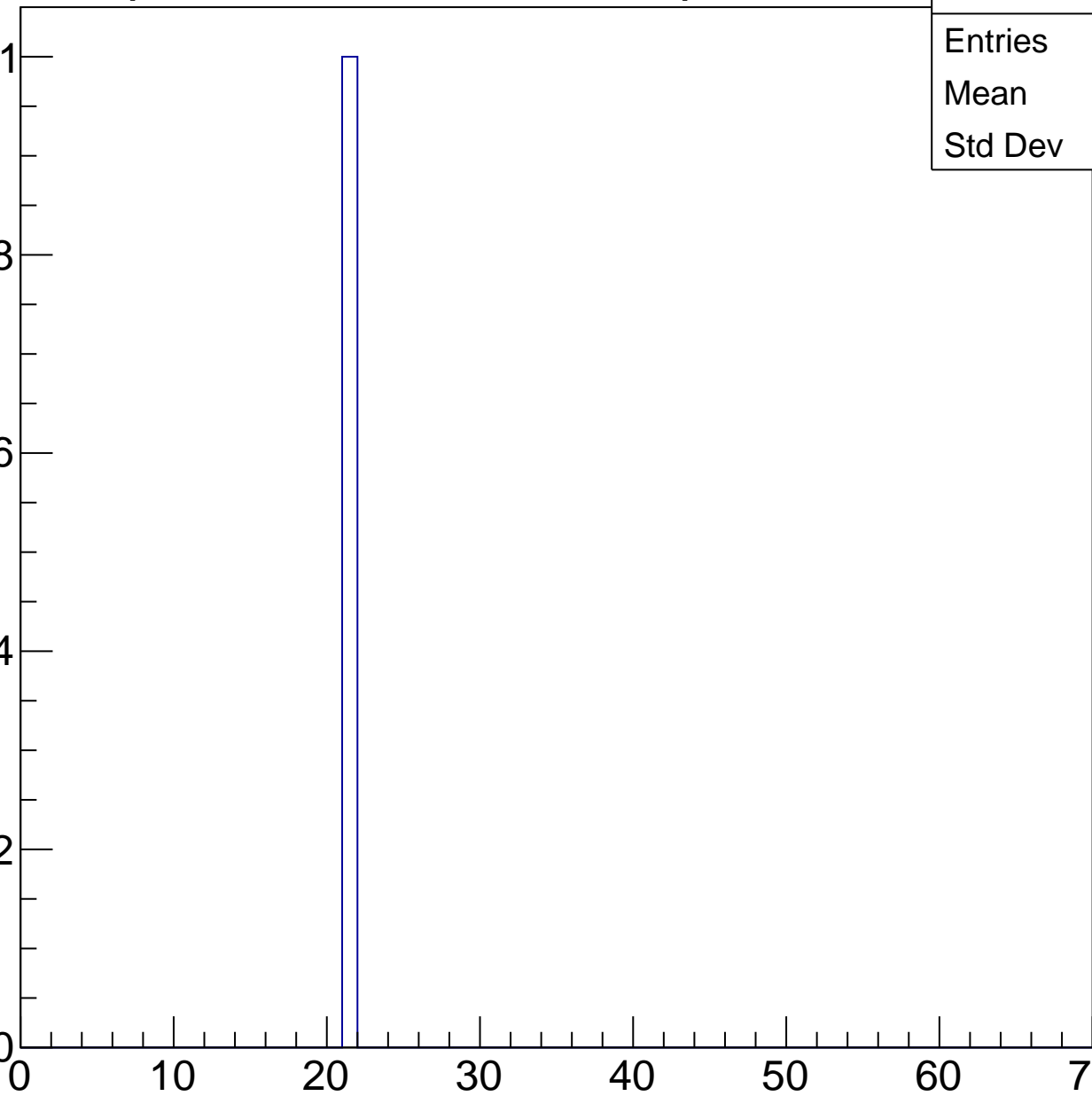
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl



# B1L002S, U8-ch114, adc0

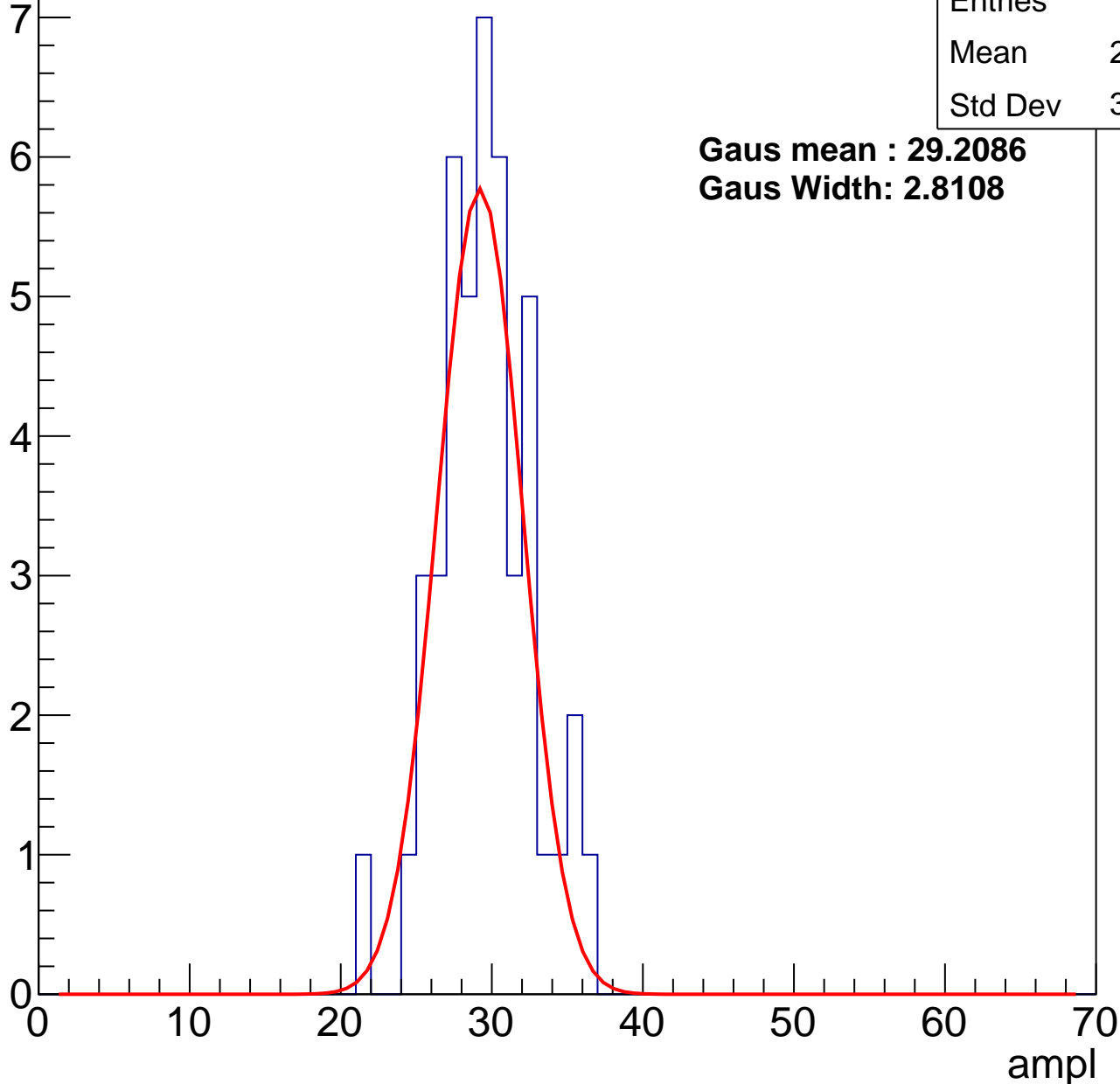
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	29.09
Std Dev	3.046

**Gaus mean : 29.2086**

**Gaus Width: 2.8108**



# B1L002S, U8-ch114, adc1

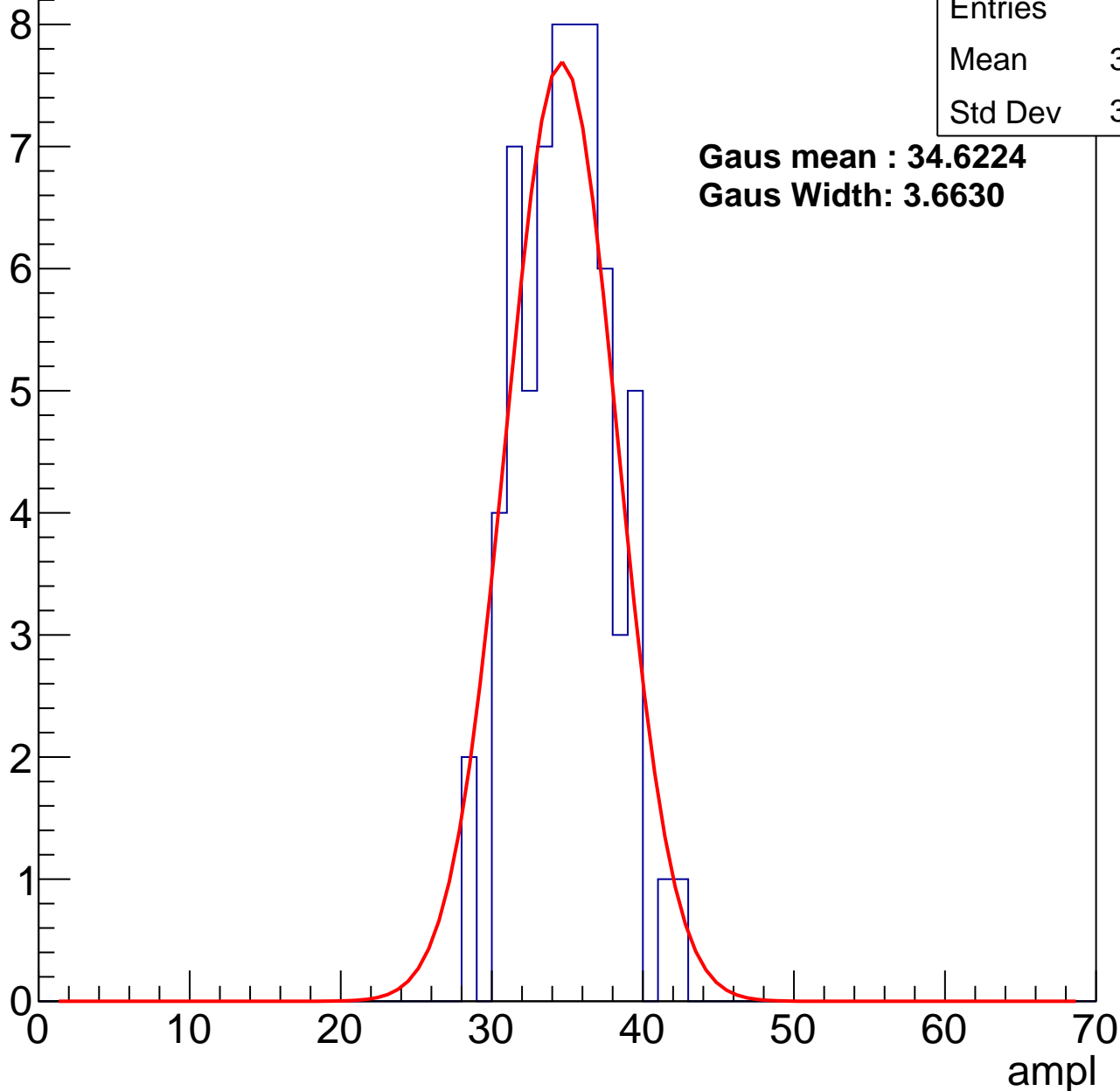
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.43
Std Dev	3.023

**Gaus mean : 34.6224**

**Gaus Width: 3.6630**



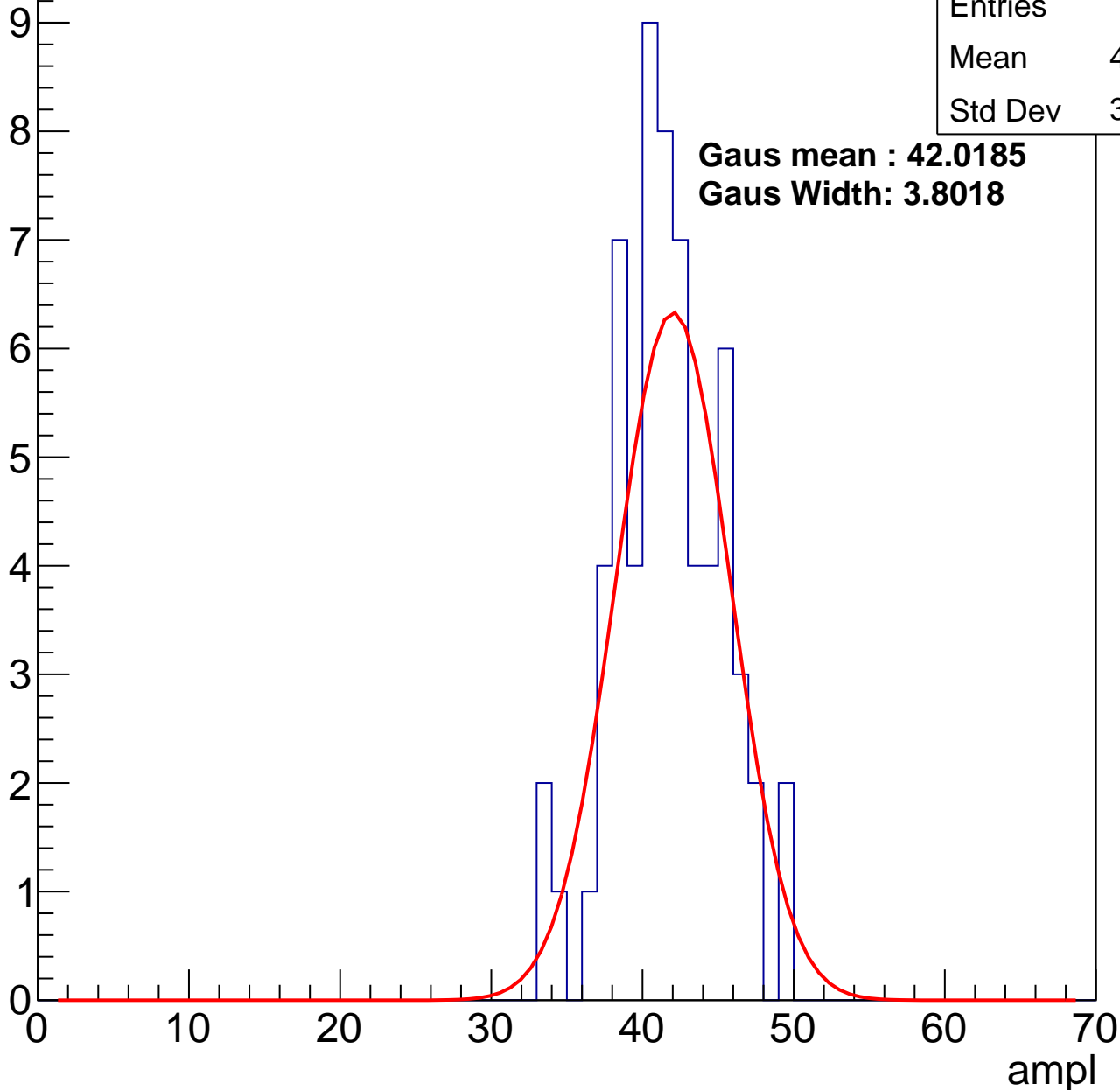
# B1L002S, U8-ch114, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.19
Std Dev	3.495

**Gaus mean : 42.0185**  
**Gaus Width: 3.8018**

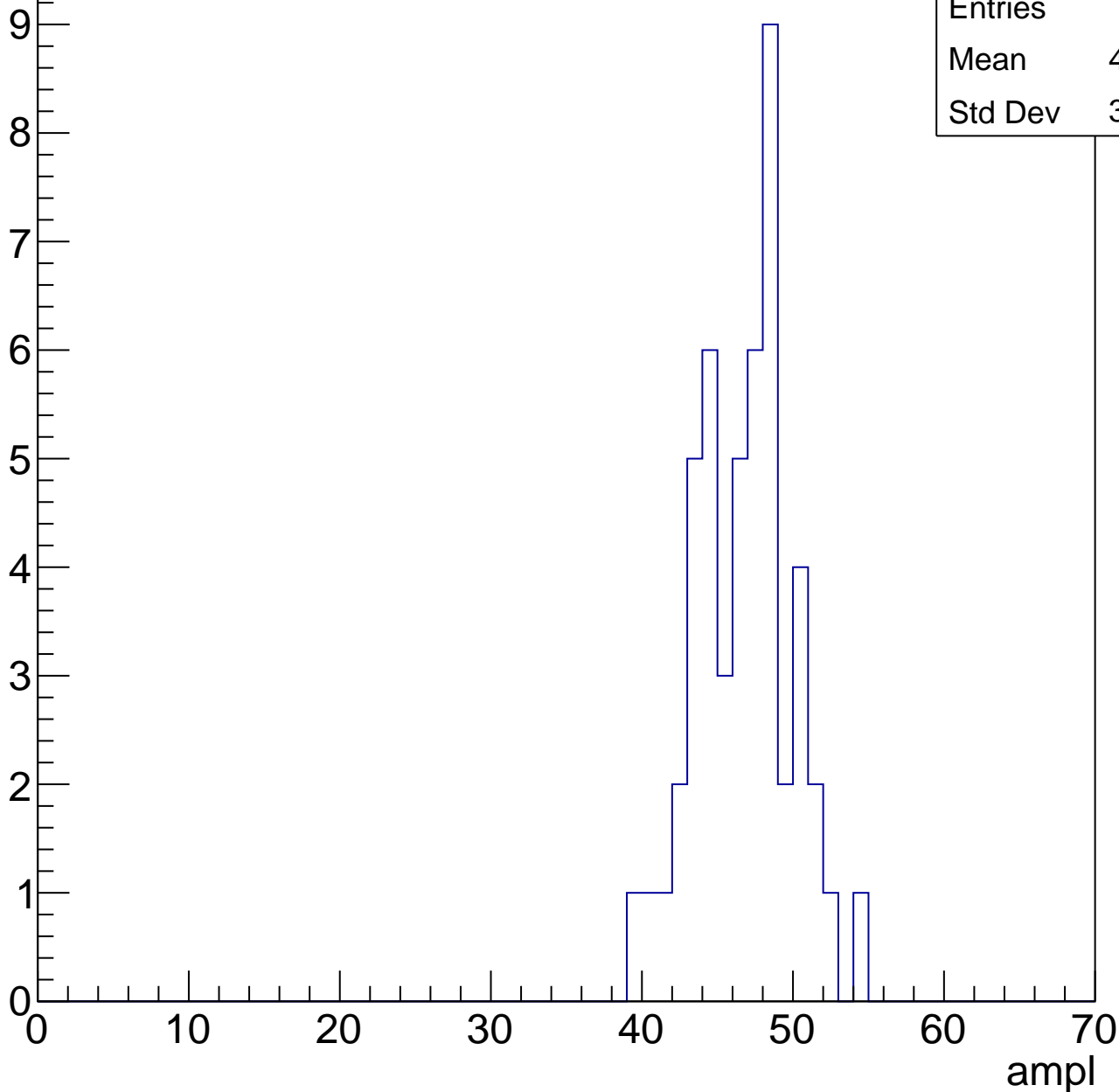


# B1L002S, U8-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	46.29
Std Dev	3.143



# B1L002S, U8-ch114, adc4

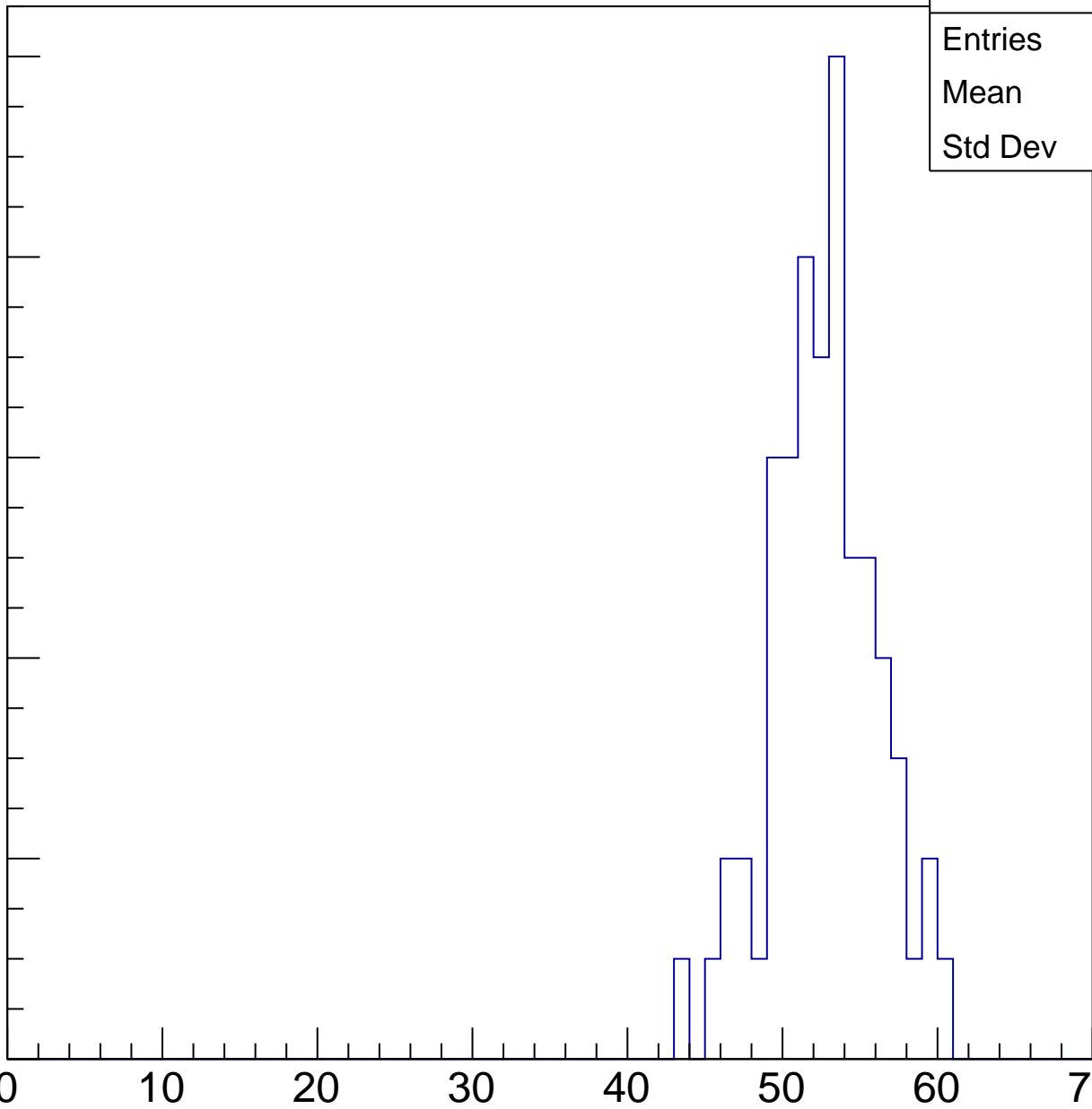
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	52.22
Std Dev	3.426

Entry

10  
8  
6  
4  
2  
0

ampl

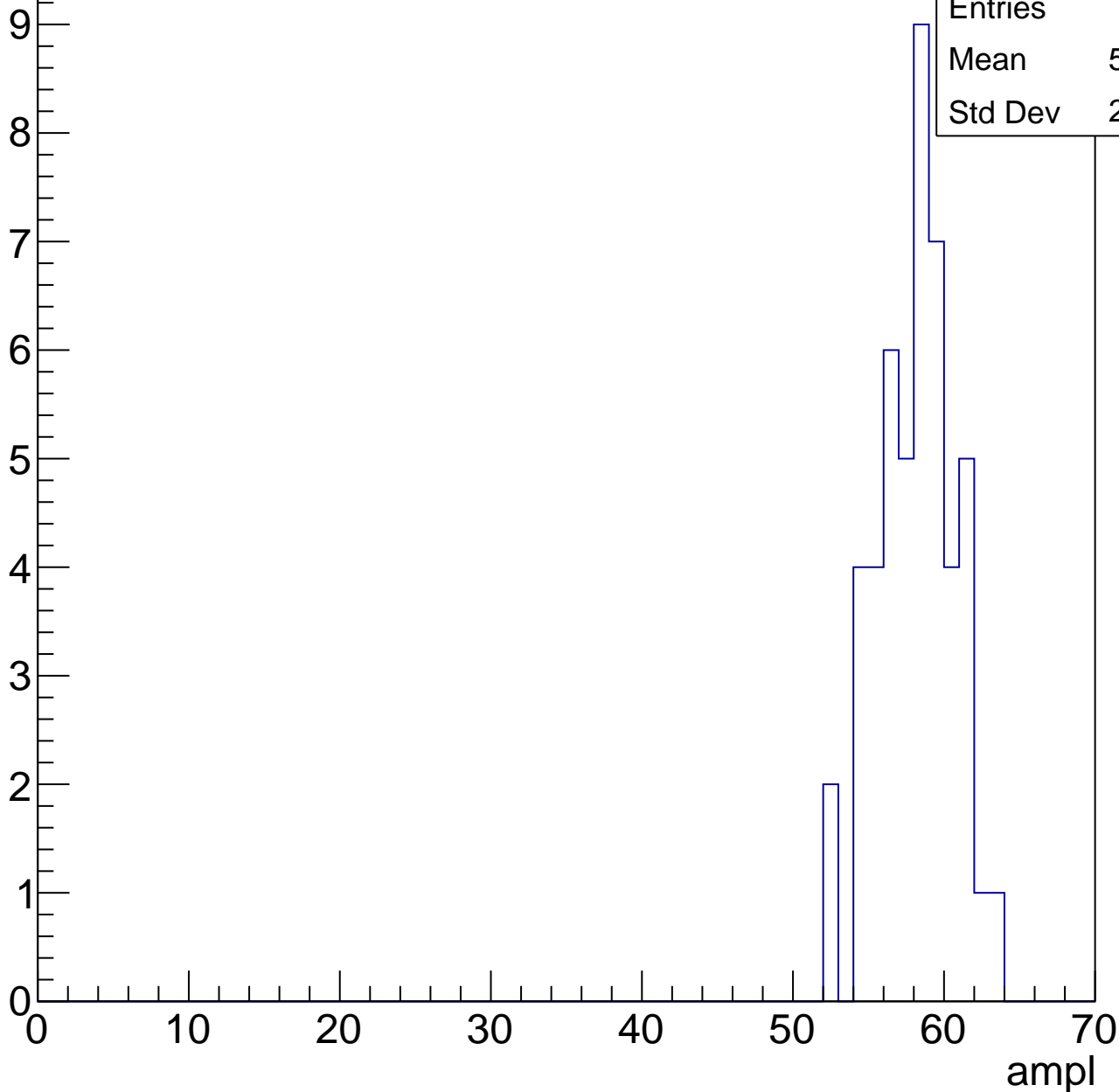


# B1L002S, U8-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	57.62
Std Dev	2.514

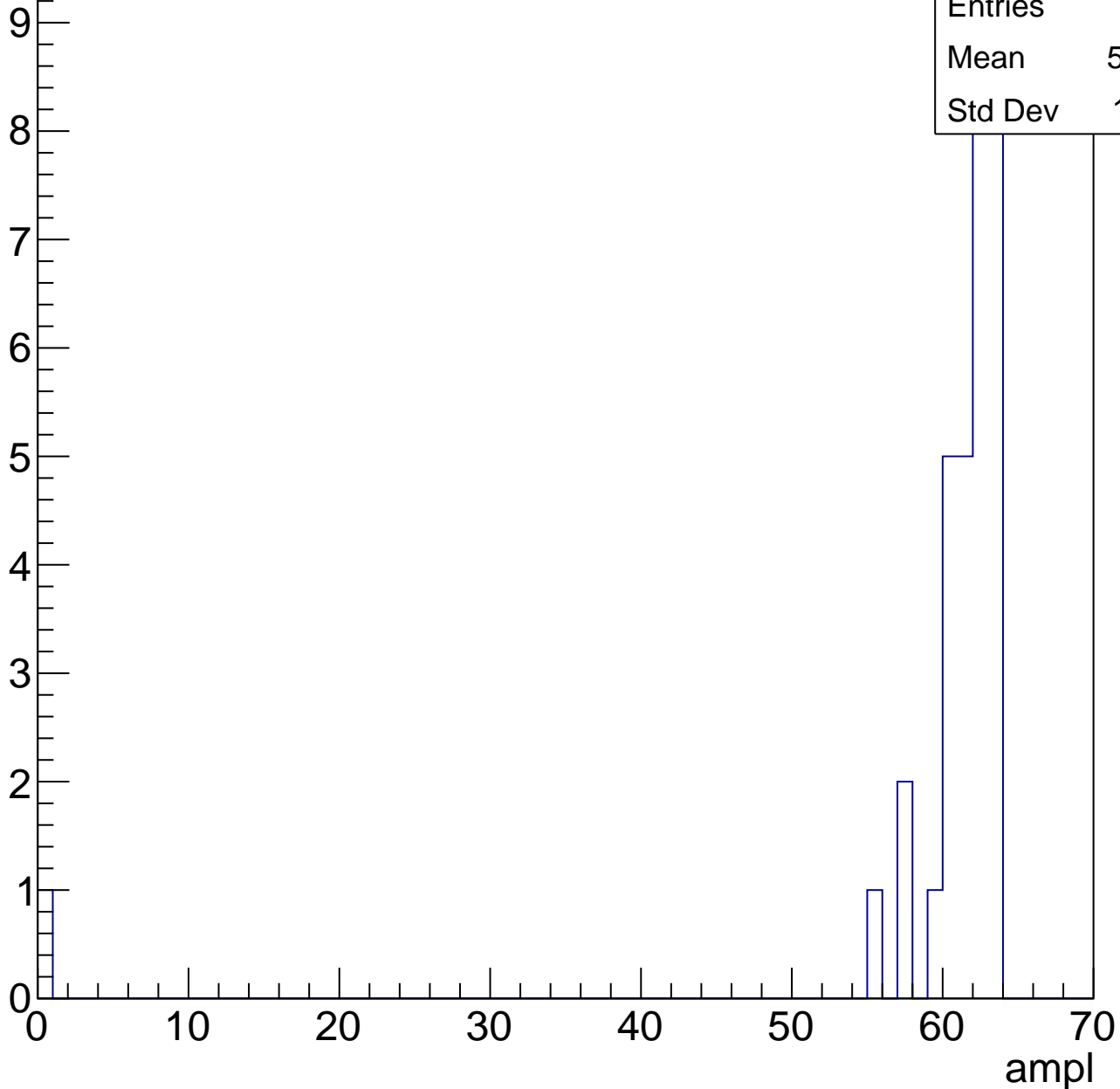


# B1L002S, U8-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.22
Std Dev	10.81





# B1L002S, U8-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U8-ch115, adc0

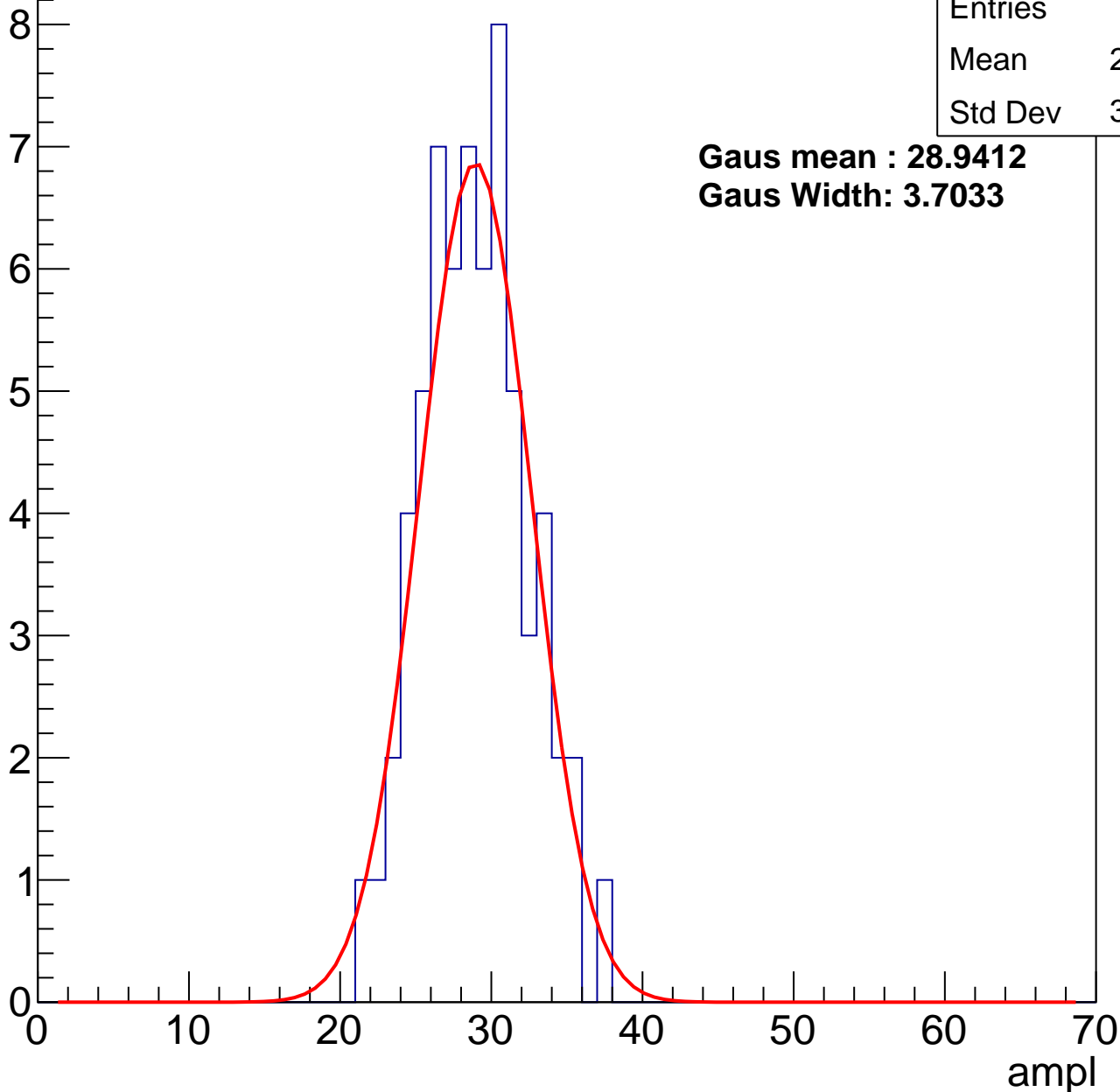
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.47
Std Dev	3.414

**Gaus mean : 28.9412**

**Gaus Width: 3.7033**



# B1L002S, U8-ch115, adc1

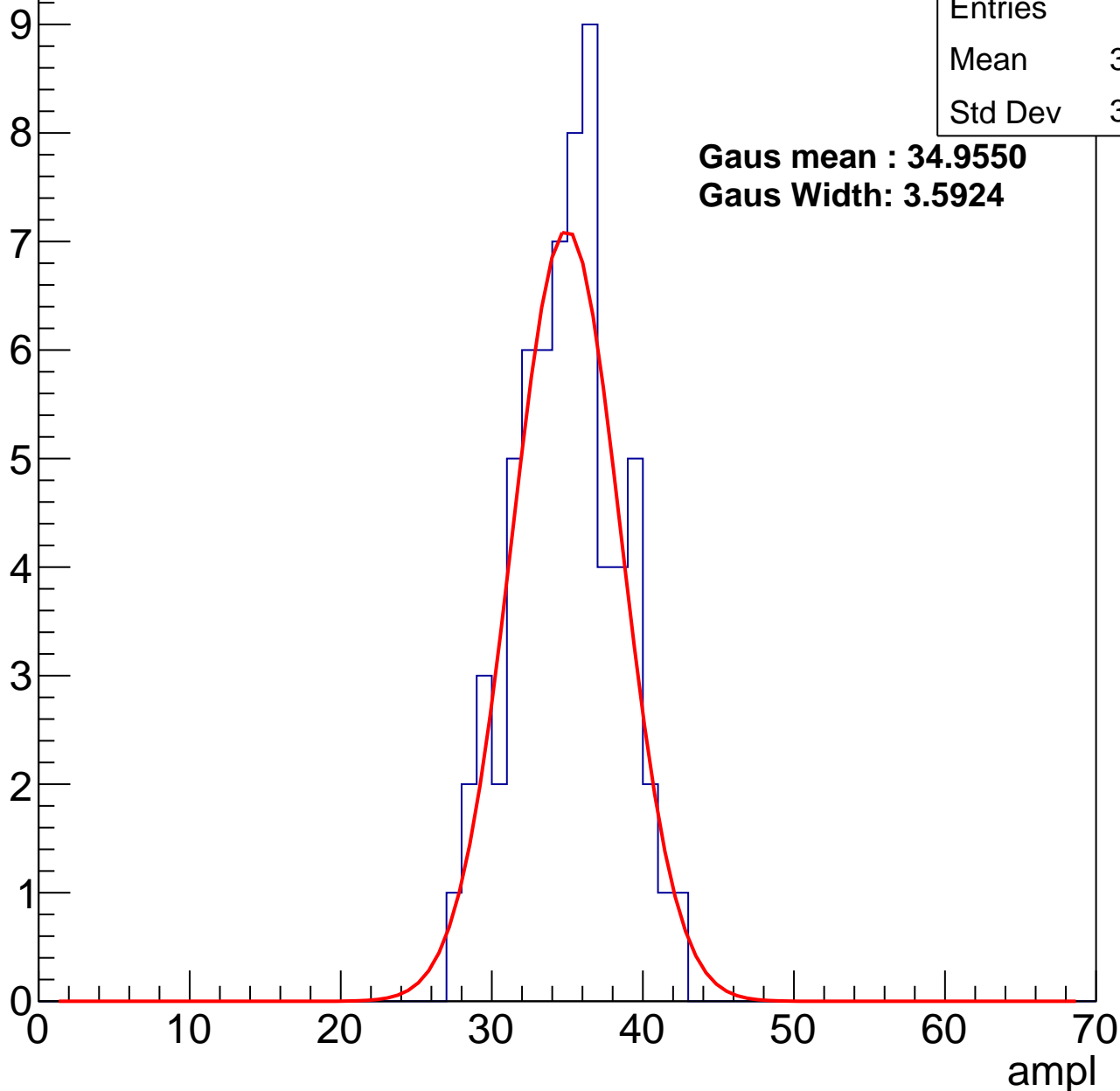
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.47
Std Dev	3.354

**Gaus mean : 34.9550**

**Gaus Width: 3.5924**



# B1L002S, U8-ch115, adc2

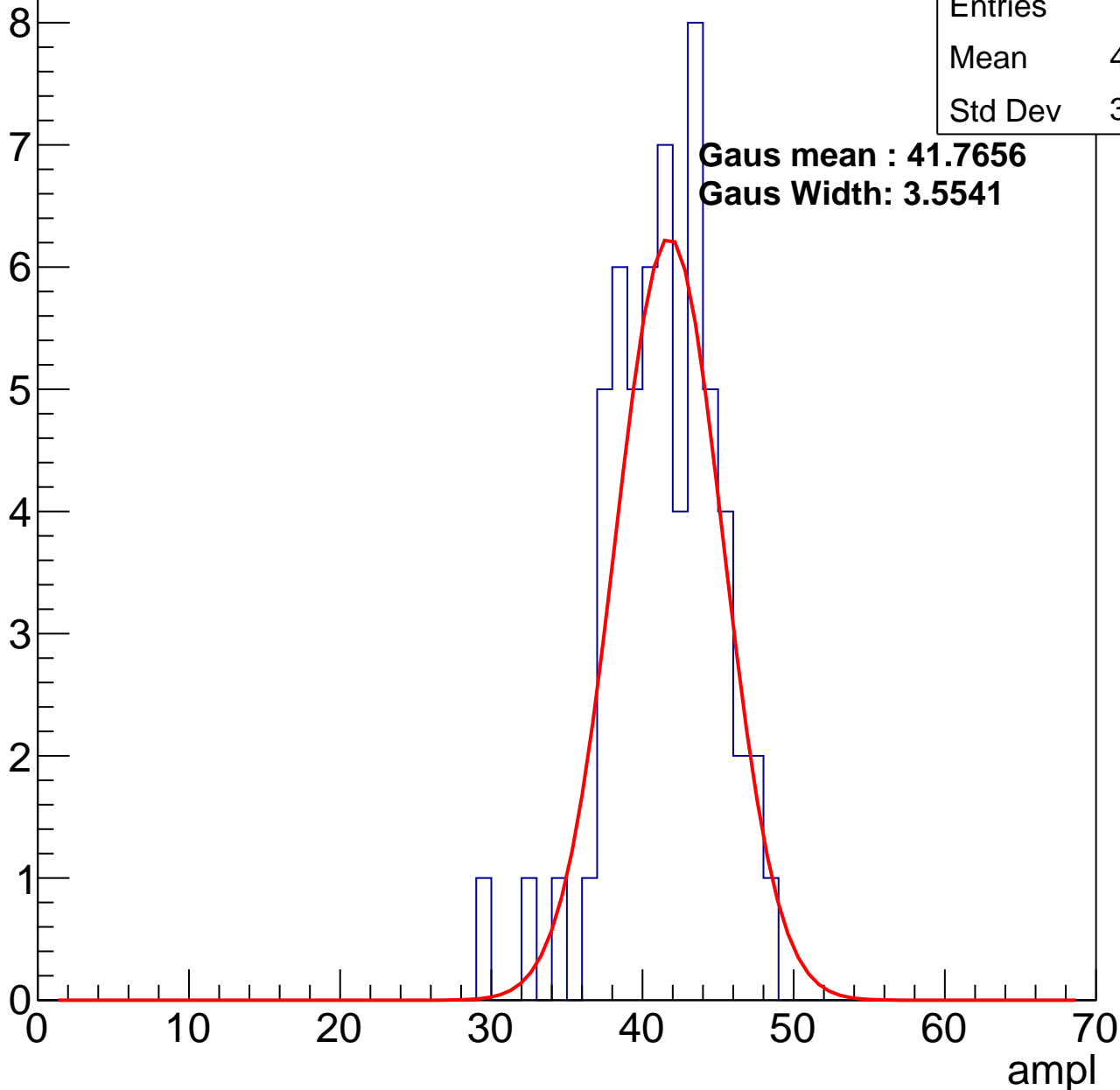
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.88
Std Dev	3.622

**Gaus mean : 41.7656**

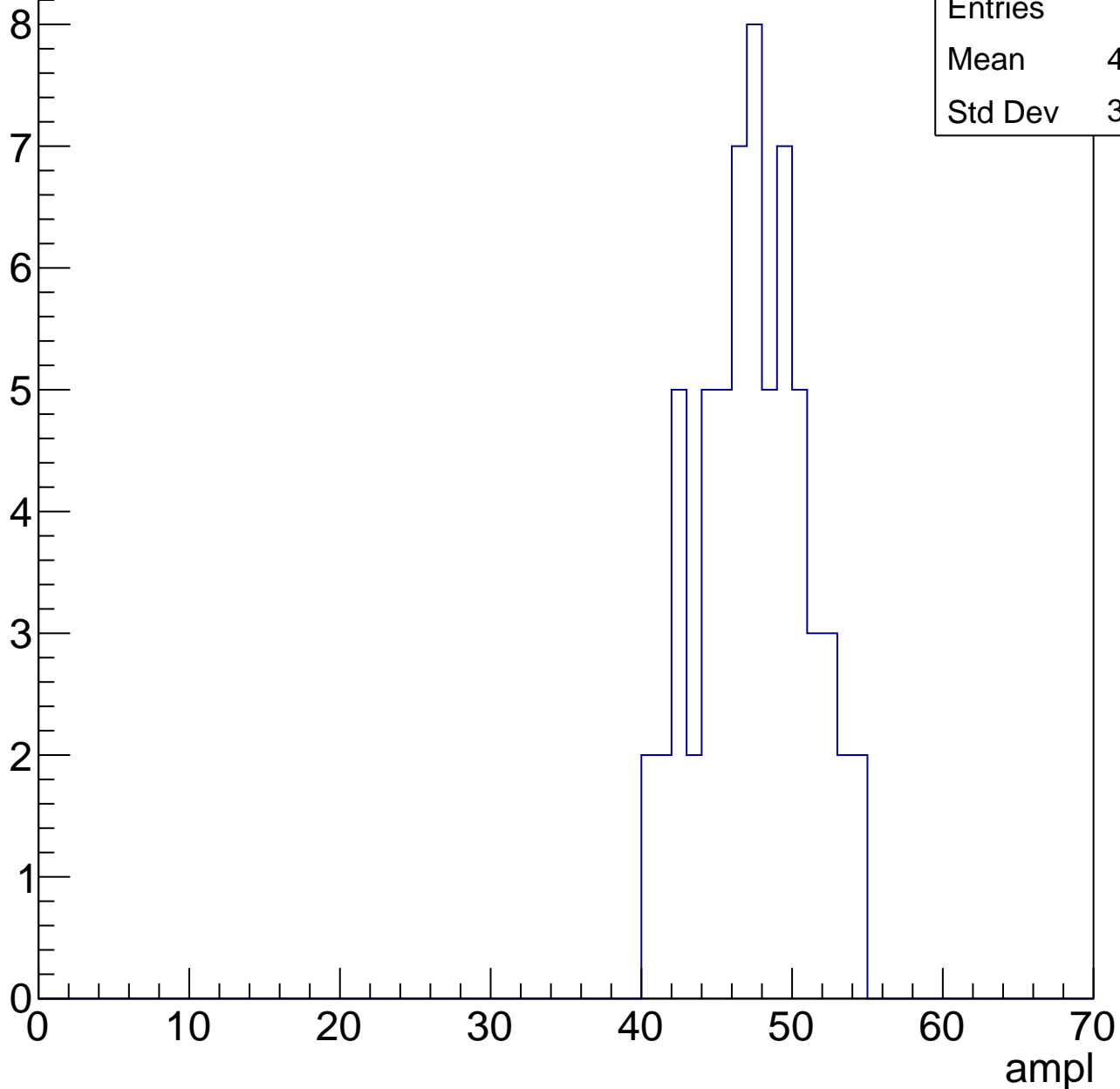
**Gaus Width: 3.5541**



# B1L002S, U8-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



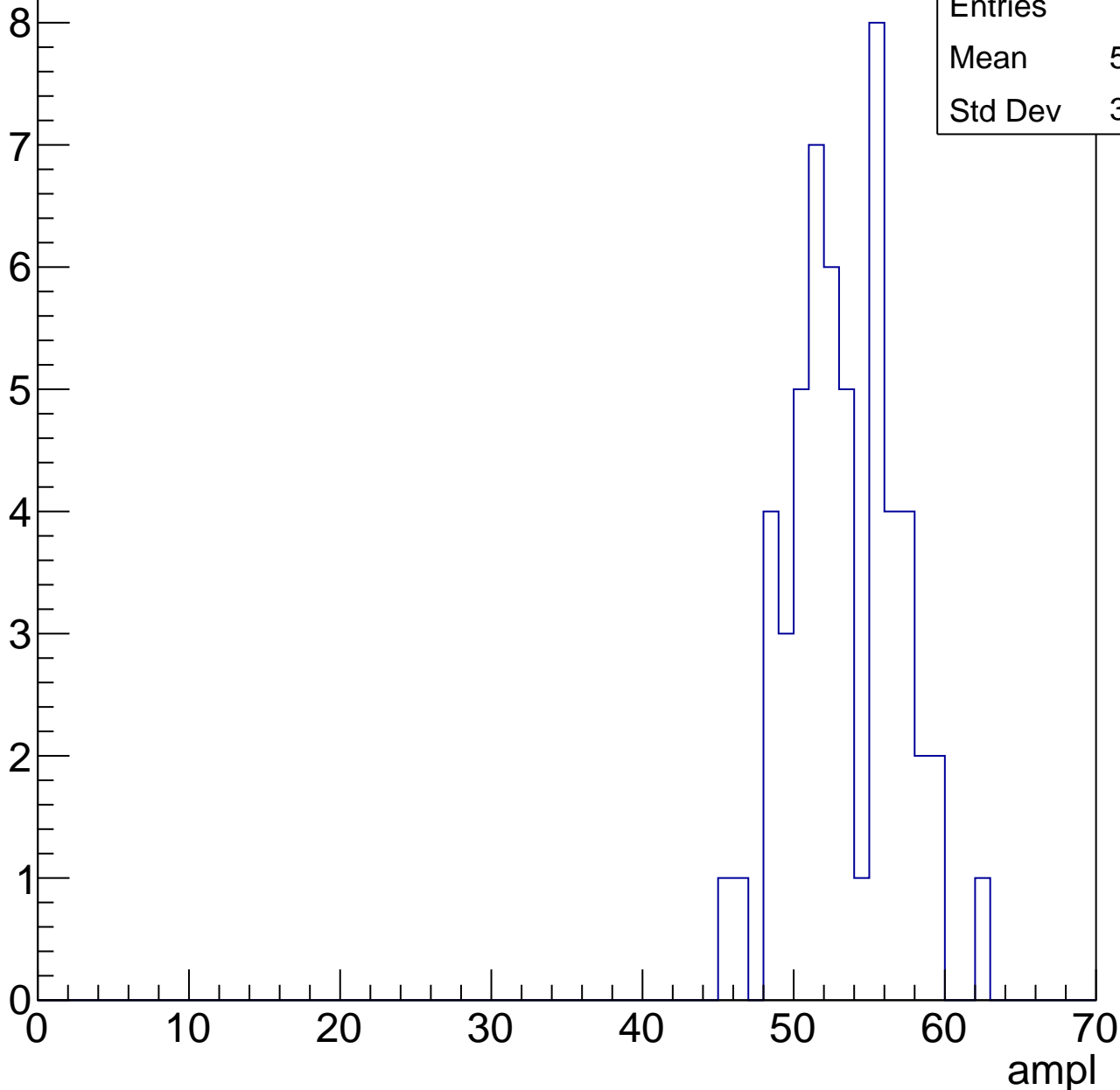
Entries	63
Mean	46.94
Std Dev	3.495

# B1L002S, U8-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	52.89
Std Dev	3.536

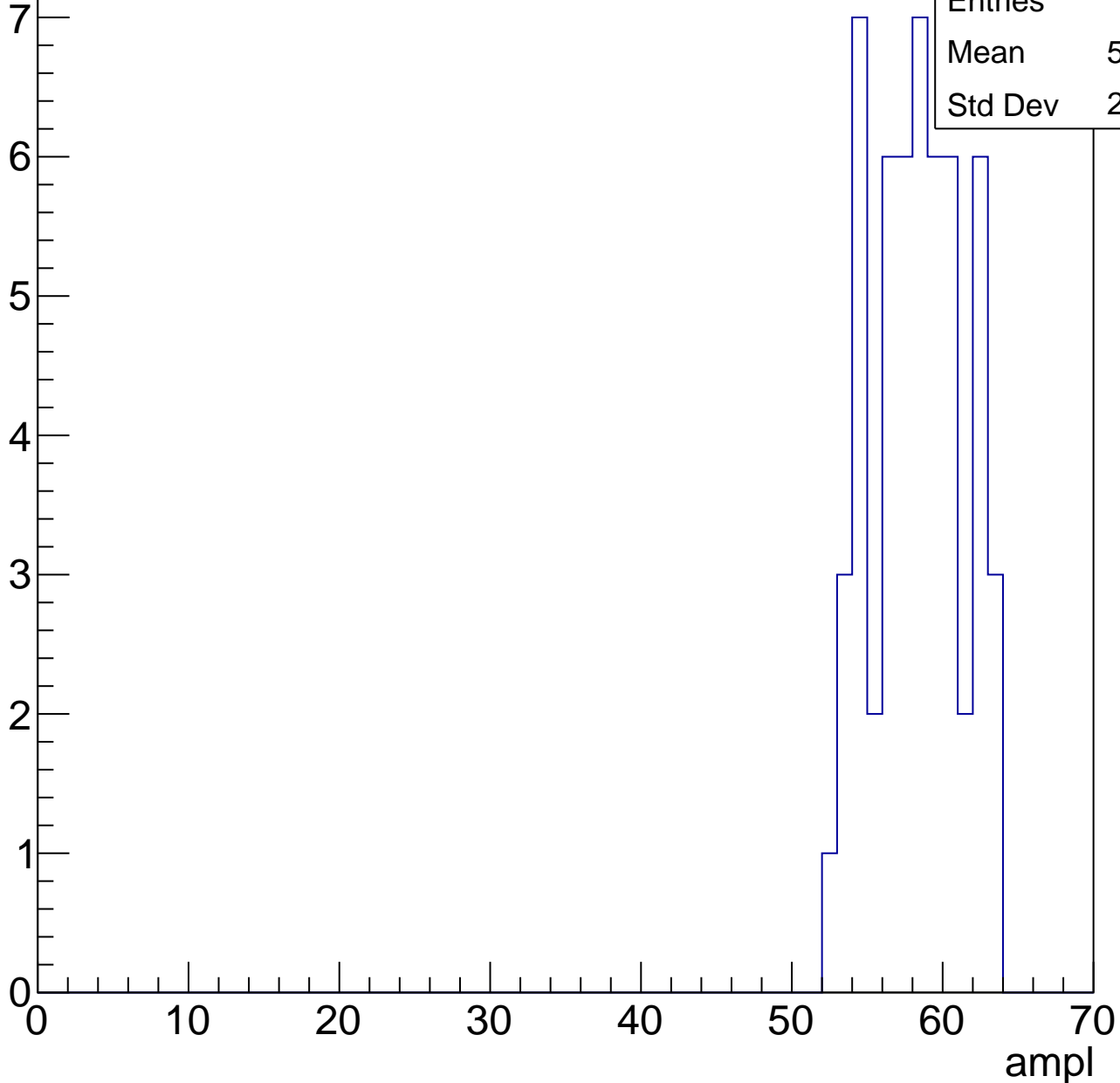


# B1L002S, U8-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

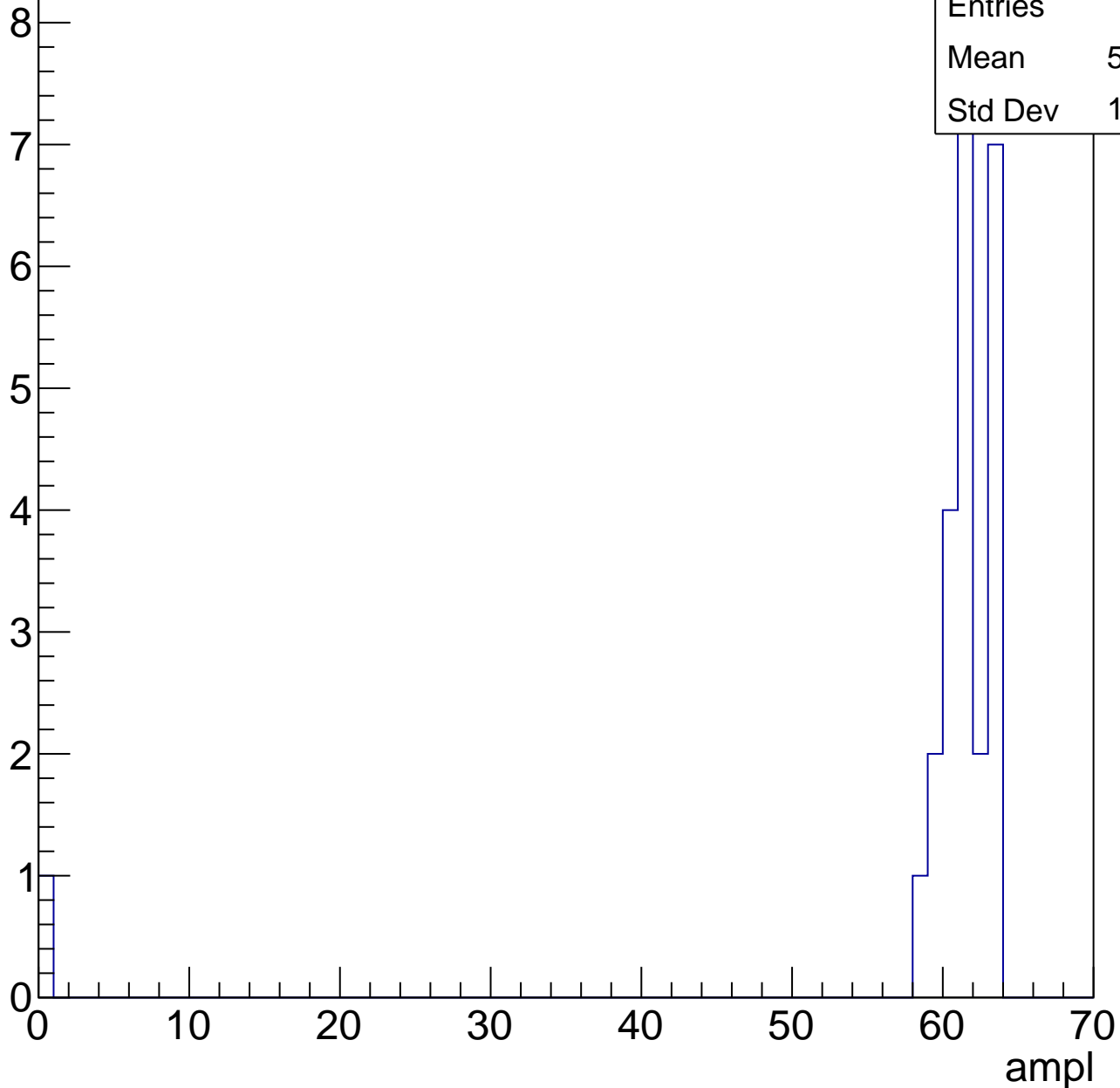
Entries	55
Mean	57.82
Std Dev	2.979



# B1L002S, U8-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch116, adc0

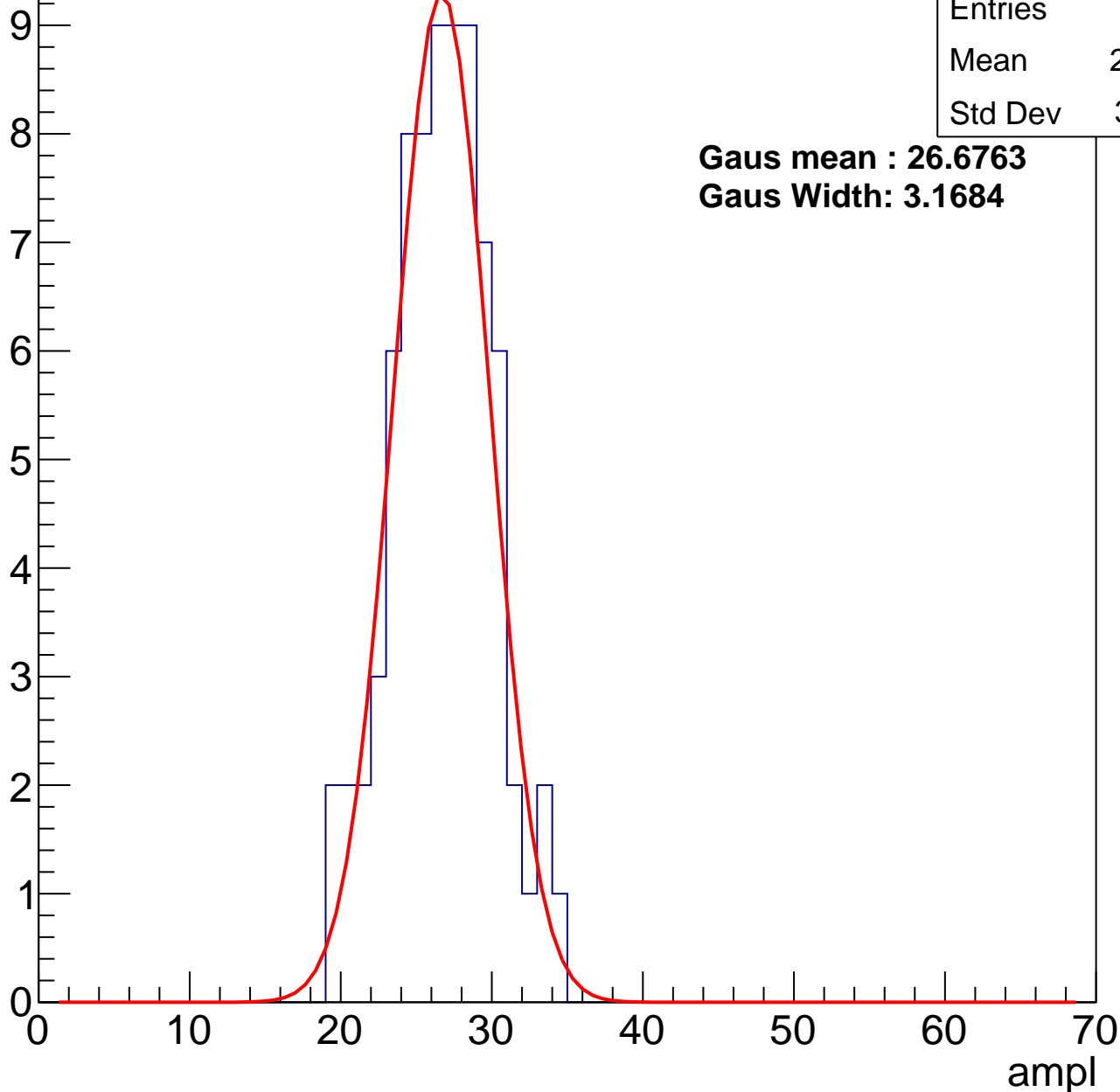
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	26.26
Std Dev	3.241

**Gaus mean : 26.6763**

**Gaus Width: 3.1684**



# B1L002S, U8-ch116, adc1

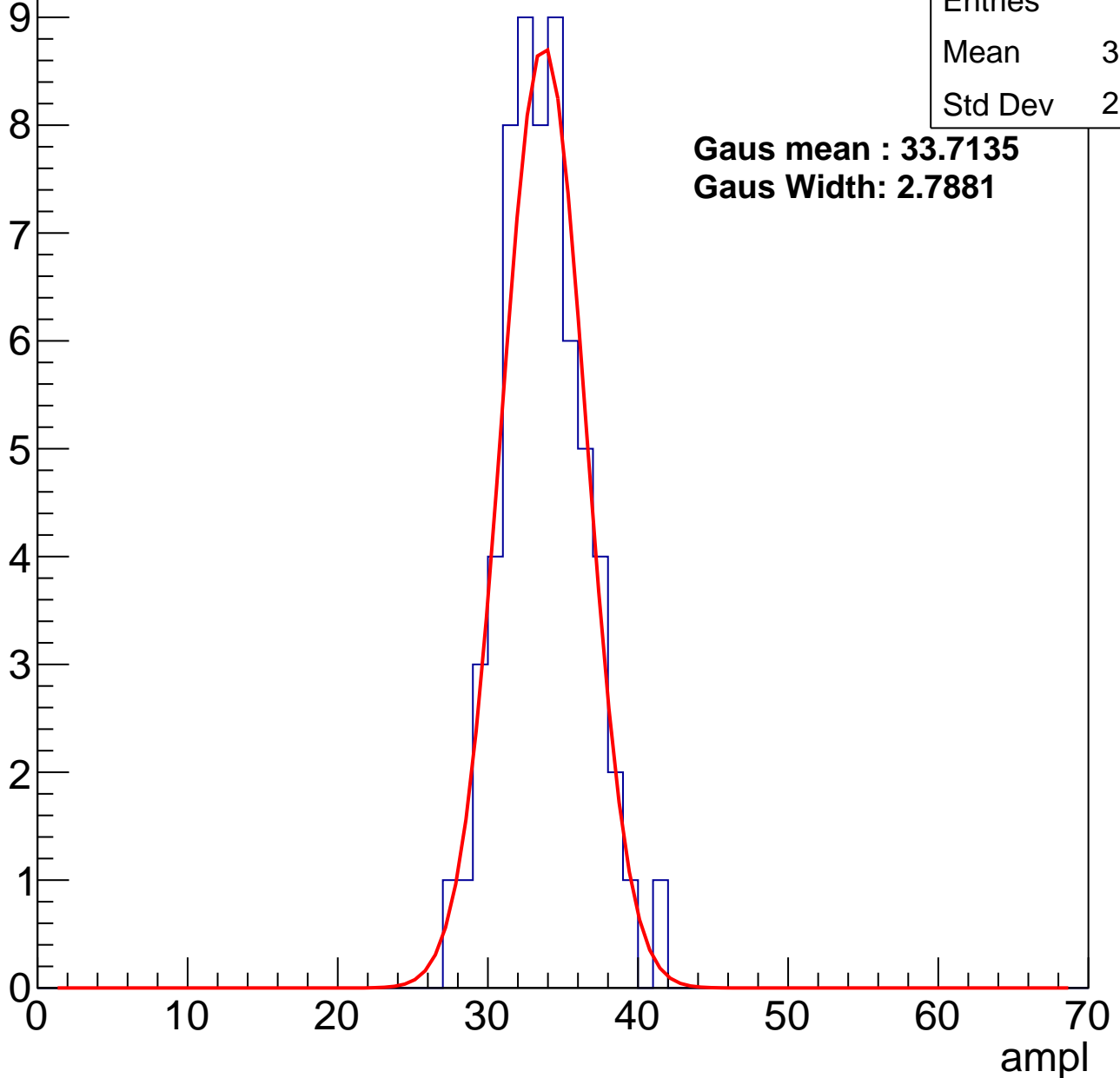
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.26
Std Dev	2.765

**Gaus mean : 33.7135**

**Gaus Width: 2.7881**



# B1L002S, U8-ch116, adc2

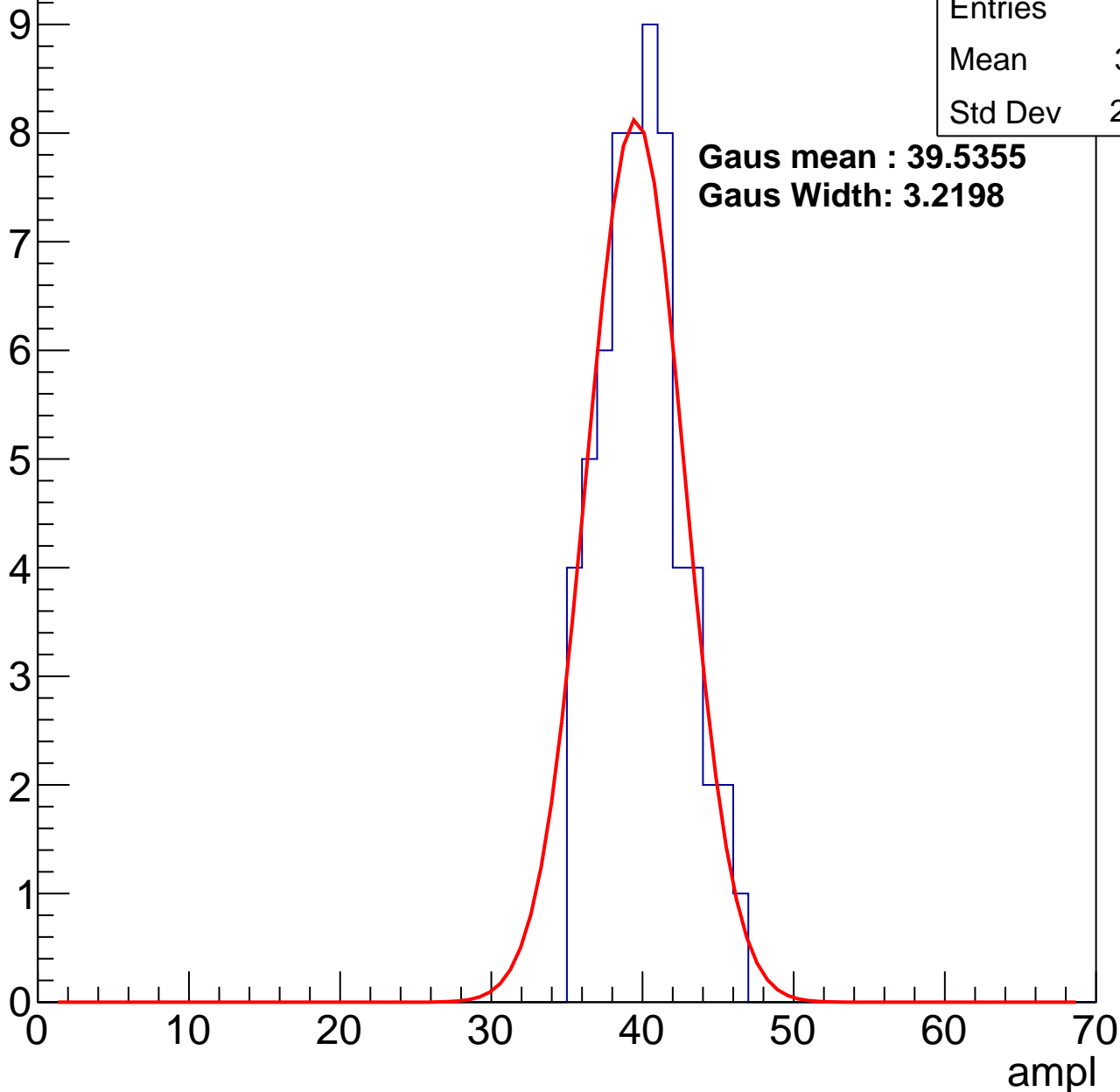
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.51
Std Dev	2.677

**Gaus mean : 39.5355**

**Gaus Width: 3.2198**

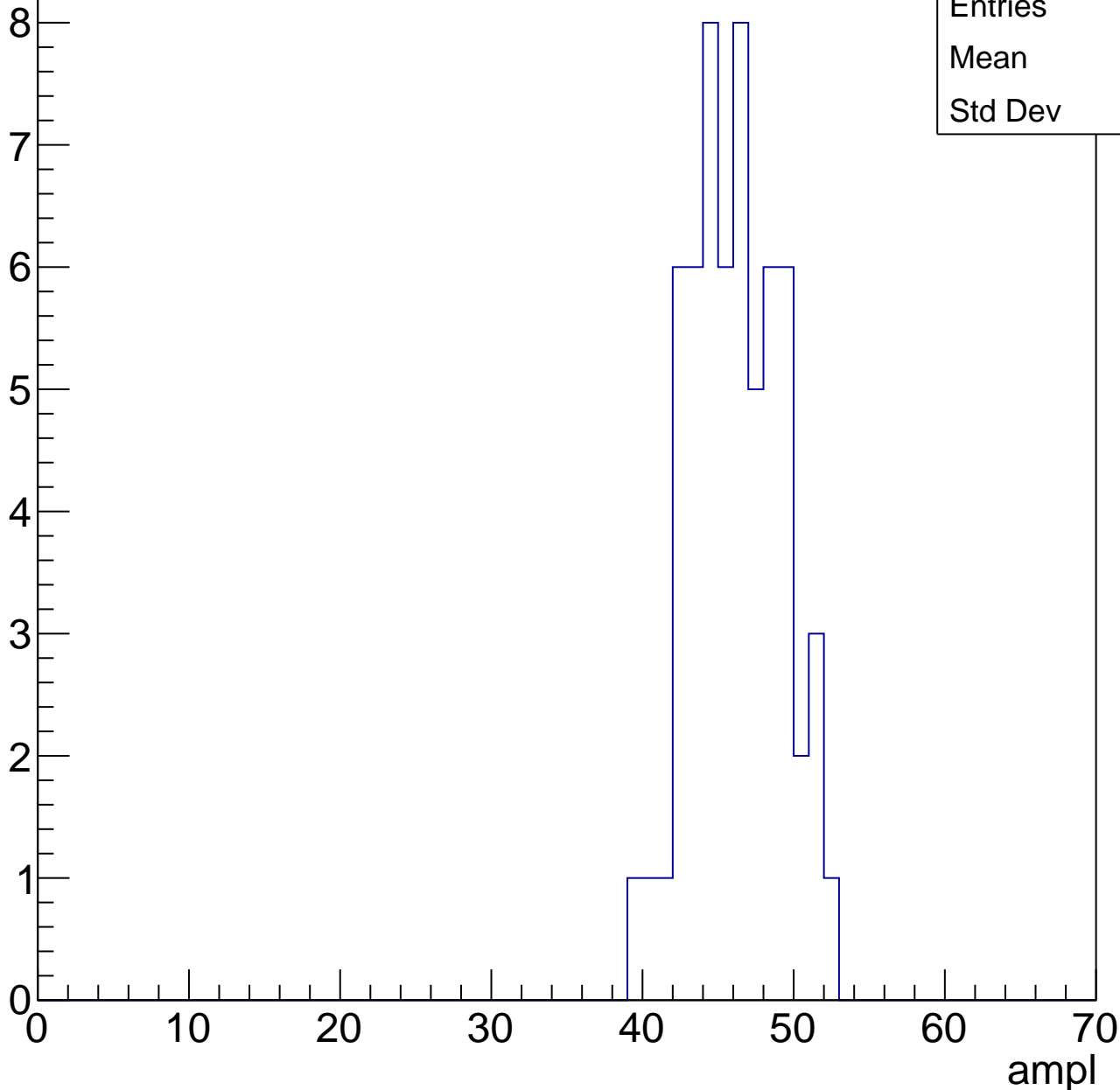


# B1L002S, U8-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.7
Std Dev	2.94

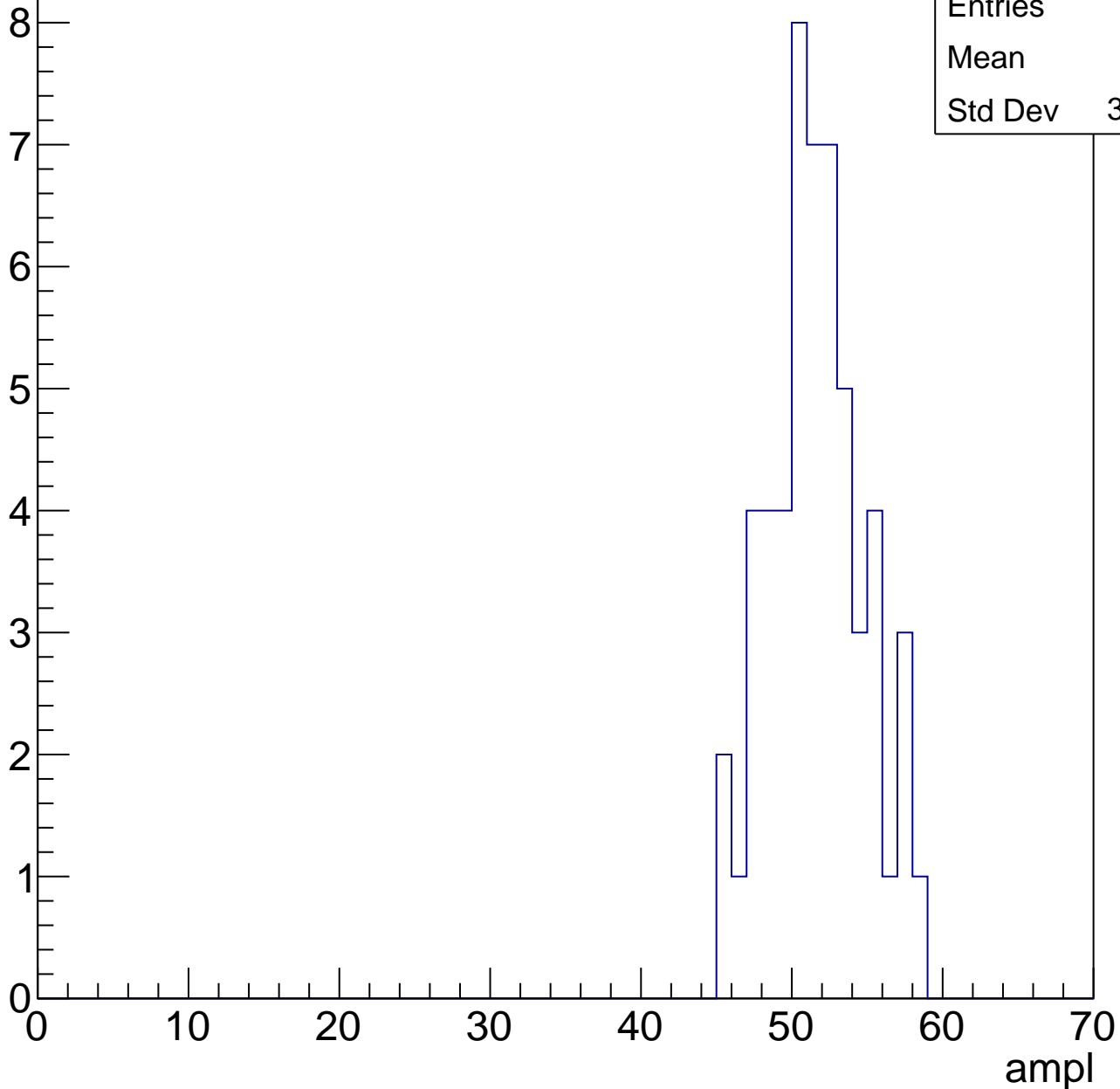


# B1L002S, U8-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.2
Std Dev	3.099

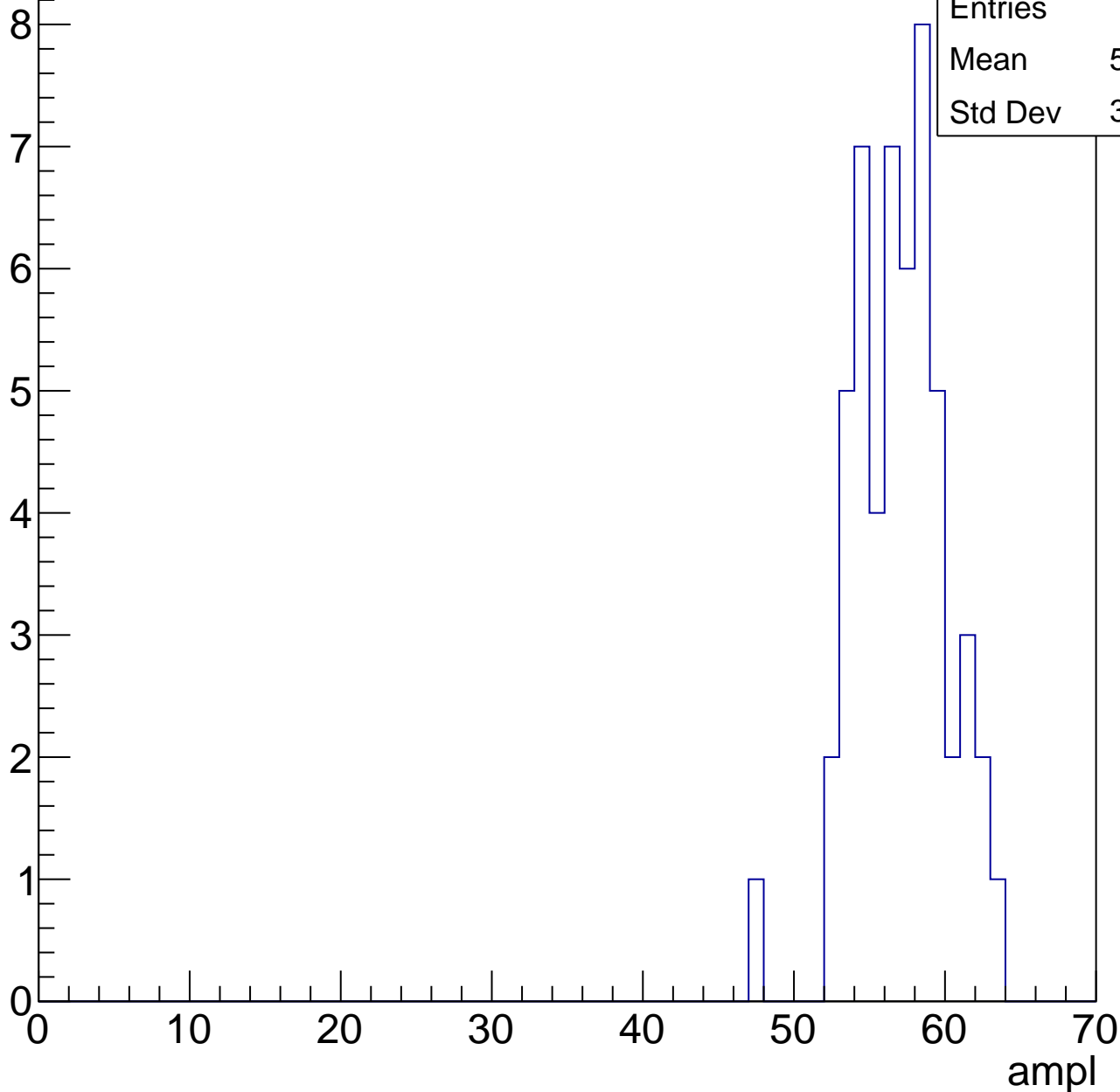


# B1L002S, U8-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	56.55
Std Dev	3.025



# B1L002S, U8-ch116, adc6

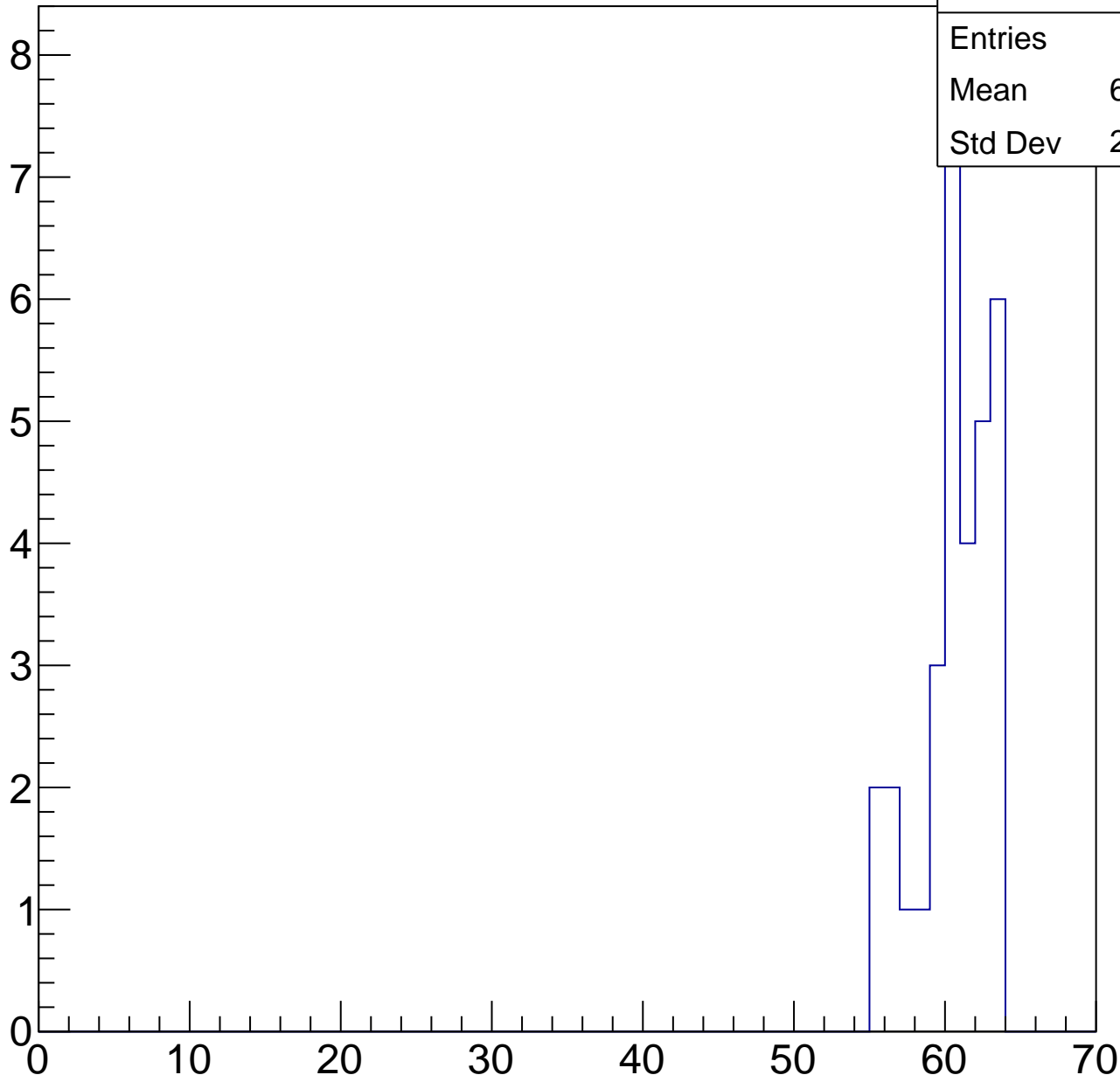
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	60.19
Std Dev	2.338

ampl

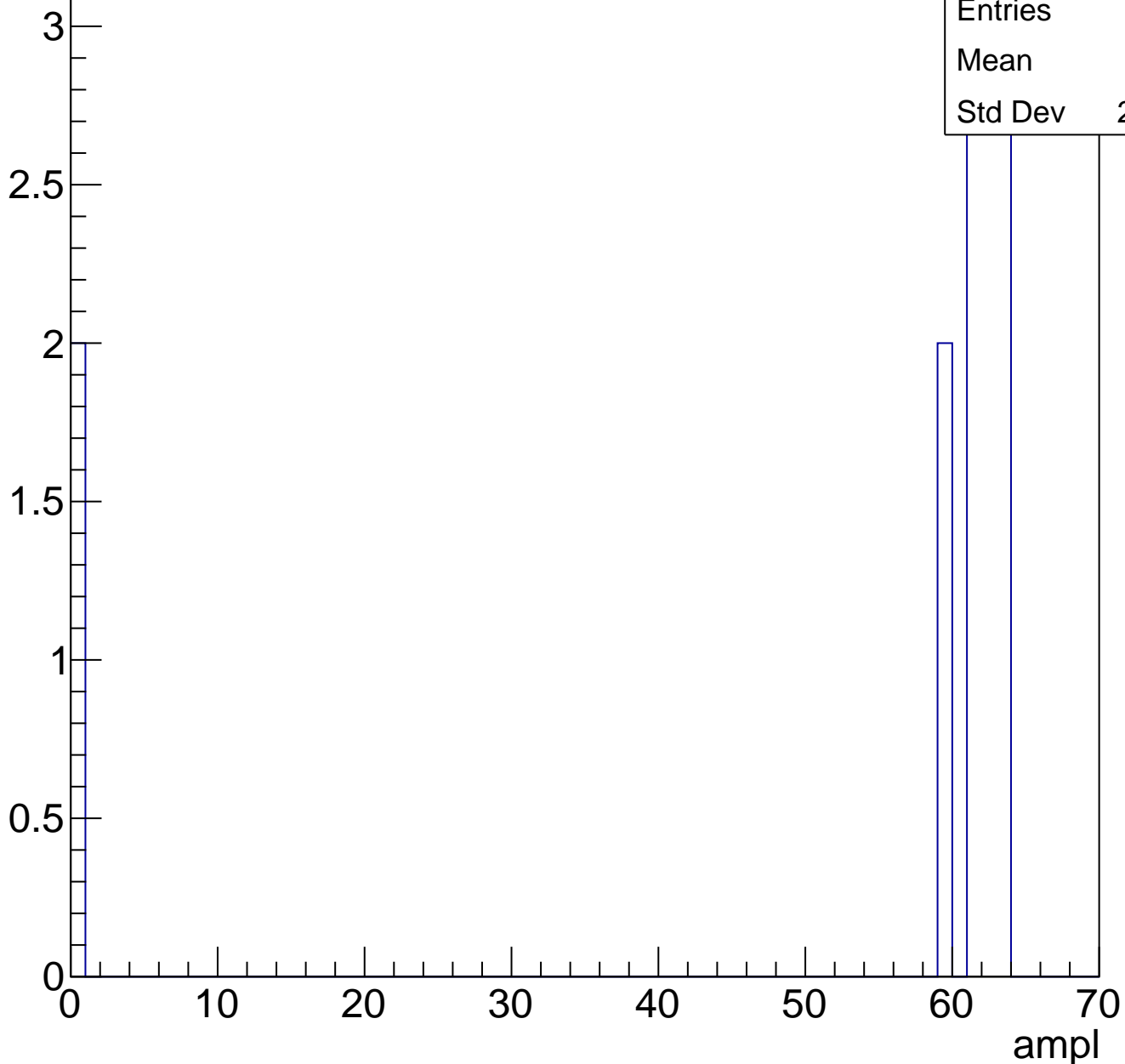




# B1L002S, U8-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch117, adc0

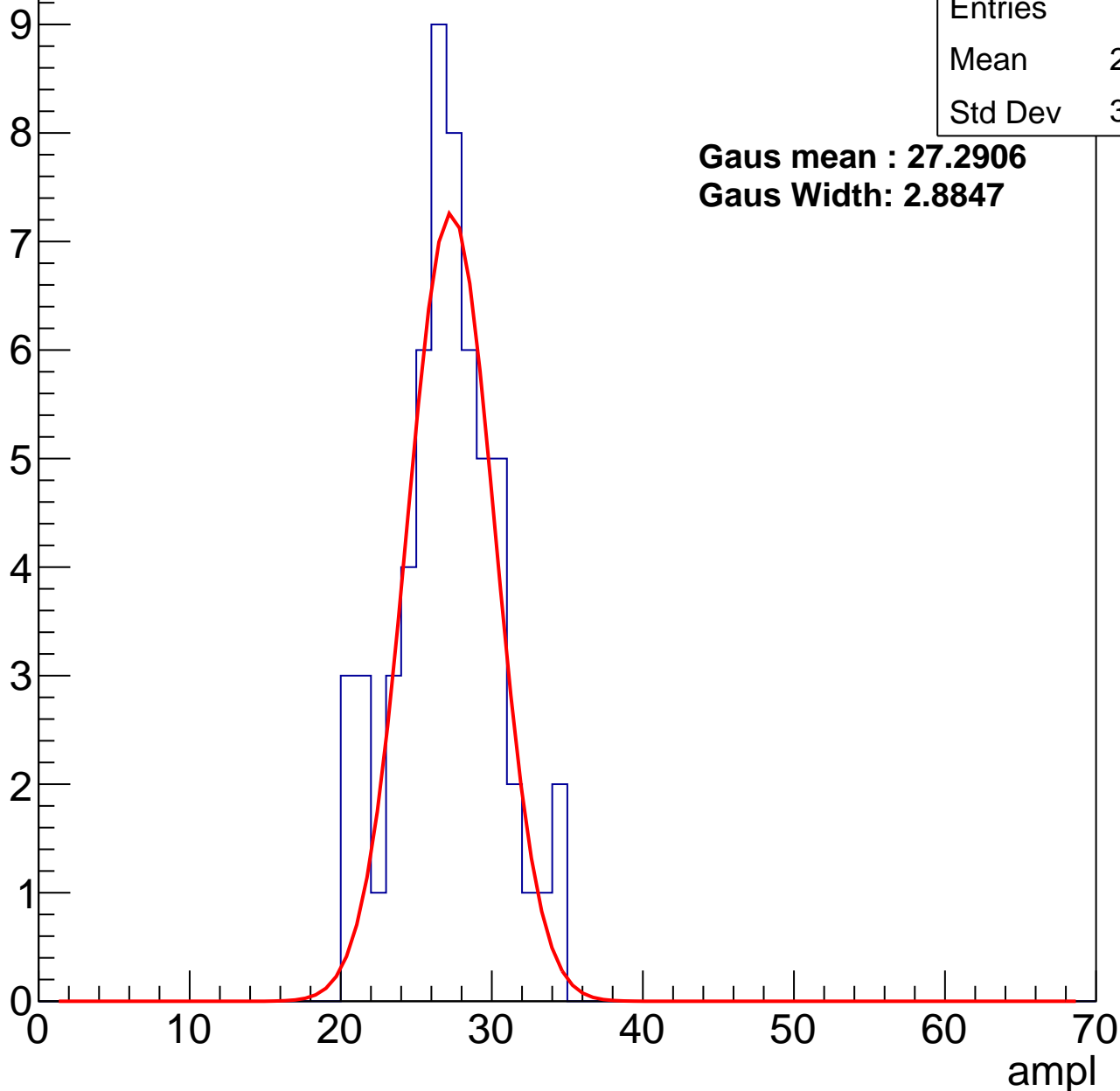
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	26.58
Std Dev	3.315

**Gaus mean : 27.2906**

**Gaus Width: 2.8847**



# B1L002S, U8-ch117, adc1

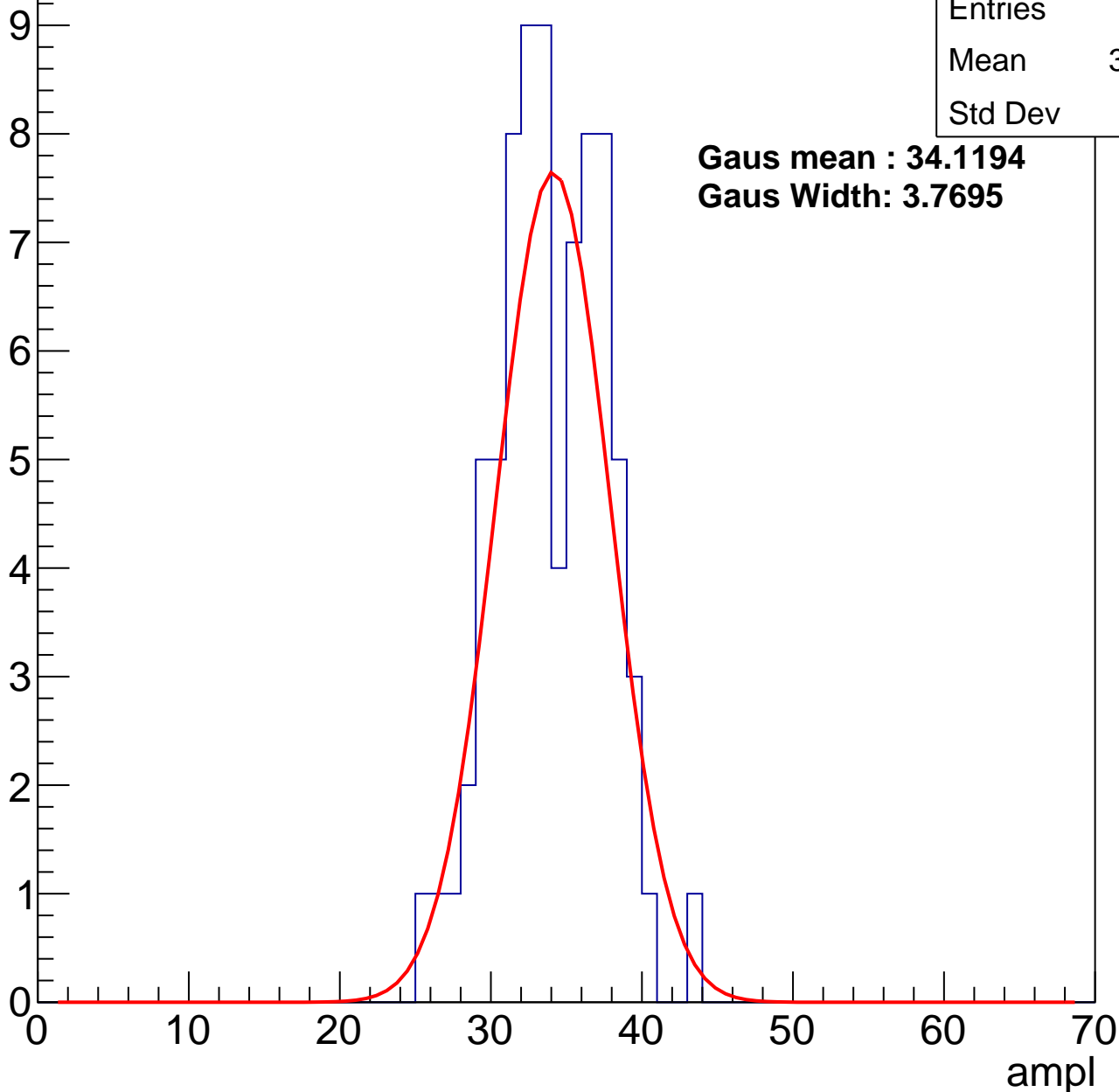
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	33.55
Std Dev	3.5

**Gaus mean : 34.1194**

**Gaus Width: 3.7695**



# B1L002S, U8-ch117, adc2

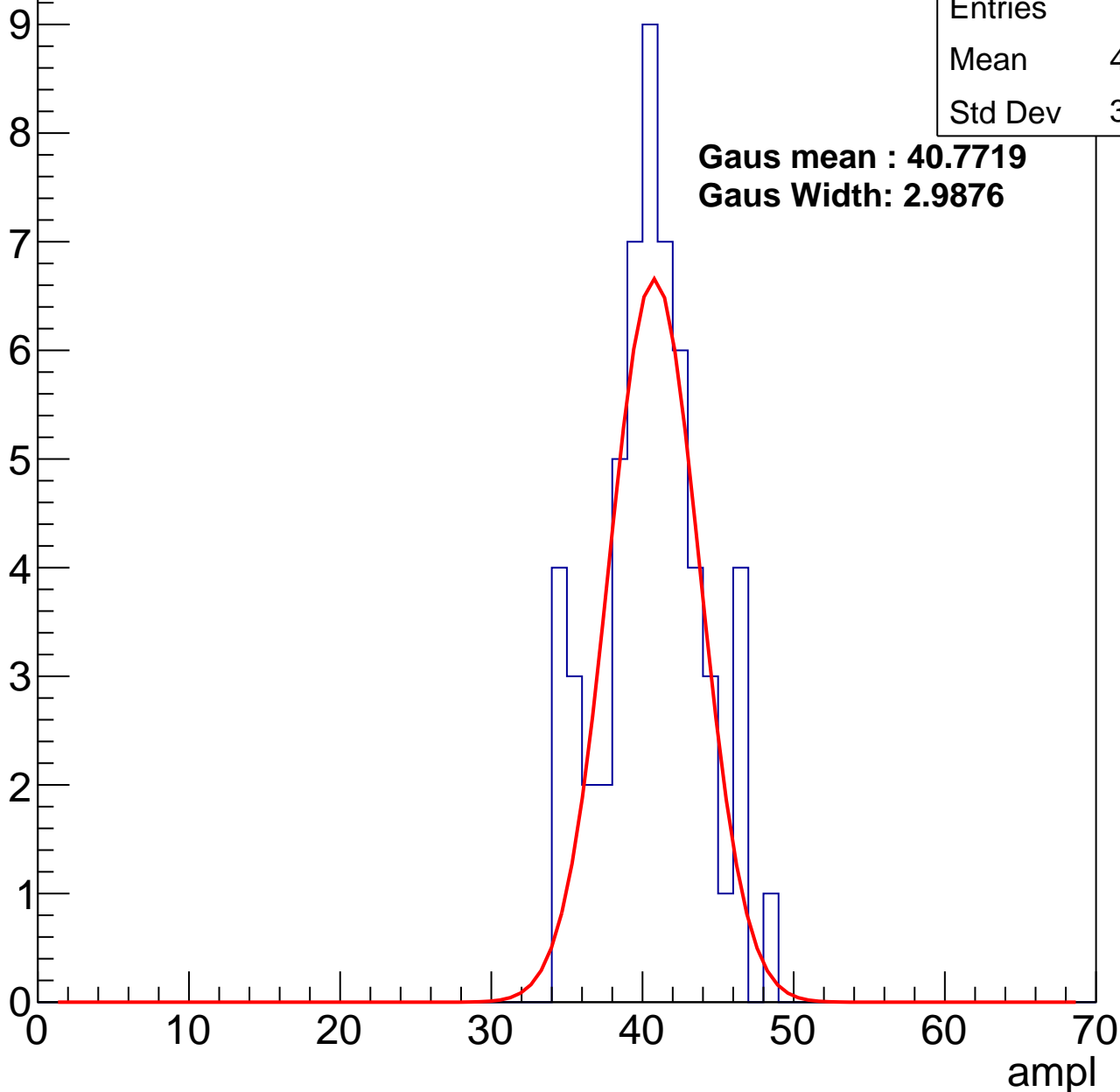
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.17
Std Dev	3.328

**Gaus mean : 40.7719**

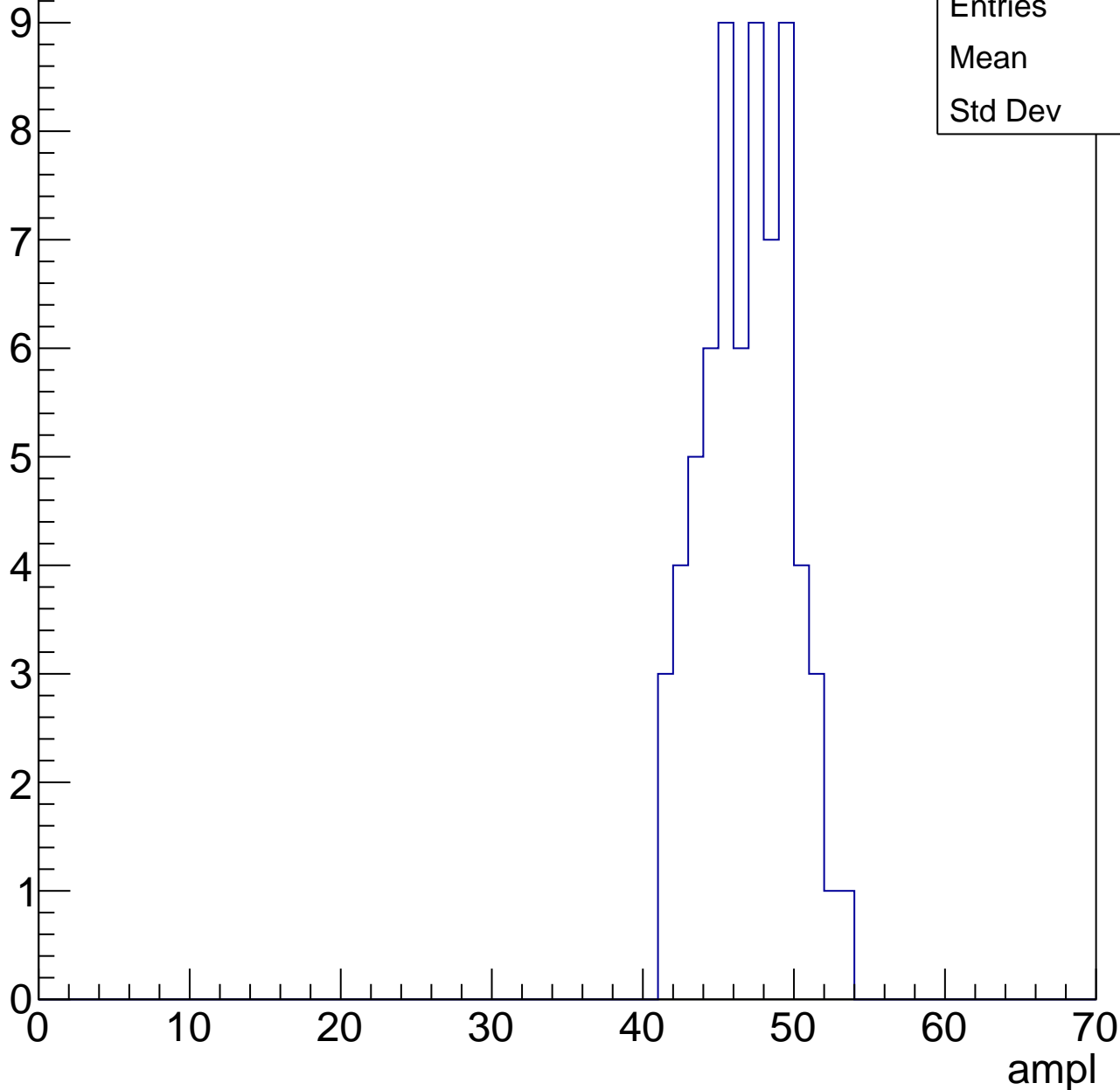
**Gaus Width: 2.9876**



# B1L002S, U8-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



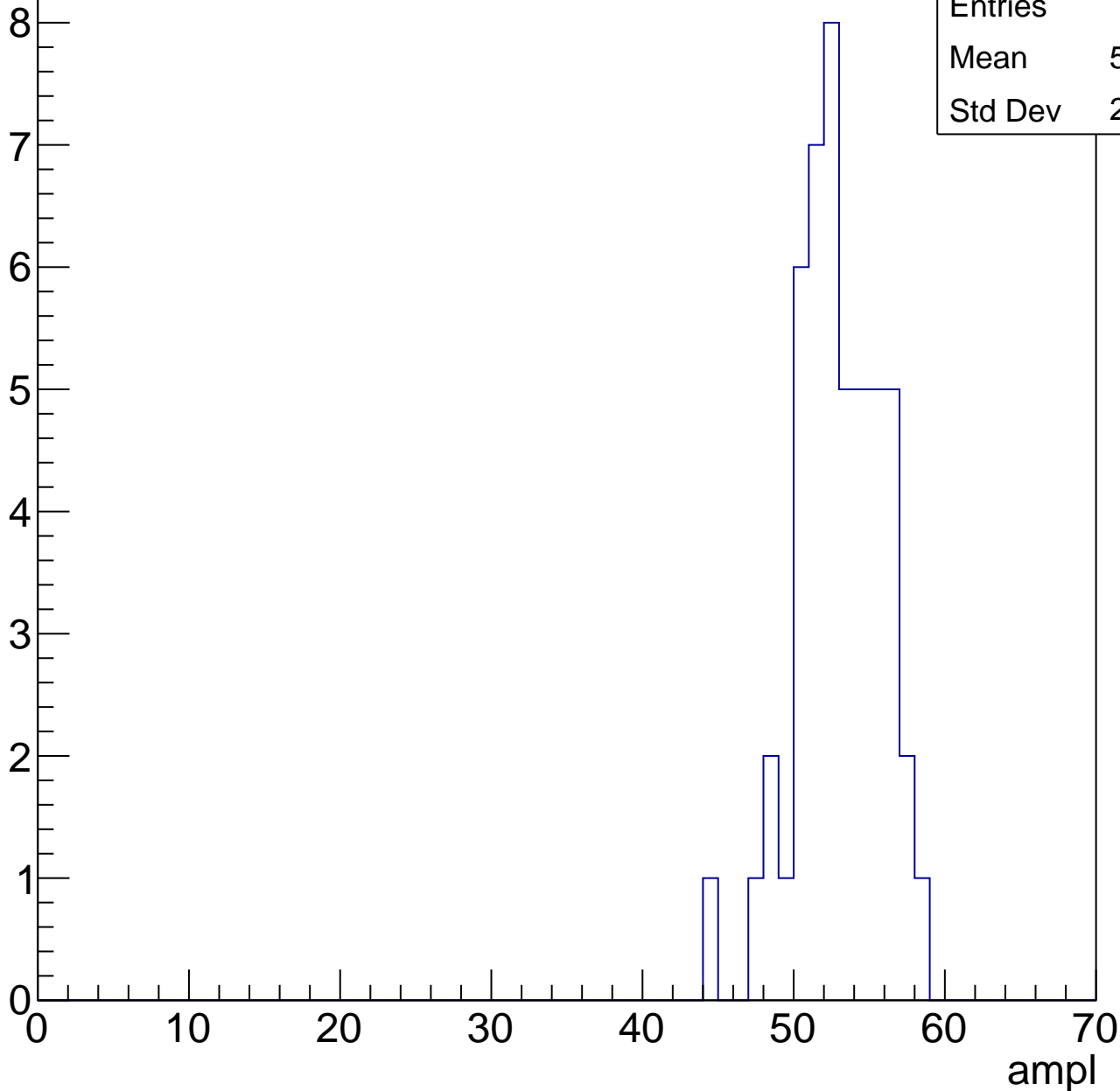
Entries	67
Mean	46.4
Std Dev	2.86

# B1L002S, U8-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.47
Std Dev	2.807

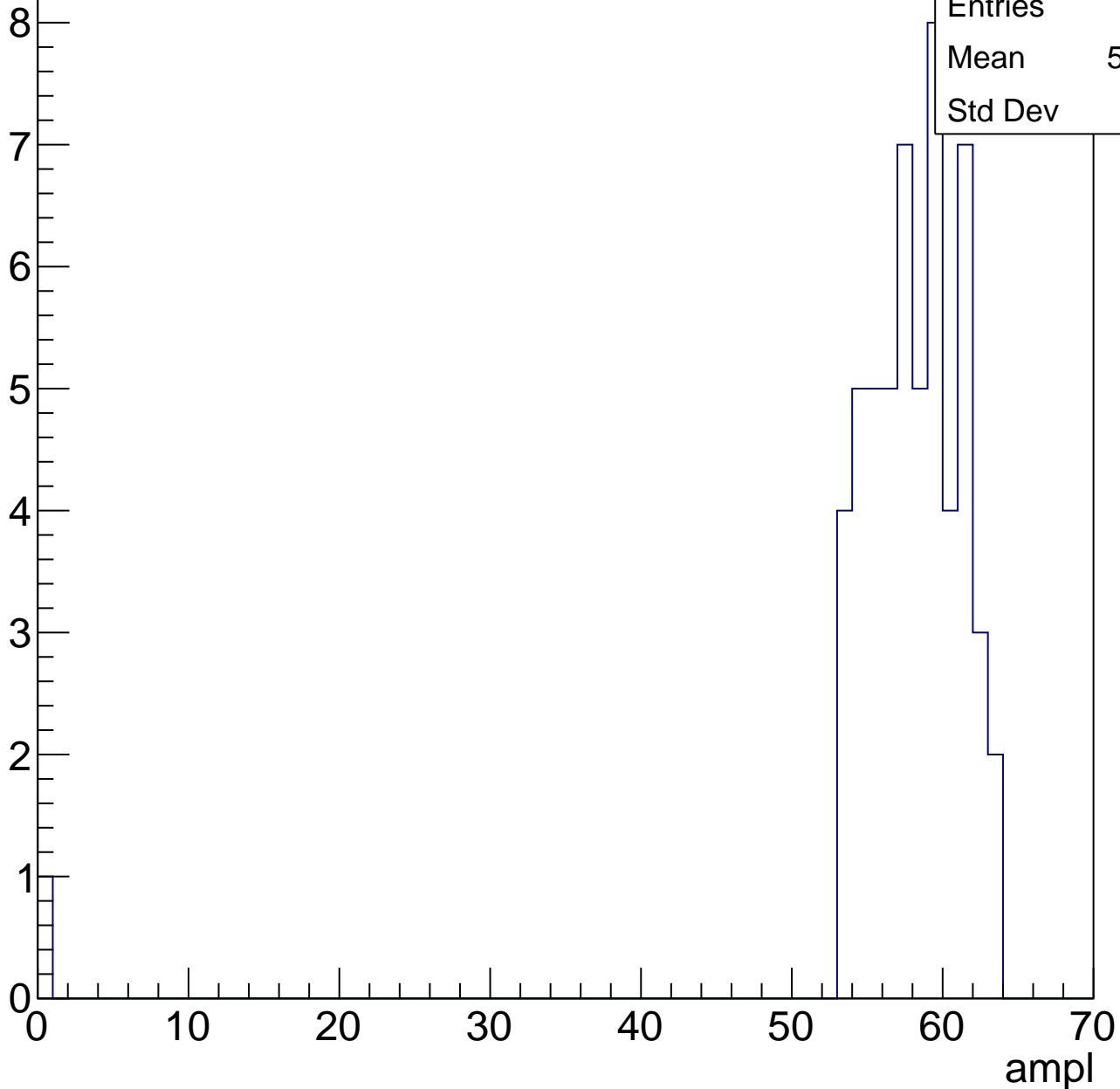


# B1L002S, U8-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.73
Std Dev	8.14

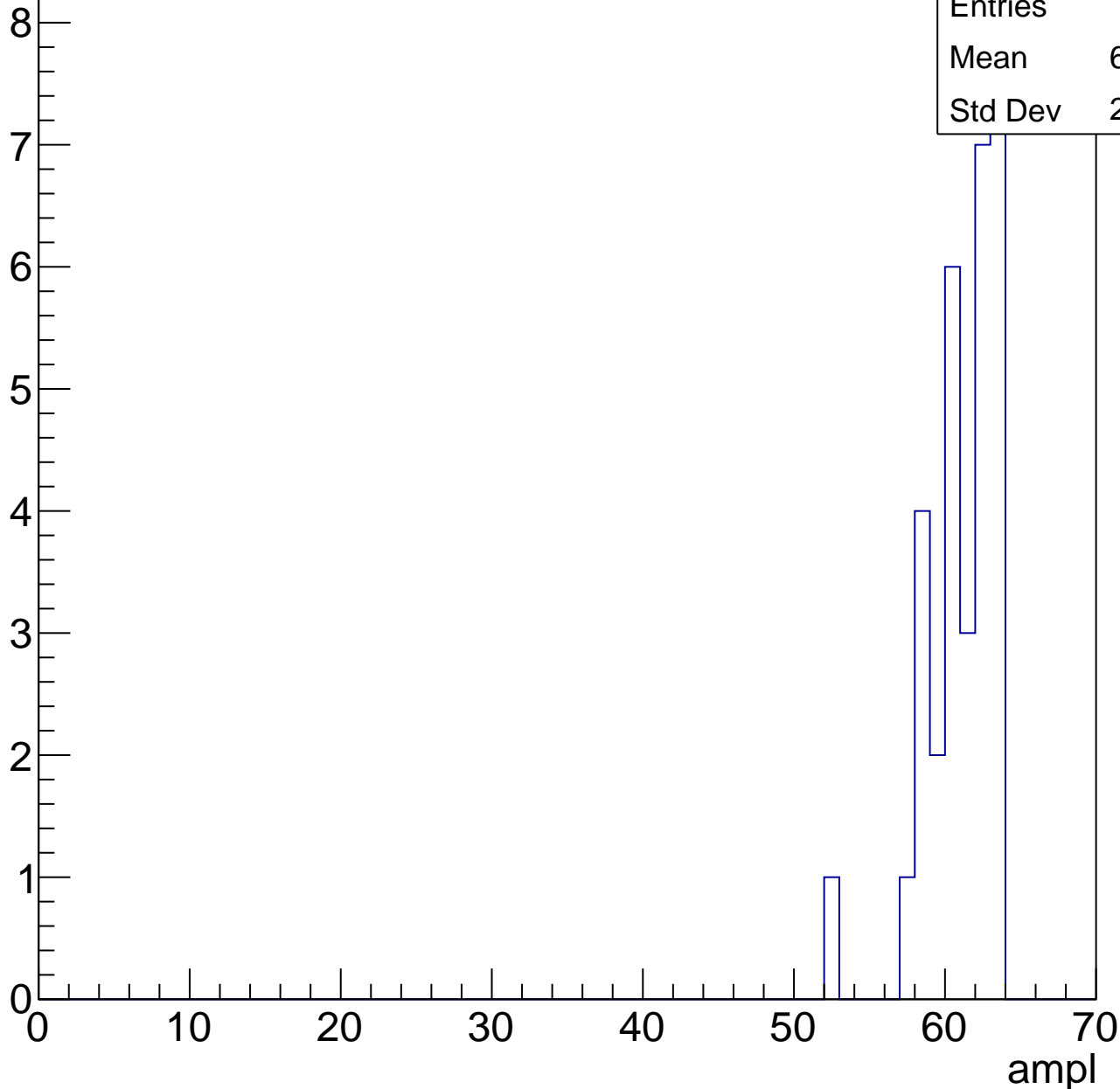


# B1L002S, U8-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	60.62
Std Dev	2.382





# B1L002S, U8-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U8-ch118, adc0

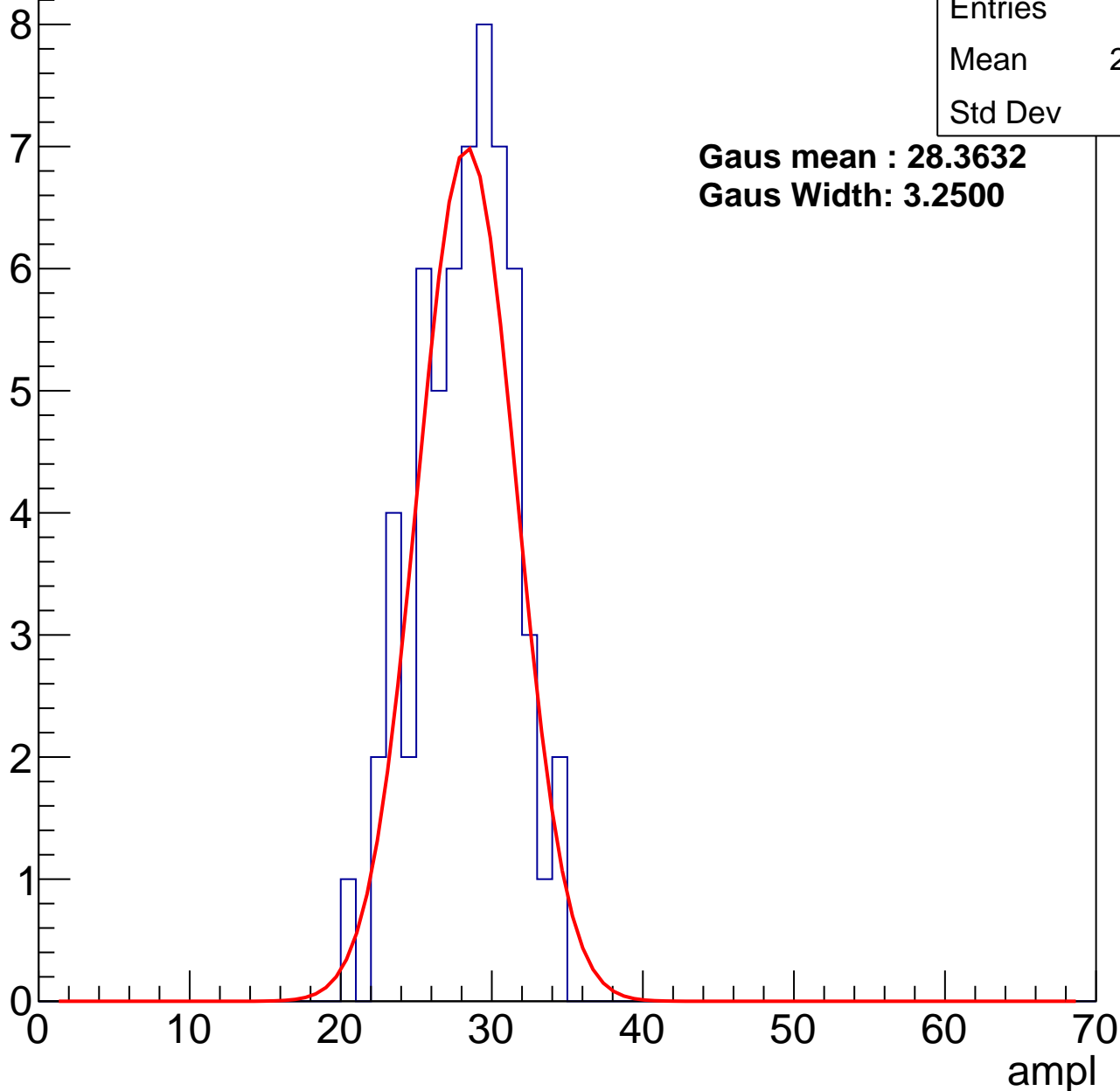
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.78
Std Dev	3.11

**Gaus mean : 28.3632**

**Gaus Width: 3.2500**



# B1L002S, U8-ch118, adc1

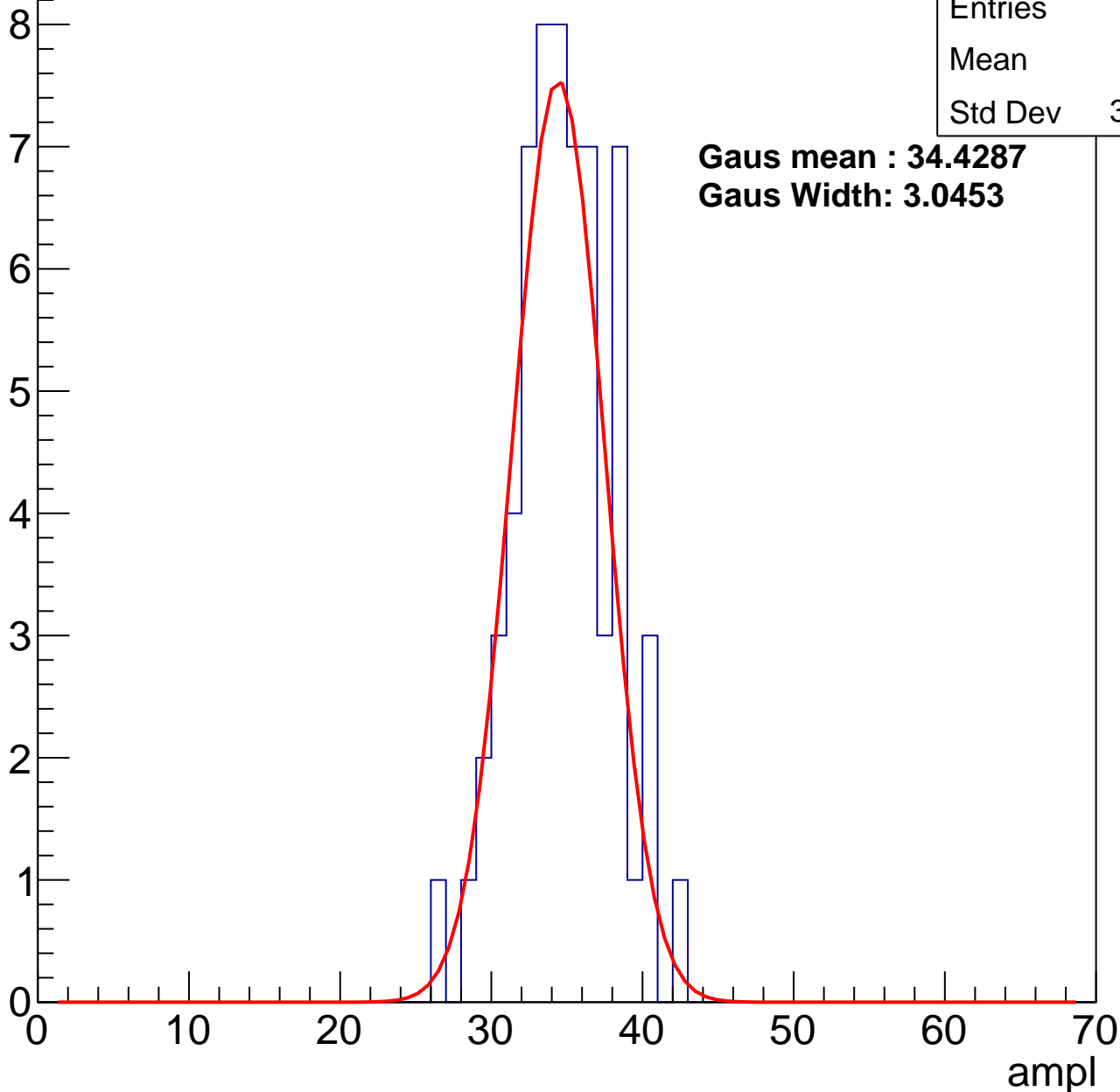
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.3
Std Dev	3.175

**Gaus mean : 34.4287**

**Gaus Width: 3.0453**



# B1L002S, U8-ch118, adc2

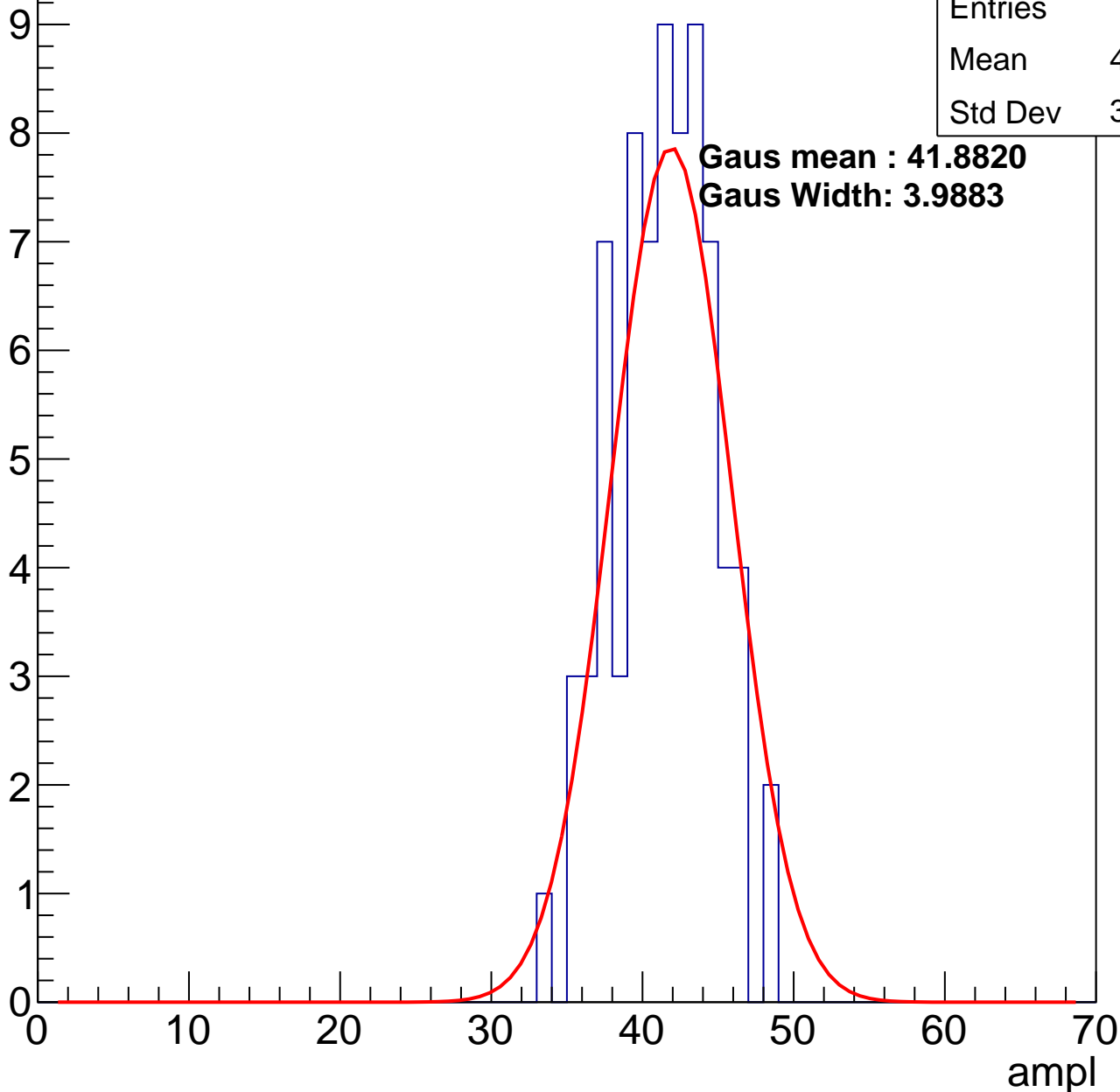
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	40.95
Std Dev	3.253

**Gaus mean : 41.8820**

**Gaus Width: 3.9883**

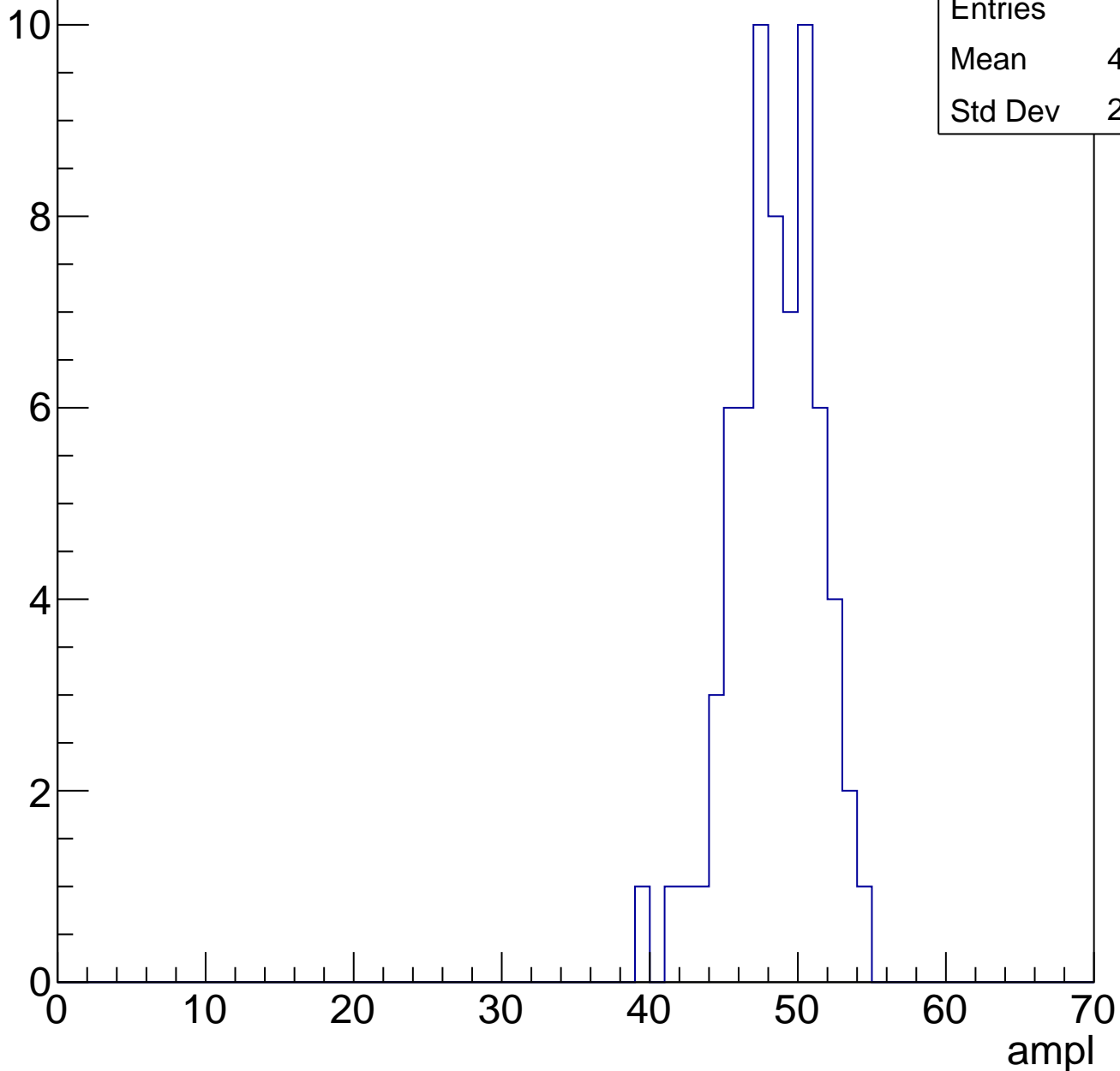


# B1L002S, U8-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

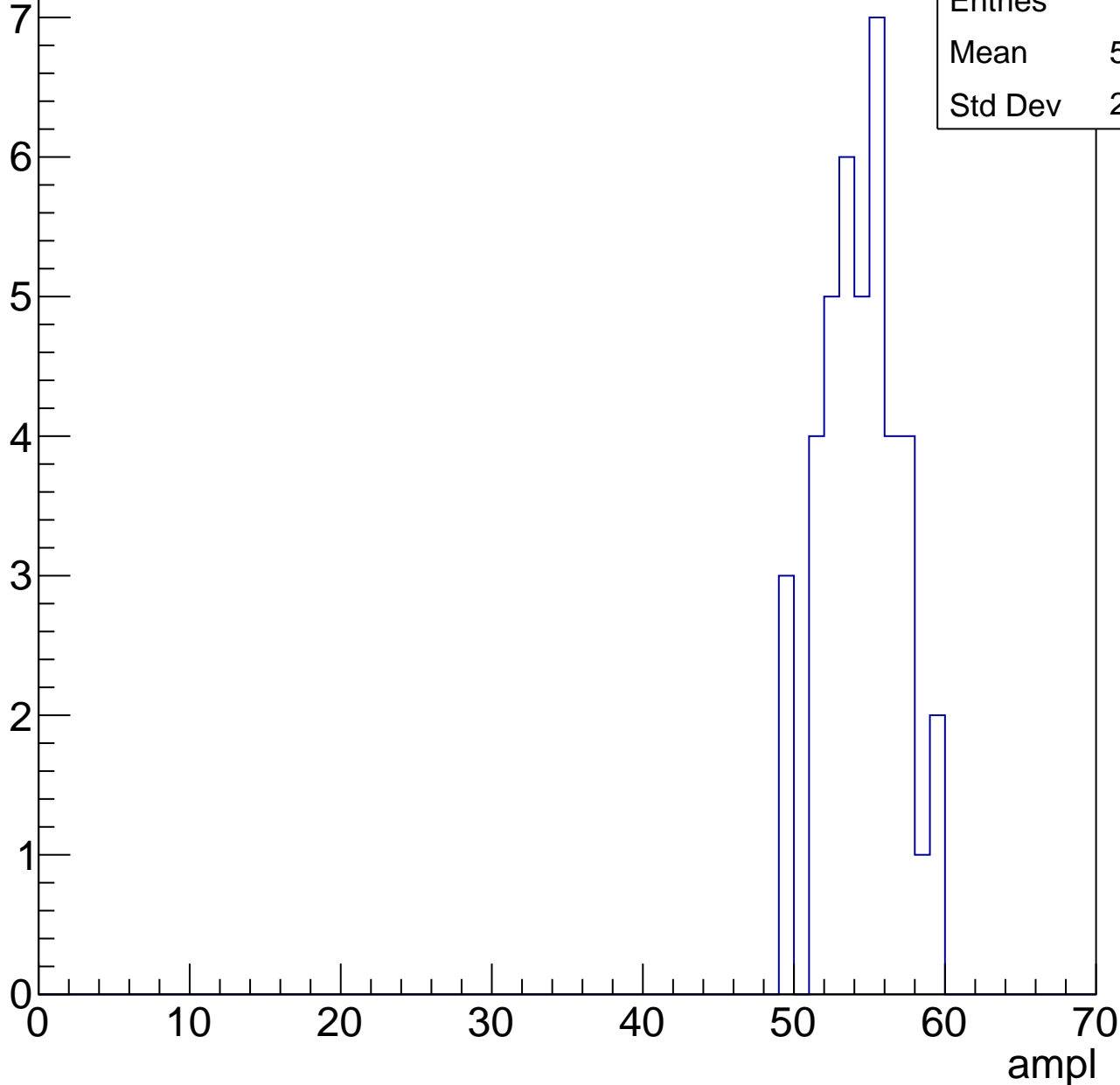
Entries	67
Mean	47.97
Std Dev	2.937



# B1L002S, U8-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

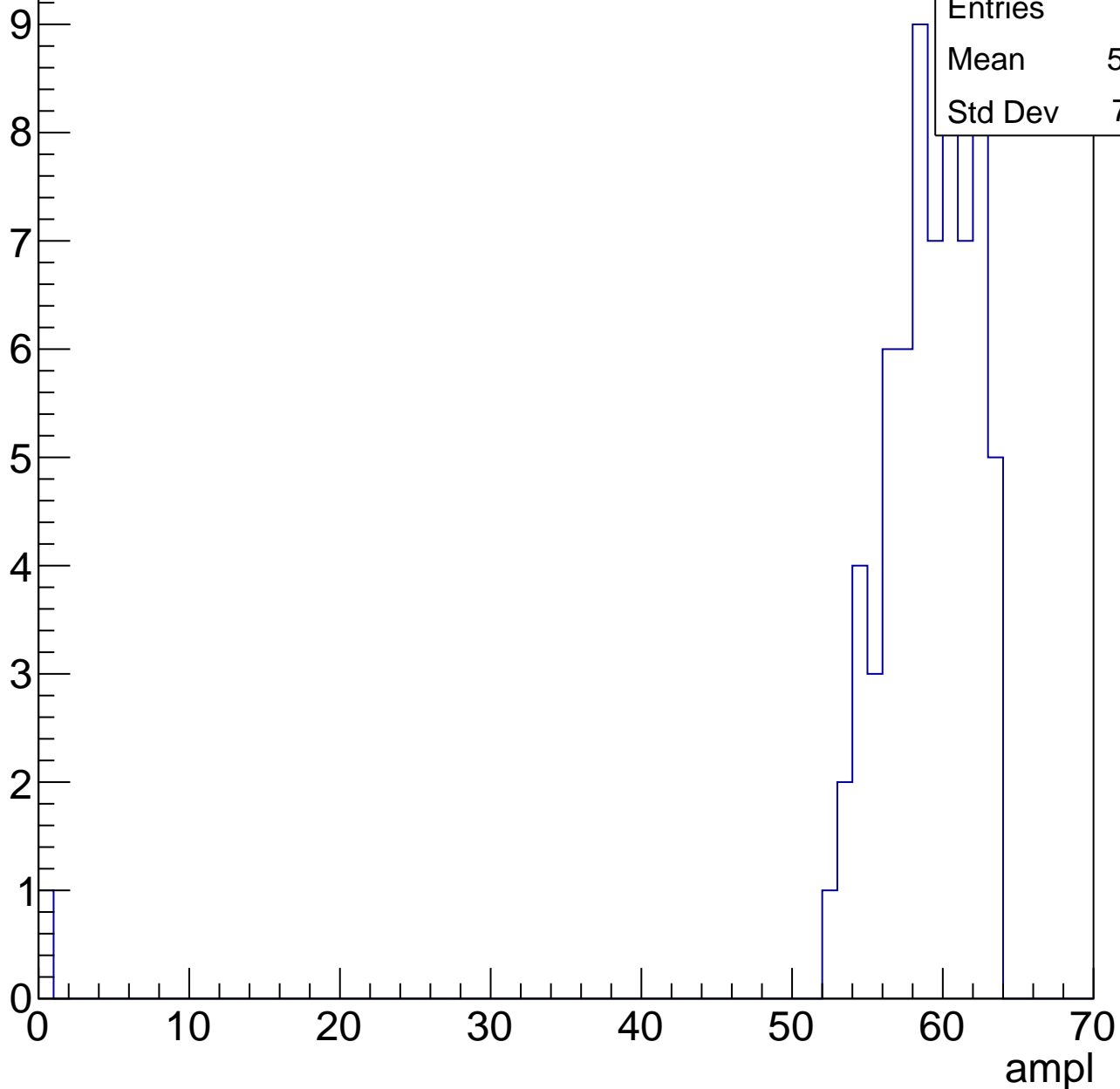


Entries	41
Mean	53.95
Std Dev	2.527

# B1L002S, U8-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

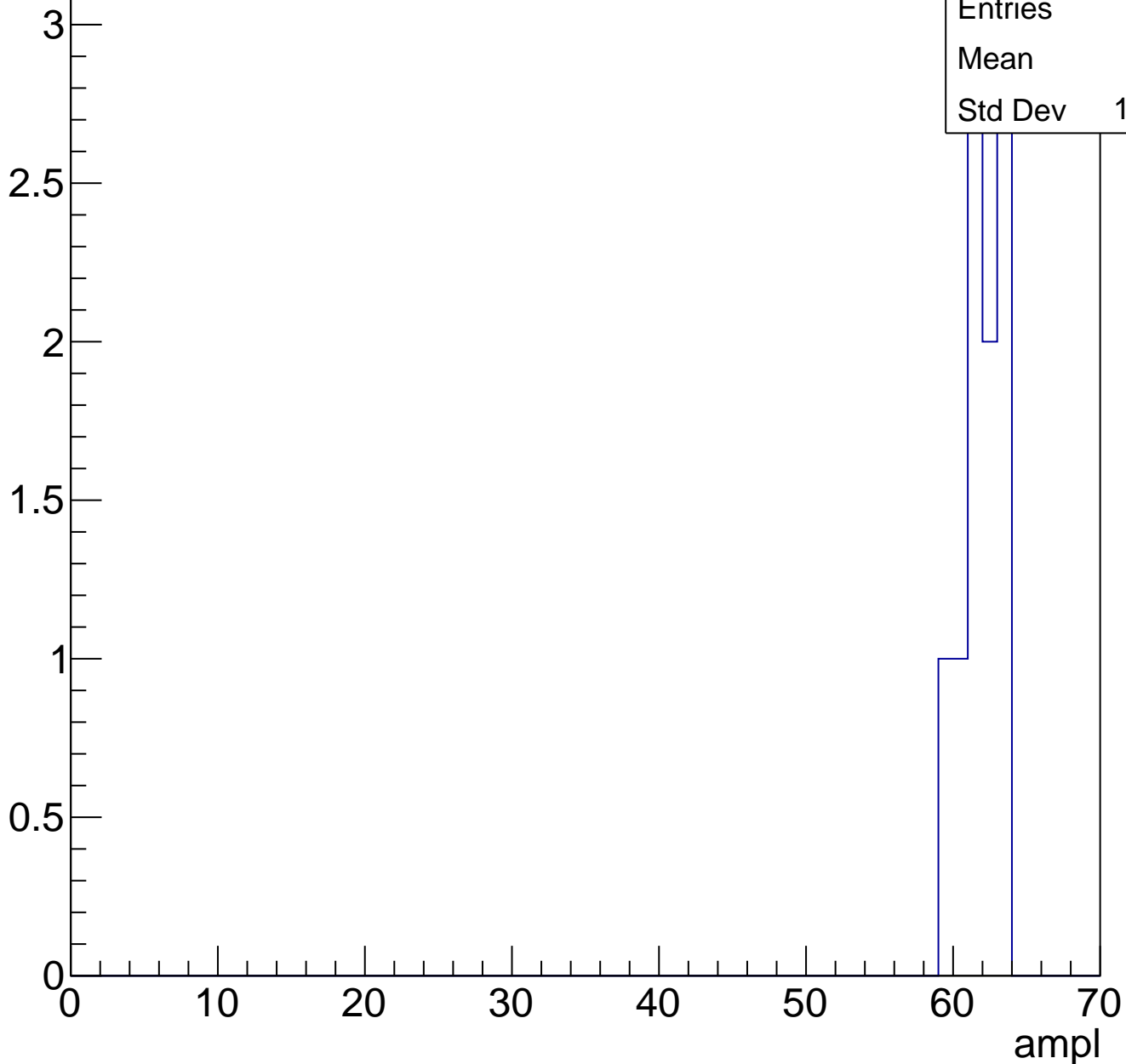
Entry



# B1L002S, U8-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

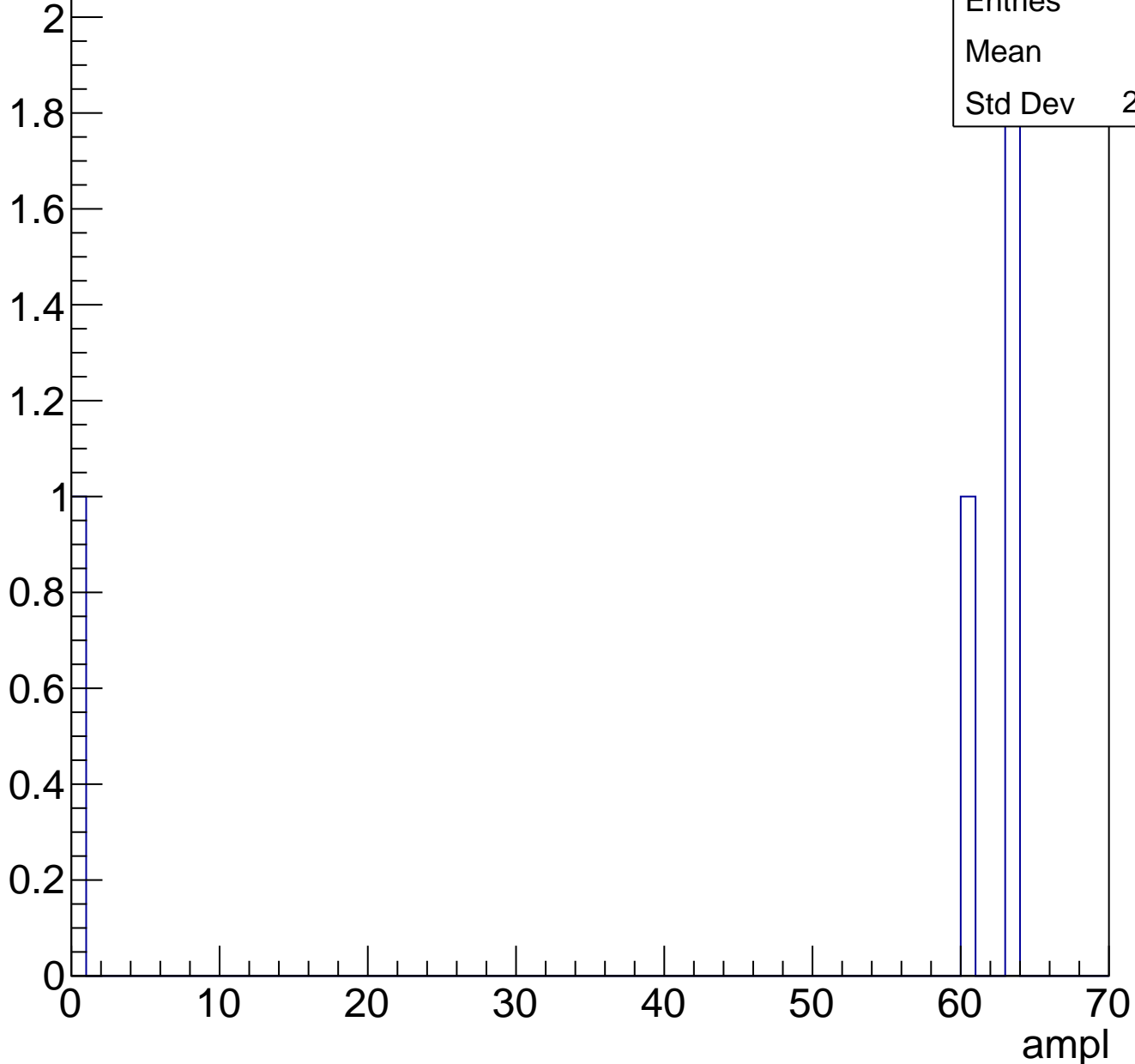




# B1L002S, U8-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	28.41
Std Dev	2.986

**Gaus mean : 28.8927**

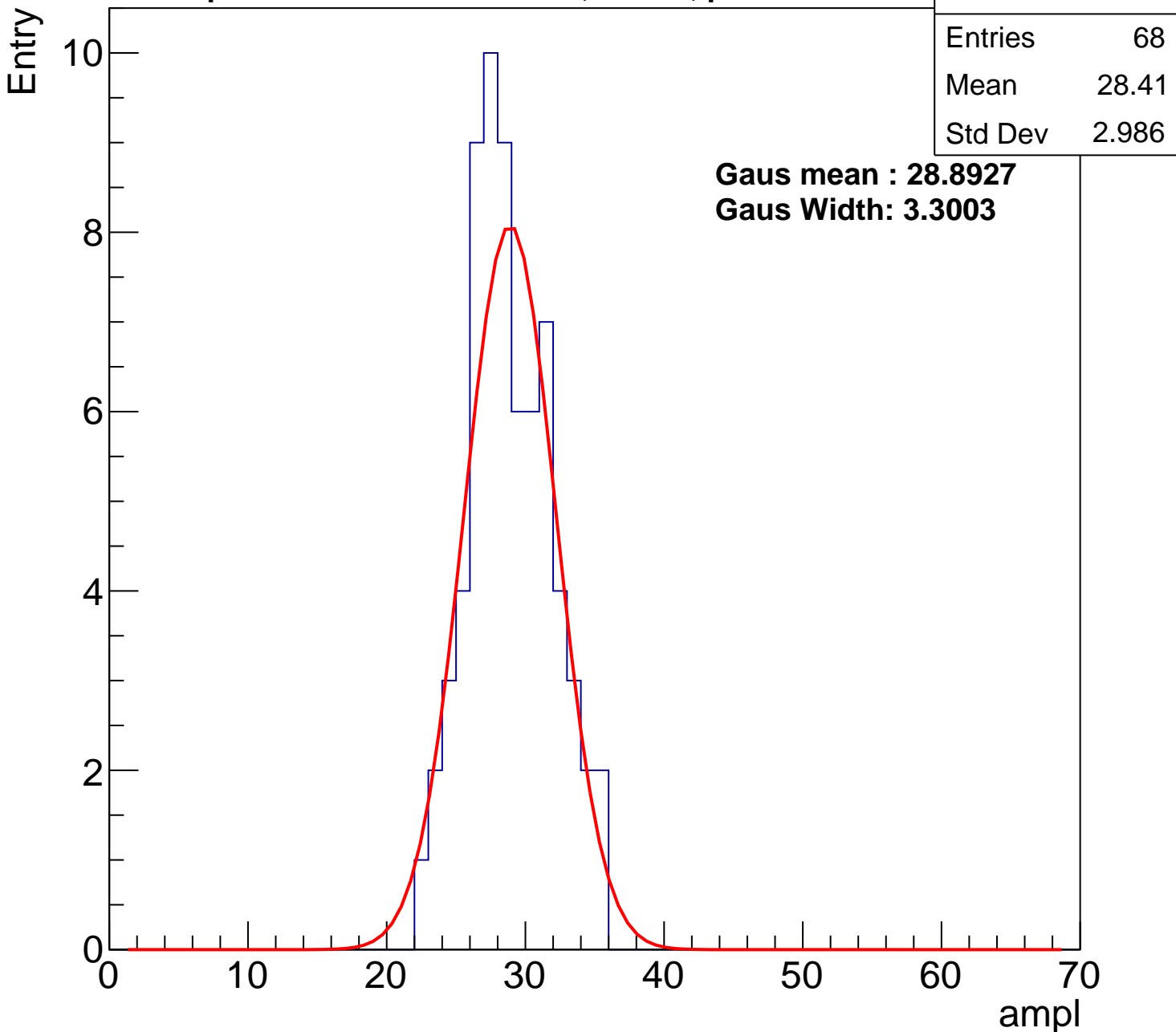
**Gaus Width: 3.3003**

Entry

10  
8  
6  
4  
2  
0

ampl

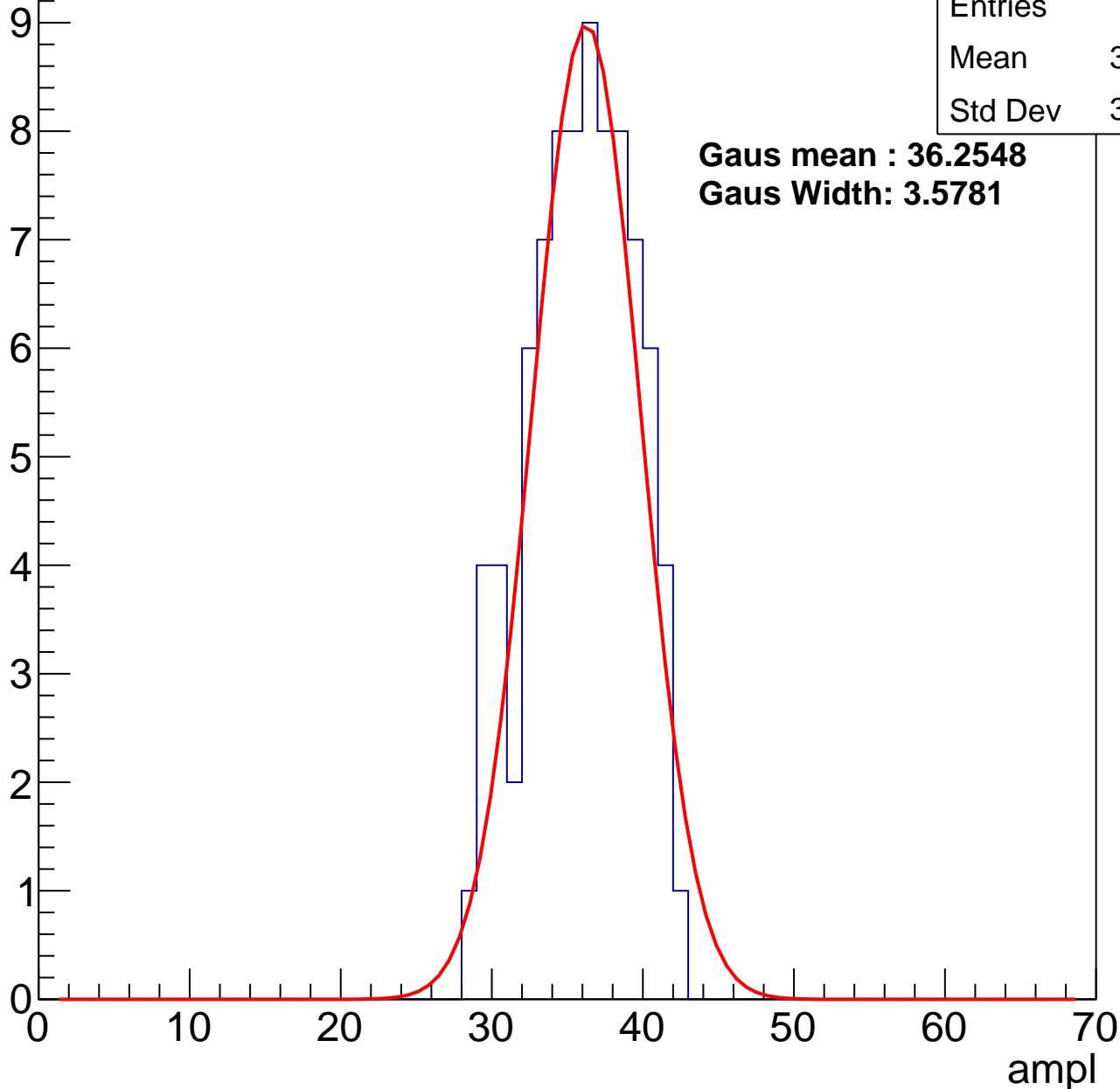
0 10 20 30 40 50 60 70



# B1L002S, U8-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch119, adc2

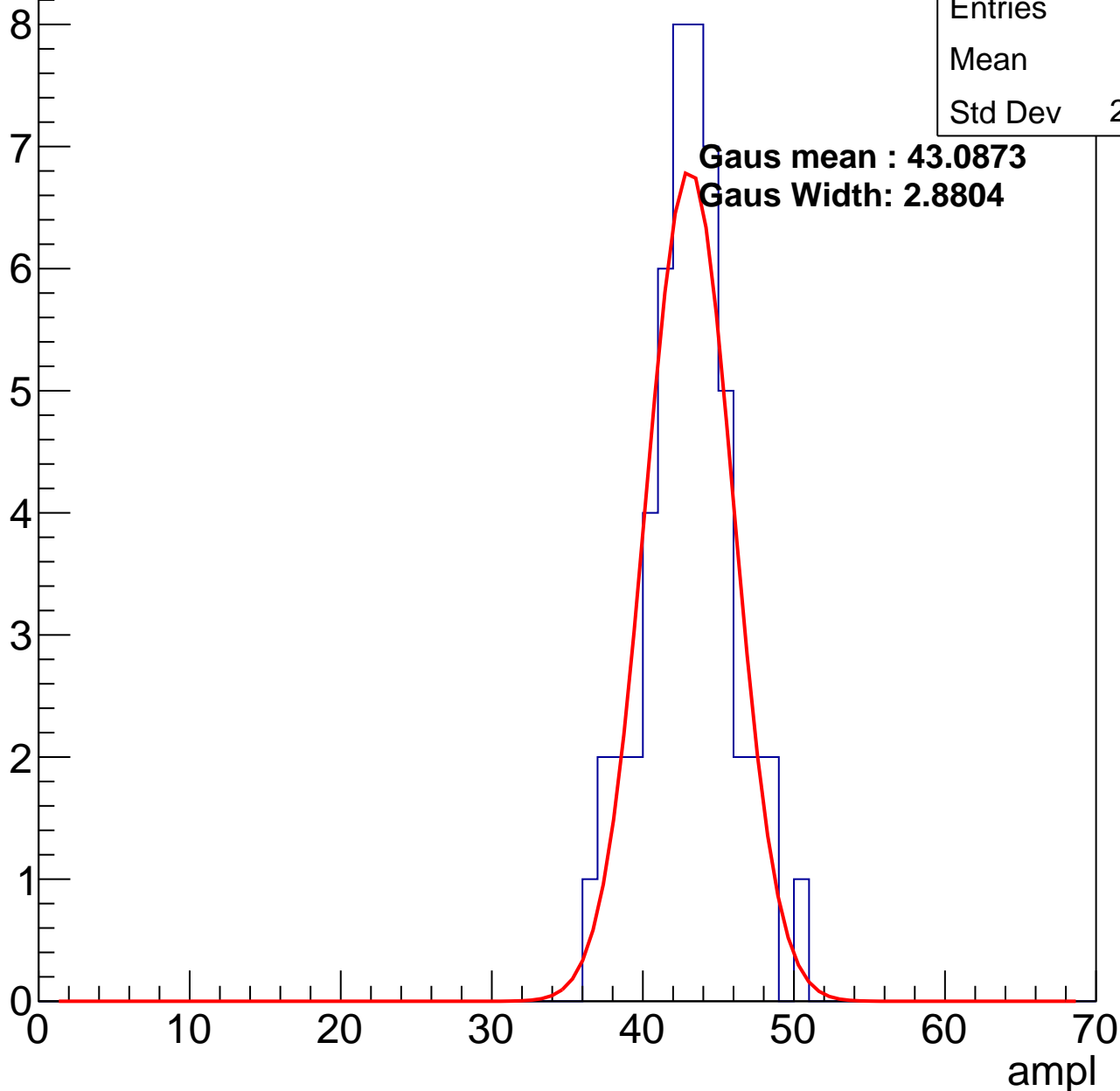
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	42.6
Std Dev	2.904

**Gaus mean : 43.0873**

**Gaus Width: 2.8804**

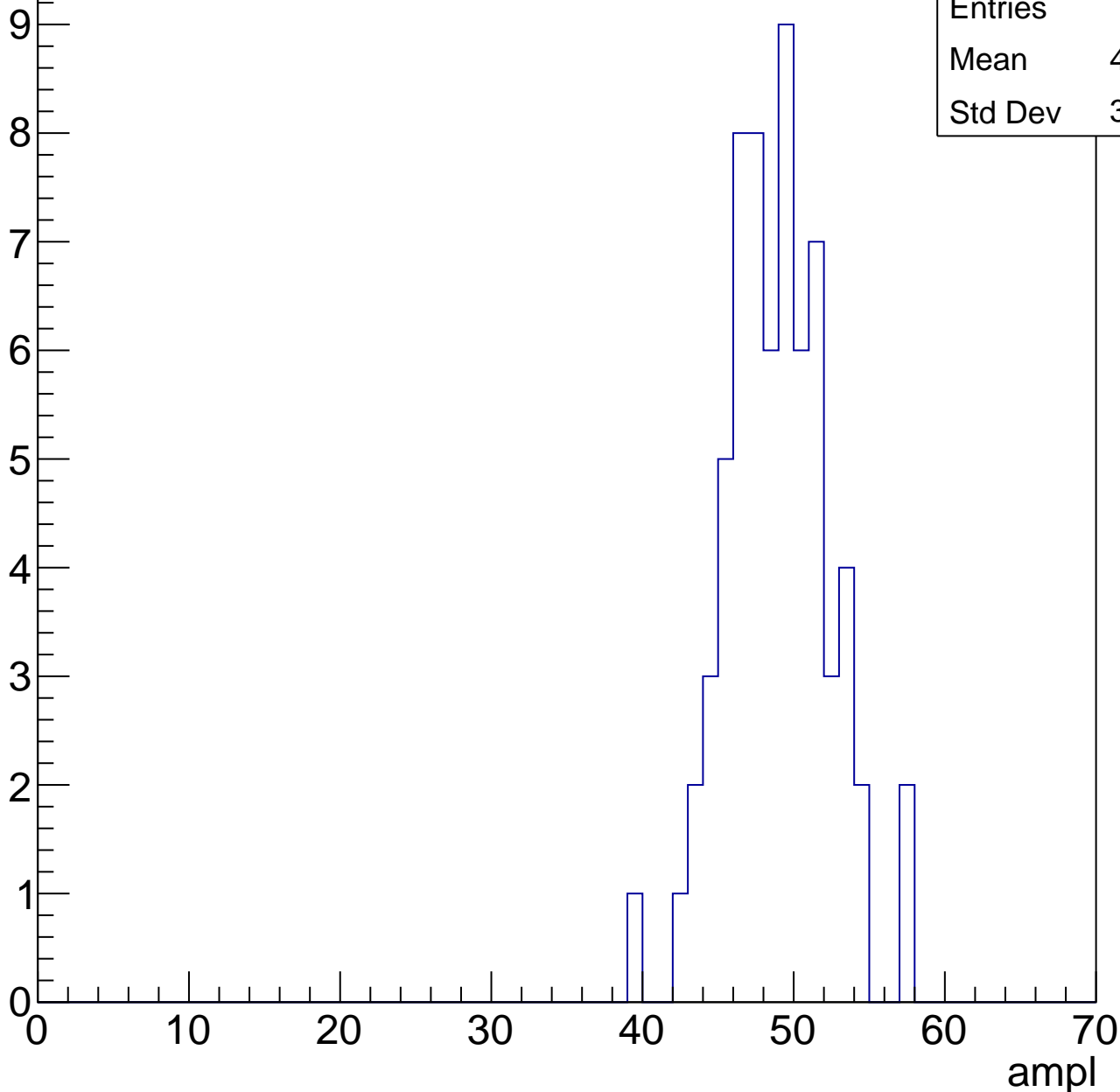


# B1L002S, U8-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.42
Std Dev	3.382

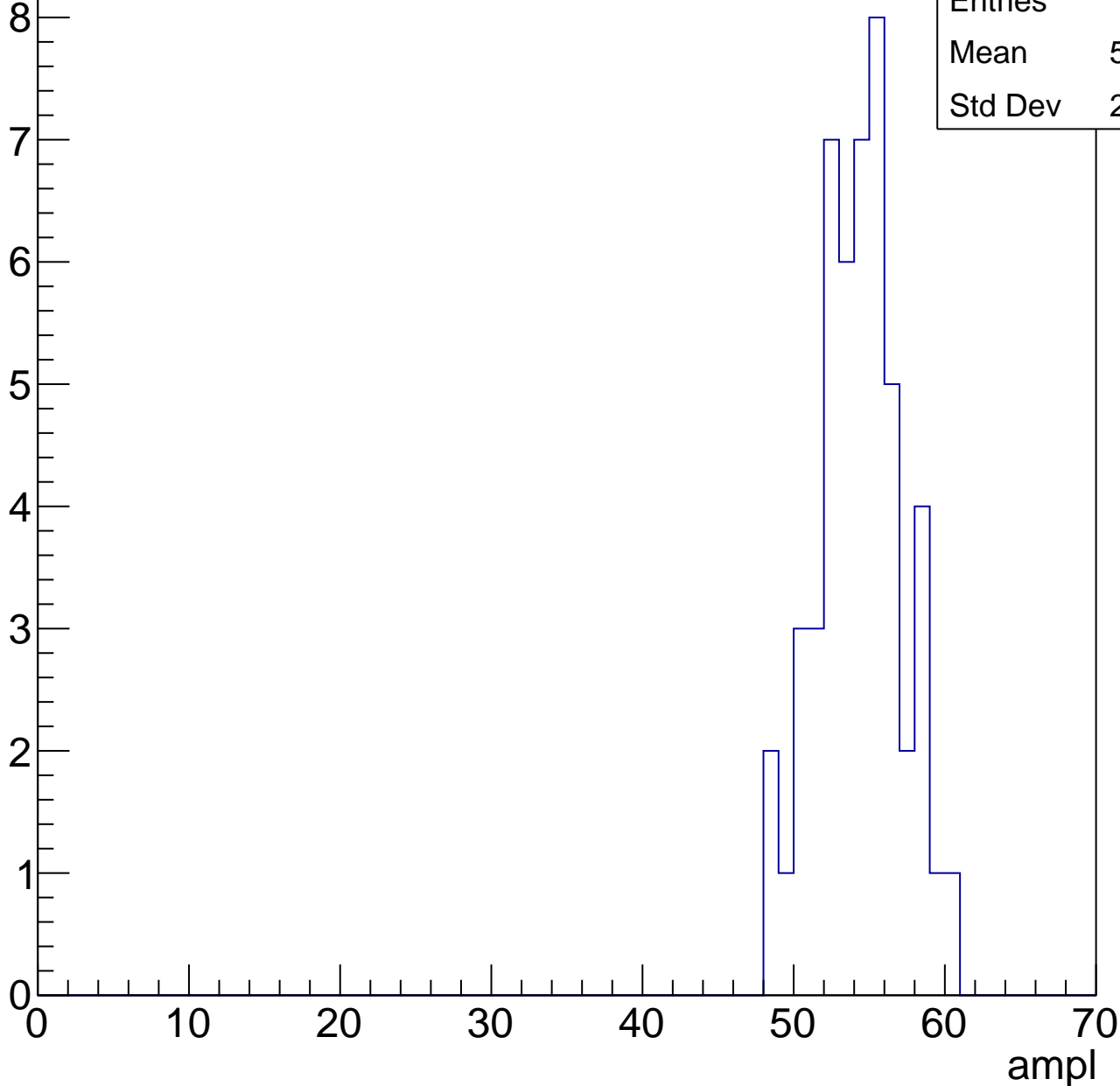


# B1L002S, U8-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

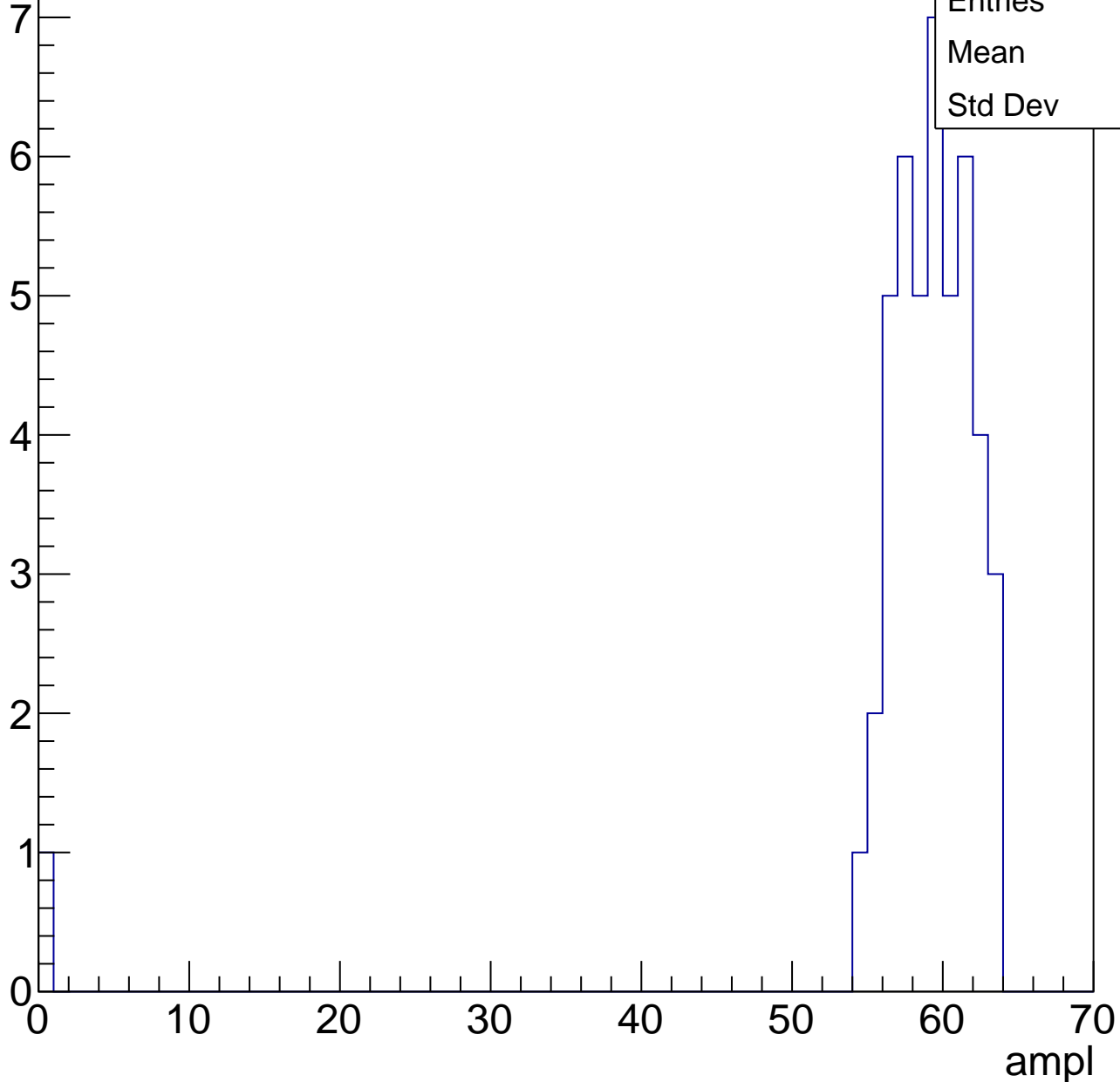
Entries	50
Mean	53.86
Std Dev	2.742



# B1L002S, U8-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

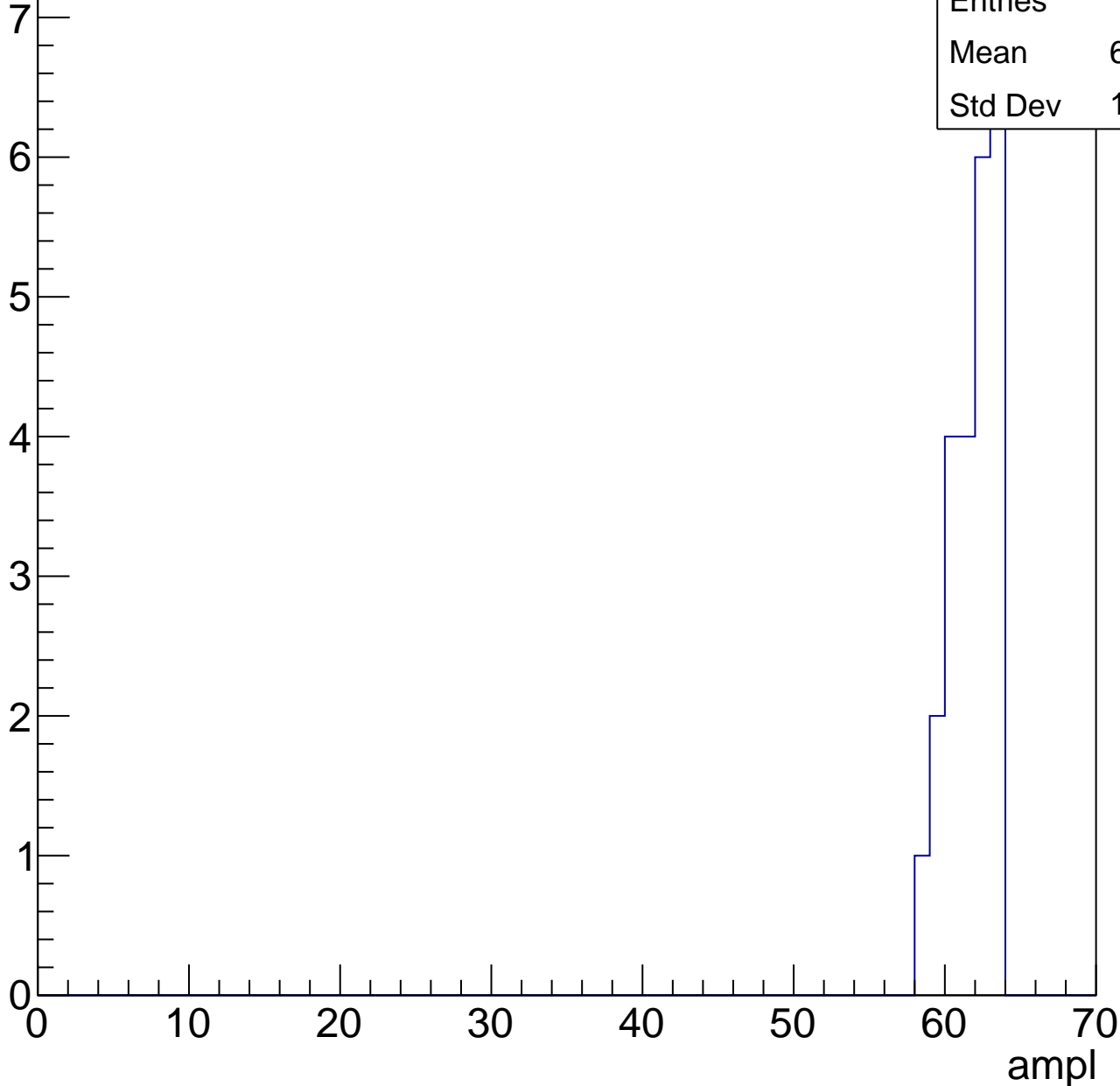


# B1L002S, U8-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	61.38
Std Dev	1.467





# B1L002S, U8-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch120, adc0

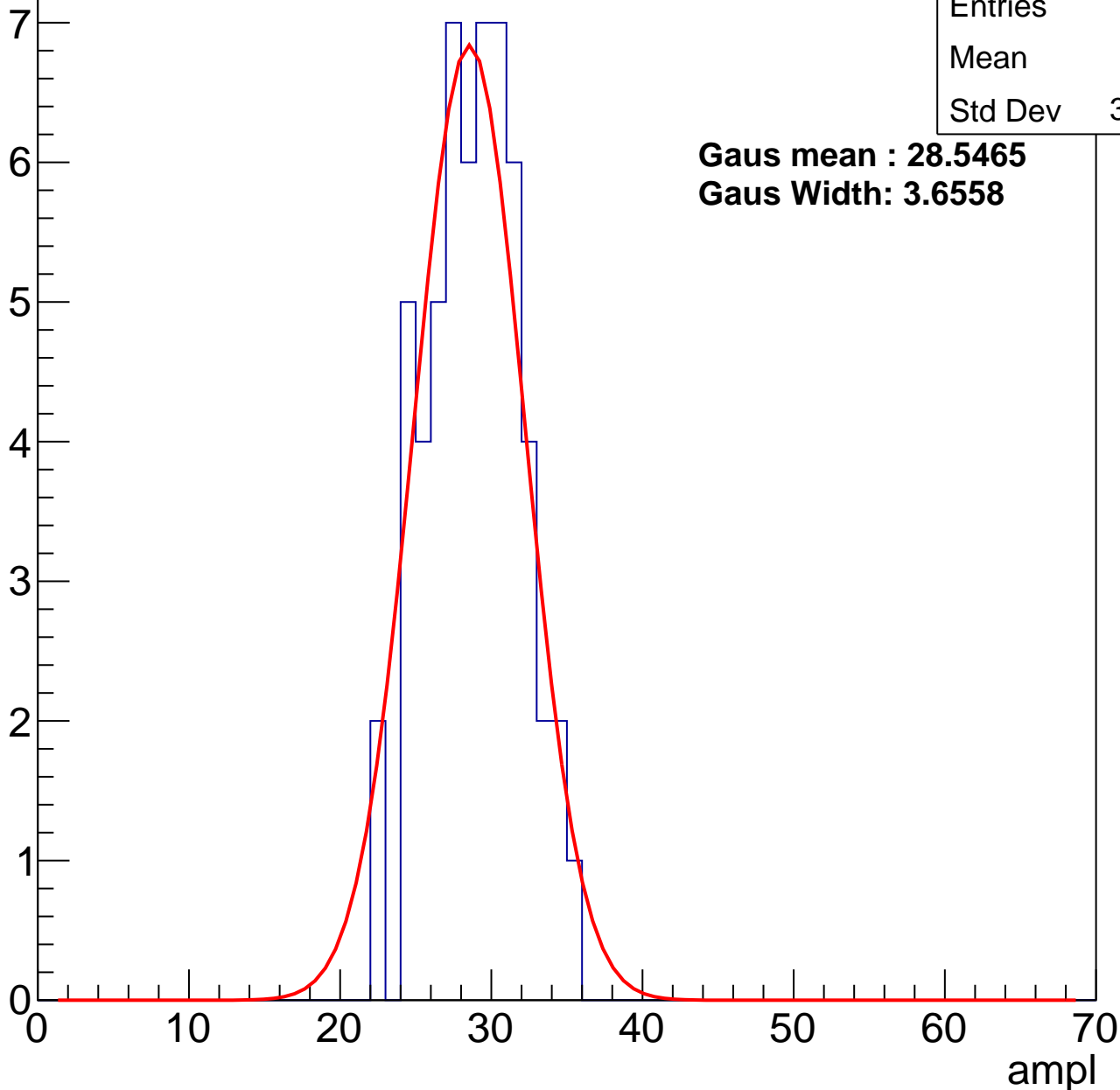
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	28.4
Std Dev	3.023

**Gaus mean : 28.5465**

**Gaus Width: 3.6558**



# B1L002S, U8-ch120, adc1

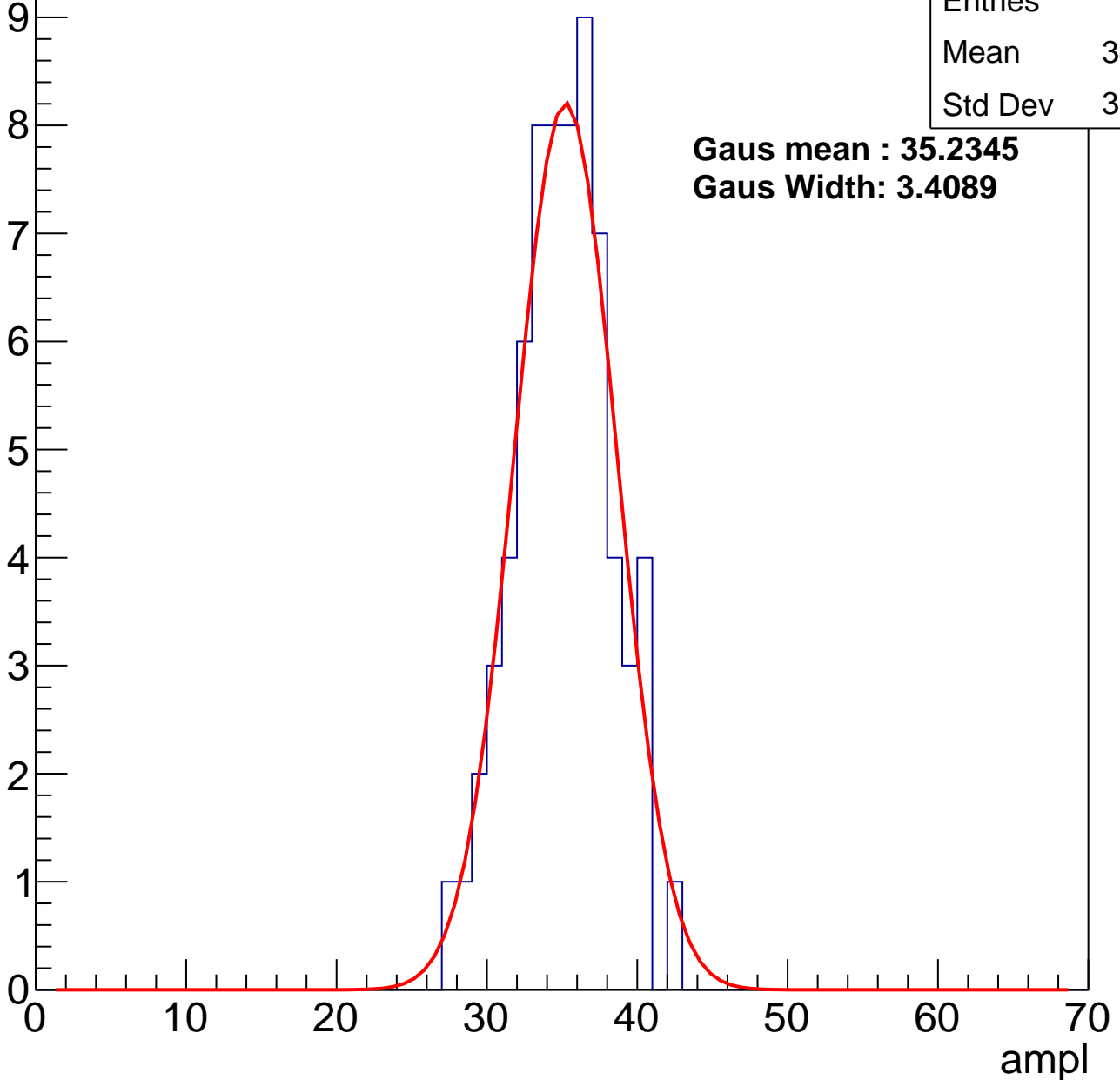
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.62
Std Dev	3.135

**Gaus mean : 35.2345**

**Gaus Width: 3.4089**



# B1L002S, U8-ch120, adc2

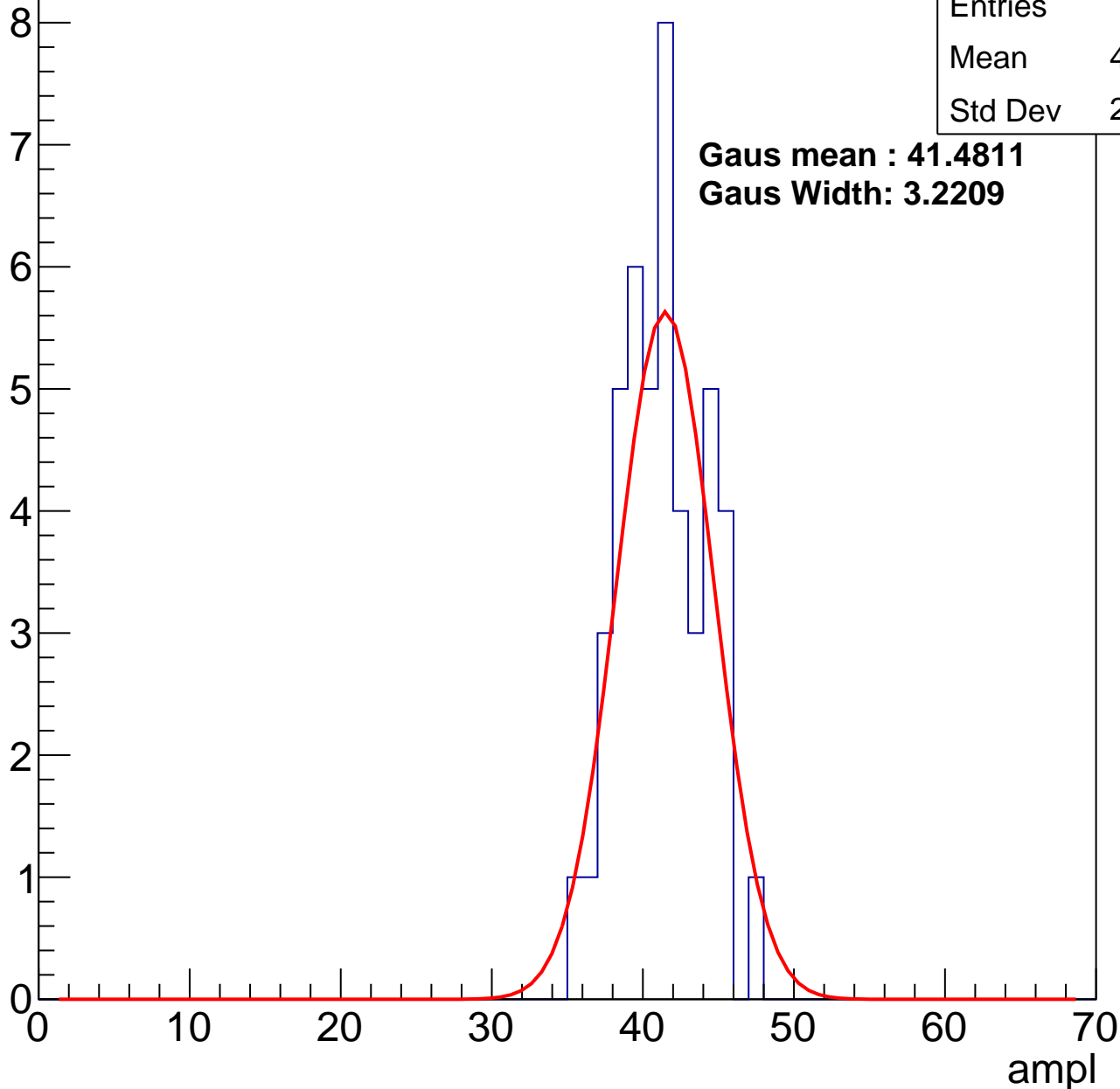
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	40.83
Std Dev	2.729

**Gaus mean : 41.4811**

**Gaus Width: 3.2209**

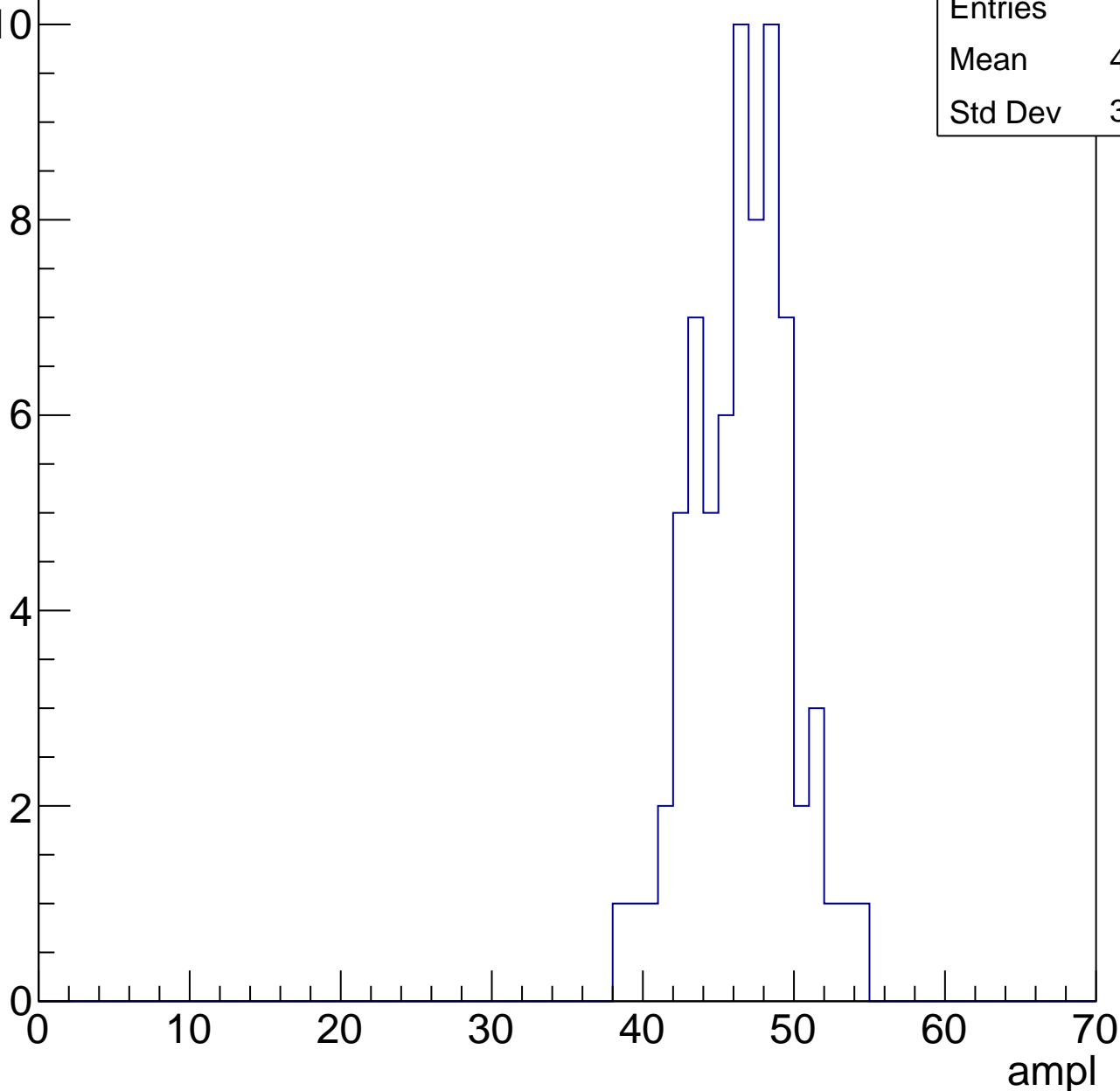


# B1L002S, U8-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	46.07
Std Dev	3.217

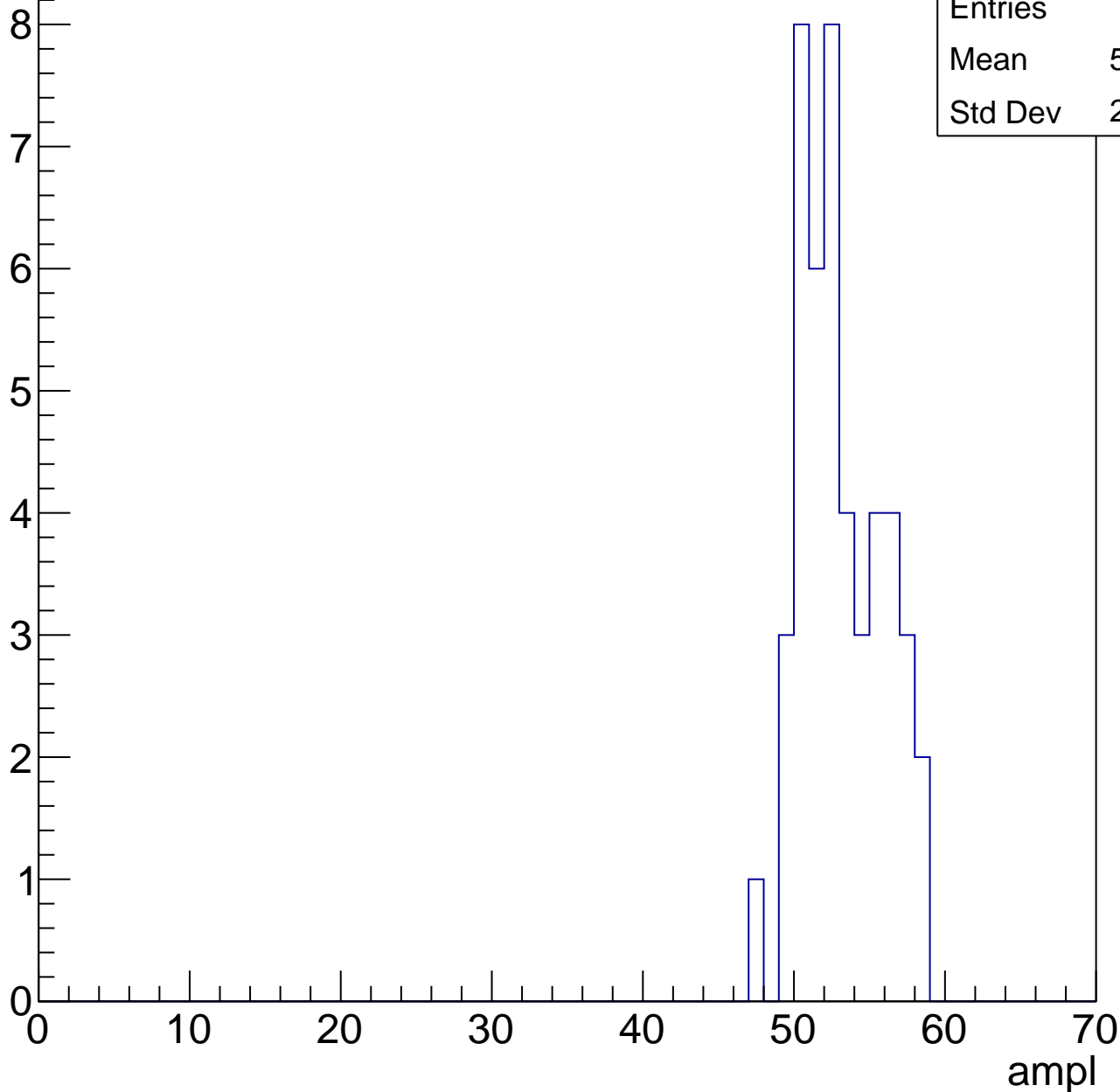


# B1L002S, U8-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	52.63
Std Dev	2.697

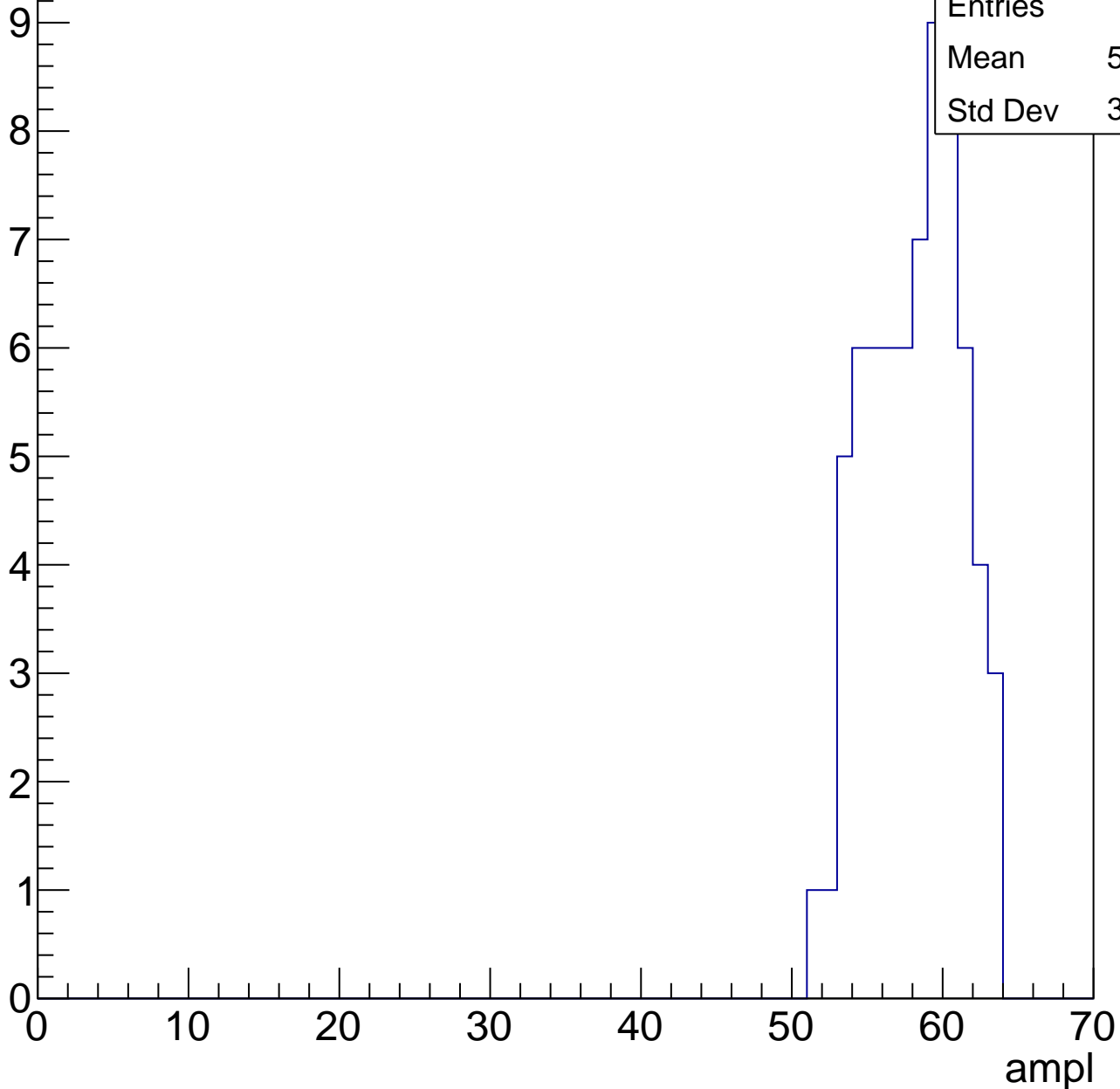


# B1L002S, U8-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	57.65
Std Dev	3.009

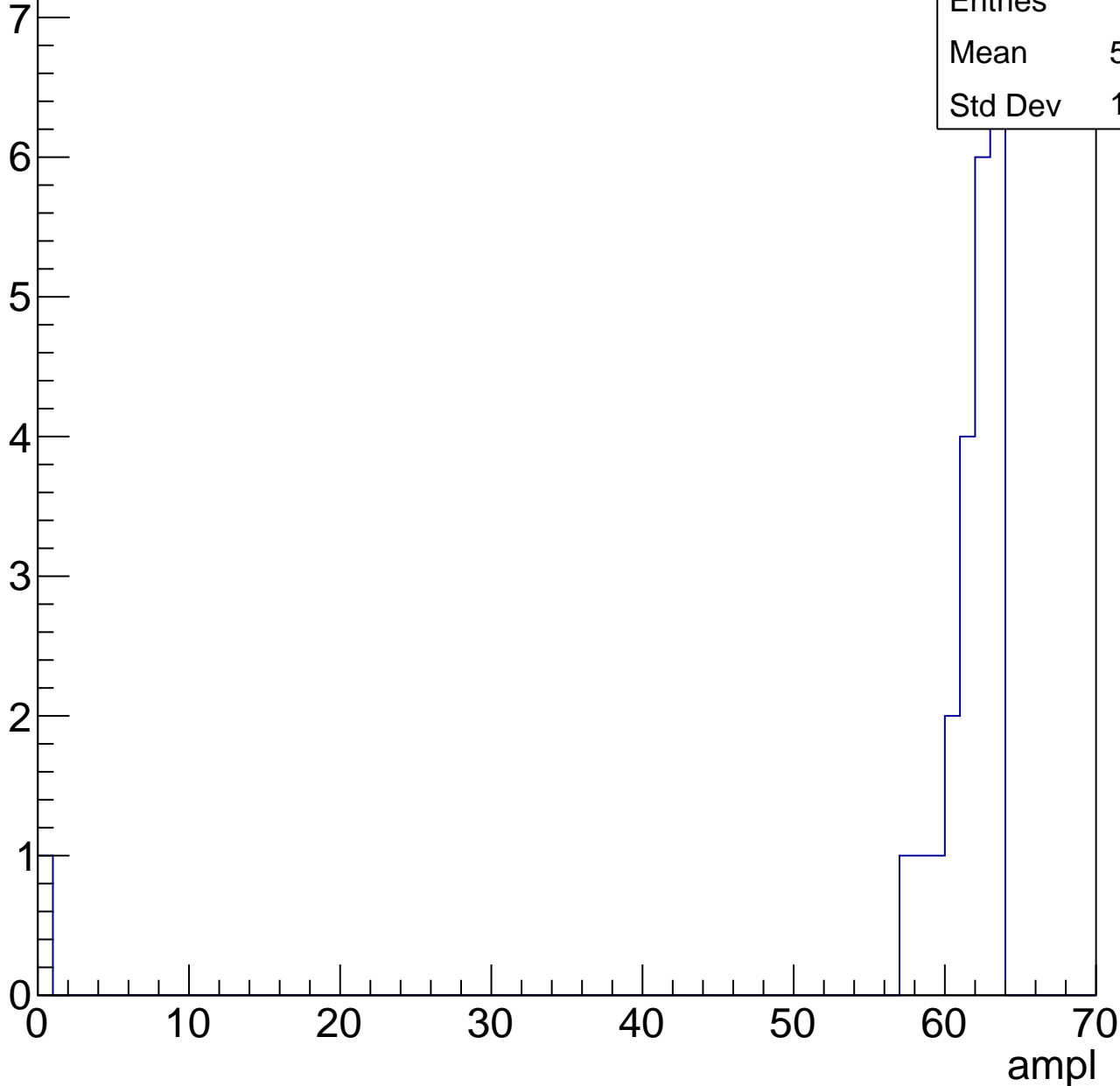


# B1L002S, U8-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.74
Std Dev	12.63

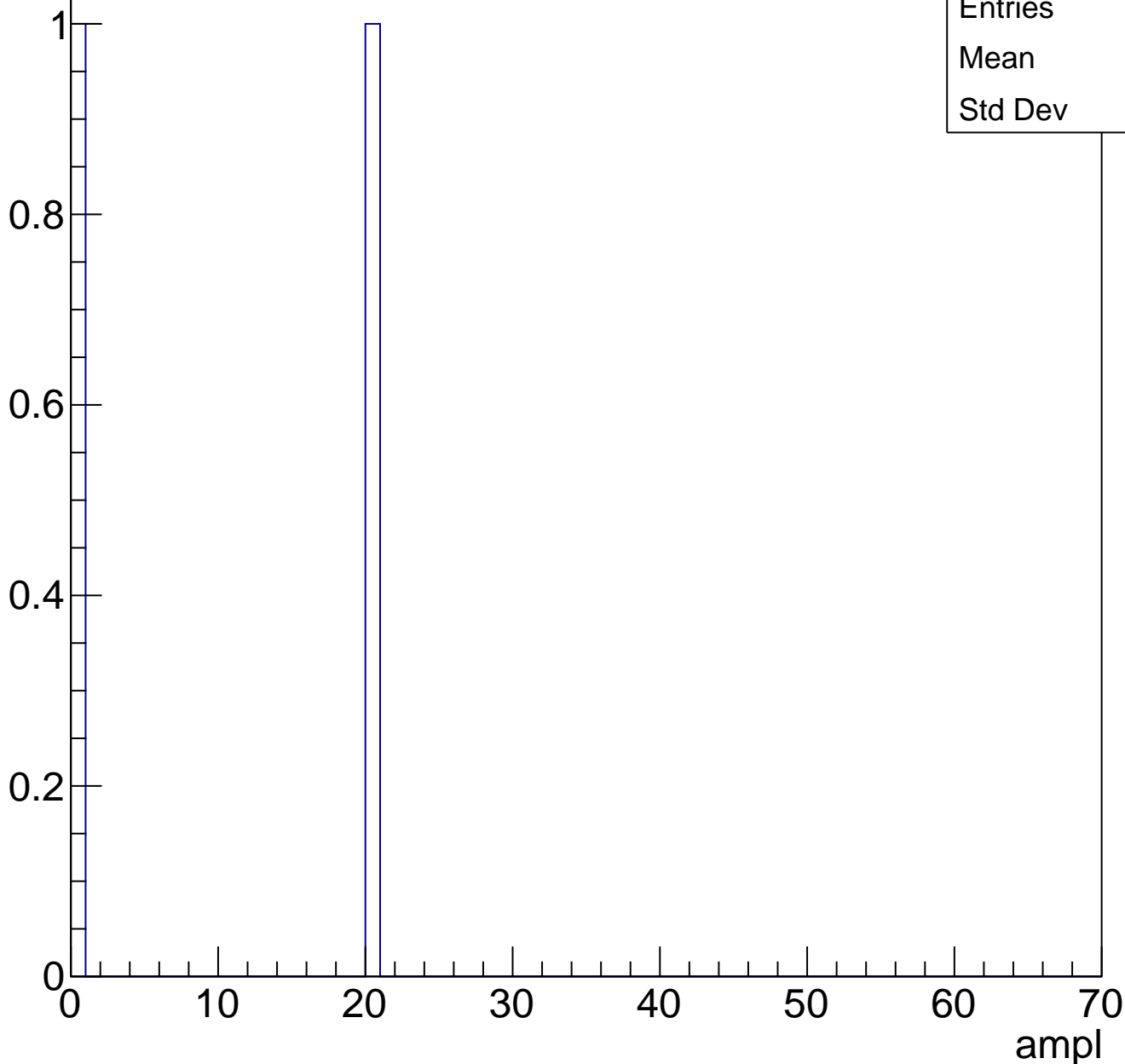




# B1L002S, U8-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L002S, U8-ch121, adc0

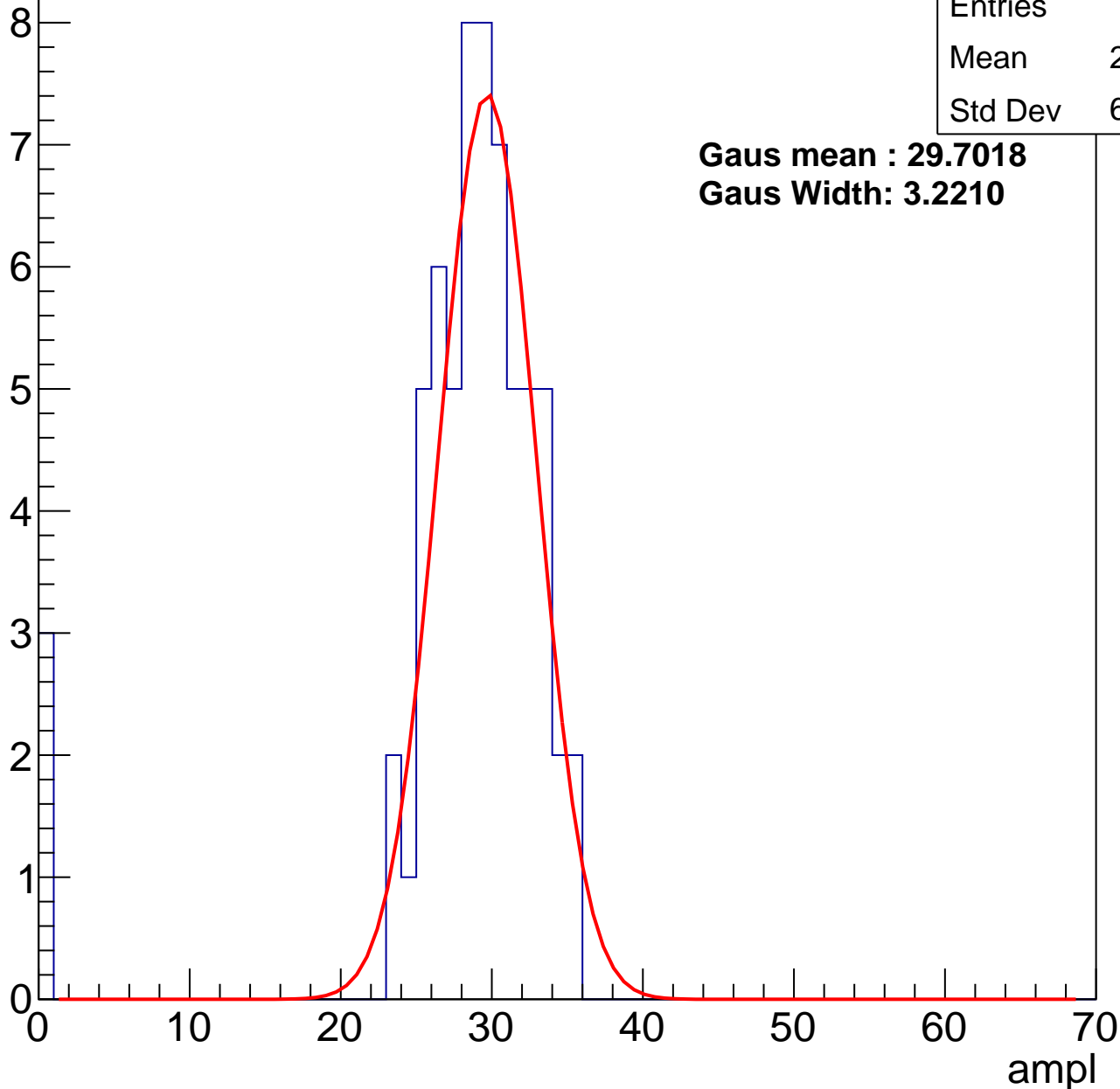
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.66
Std Dev	6.778

**Gaus mean : 29.7018**

**Gaus Width: 3.2210**



# B1L002S, U8-ch121, adc1

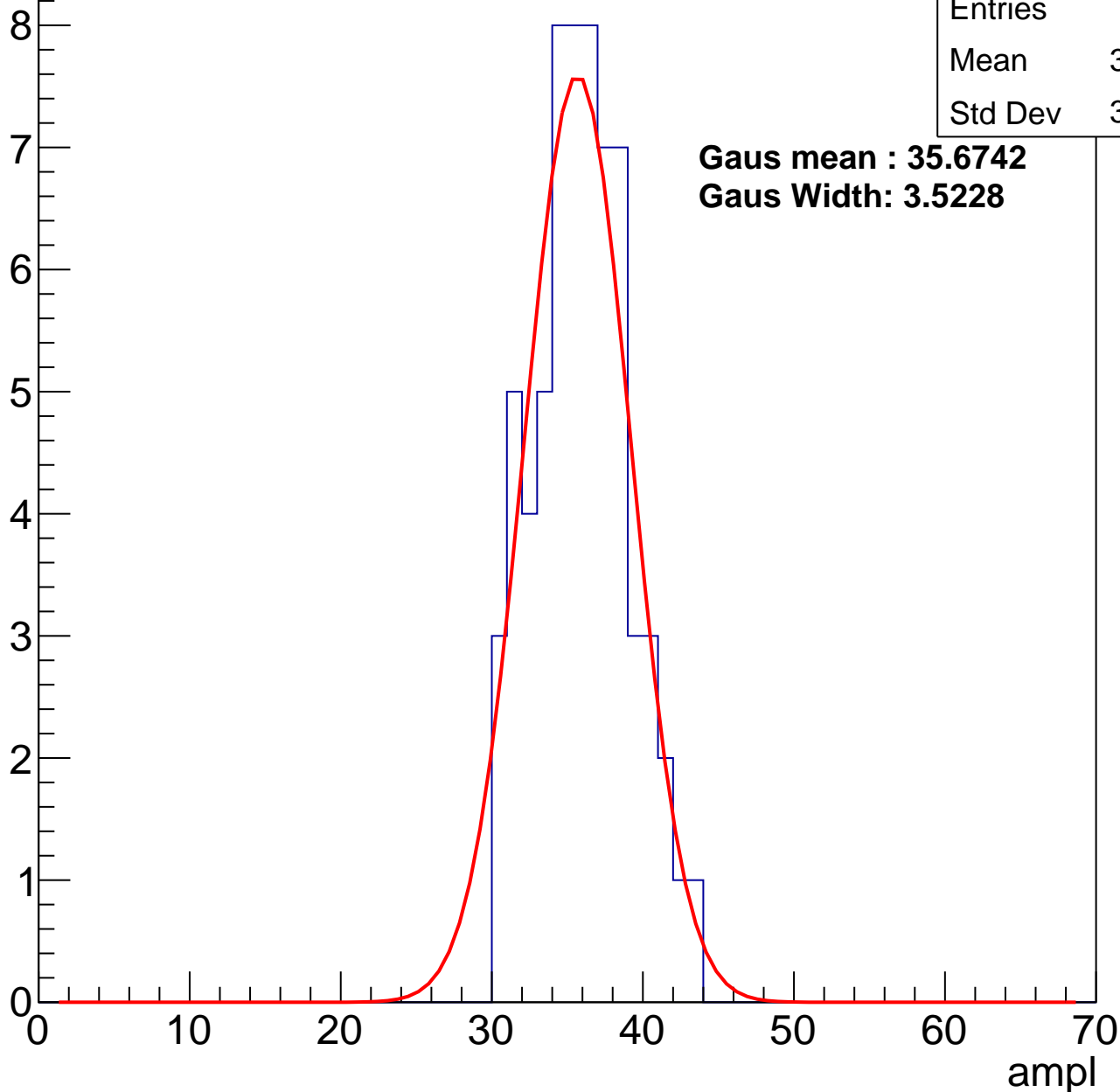
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35.49
Std Dev	3.064

**Gaus mean : 35.6742**

**Gaus Width: 3.5228**



# B1L002S, U8-ch121, adc2

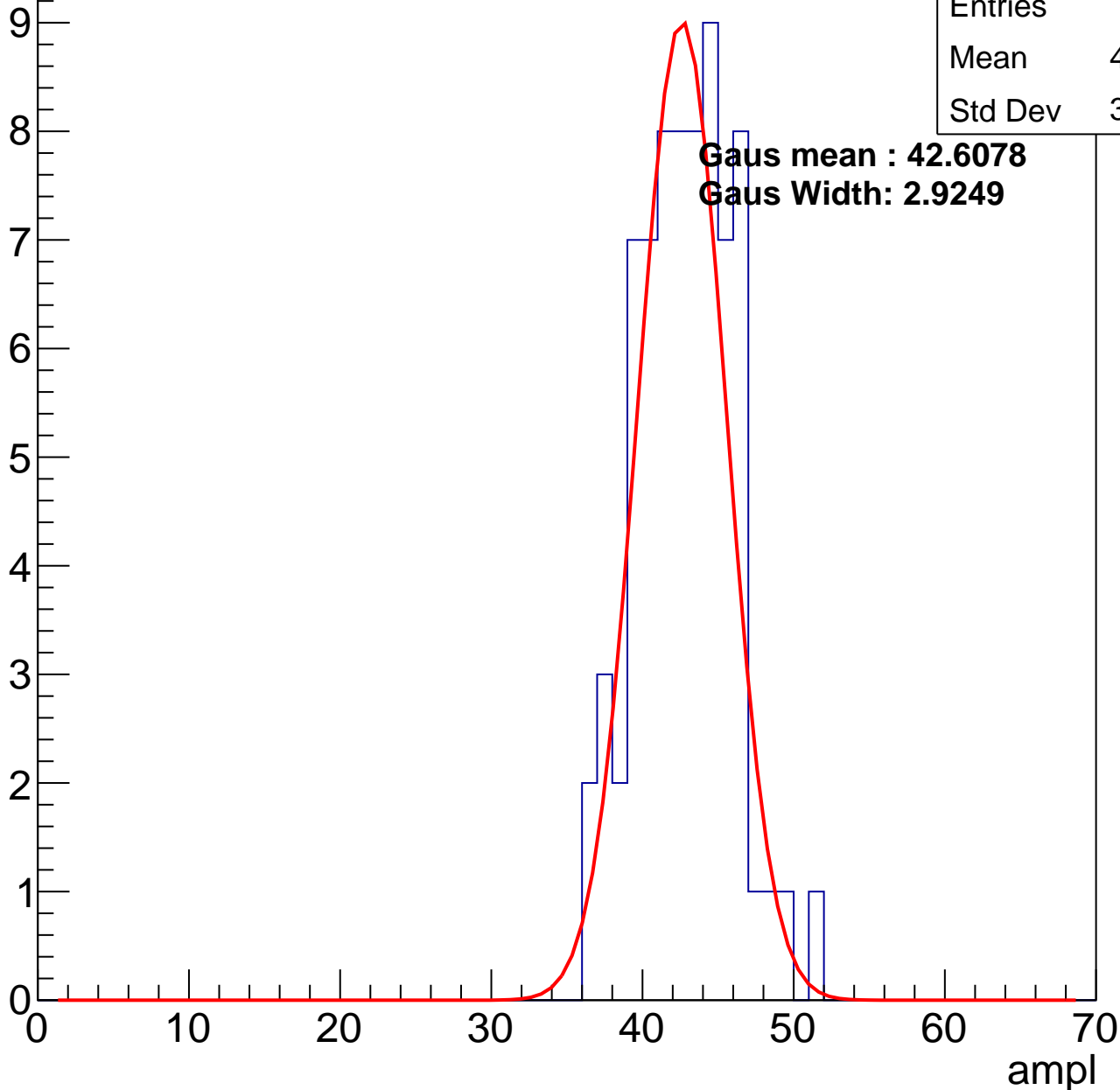
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	42.38
Std Dev	3.082

**Gaus mean : 42.6078**

**Gaus Width: 2.9249**

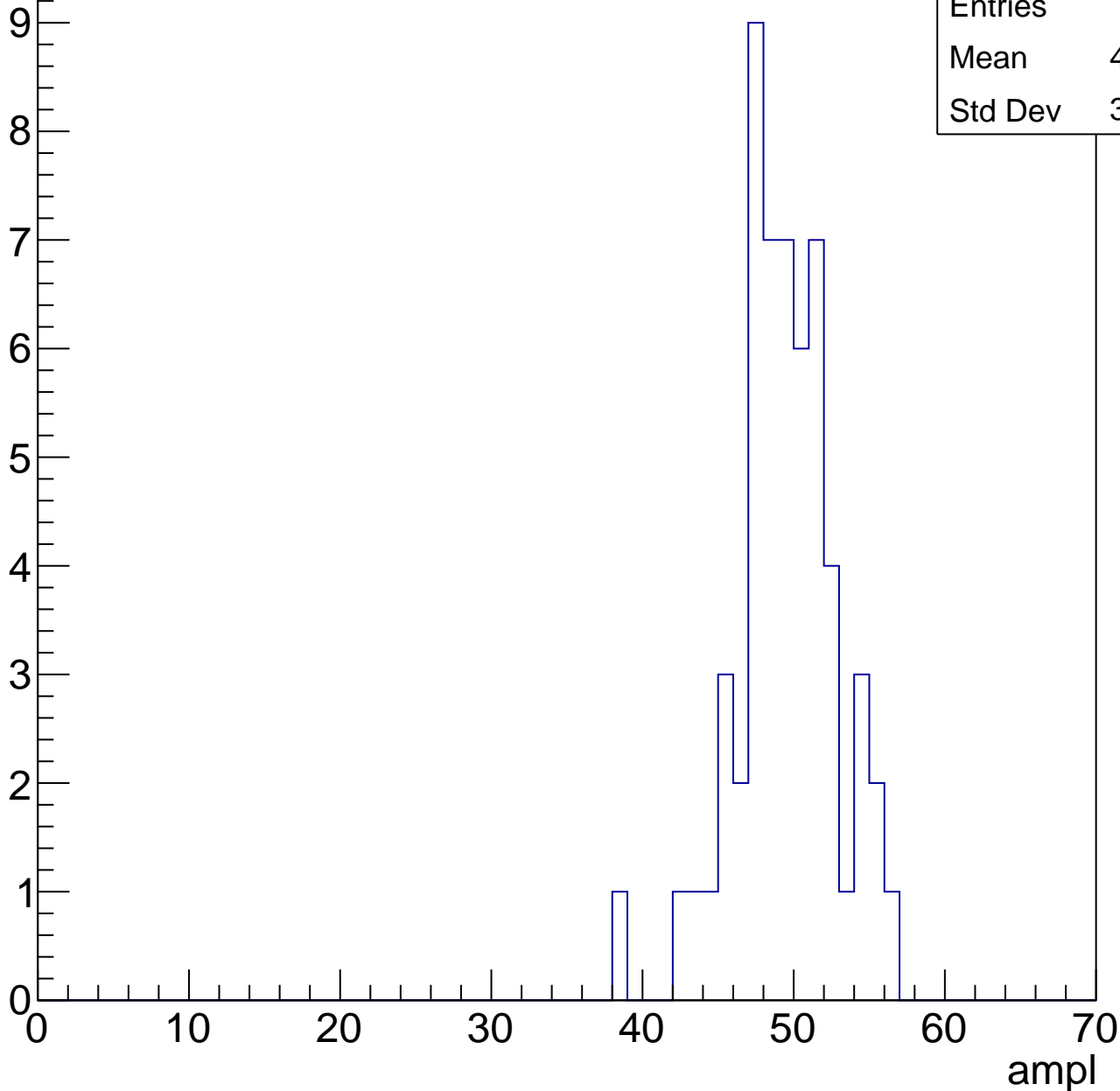


# B1L002S, U8-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	48.96
Std Dev	3.333

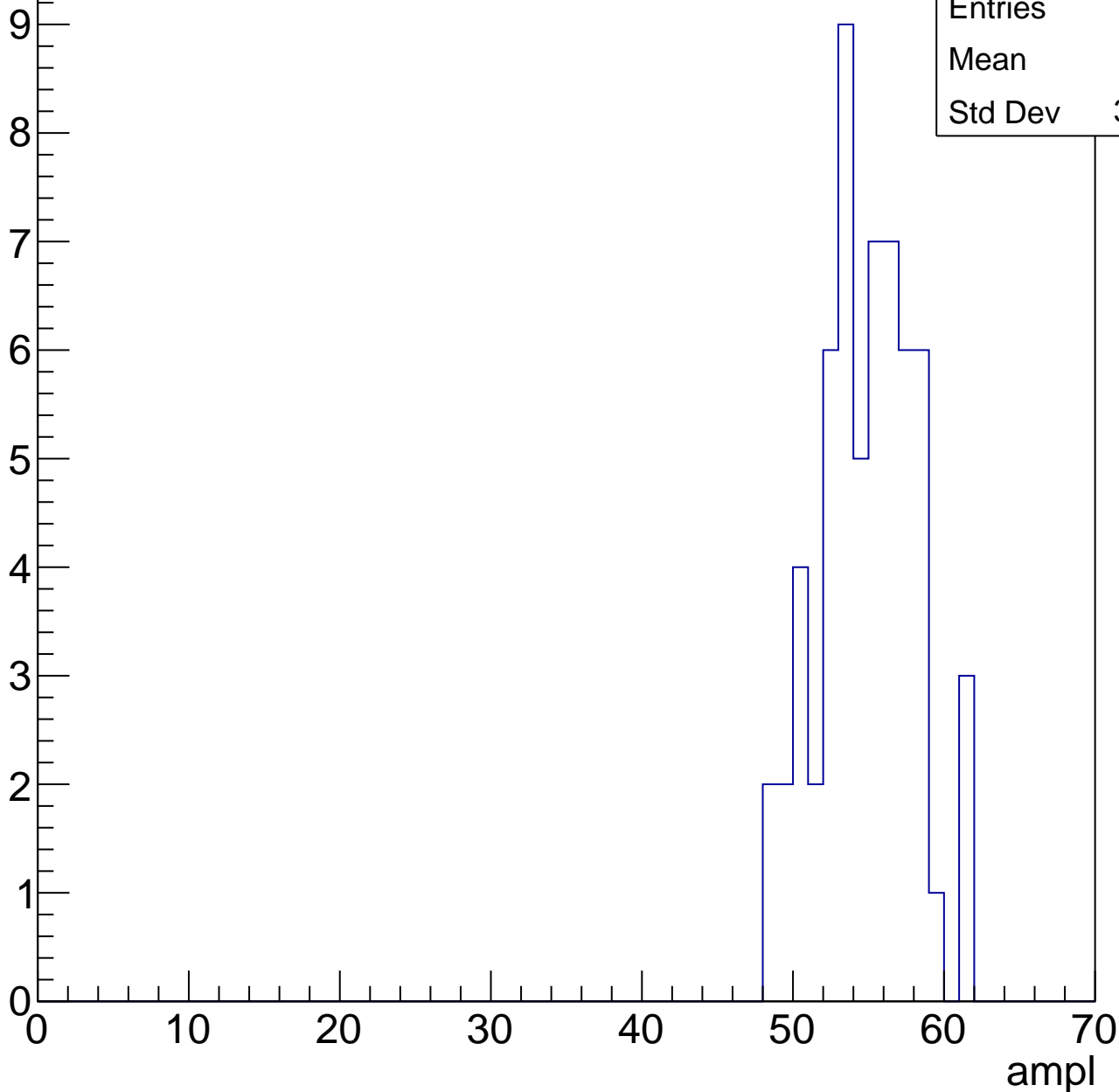


# B1L002S, U8-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

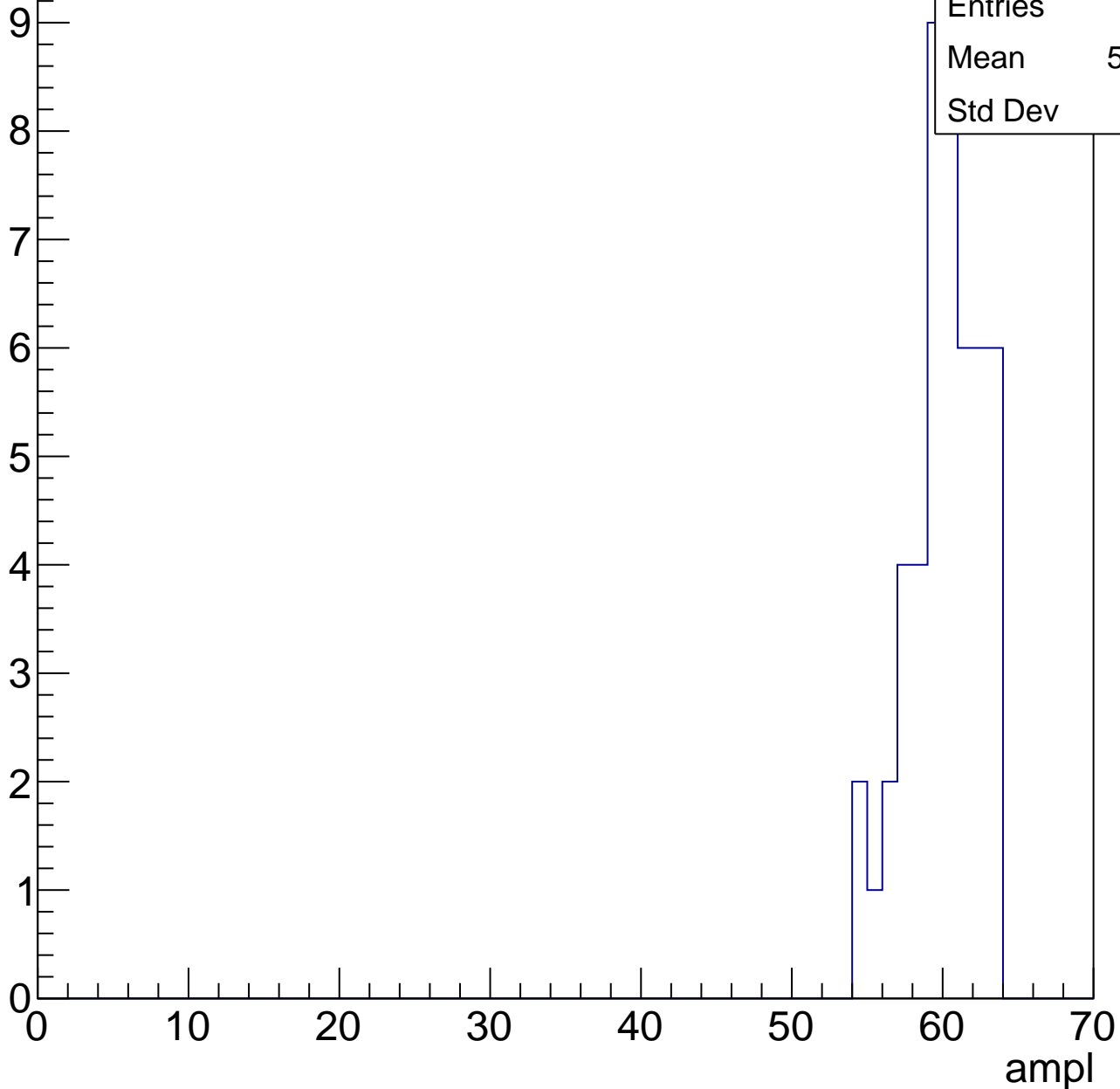
Entries	60
Mean	54.4
Std Dev	3.121



# B1L002S, U8-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

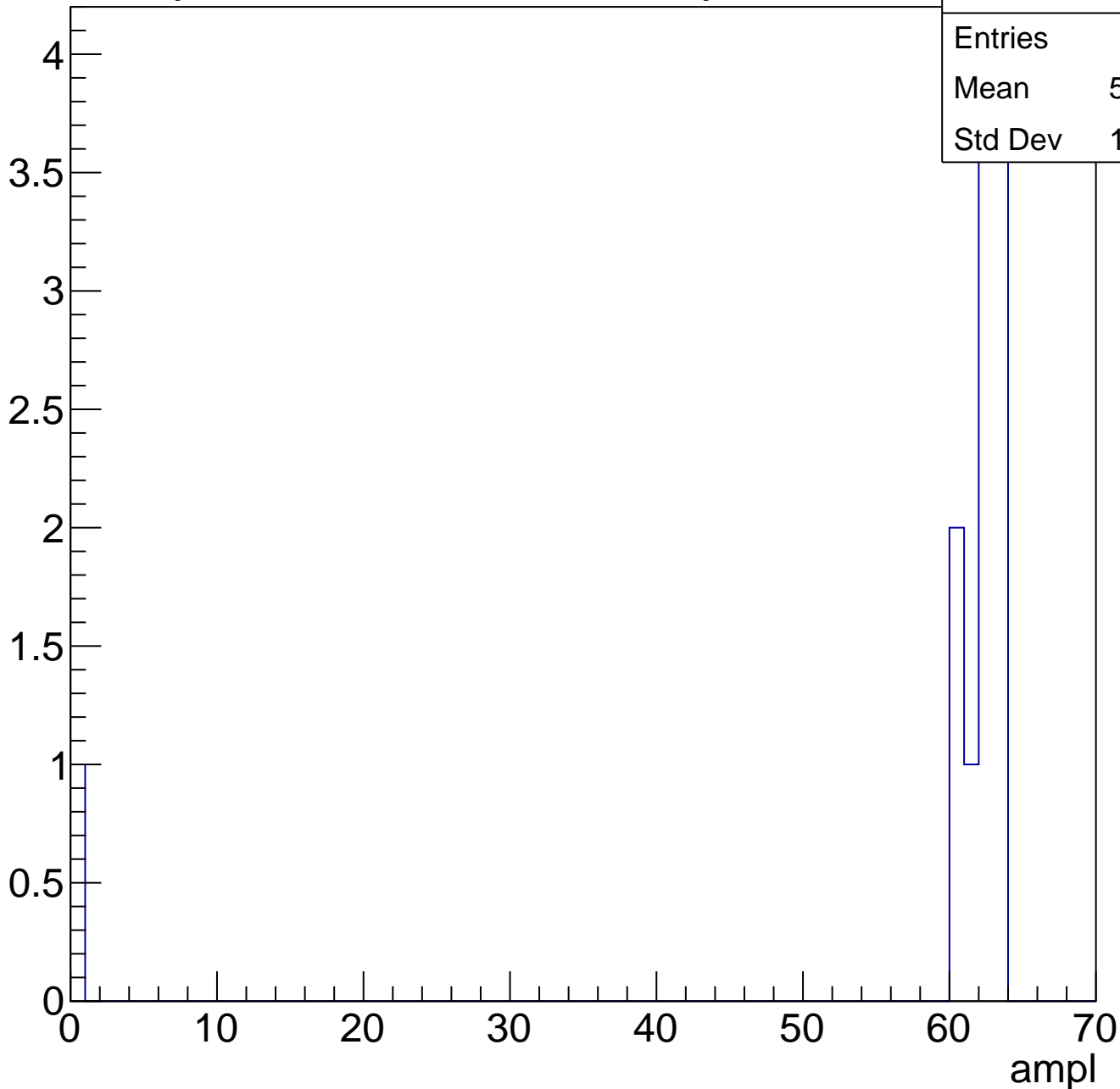


Entries	48
Mean	59.62
Std Dev	2.36

# B1L002S, U8-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U8-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U8-ch122, adc0

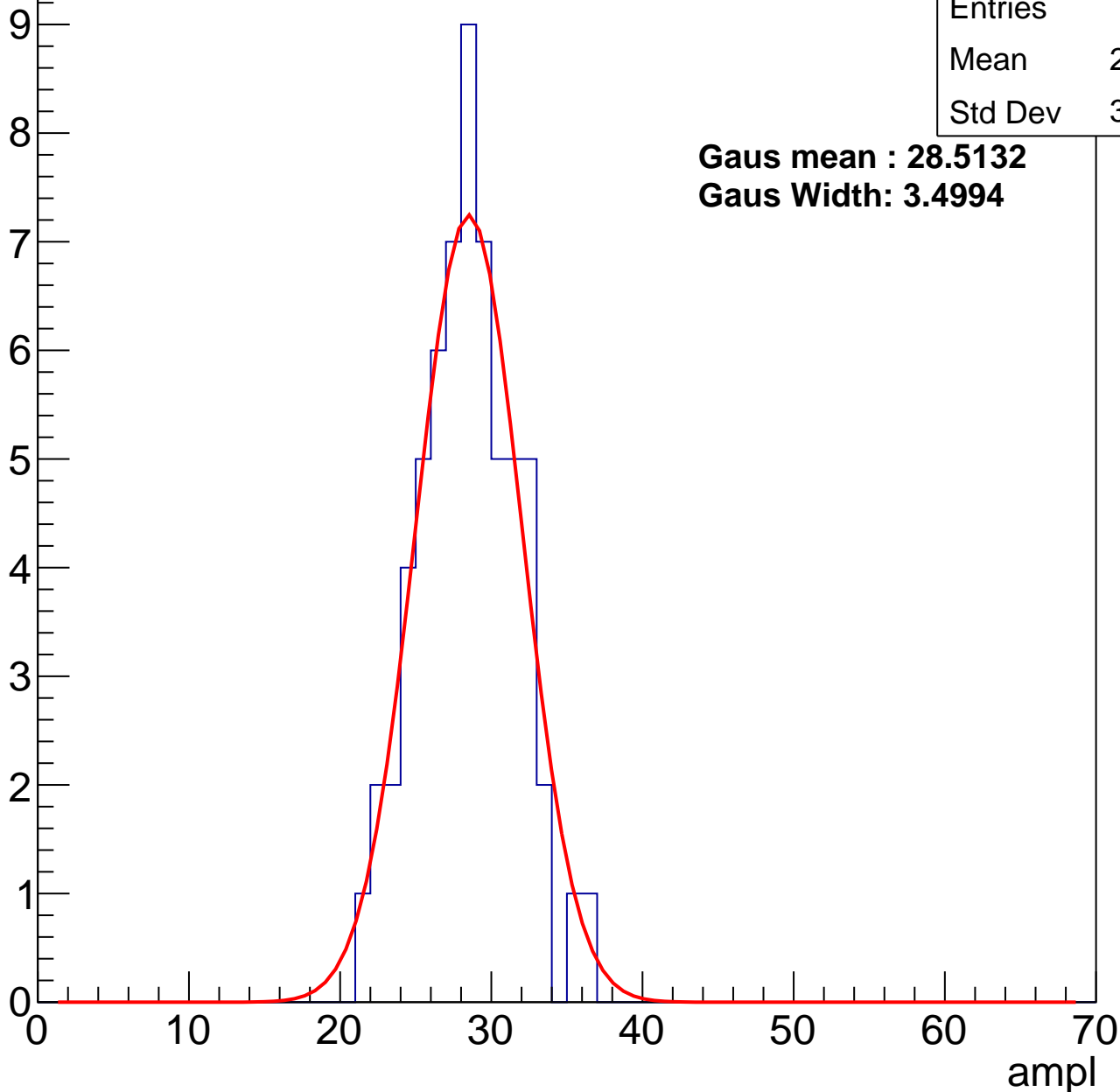
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.97
Std Dev	3.177

**Gaus mean : 28.5132**

**Gaus Width: 3.4994**



# B1L002S, U8-ch122, adc1

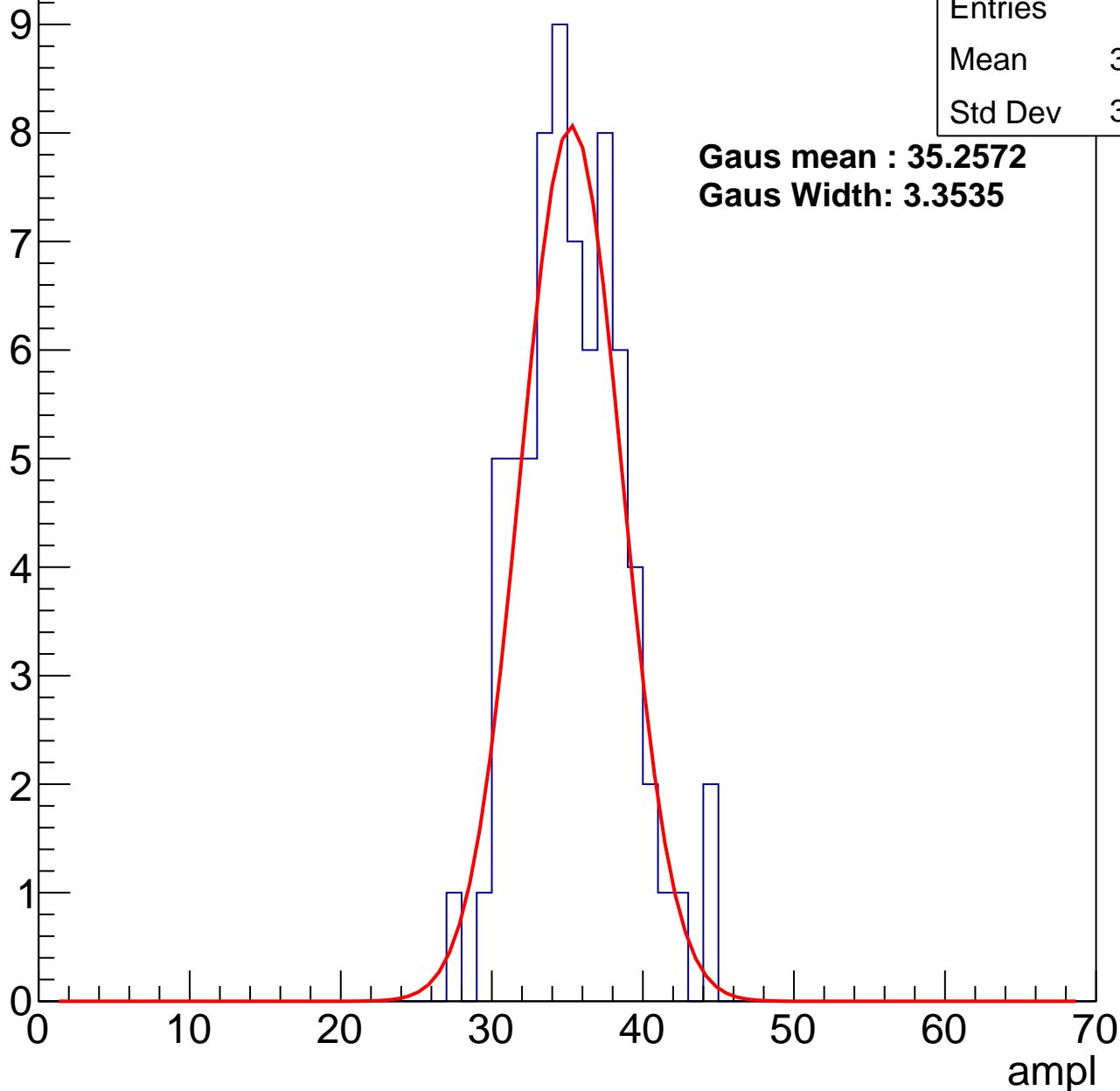
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.97
Std Dev	3.448

**Gaus mean : 35.2572**

**Gaus Width: 3.3535**



# B1L002S, U8-ch122, adc2

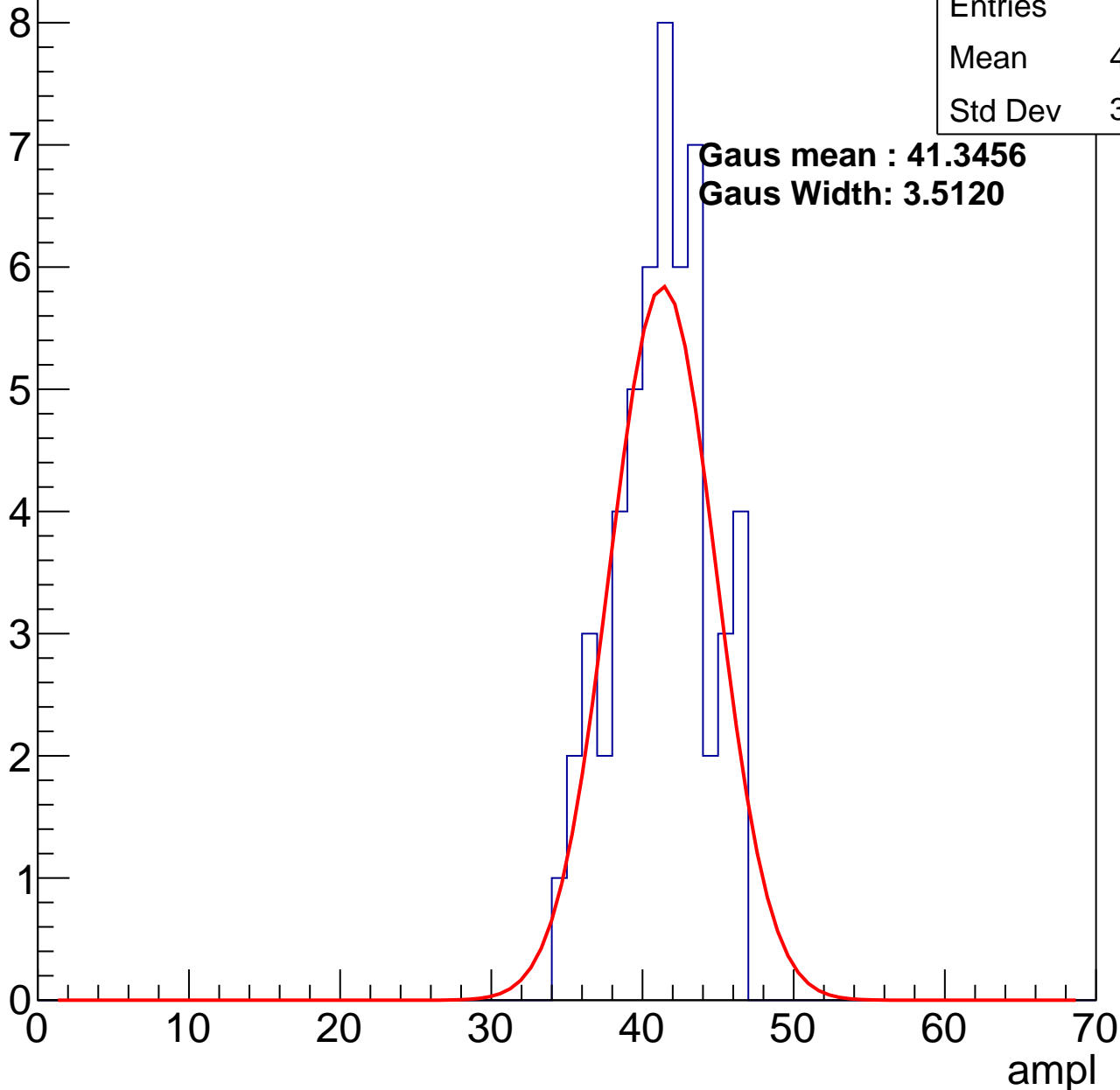
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.77
Std Dev	3.032

**Gaus mean : 41.3456**

**Gaus Width: 3.5120**



# B1L002S, U8-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

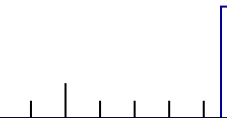
Entries	72
Mean	47.17
Std Dev	3.424

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

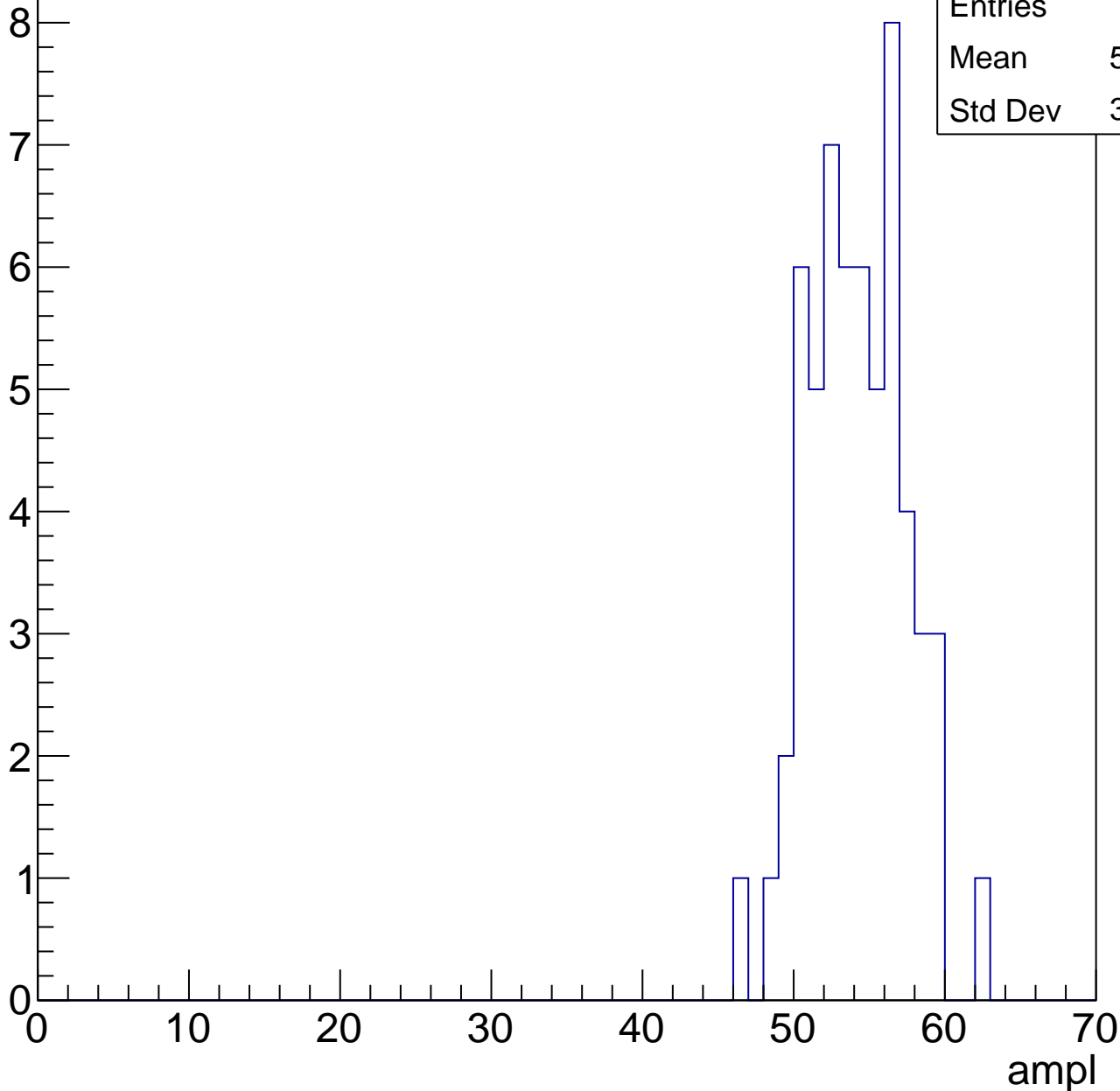


# B1L002S, U8-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.74
Std Dev	3.165

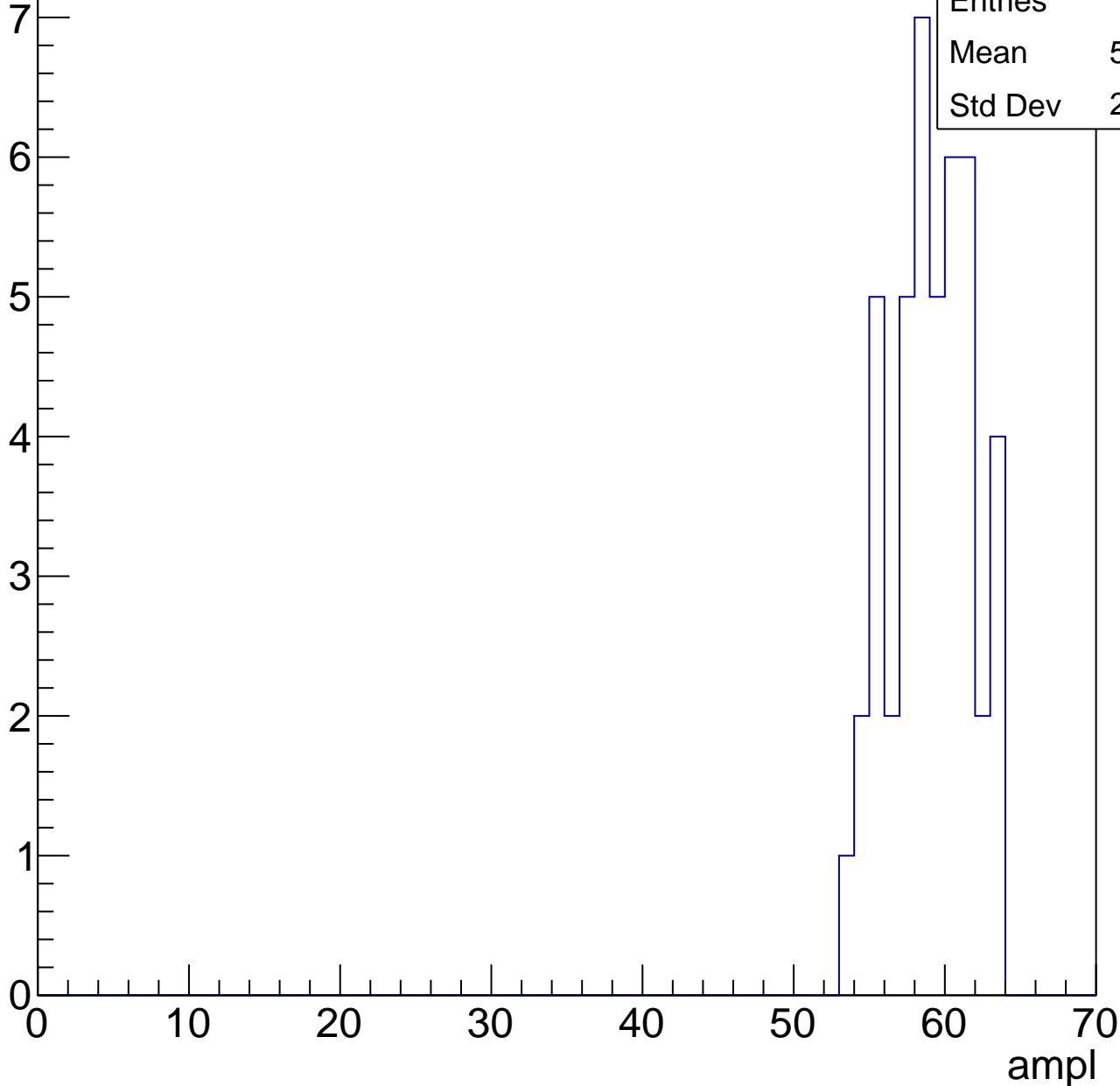


# B1L002S, U8-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	58.58
Std Dev	2.646

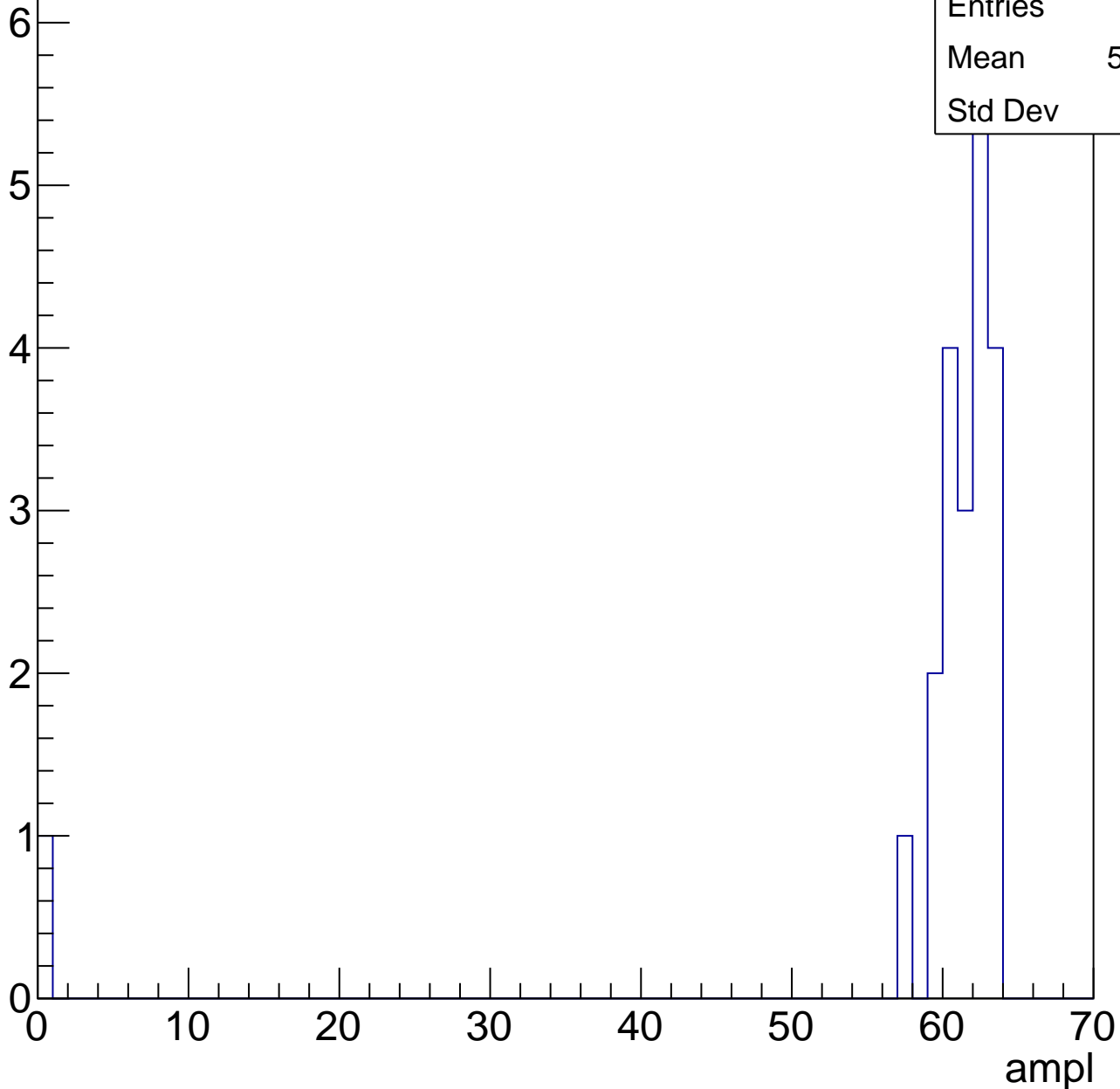


# B1L002S, U8-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.19
Std Dev	13.1

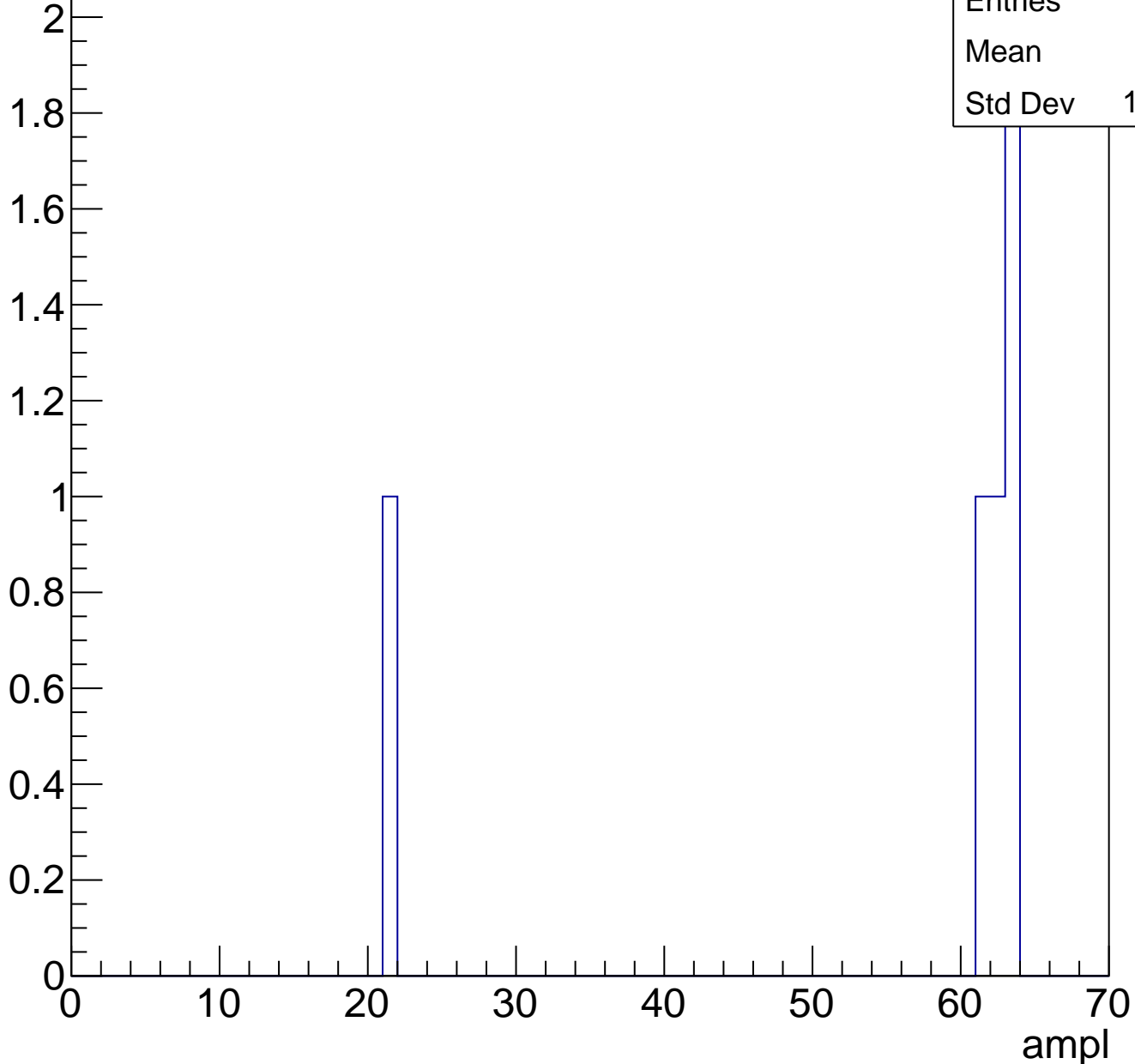




# B1L002S, U8-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch123, adc0

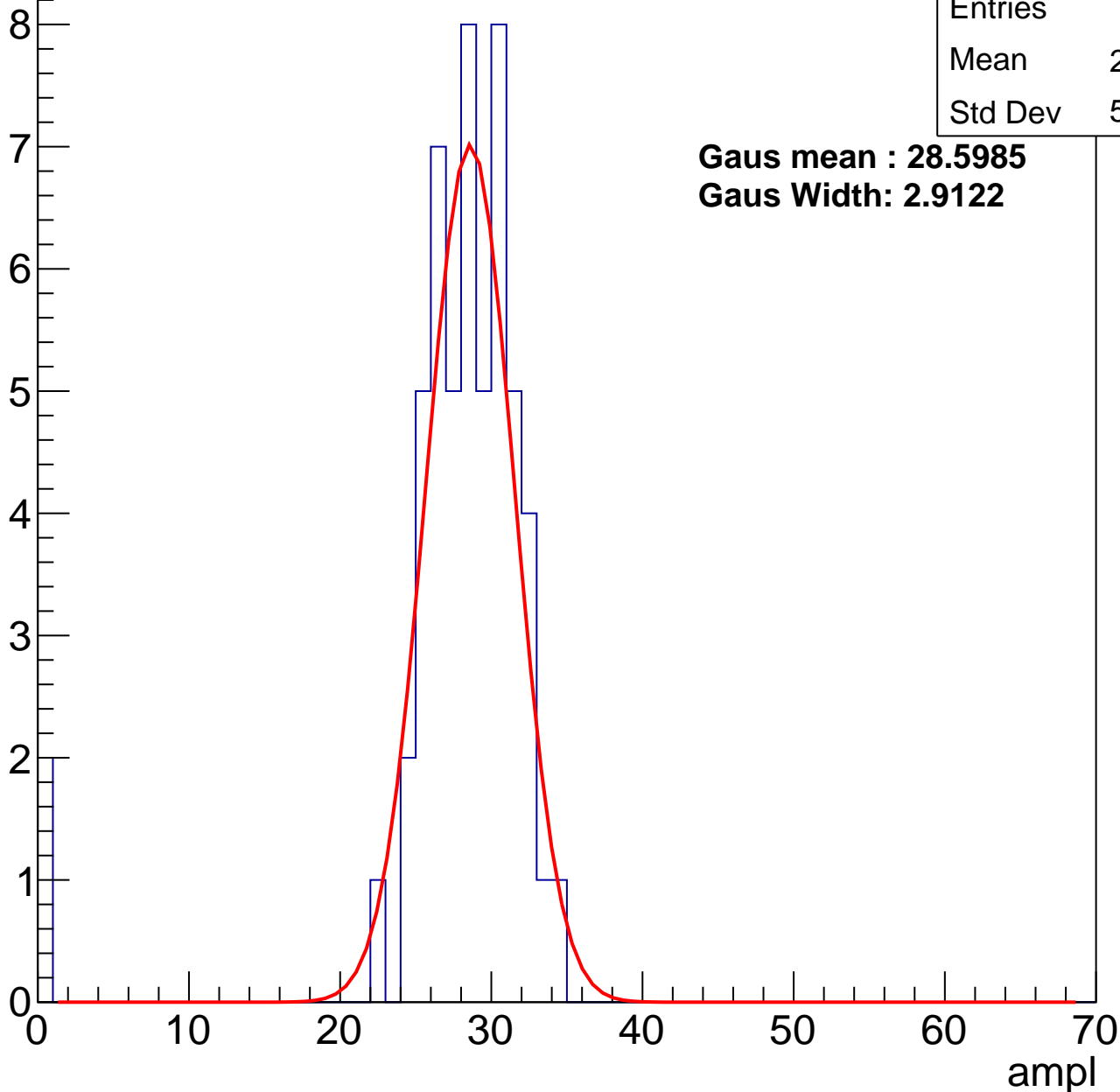
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	27.24
Std Dev	5.916

**Gaus mean : 28.5985**

**Gaus Width: 2.9122**



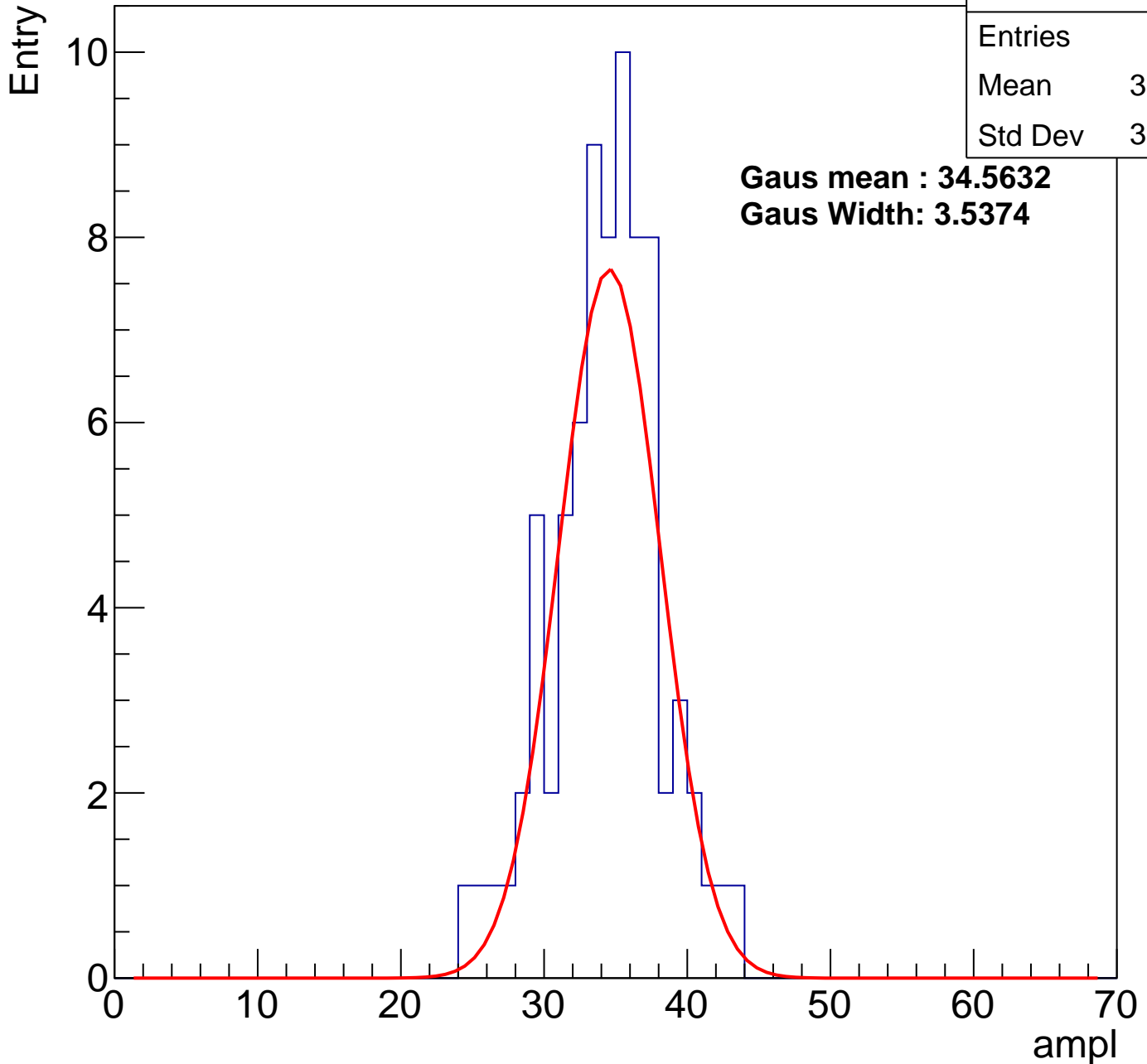
# B1L002S, U8-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	33.92
Std Dev	3.758

**Gaus mean : 34.5632**

**Gaus Width: 3.5374**



# B1L002S, U8-ch123, adc2

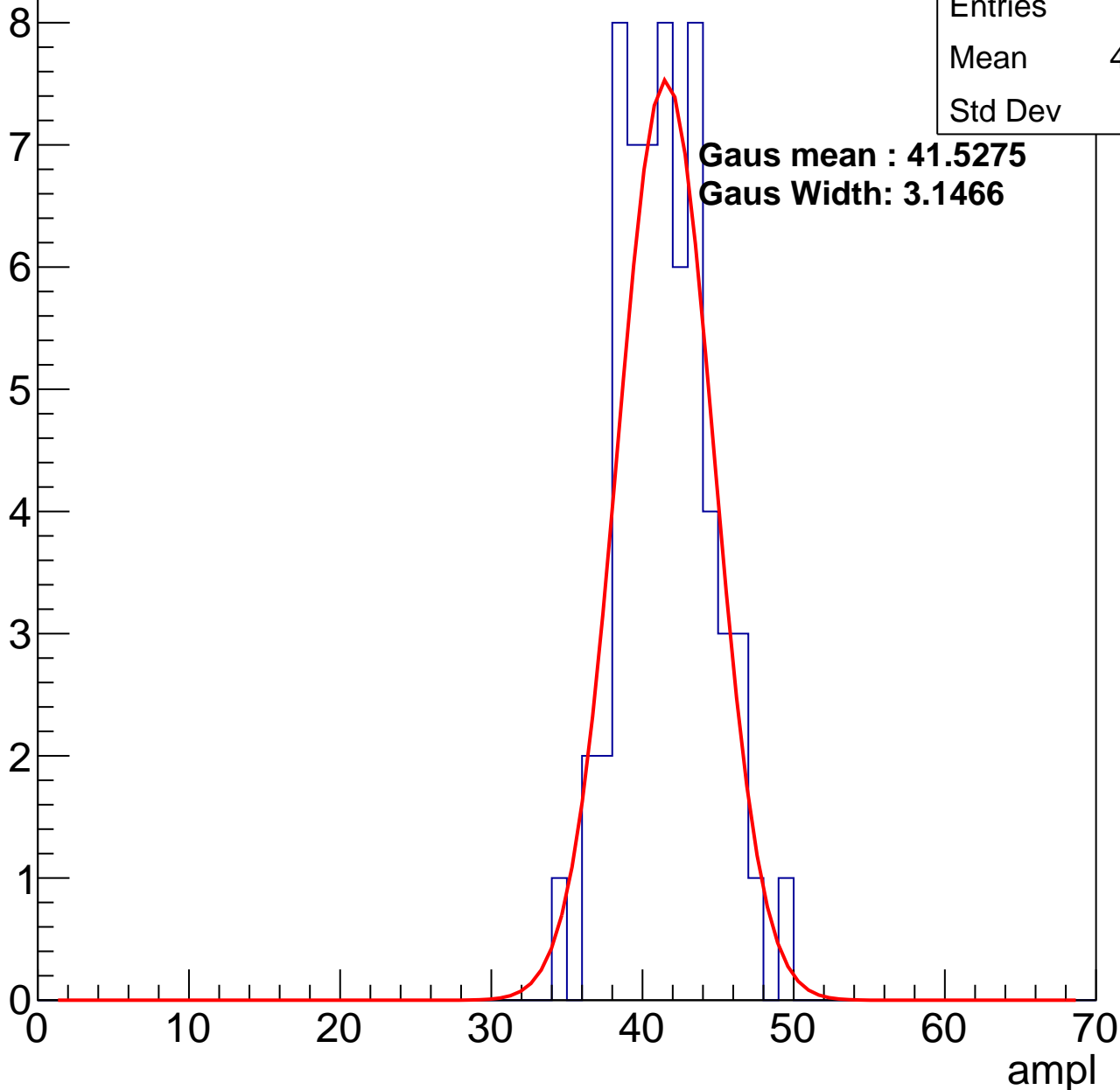
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.08
Std Dev	2.96

**Gaus mean : 41.5275**

**Gaus Width: 3.1466**

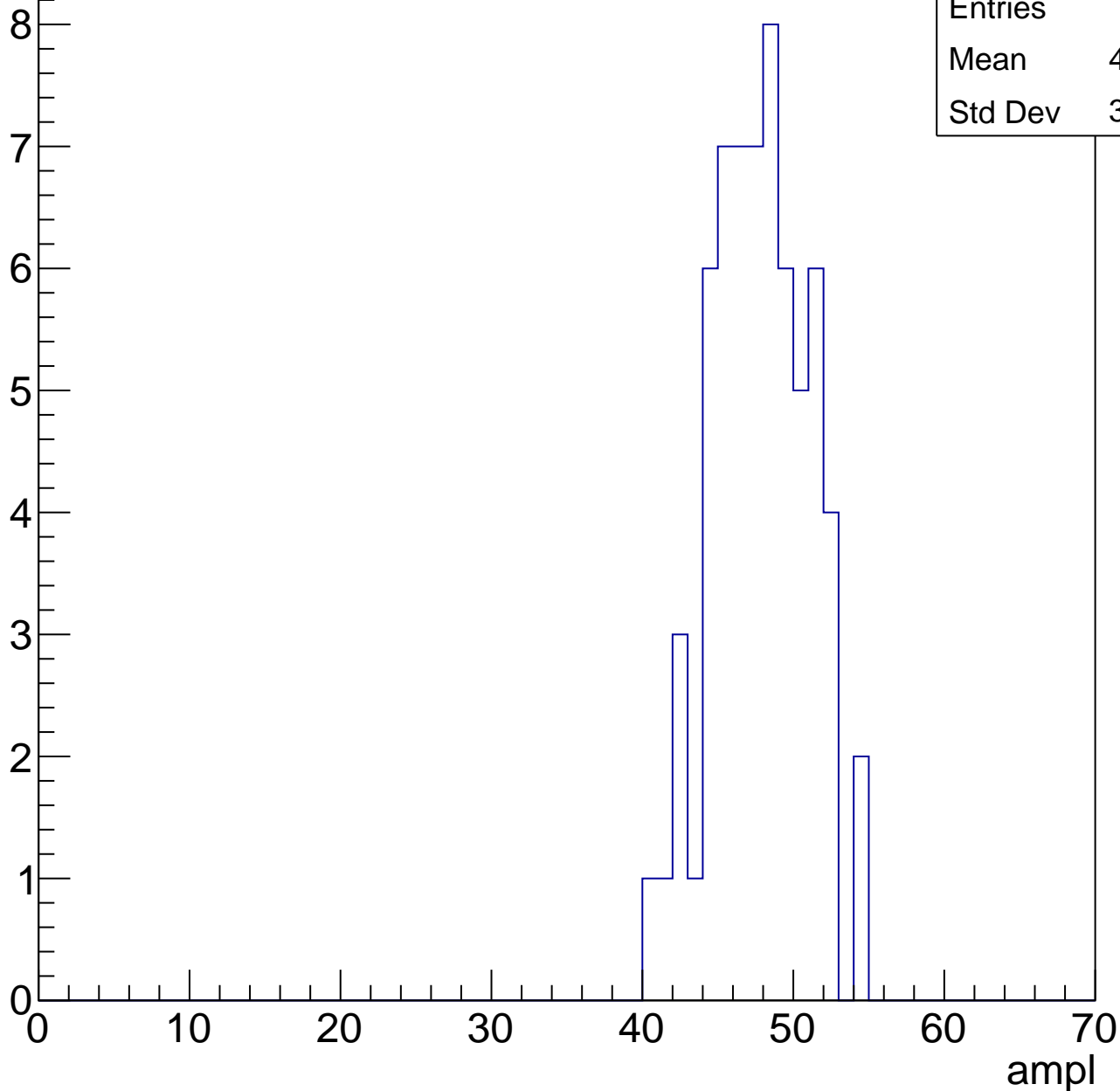


# B1L002S, U8-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

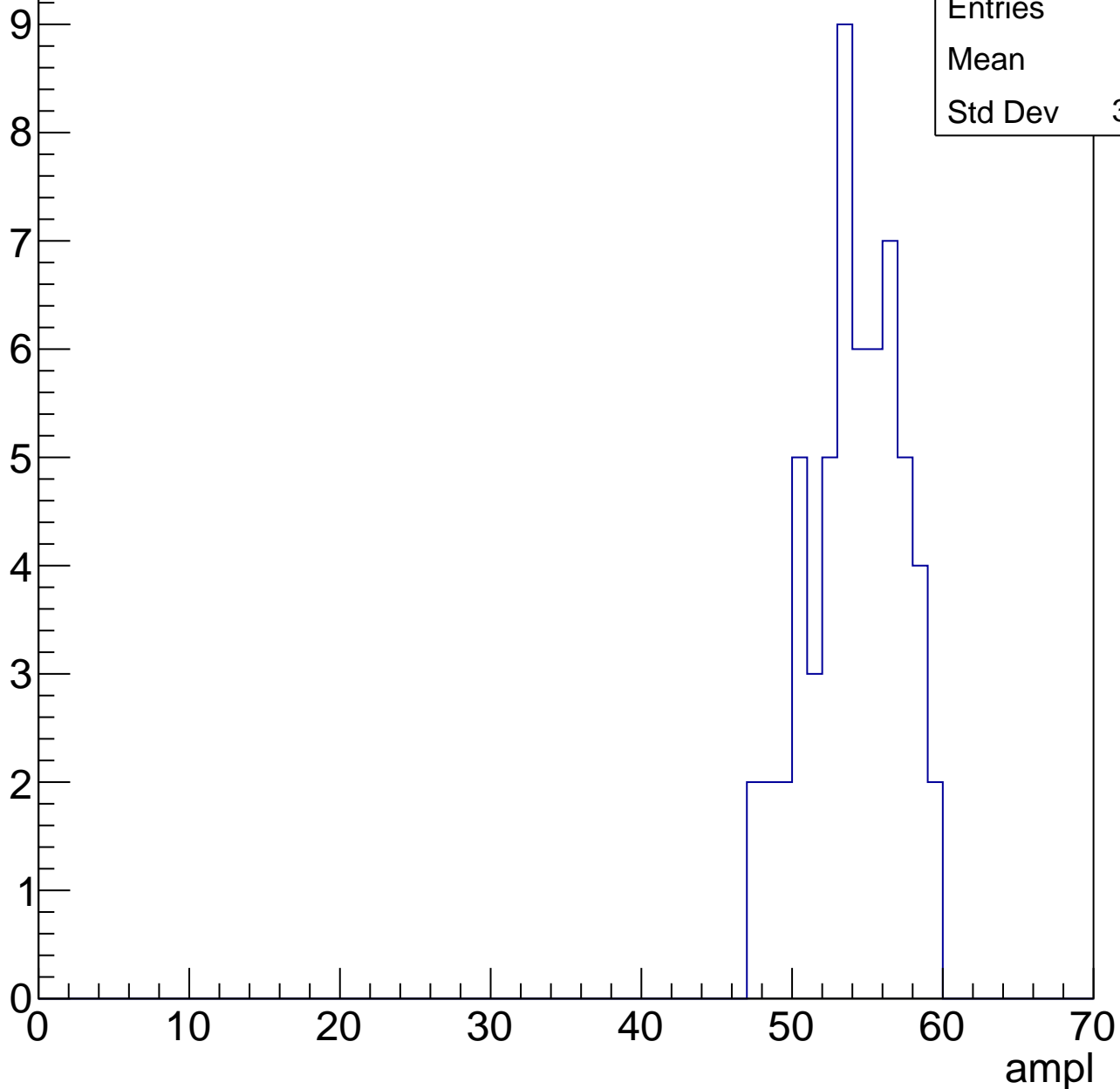
Entries	64
Mean	47.34
Std Dev	3.134



# B1L002S, U8-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

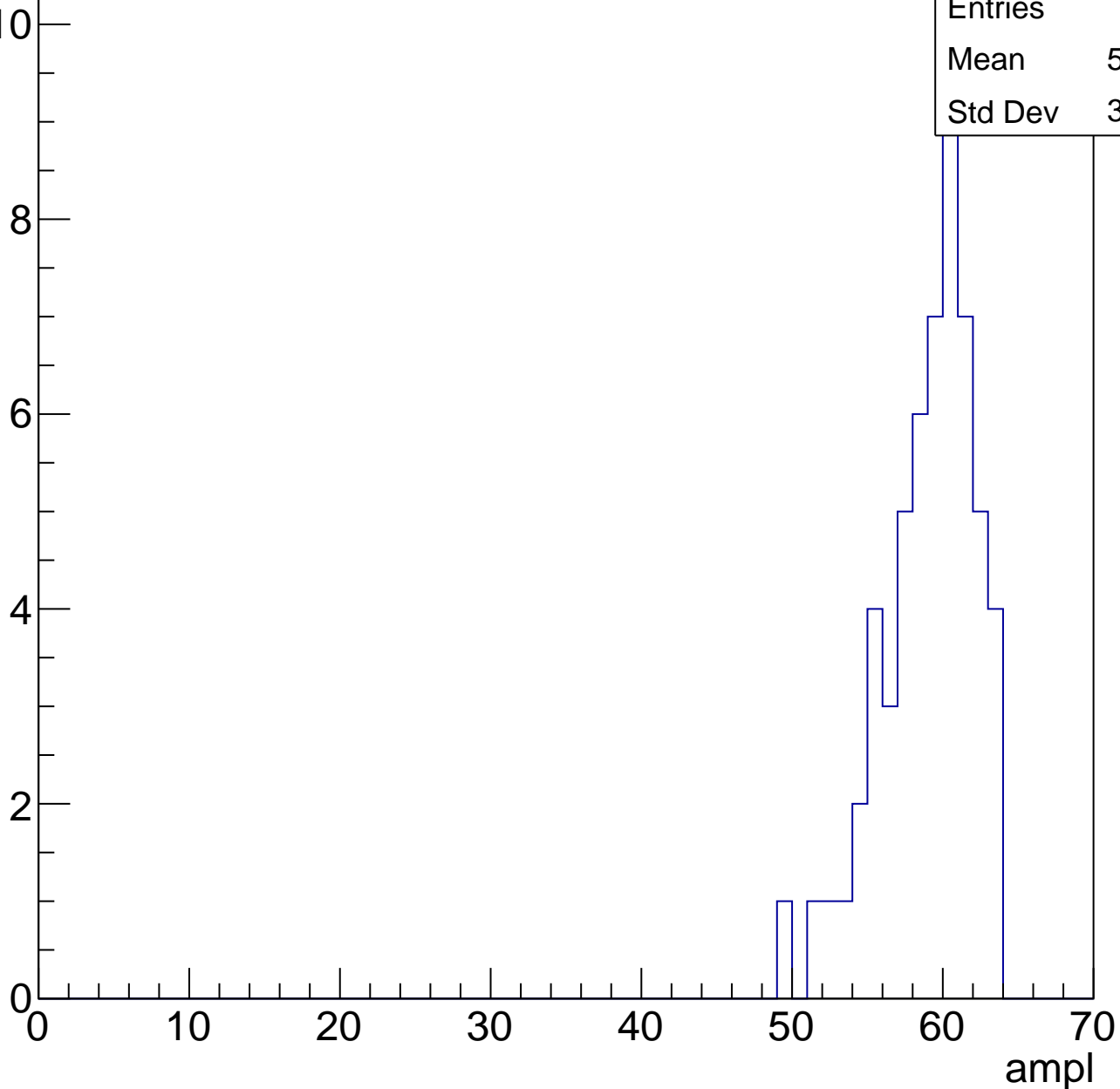


# B1L002S, U8-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	58.53
Std Dev	3.107

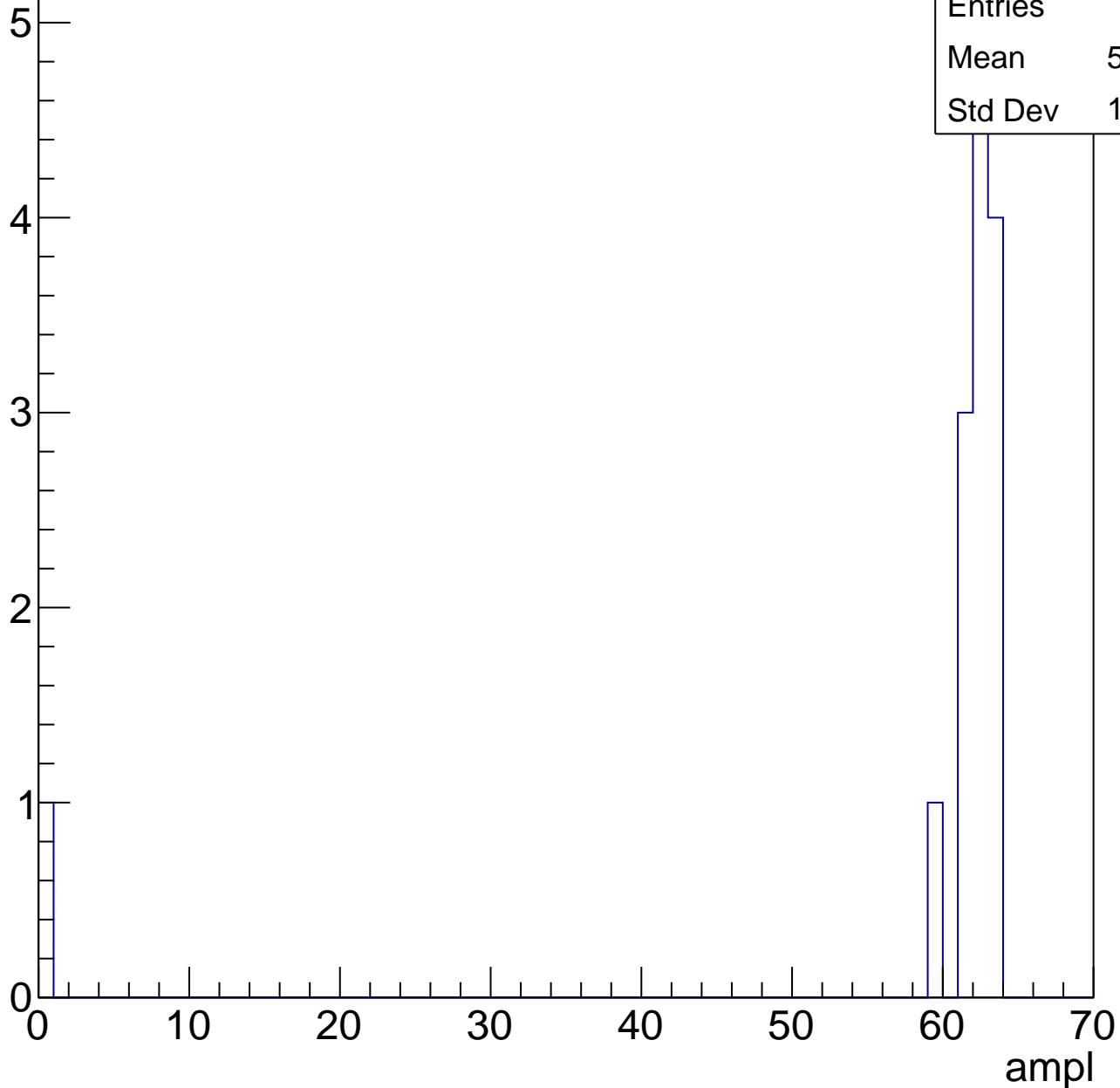


# B1L002S, U8-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.43
Std Dev	15.96





# B1L002S, U8-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch124, adc0

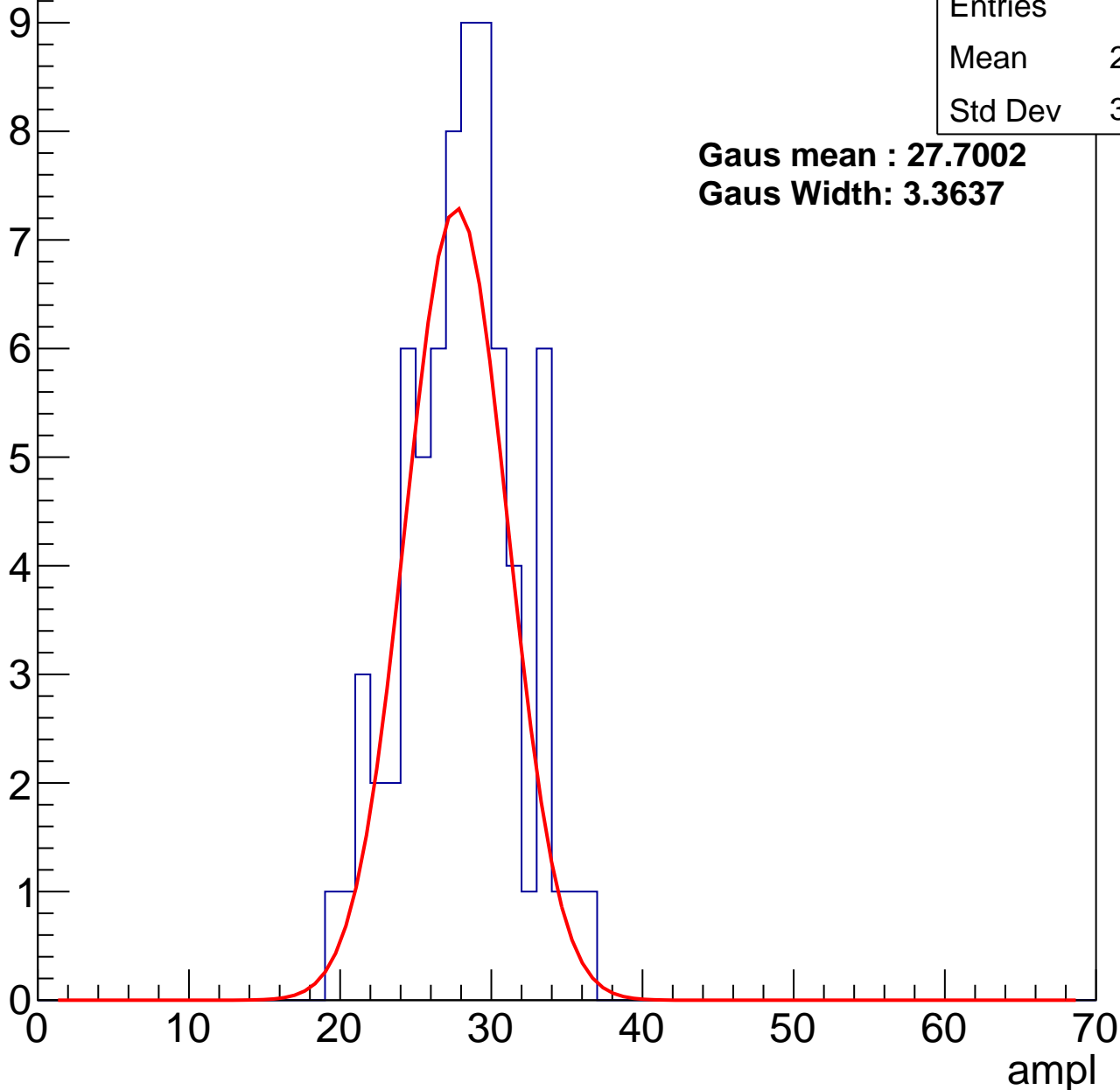
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.57
Std Dev	3.647

**Gaus mean : 27.7002**

**Gaus Width: 3.3637**



# B1L002S, U8-ch124, adc1

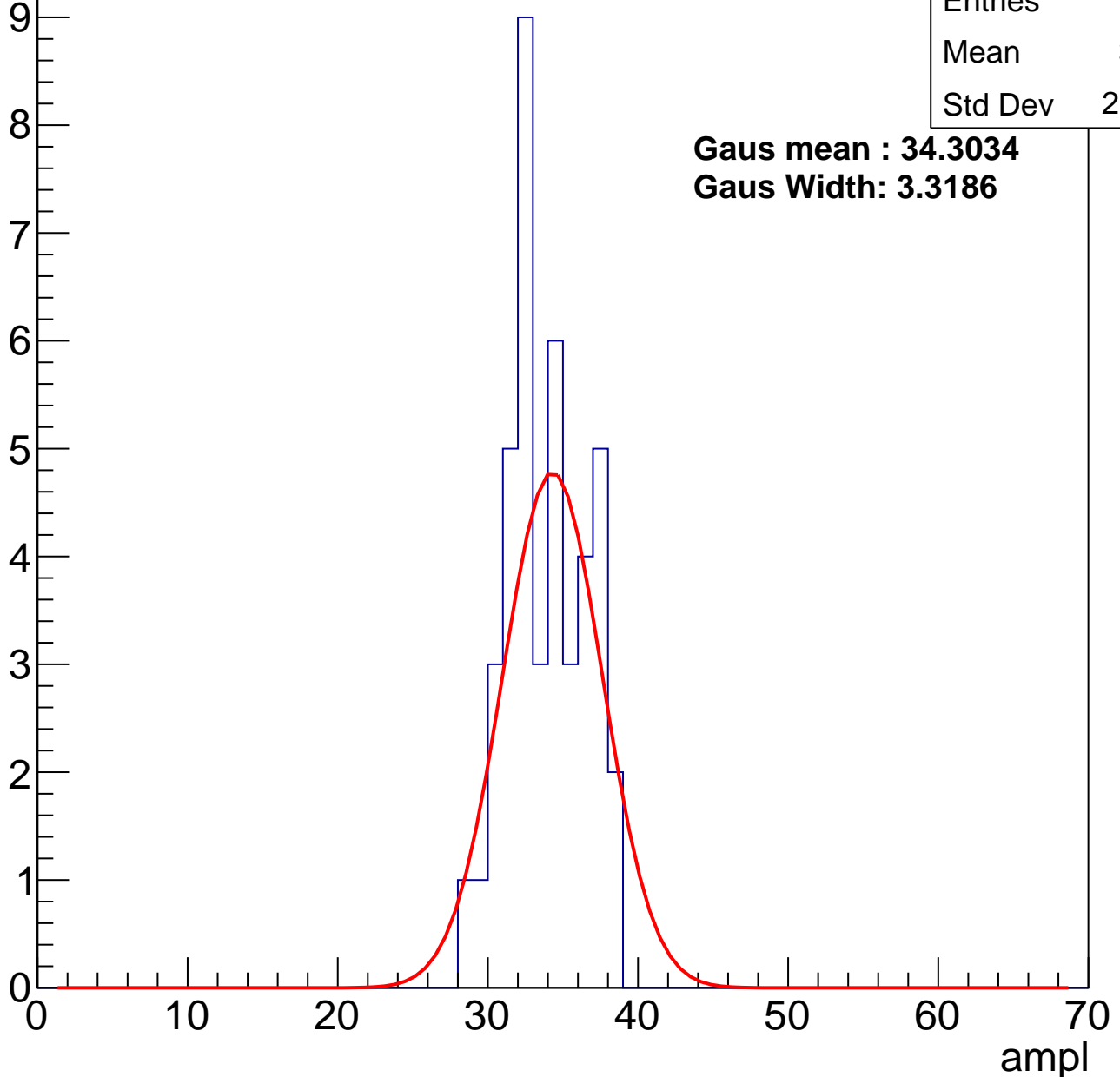
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	33.4
Std Dev	2.555

**Gaus mean : 34.3034**

**Gaus Width: 3.3186**



# B1L002S, U8-ch124, adc2

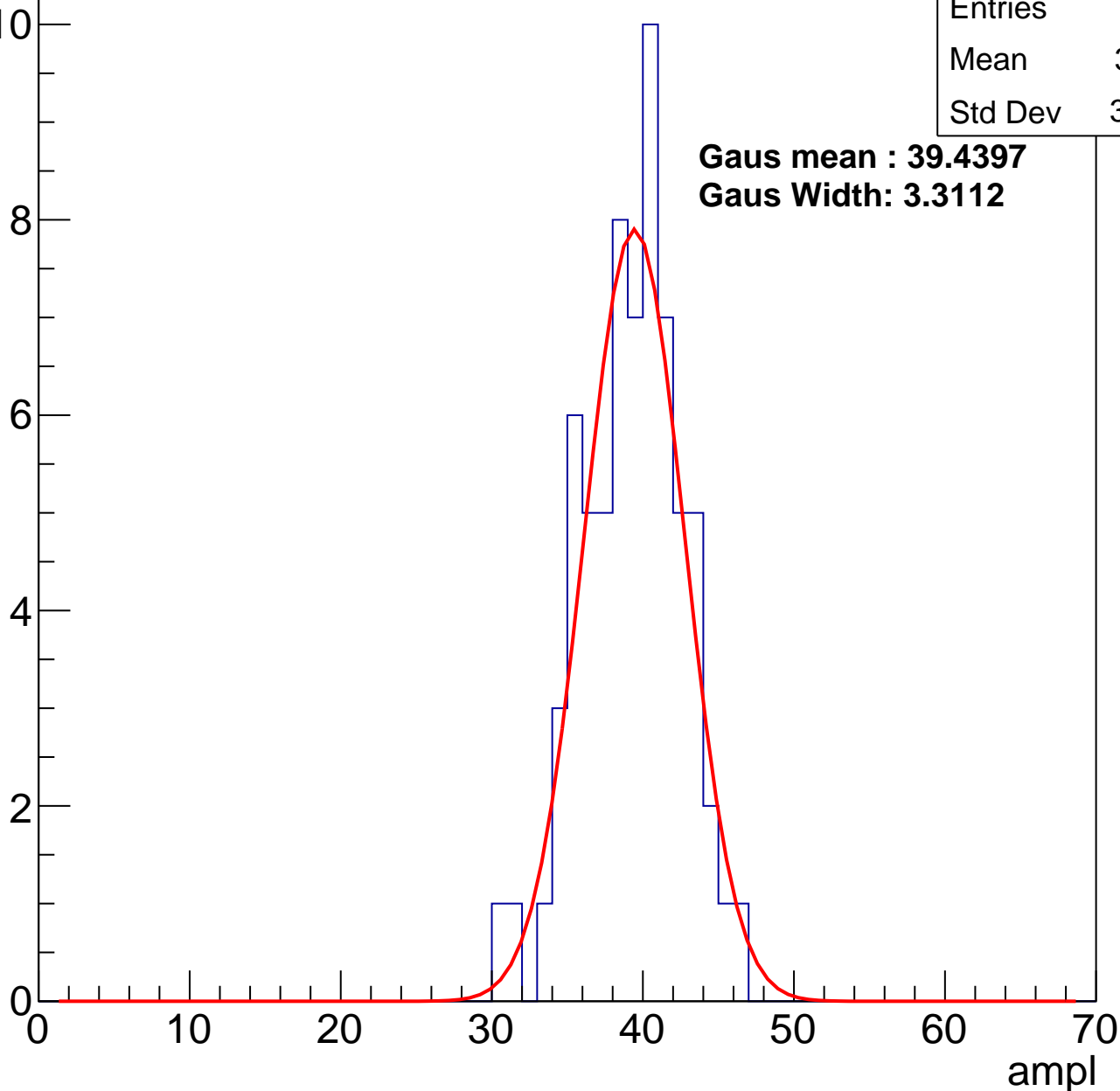
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	38.81
Std Dev	3.255

**Gaus mean : 39.4397**

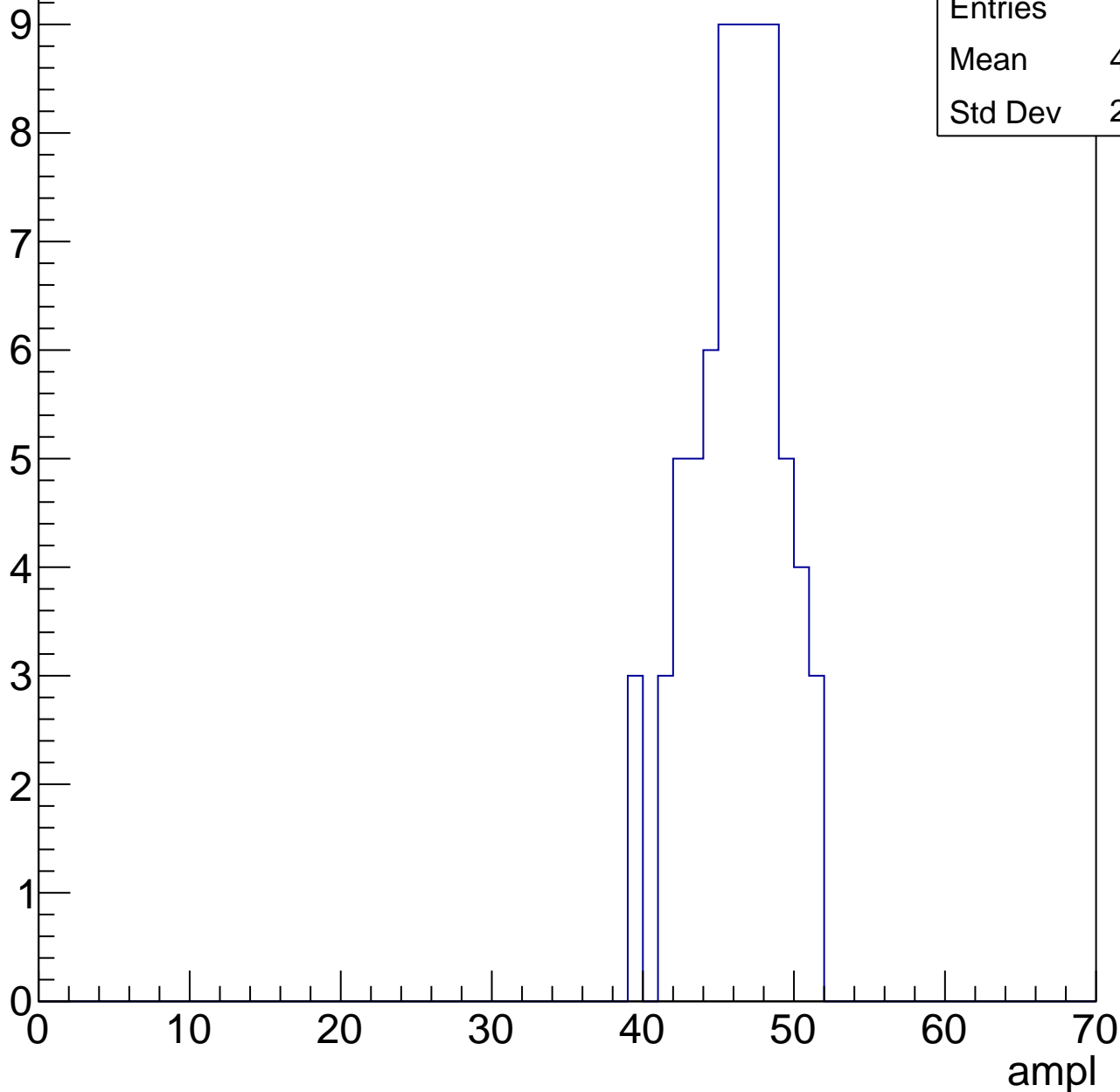
**Gaus Width: 3.3112**



# B1L002S, U8-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



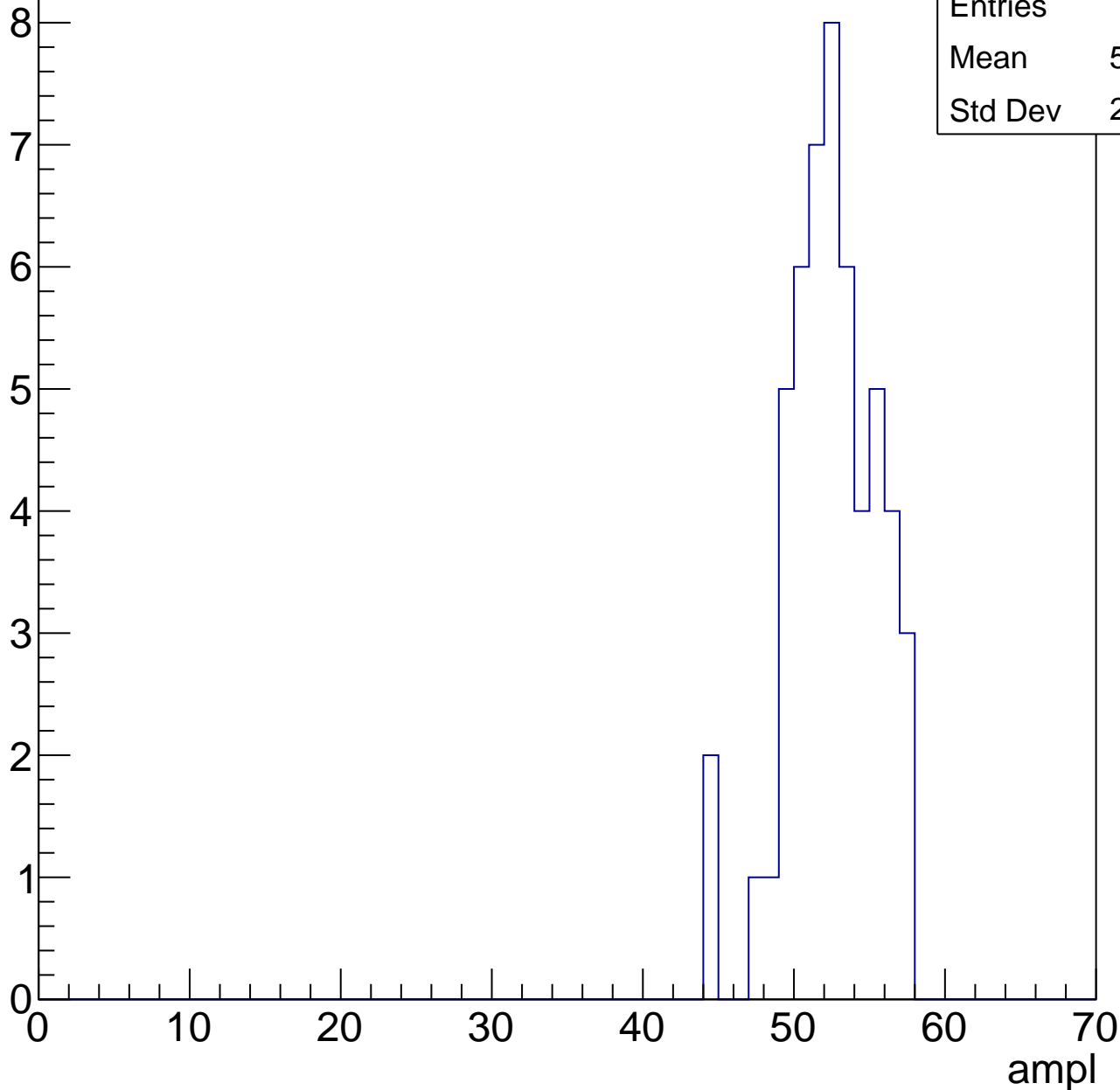
Entries	70
Mean	45.73
Std Dev	2.937

# B1L002S, U8-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.02
Std Dev	2.945

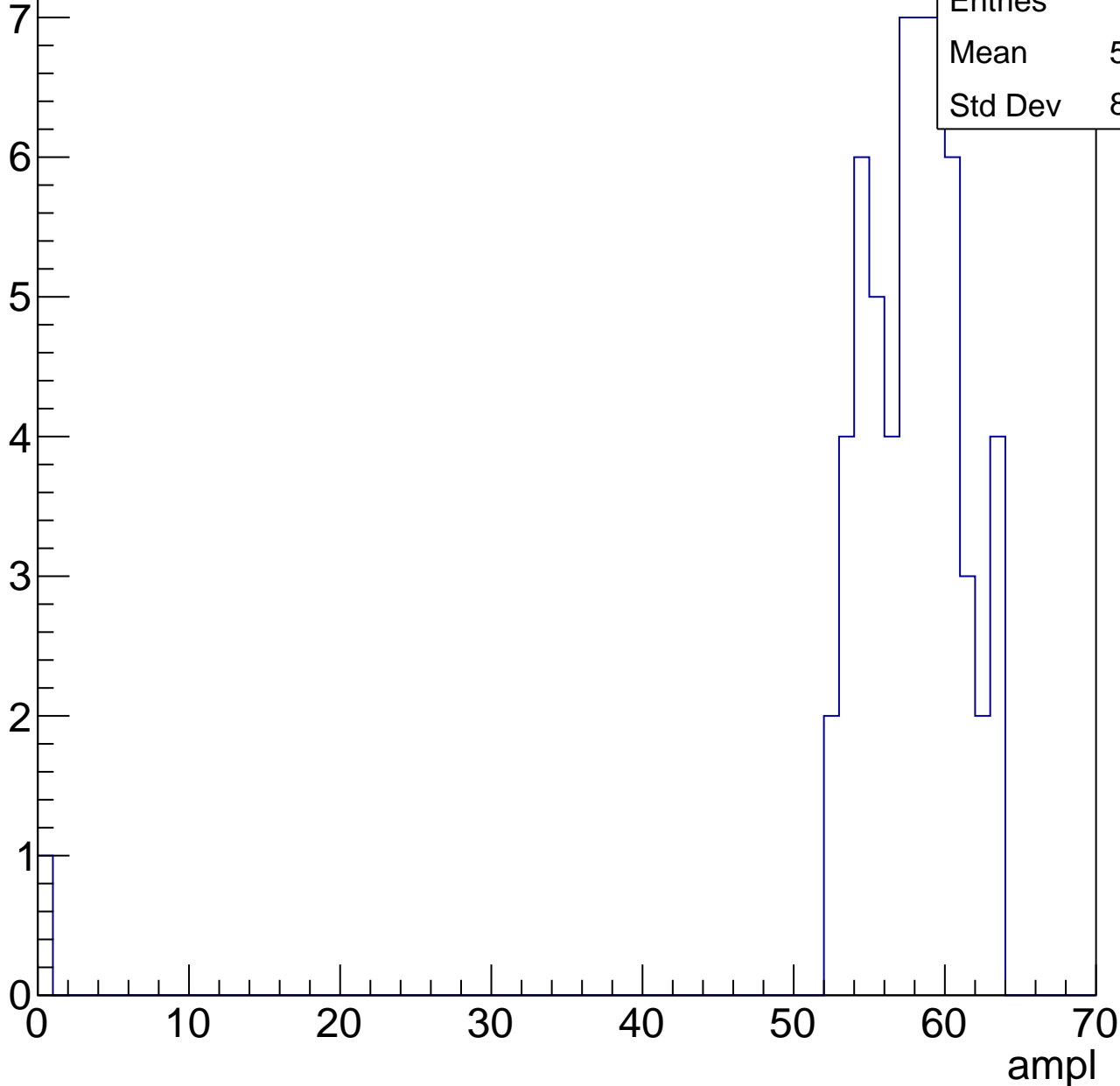


# B1L002S, U8-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.48
Std Dev	8.048

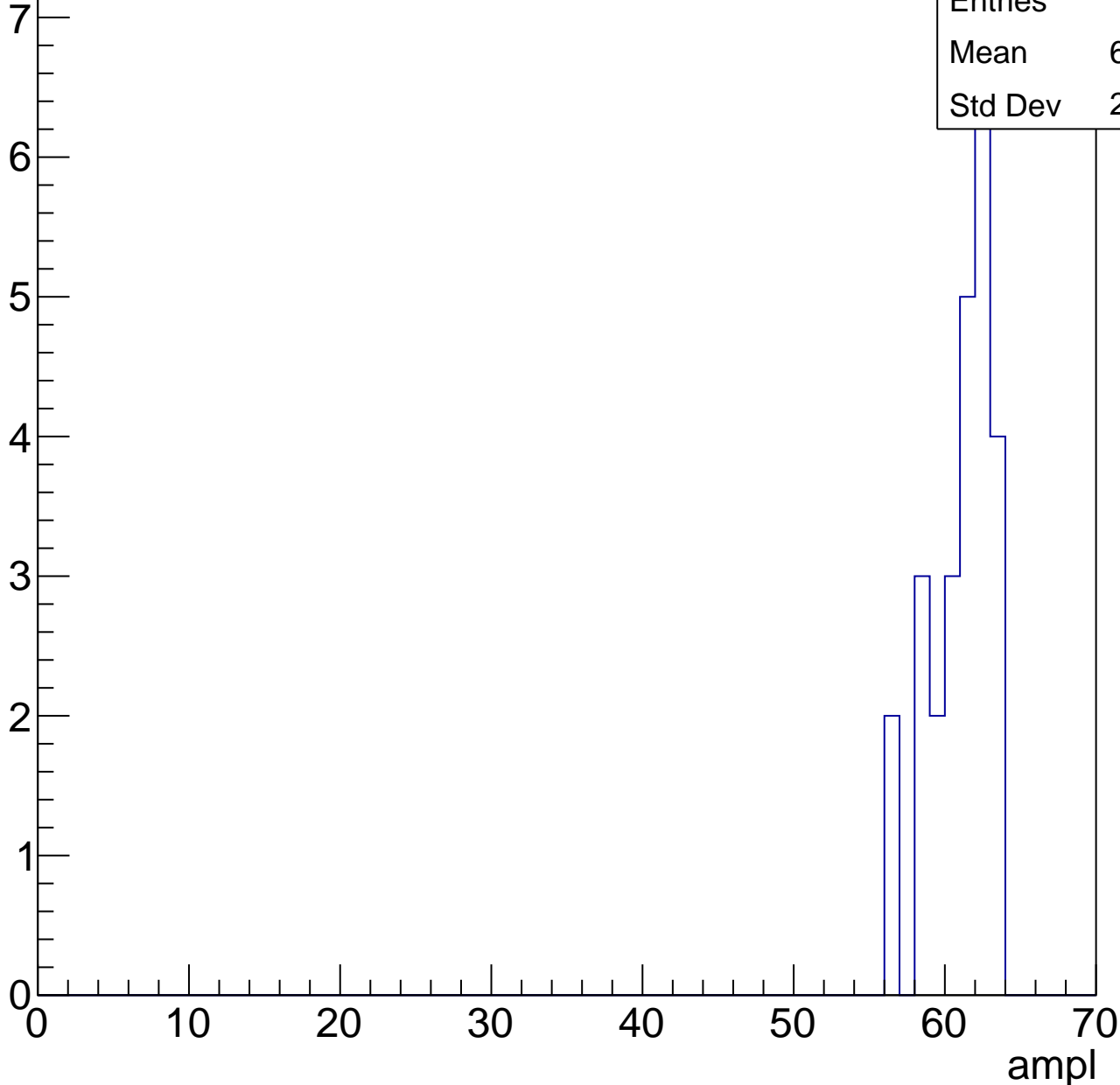


# B1L002S, U8-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	60.58
Std Dev	2.022





# B1L002S, U8-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

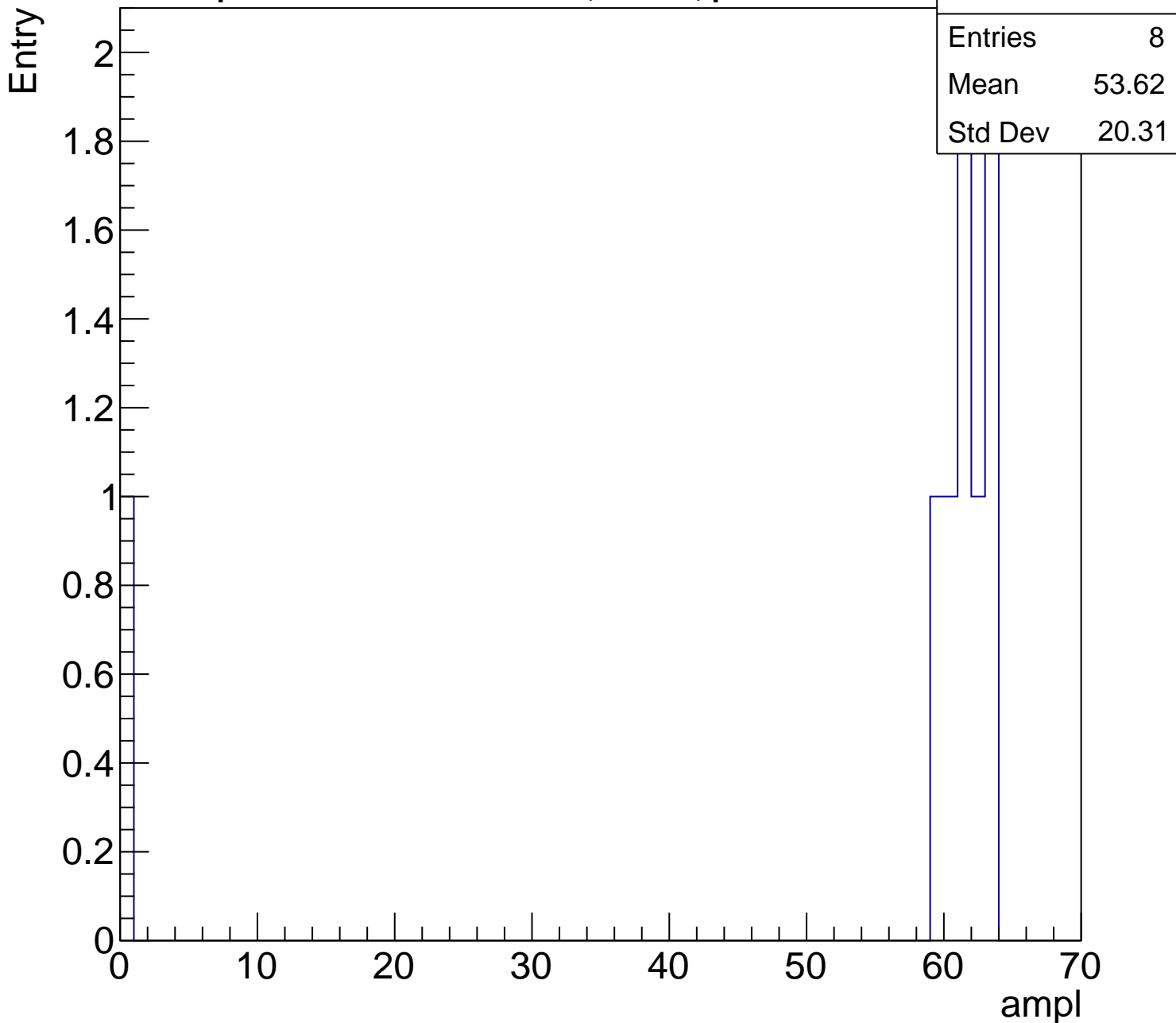
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.62
Std Dev	20.31

0 10 20 30 40 50 60 70

ampl



# B1L002S, U8-ch125, adc0

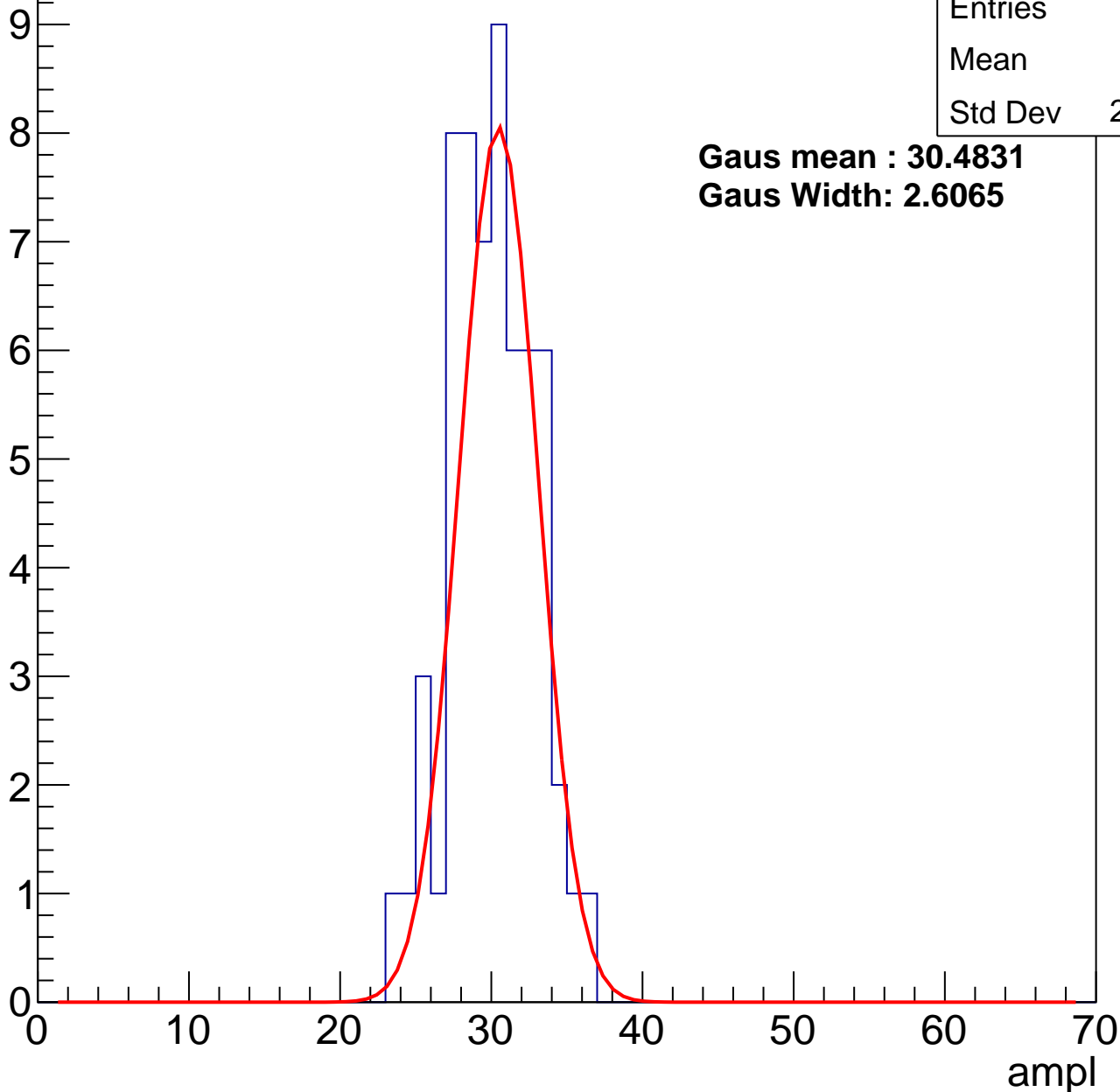
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	29.6
Std Dev	2.752

**Gaus mean : 30.4831**

**Gaus Width: 2.6065**



# B1L002S, U8-ch125, adc1

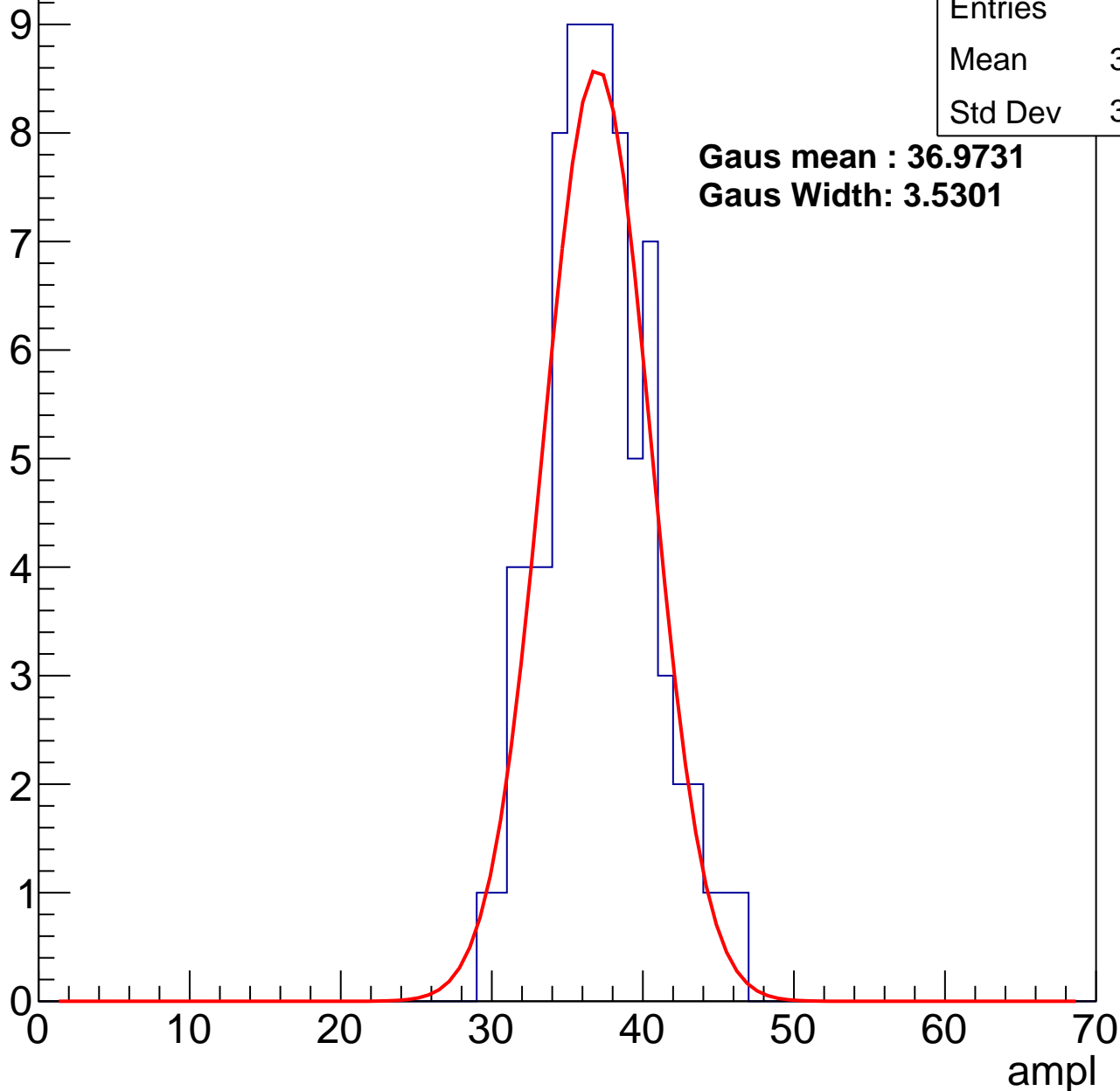
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	36.63
Std Dev	3.526

**Gaus mean : 36.9731**

**Gaus Width: 3.5301**



# B1L002S, U8-ch125, adc2

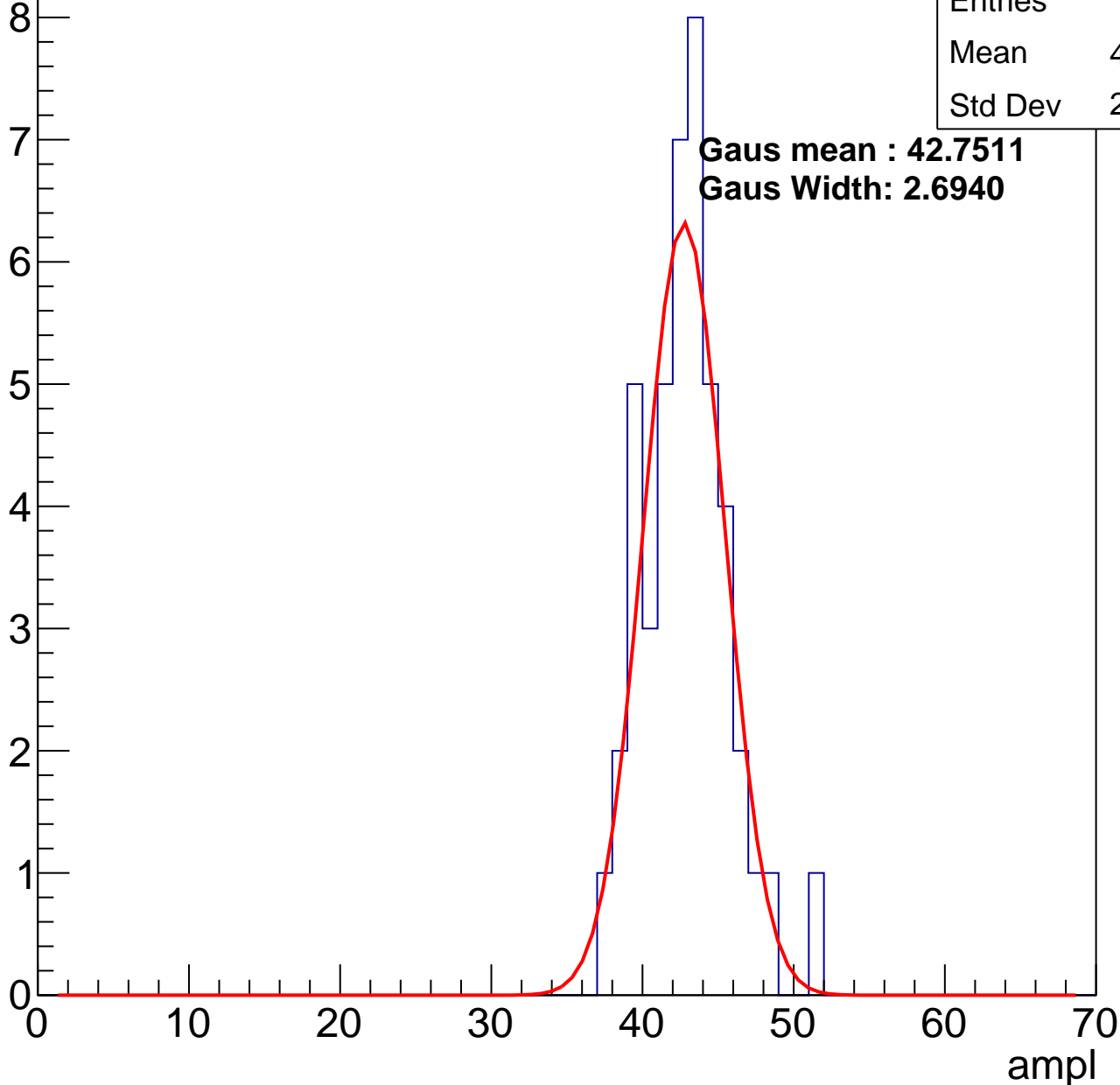
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	42.42
Std Dev	2.785

**Gaus mean : 42.7511**

**Gaus Width: 2.6940**



# B1L002S, U8-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

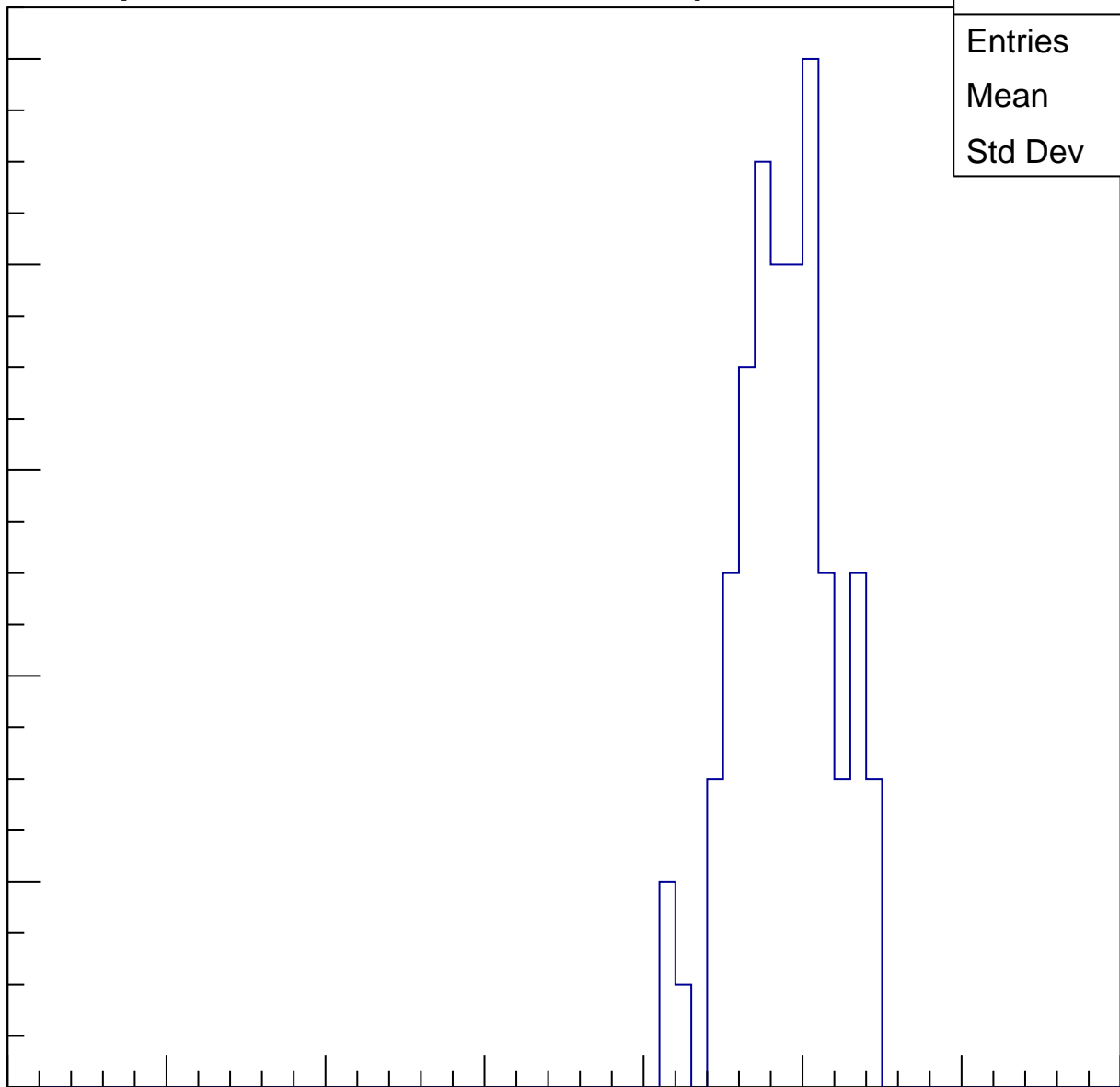
Entries	69
Mean	48.41
Std Dev	3.014

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U8-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	57
Mean	54.49
Std Dev	3.223

Entry

10

8

6

4

2

0

0

10

20

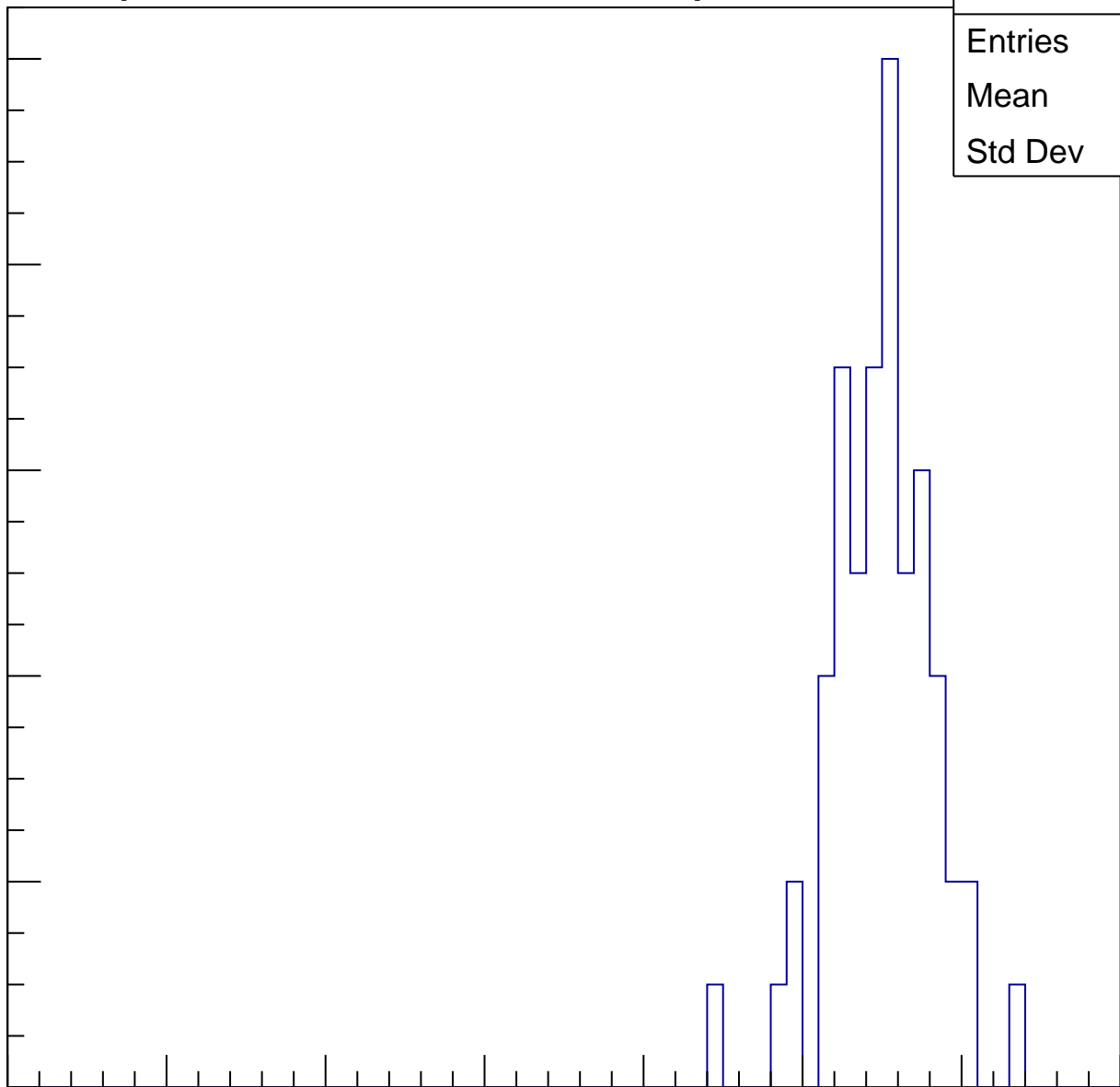
30

40

50

60

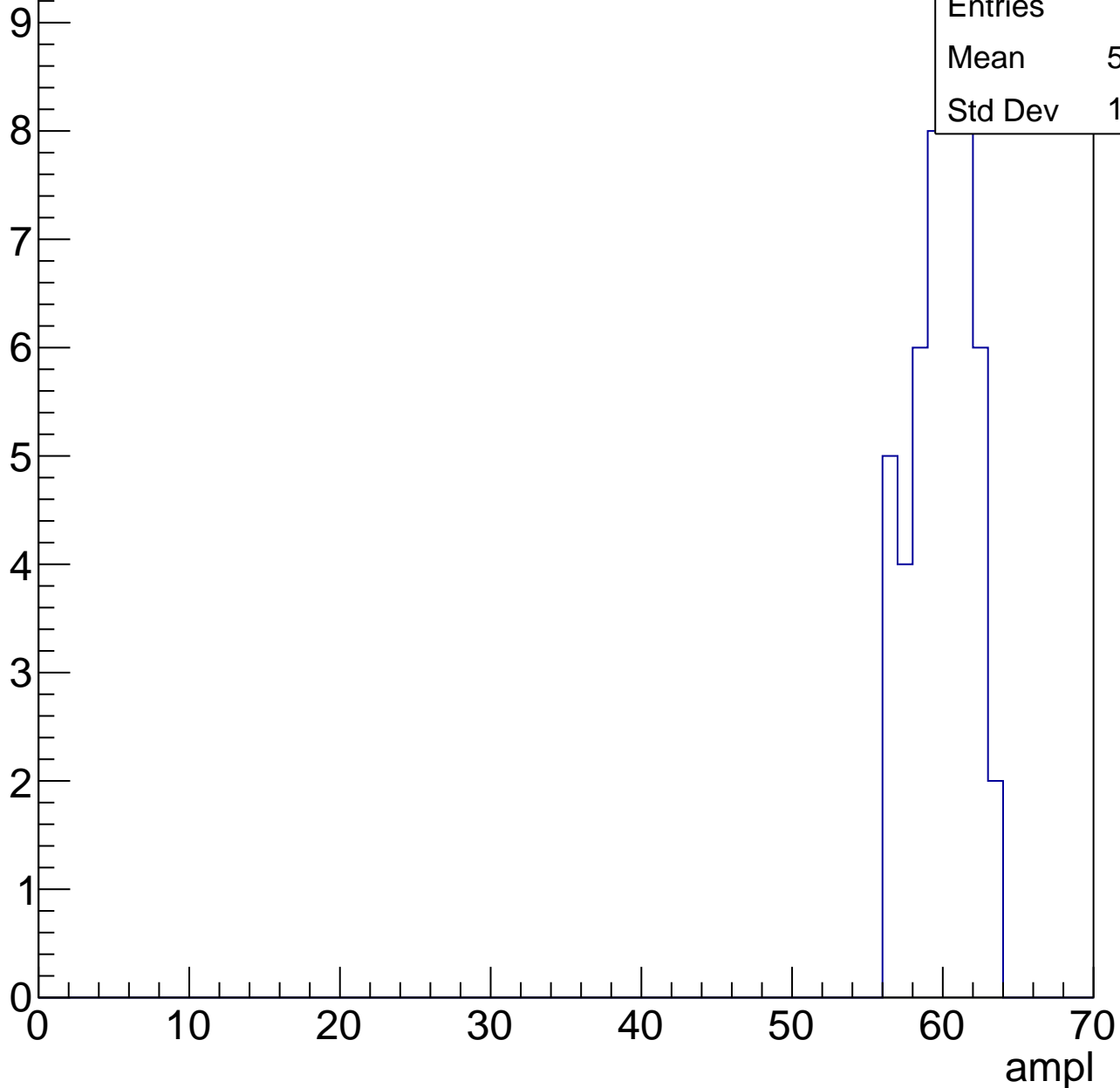
ampl



# B1L002S, U8-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



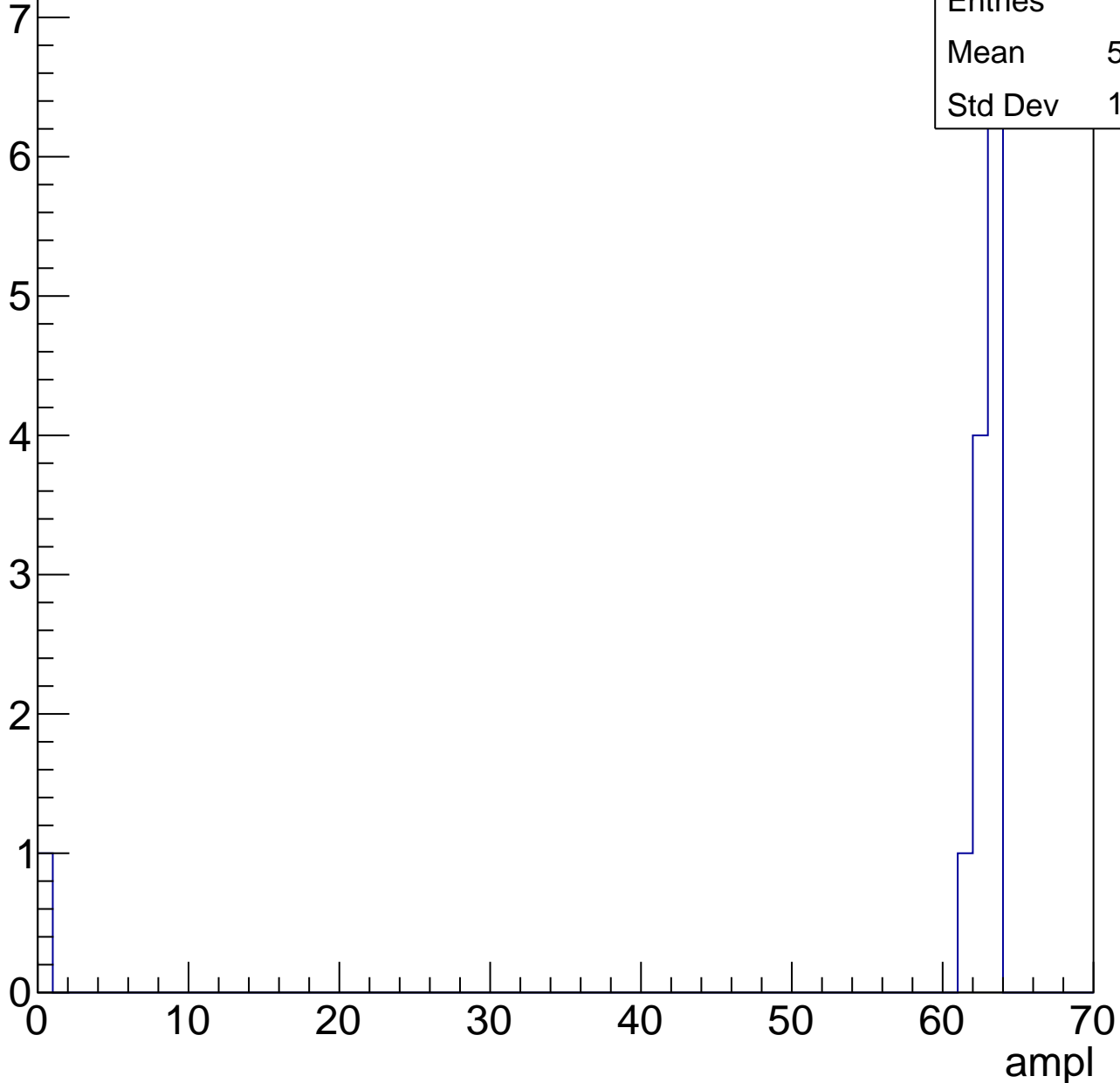
Entries	48
Mean	59.48
Std Dev	1.968

# B1L002S, U8-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	57.69
Std Dev	16.67





# B1L002S, U8-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U8-ch126, adc0

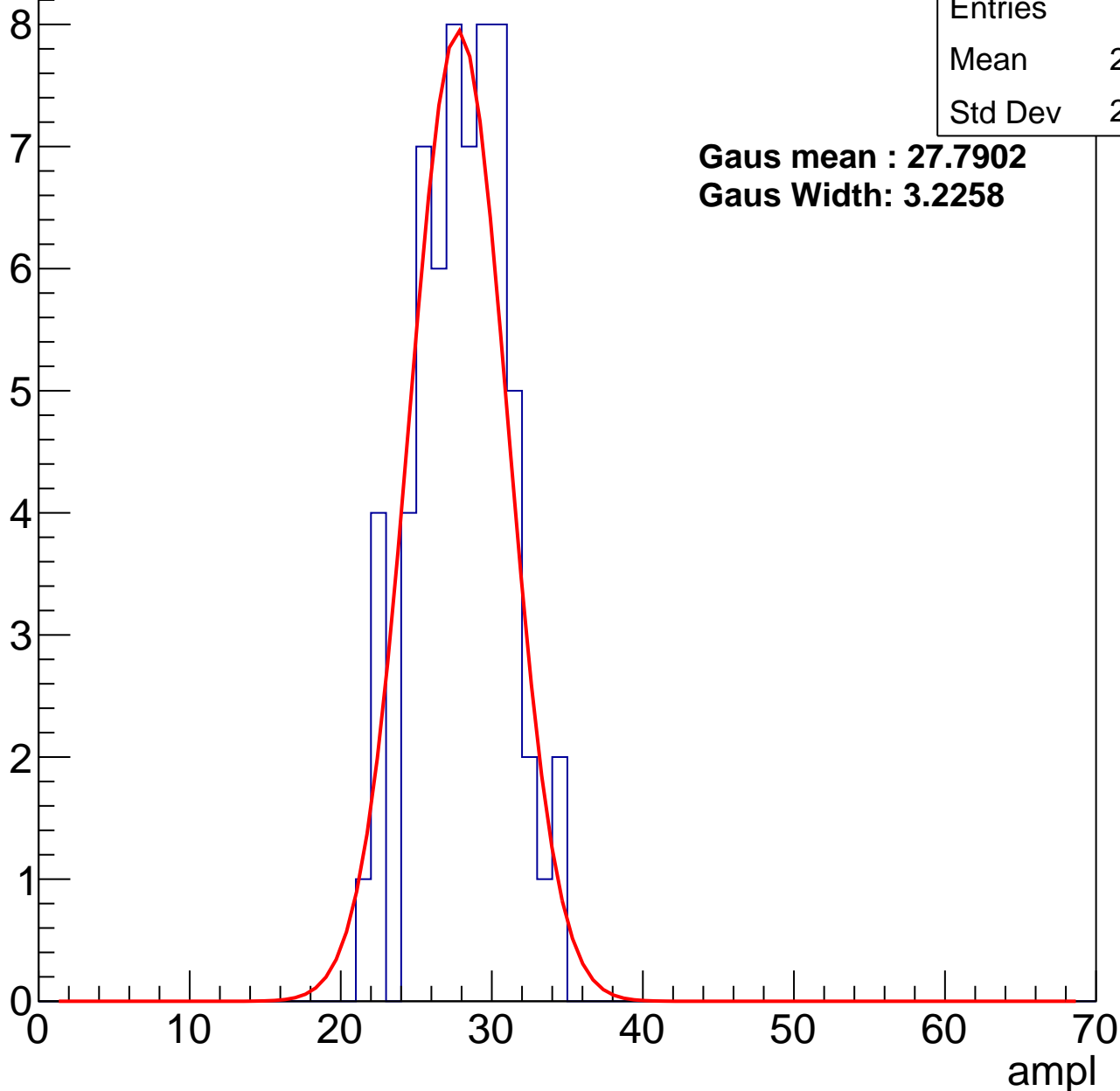
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.62
Std Dev	2.973

**Gaus mean : 27.7902**

**Gaus Width: 3.2258**



# B1L002S, U8-ch126, adc1

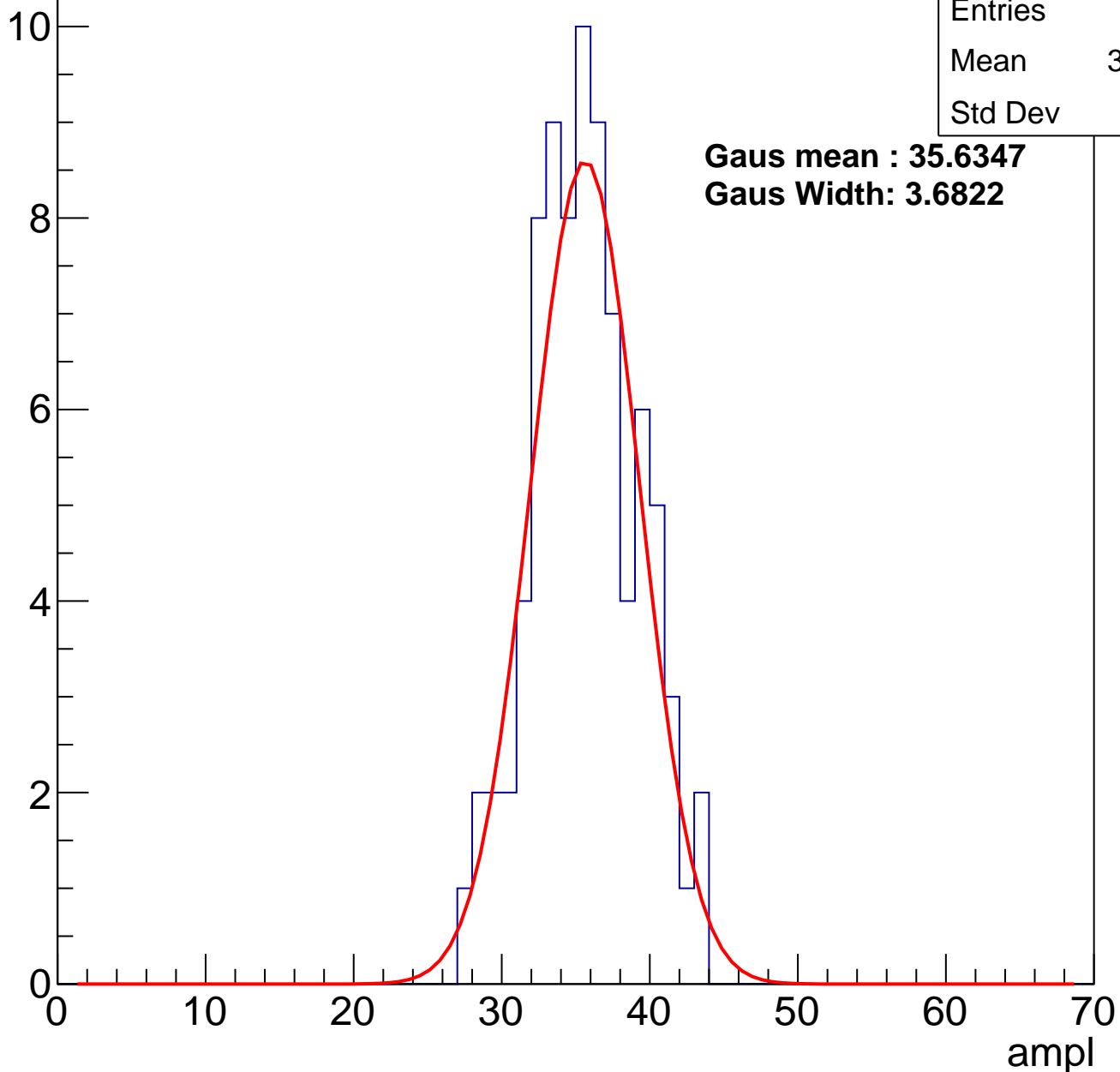
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	35.18
Std Dev	3.54

**Gaus mean : 35.6347**

**Gaus Width: 3.6822**

Entry



# B1L002S, U8-ch126, adc2

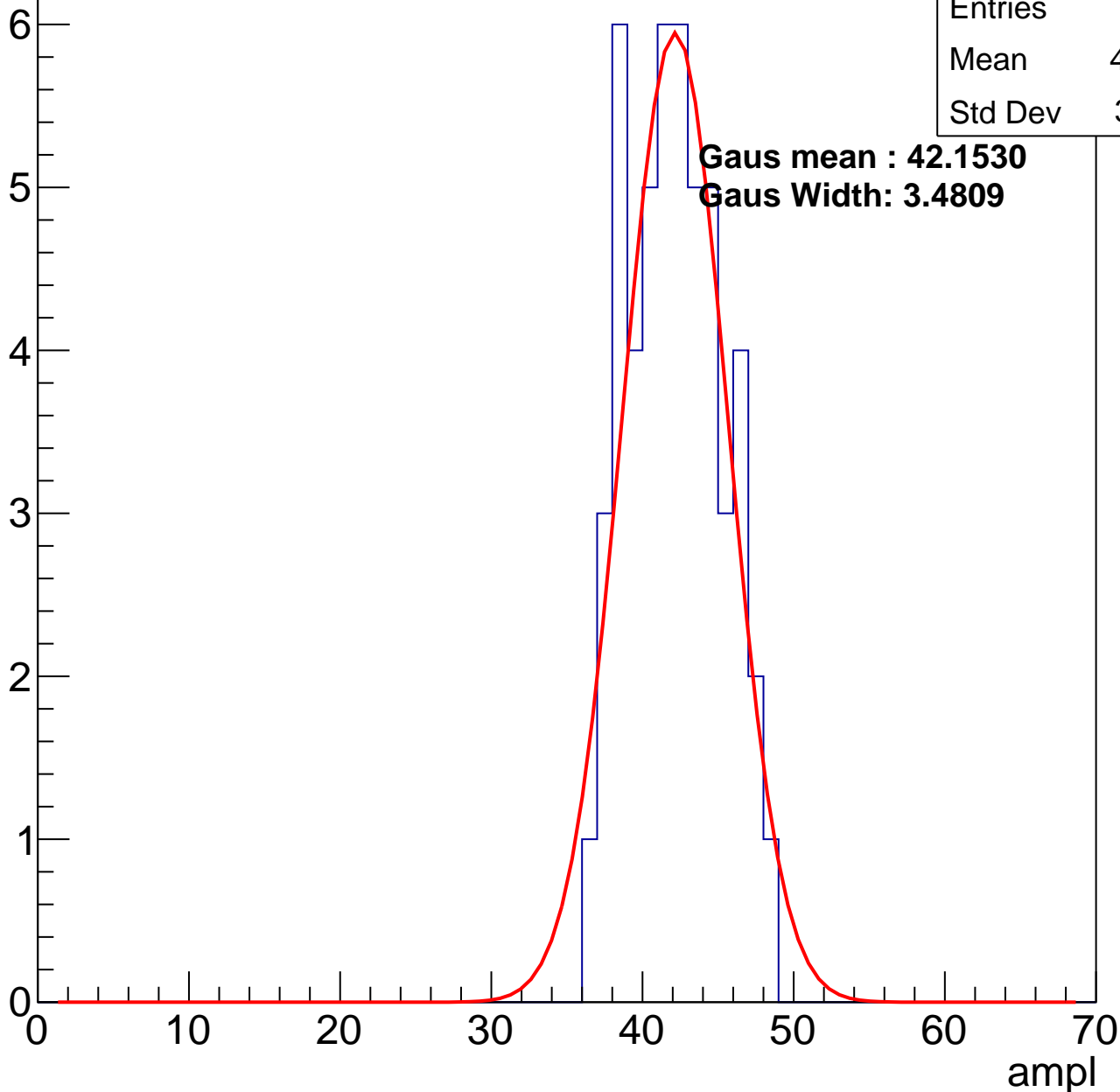
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.67
Std Dev	3.021

**Gaus mean : 42.1530**

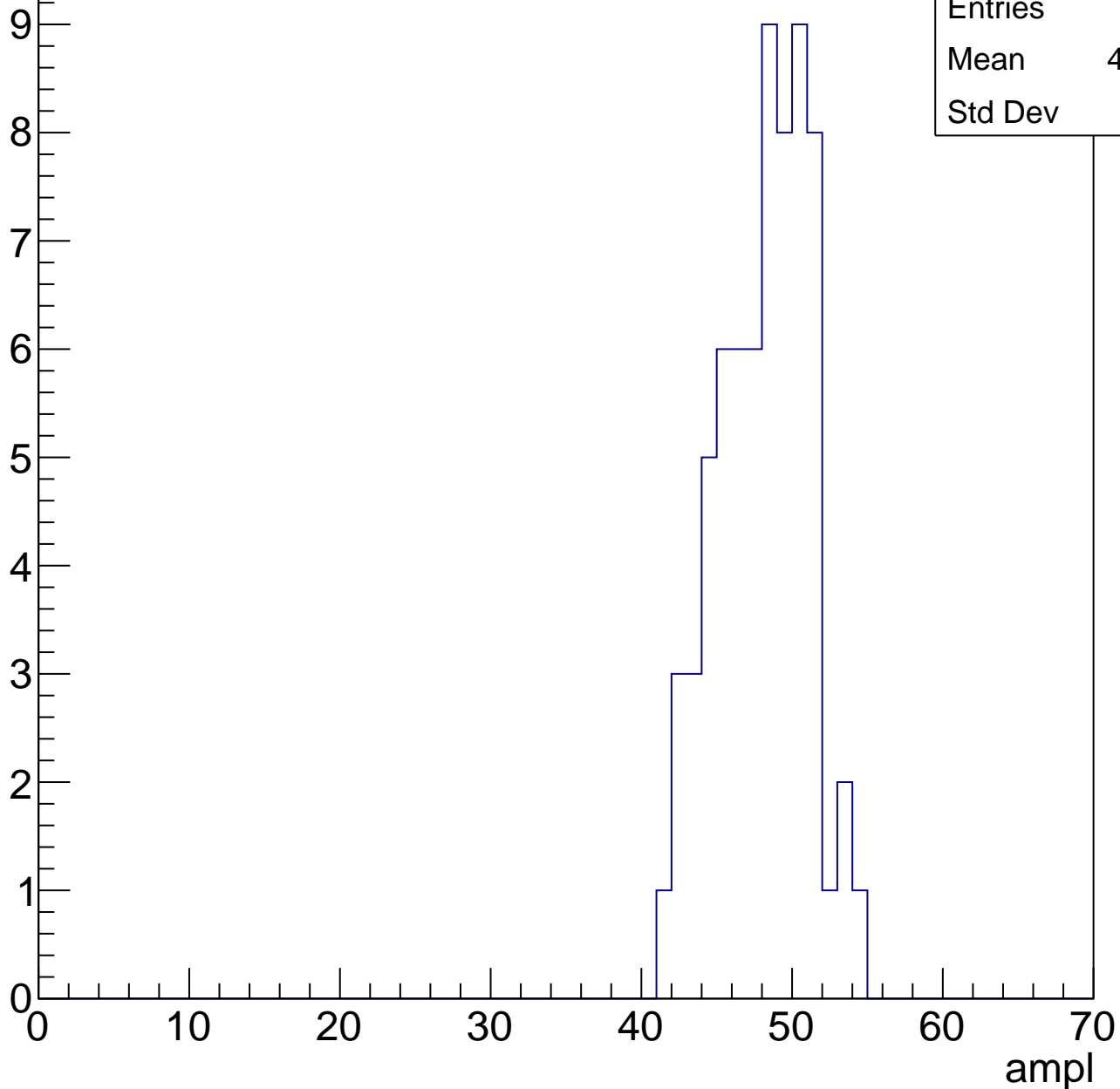
**Gaus Width: 3.4809**



# B1L002S, U8-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

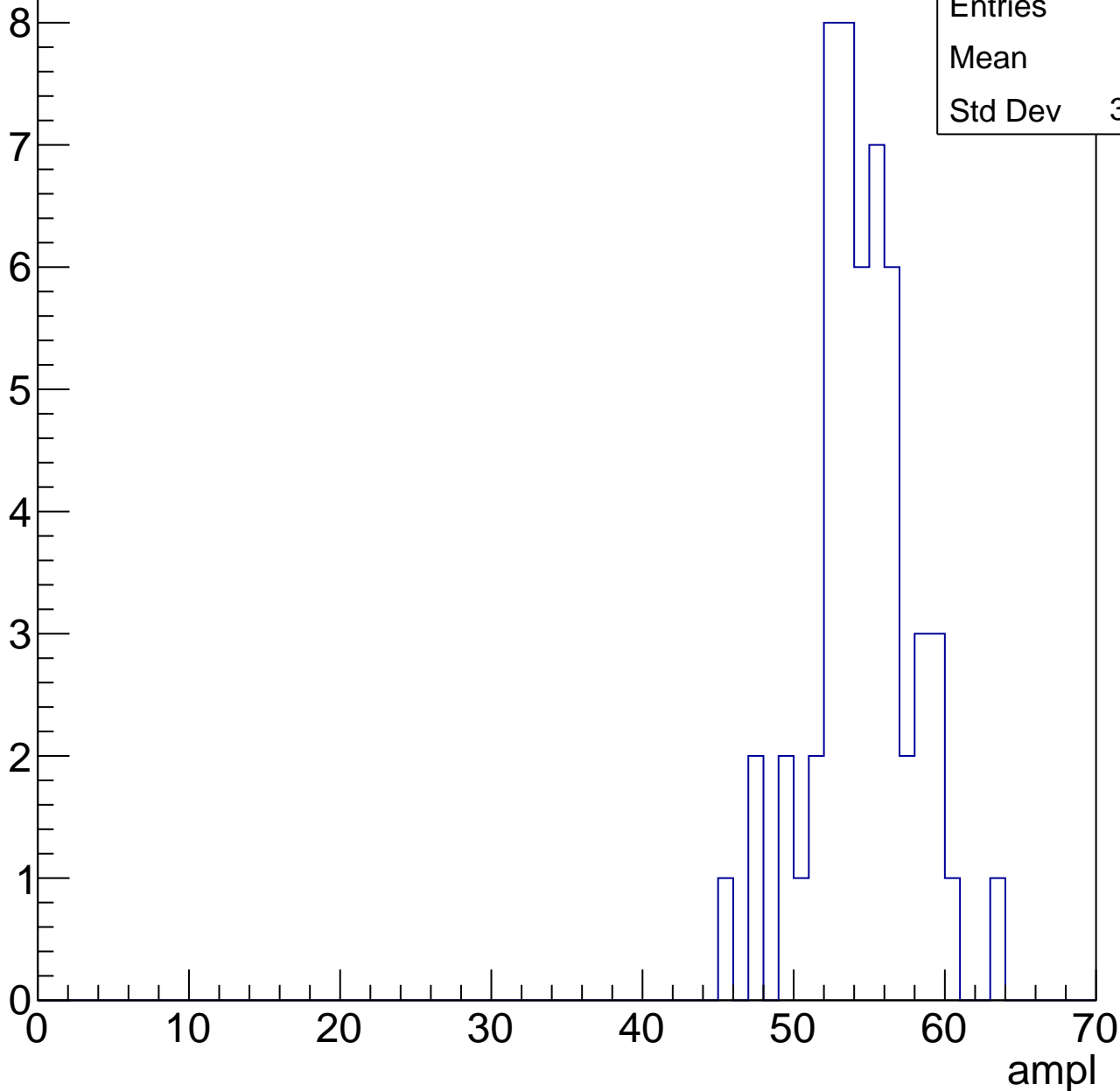


# B1L002S, U8-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	54
Std Dev	3.342

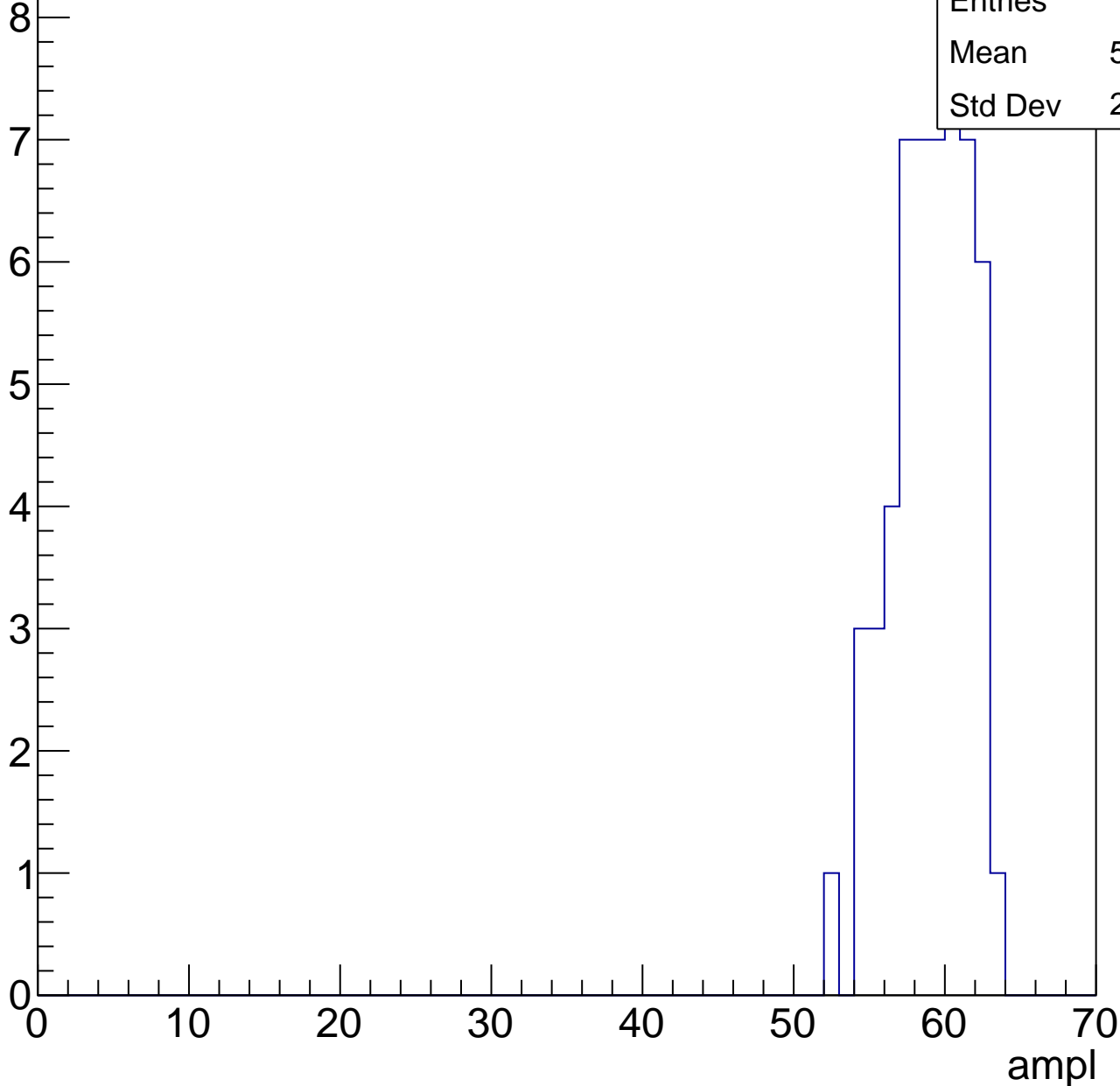


# B1L002S, U8-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.57
Std Dev	2.506

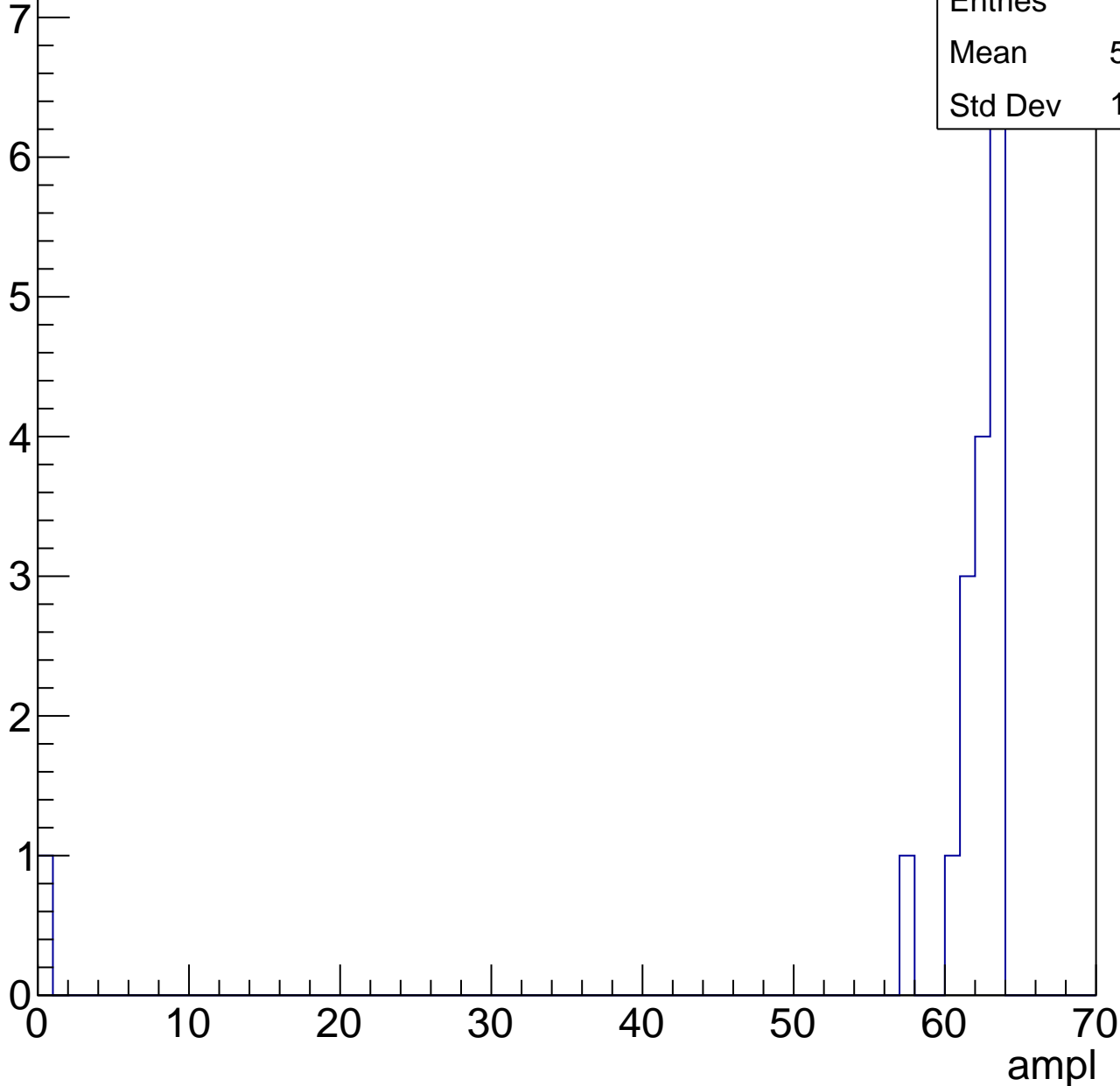


# B1L002S, U8-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58.18
Std Dev	14.62





# B1L002S, U8-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L002S, U8-ch127, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	28.78
Std Dev	3.19

**Gaus mean : 28.7177**

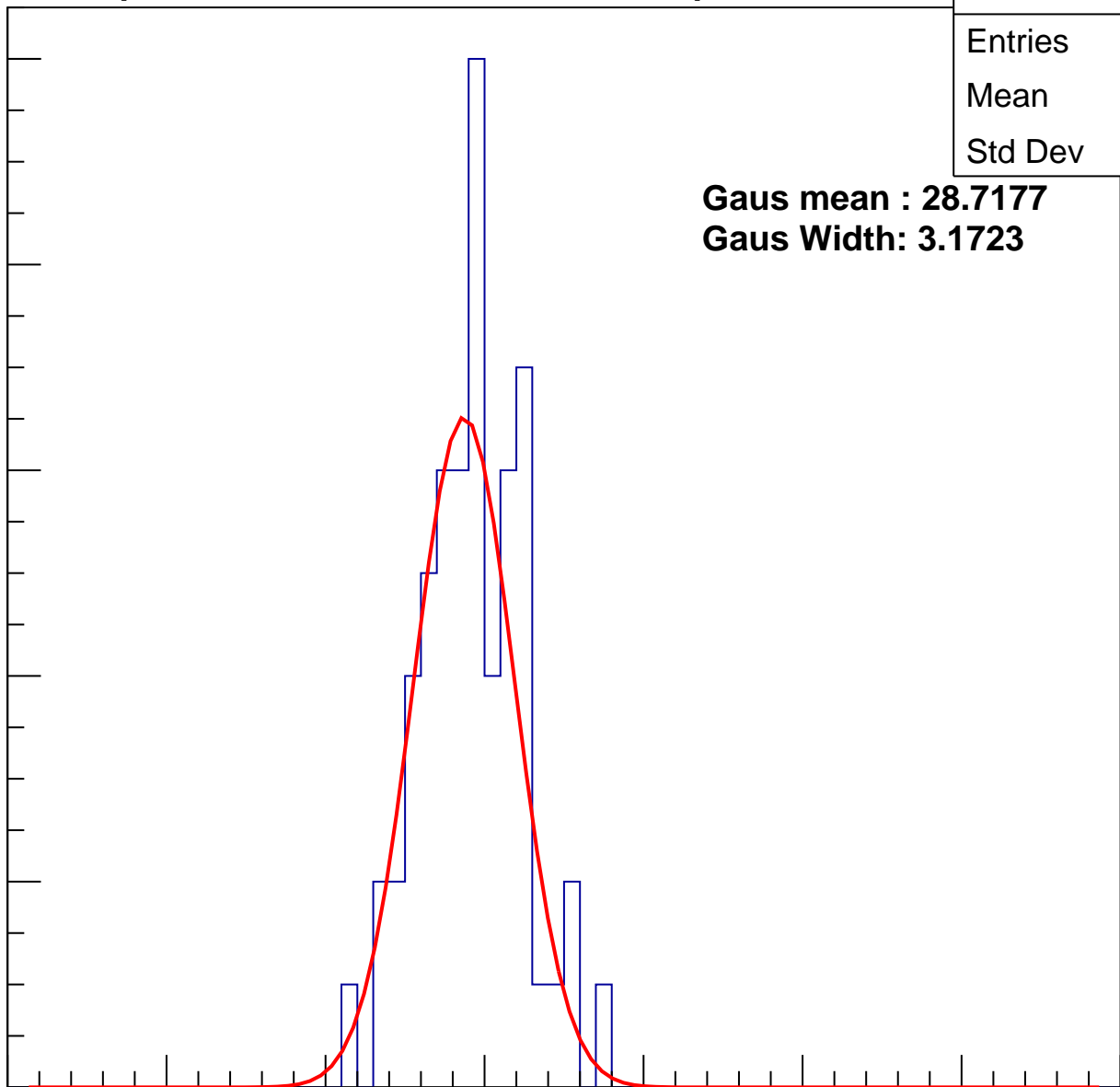
**Gaus Width: 3.1723**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U8-ch127, adc1

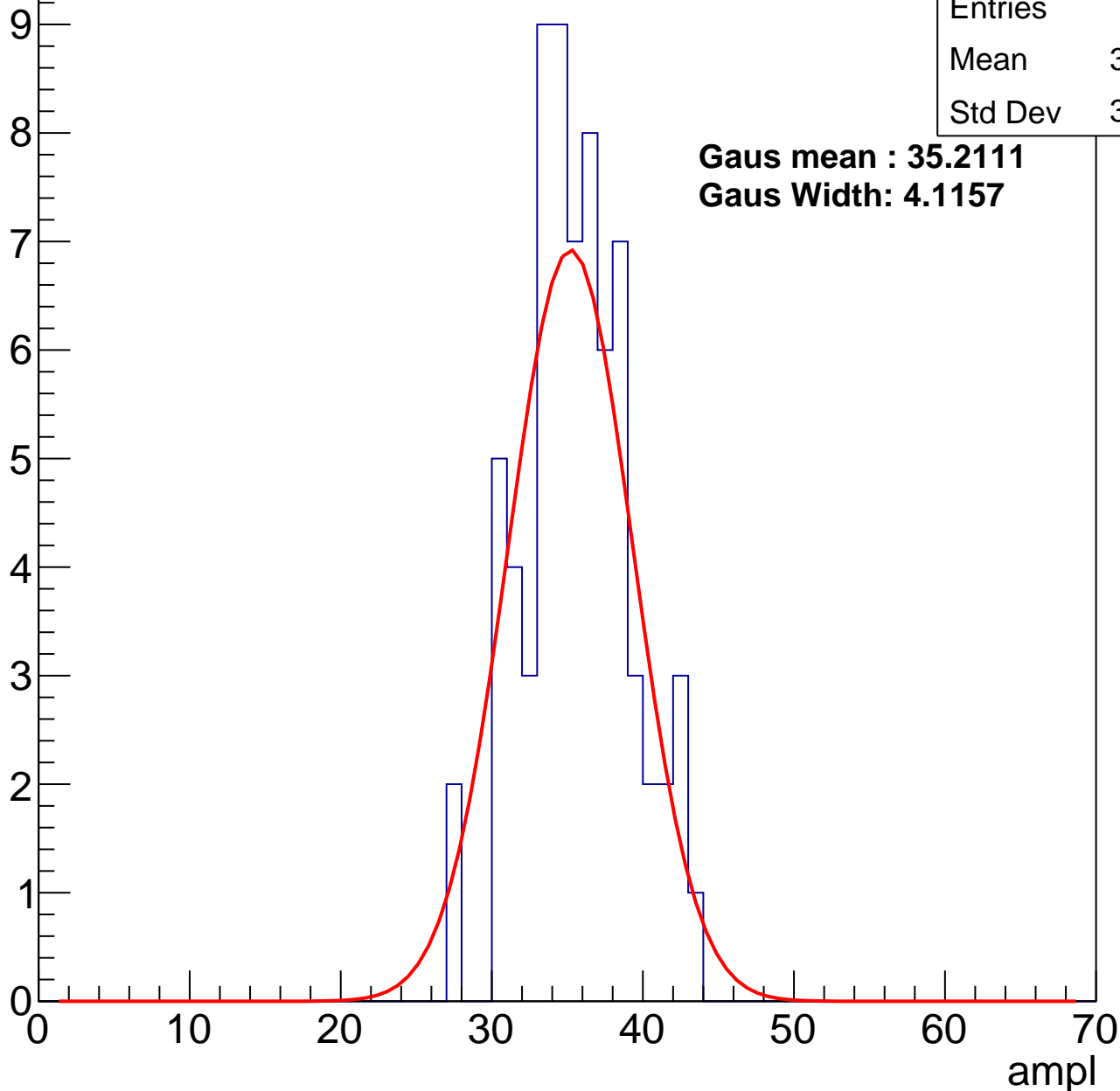
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	35.15
Std Dev	3.487

**Gaus mean : 35.2111**

**Gaus Width: 4.1157**



# B1L002S, U8-ch127, adc2

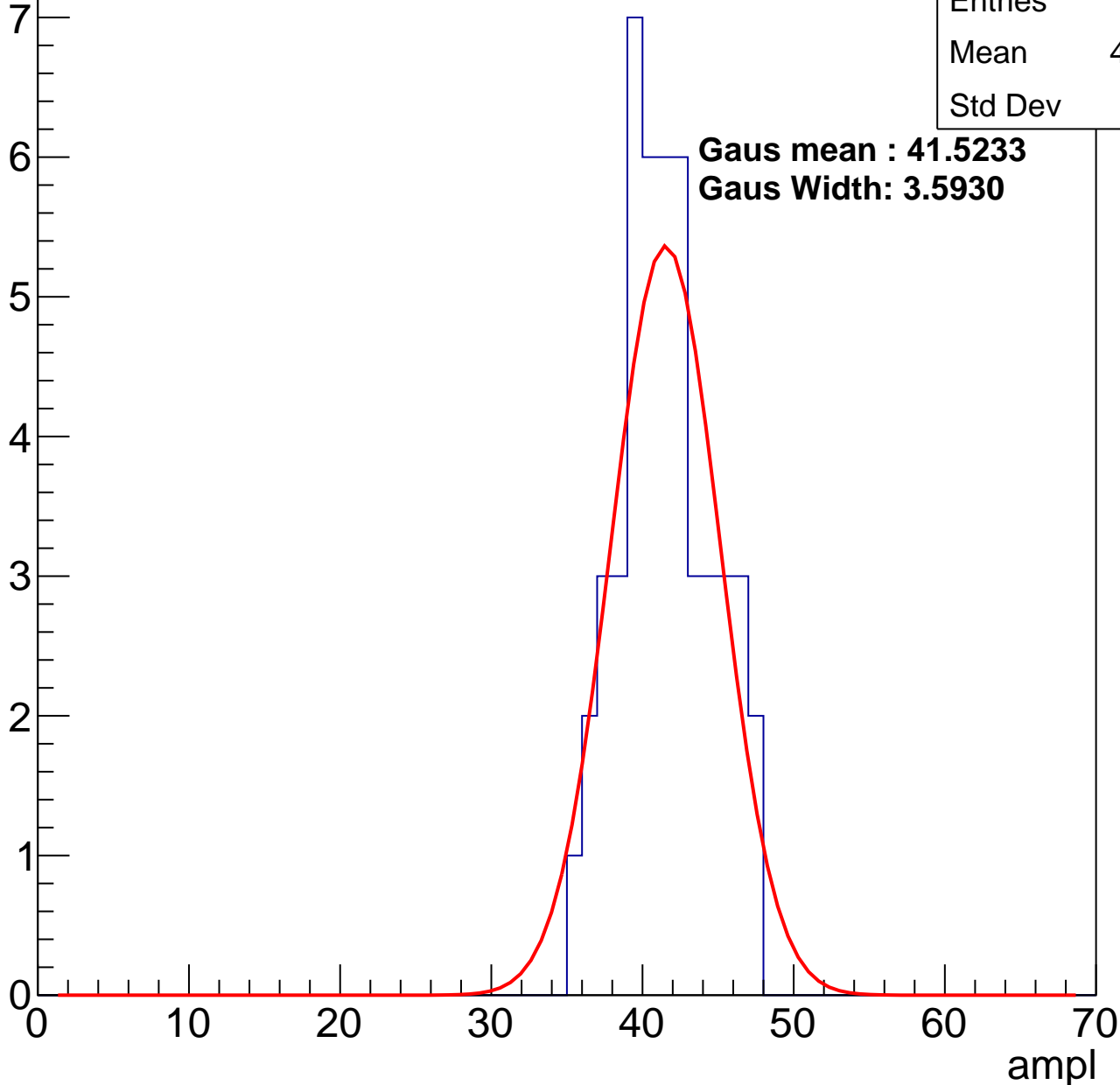
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	41.06
Std Dev	3.01

**Gaus mean : 41.5233**

**Gaus Width: 3.5930**

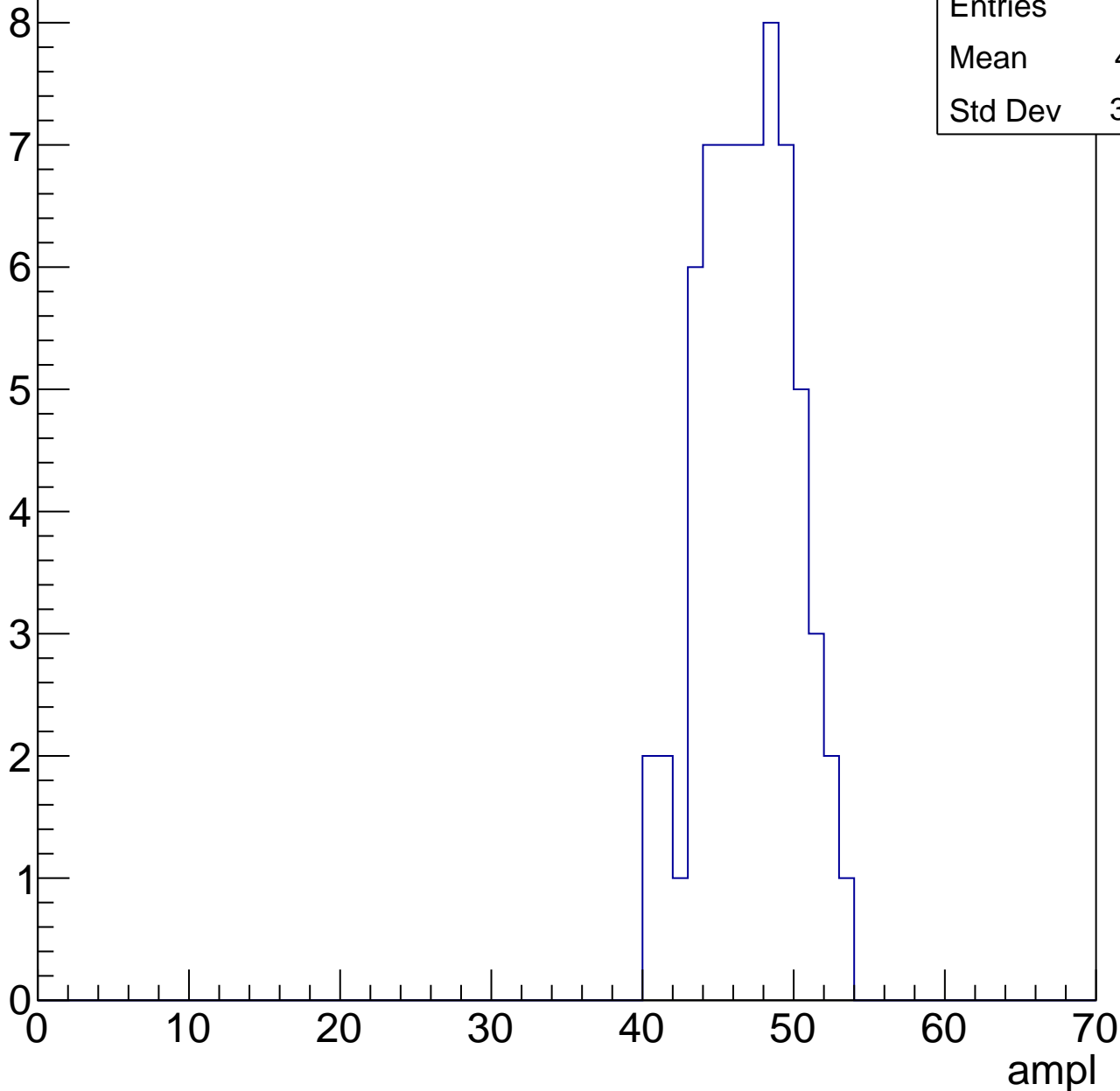


# B1L002S, U8-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.51
Std Dev	3.008

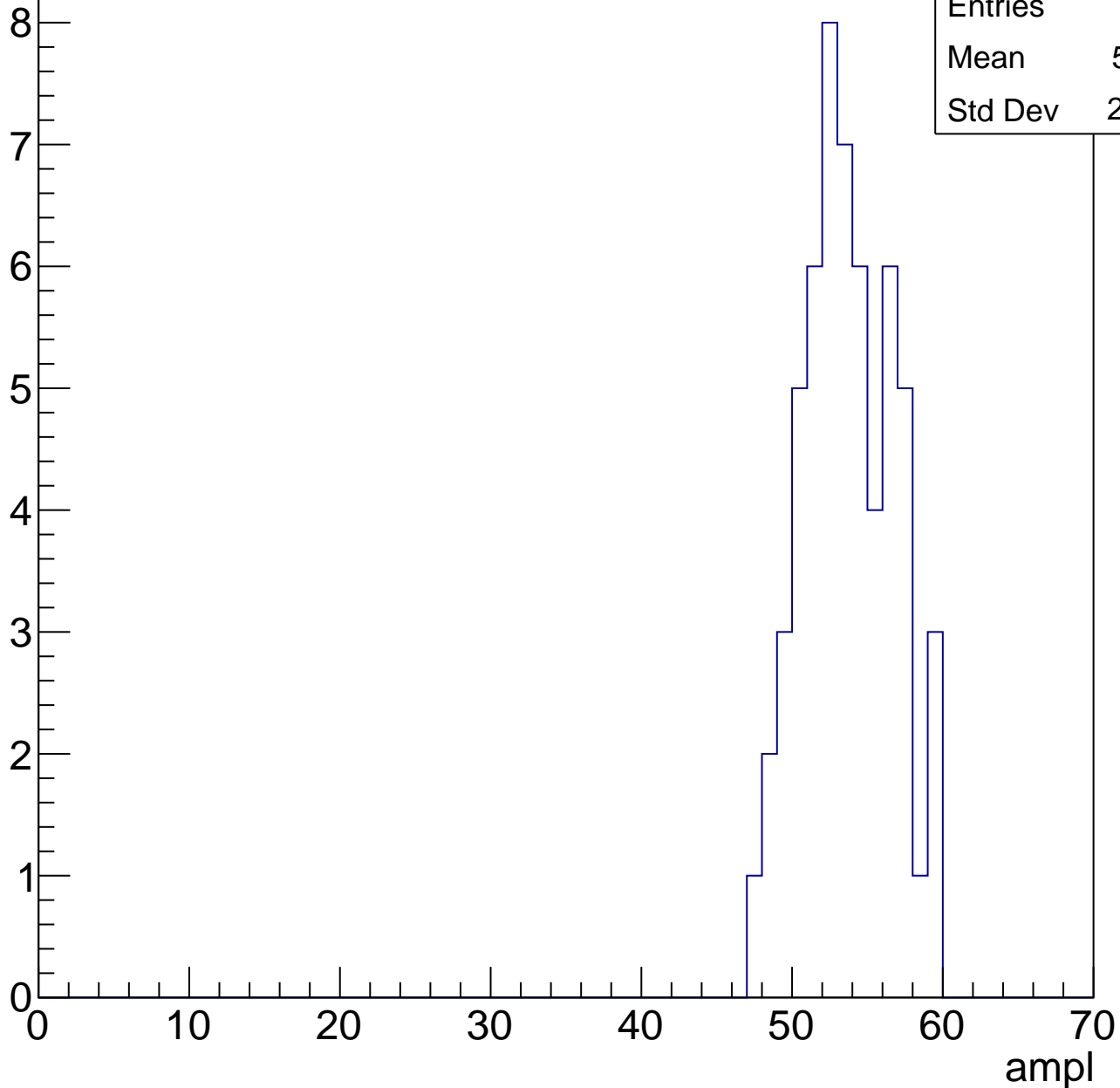


# B1L002S, U8-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.21
Std Dev	2.954

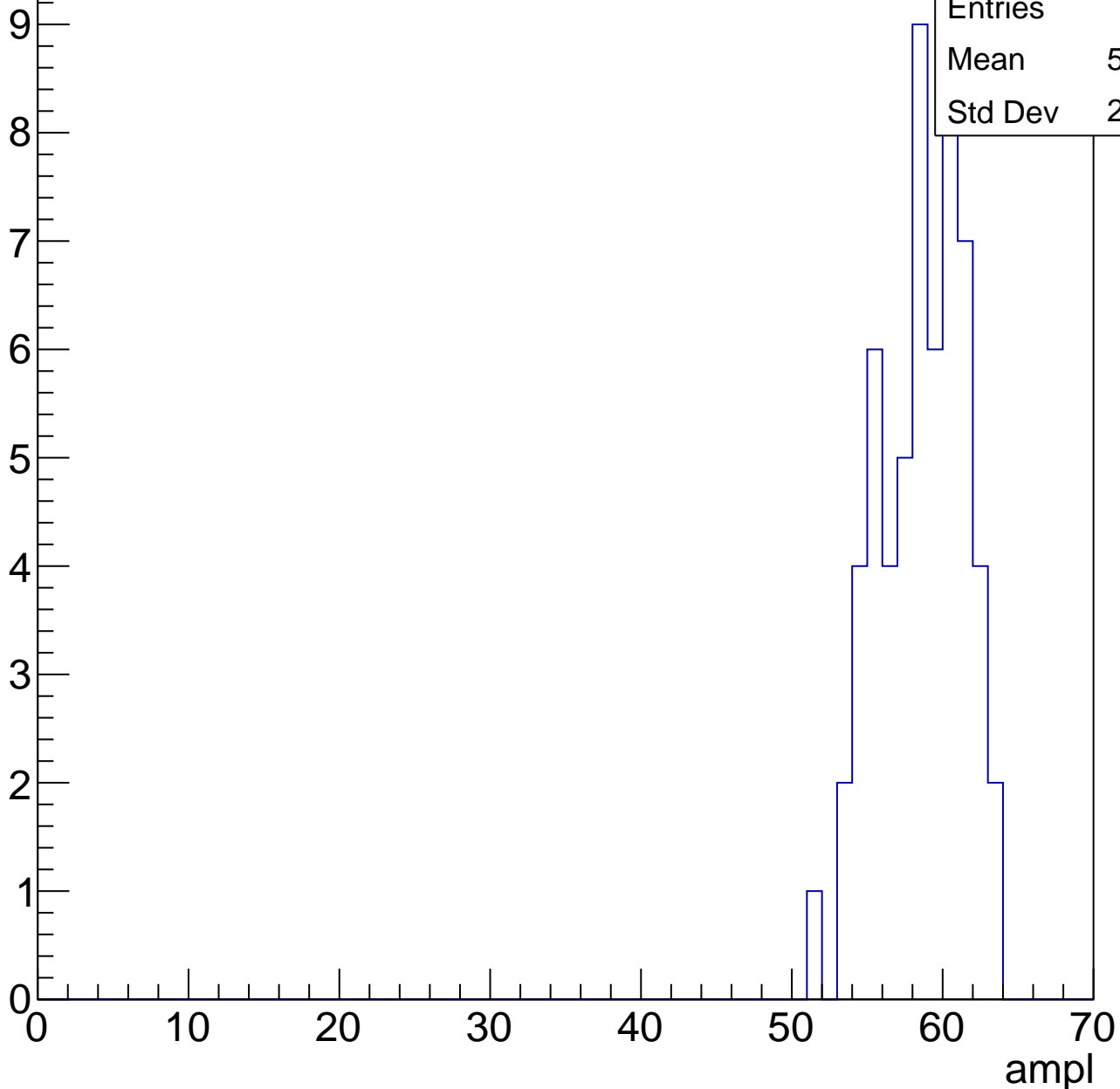


# B1L002S, U8-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.09
Std Dev	2.793

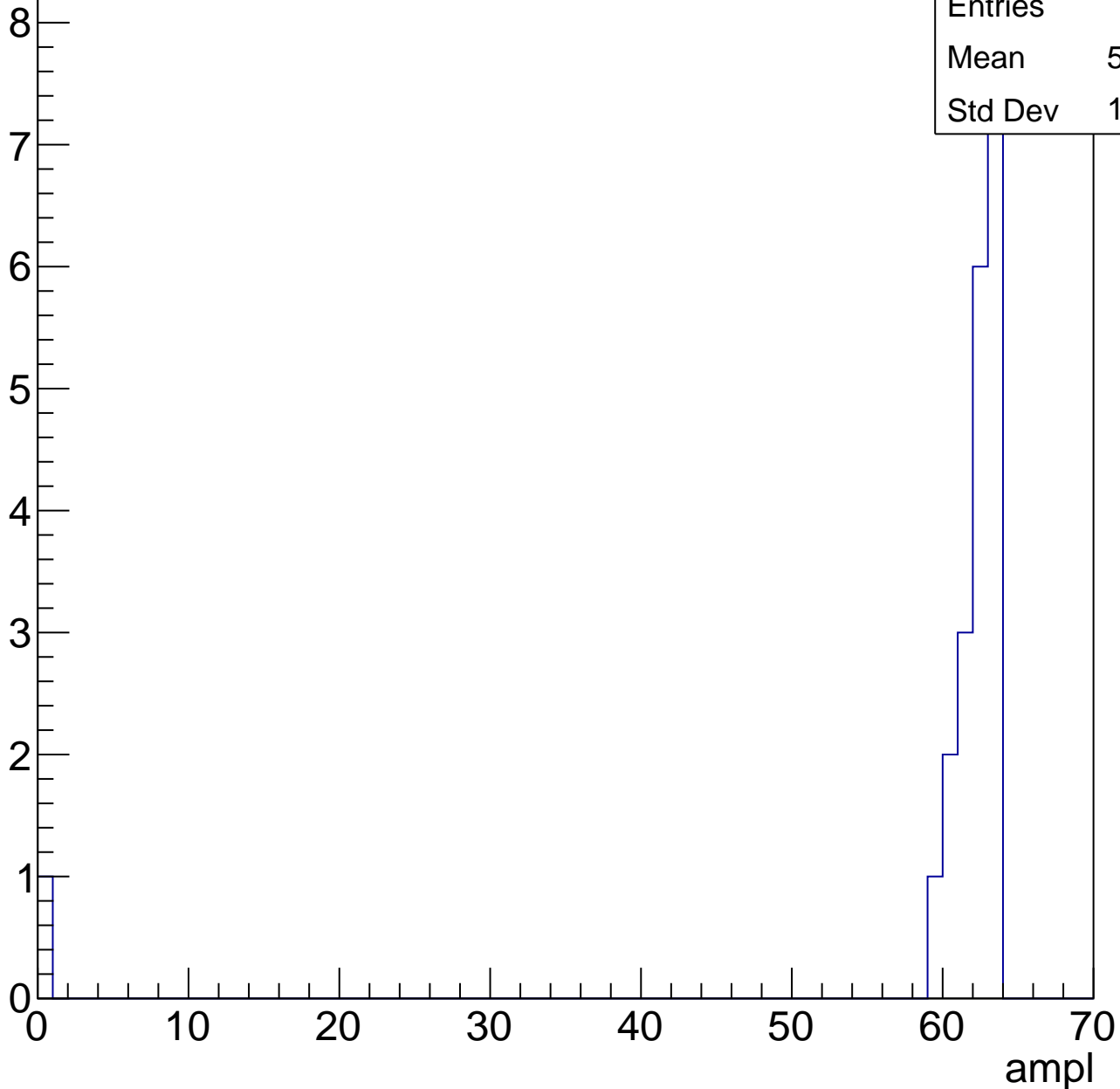


# B1L002S, U8-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.95
Std Dev	13.23





# B1L002S, U8-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	20
Std Dev	0

ampl

# B1L002S, U8-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	20
Std Dev	0

ampl