

B1L104S, U25-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	31.11
Std Dev	28.68

Turn on : 55.2118

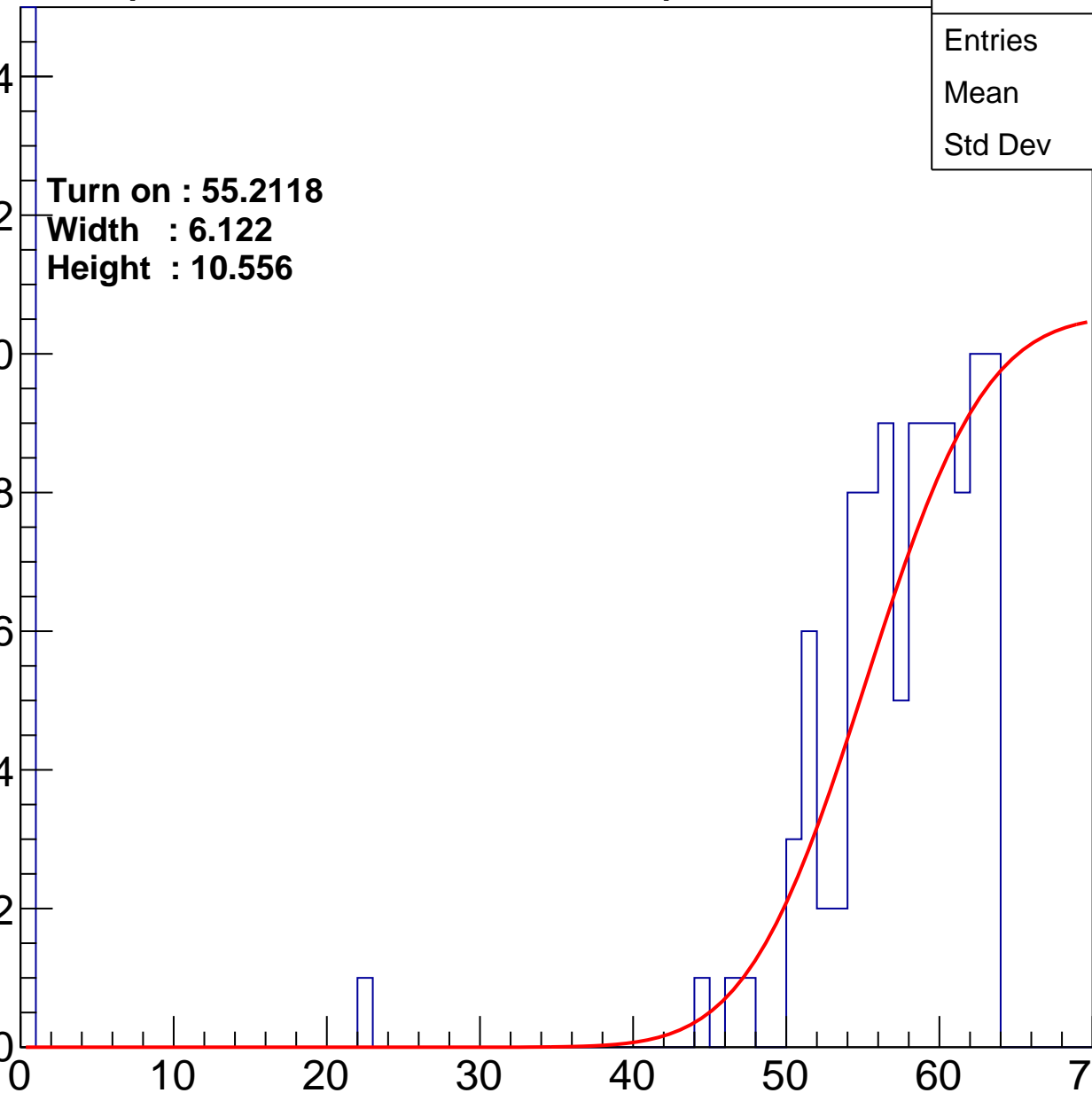
Width : 6.122

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	39.33
Std Dev	27.17

Turn on : 53.9412

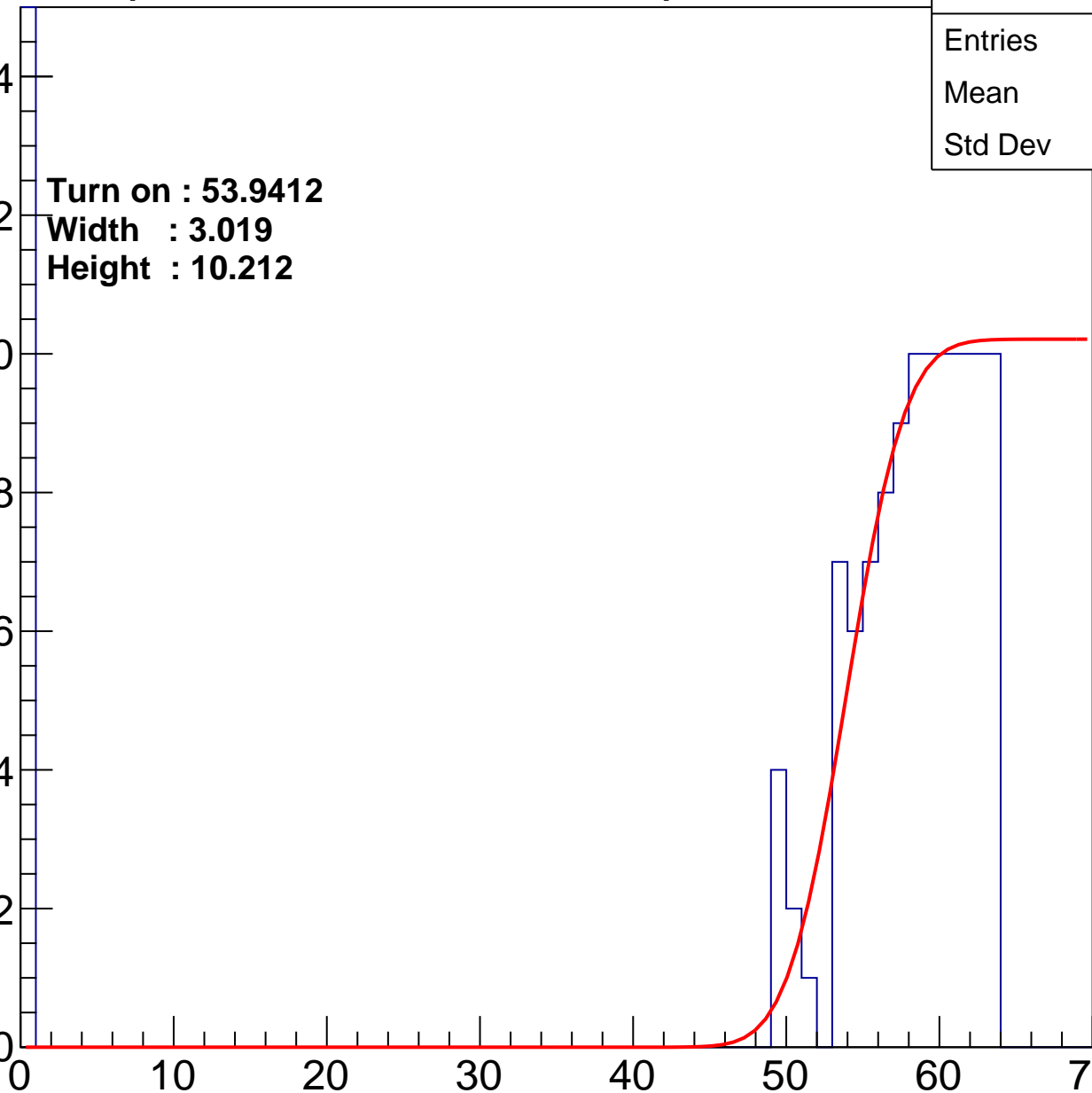
Width : 3.019

Height : 10.212

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	31.12
Std Dev	29.17

Turn on : 53.7729

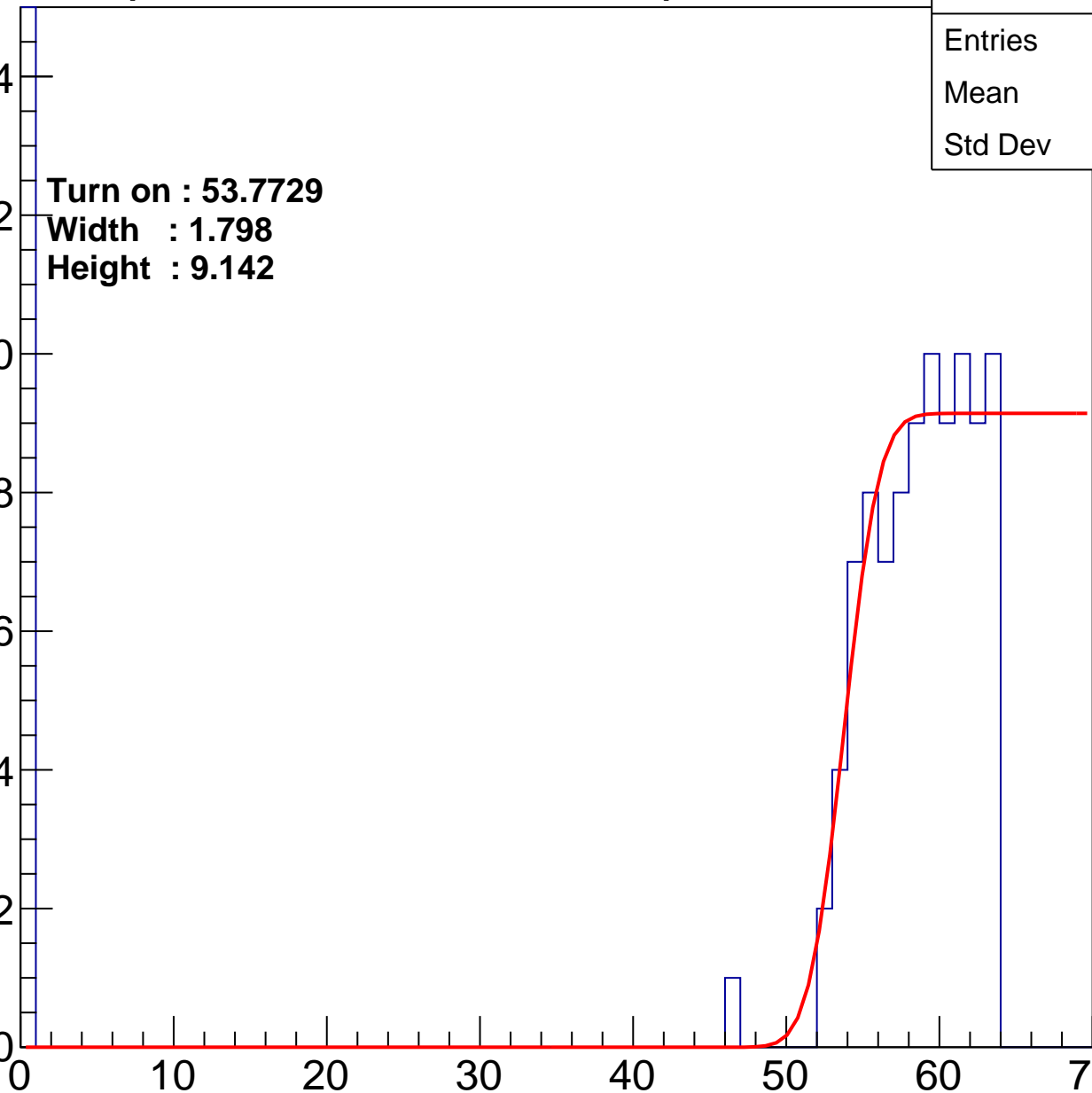
Width : 1.798

Height : 9.142

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	139
Mean	38.65
Std Dev	27.77

Turn on : 55.0479

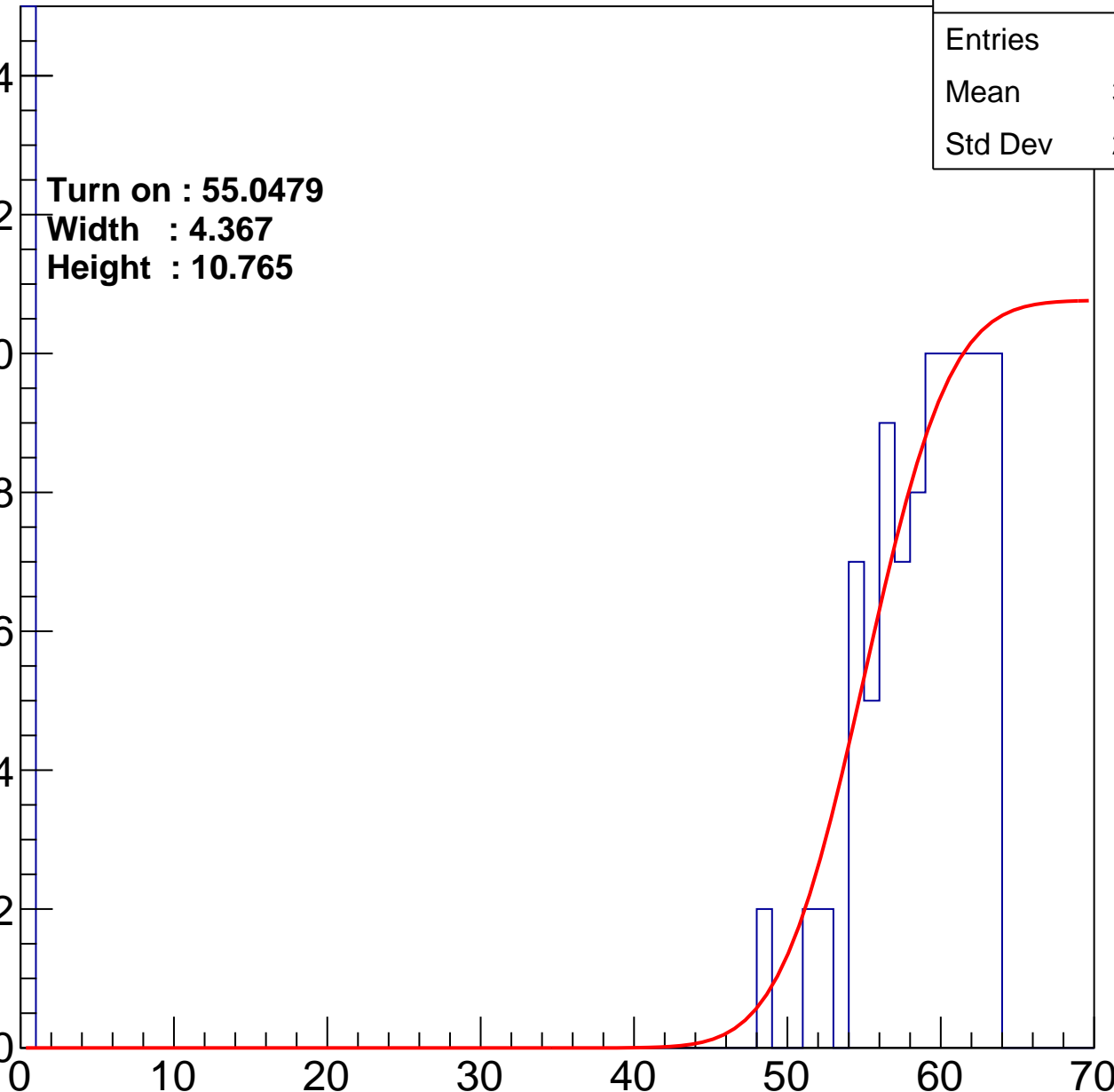
Width : 4.367

Height : 10.765

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	34.95
Std Dev	28.49

Turn on : 55.2289

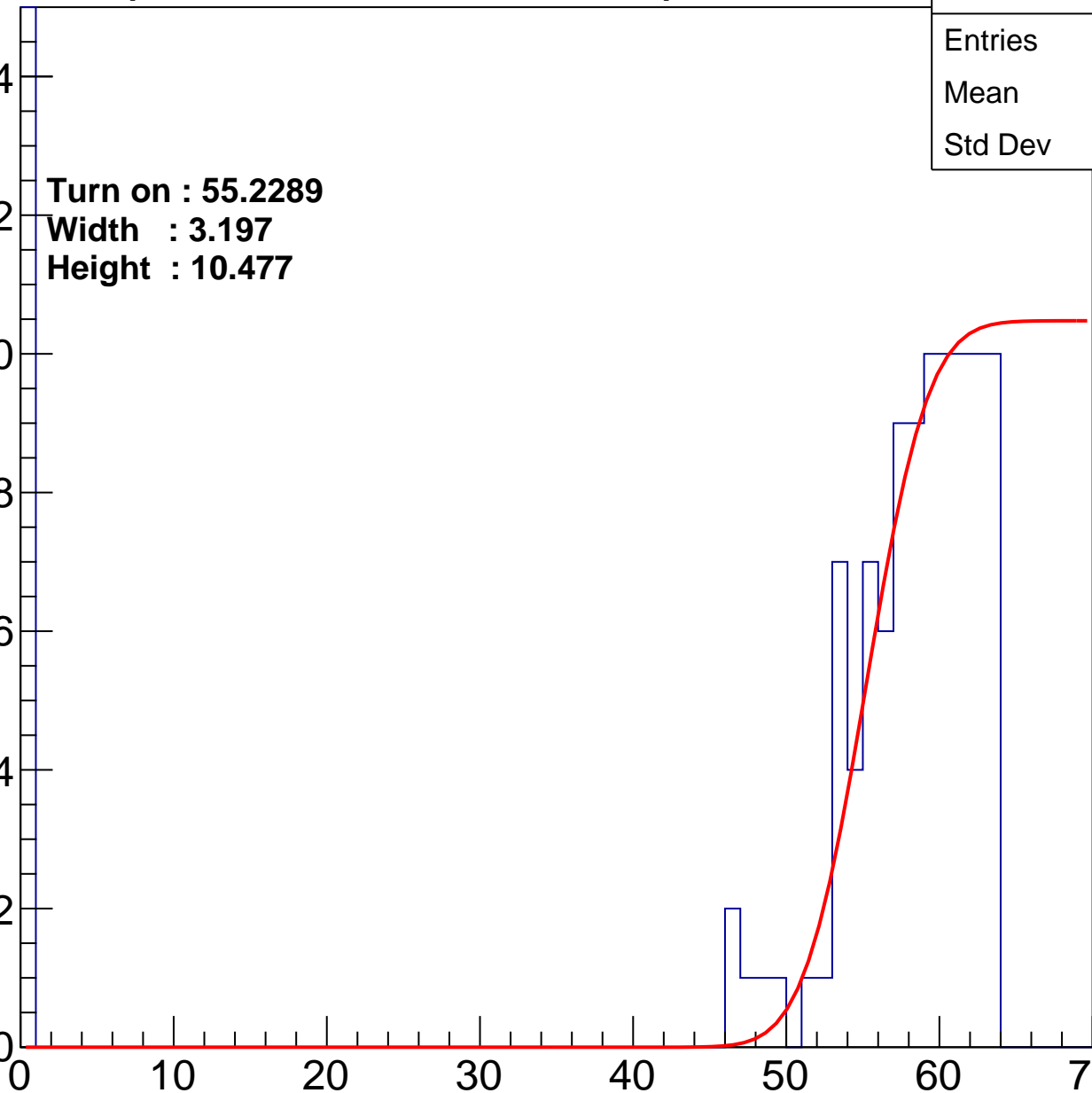
Width : 3.197

Height : 10.477

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	31.34
Std Dev	29.46

Turn on : 55.9738

Width : 3.883

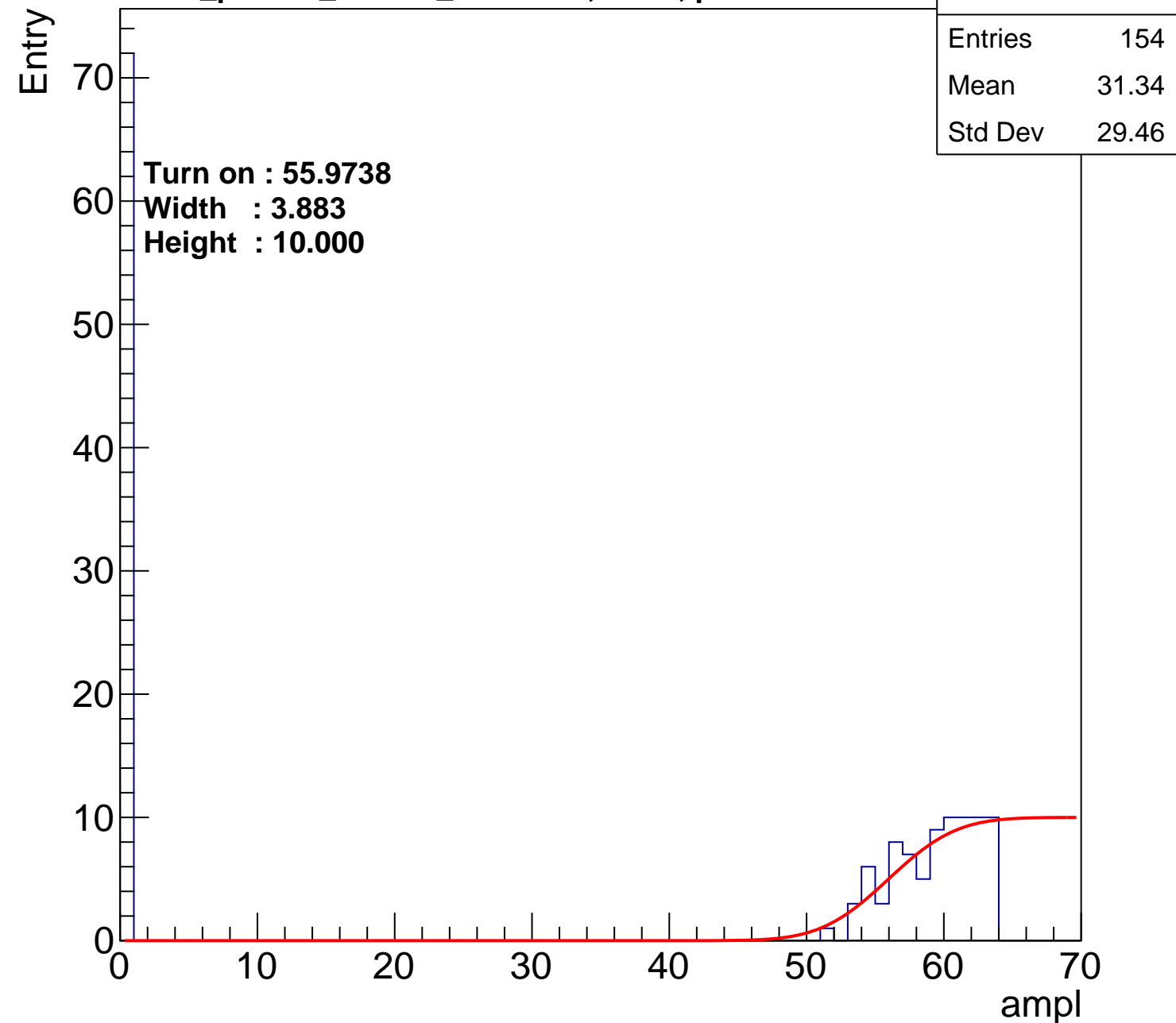
Height : 10.000

Entry

70
60
50
40
30
20
10
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U25-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	36.89
Std Dev	28.04

Turn on : 54.3833

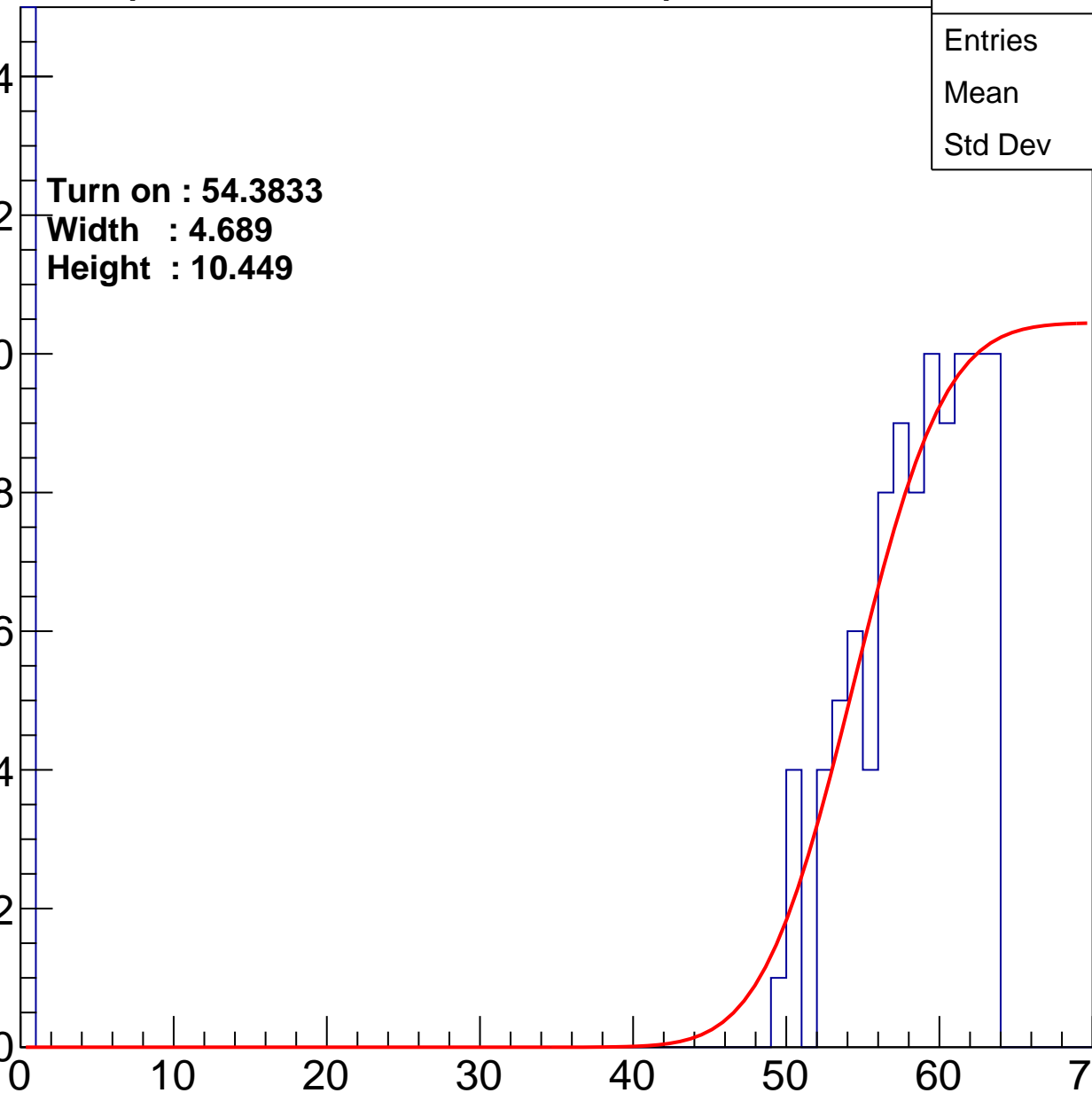
Width : 4.689

Height : 10.449

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch7

calib_packv5_033123_0516.root, FC#4, port A1

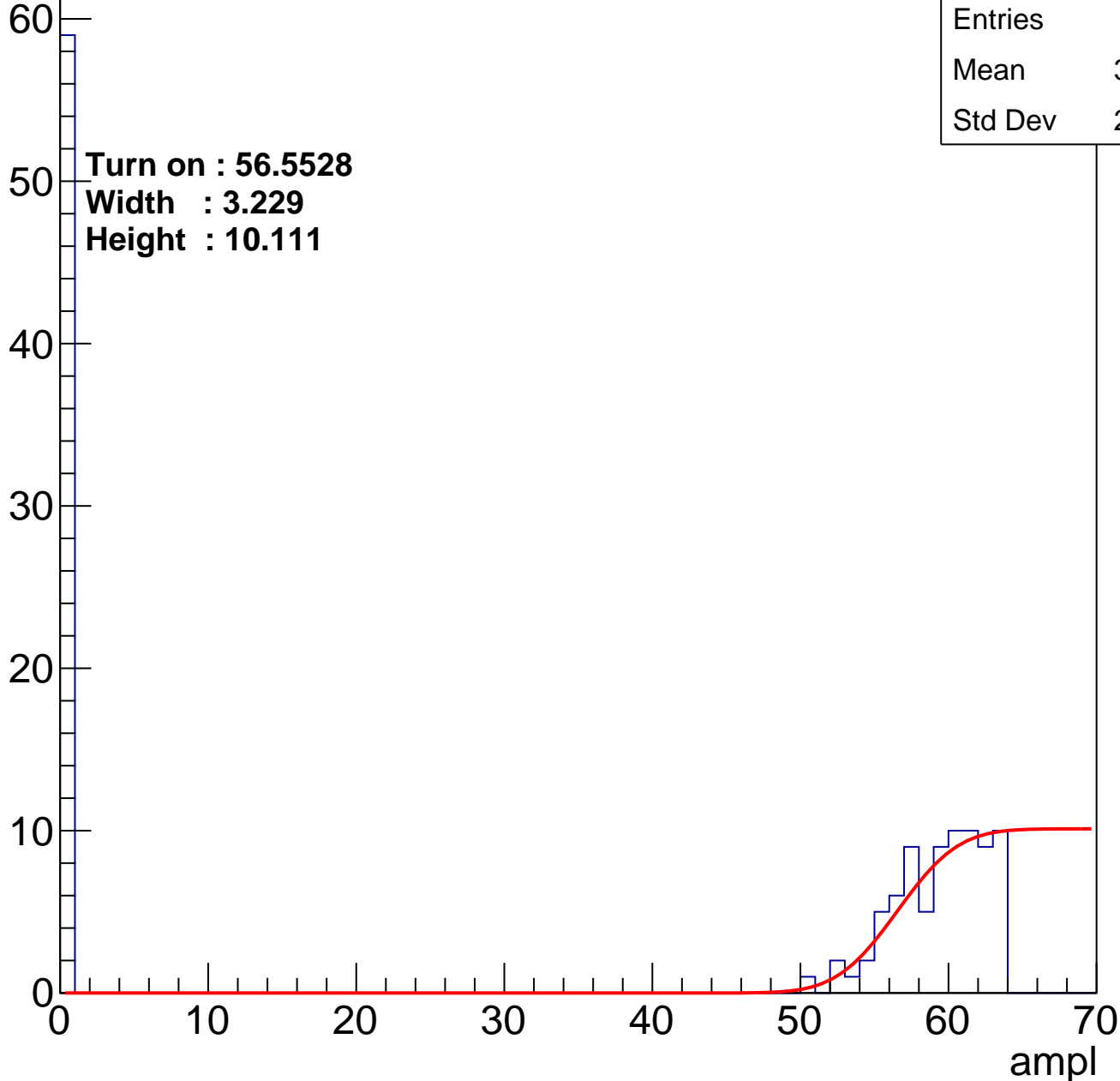
Entries	138
Mean	33.75
Std Dev	29.26

Turn on : 56.5528

Width : 3.229

Height : 10.111

Entry



B1L104S, U25-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	36.98
Std Dev	27.96

Turn on : 53.2295

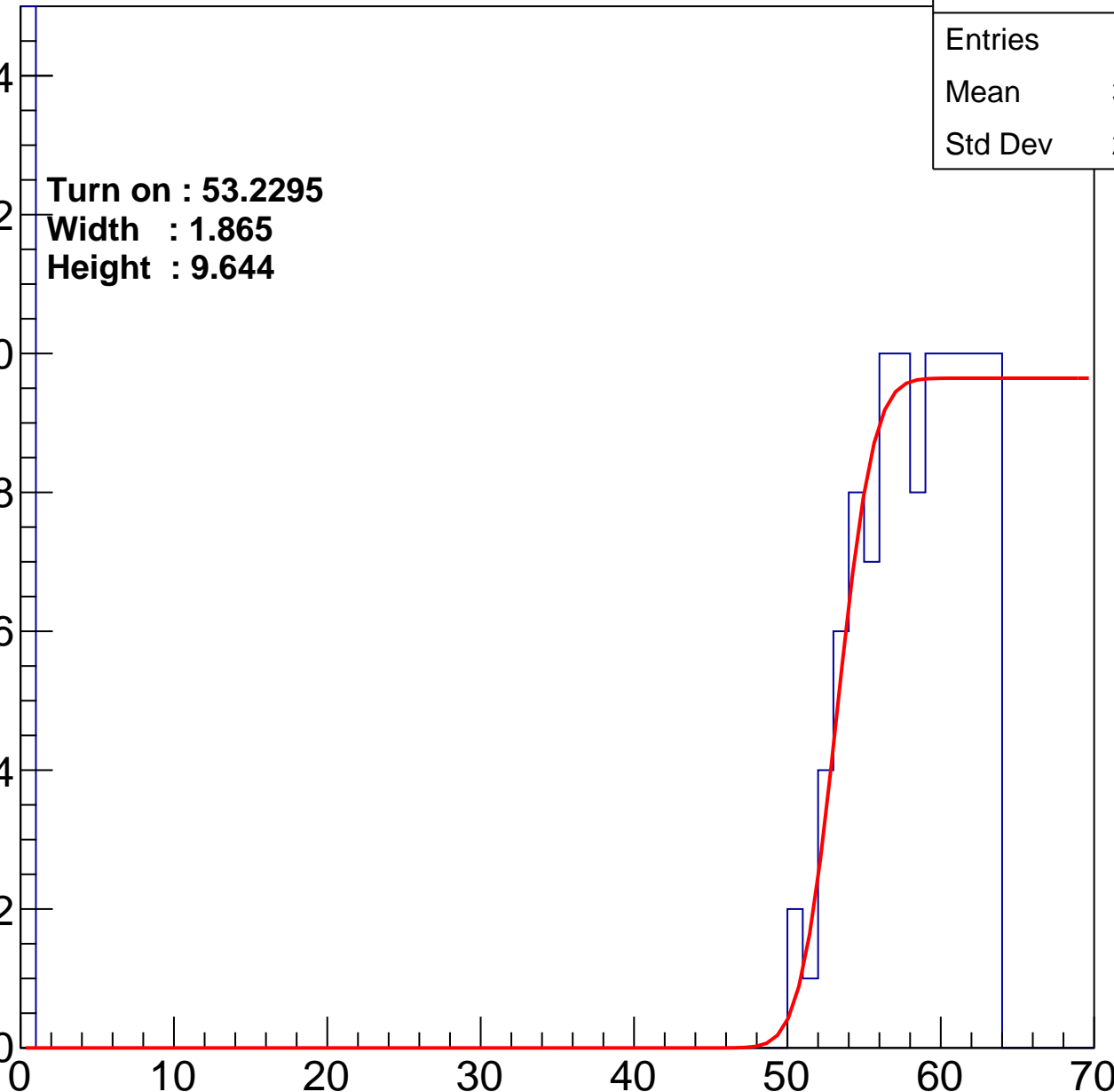
Width : 1.865

Height : 9.644

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	34.49
Std Dev	28.74

Turn on : 55.3335

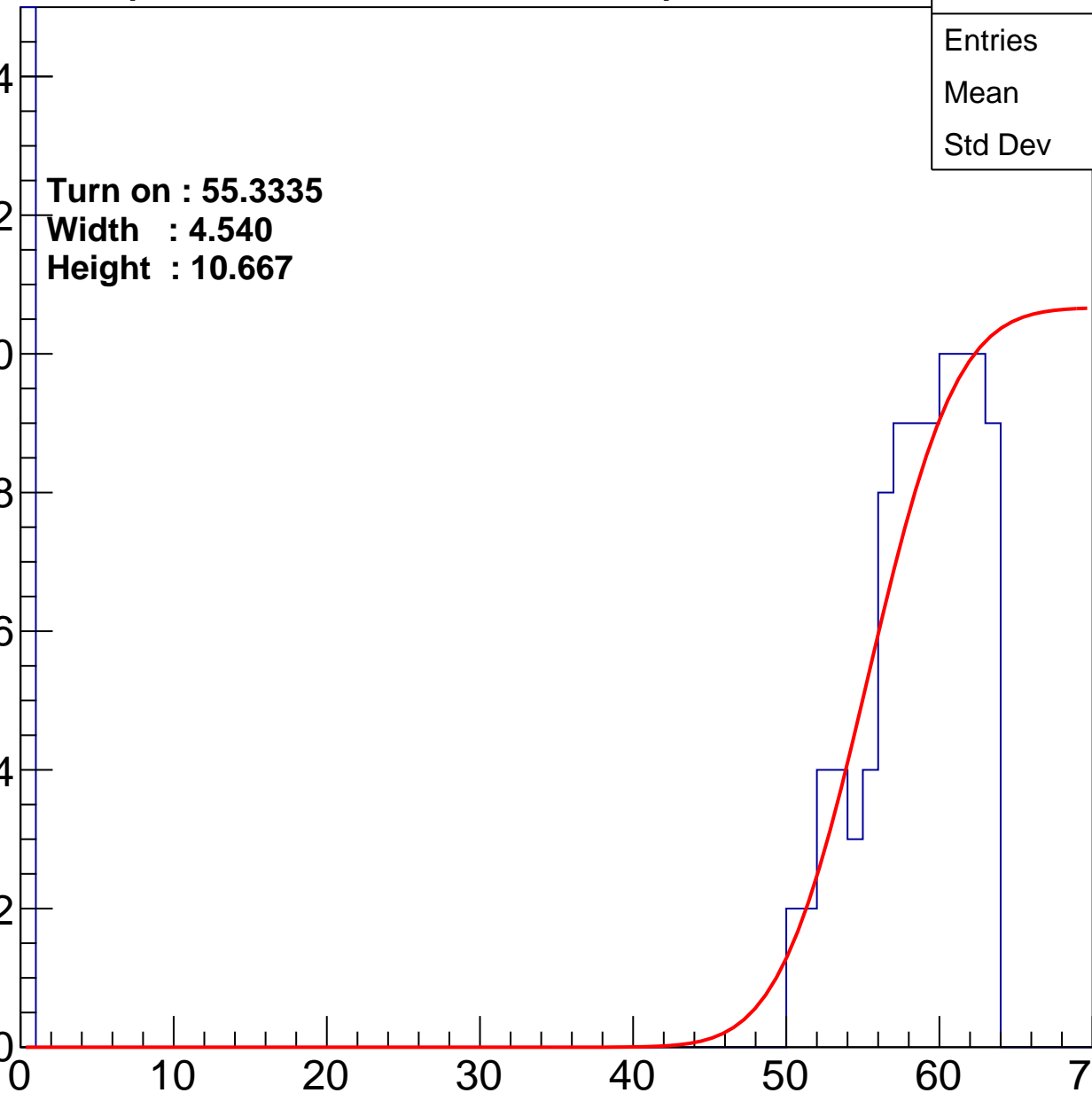
Width : 4.540

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl

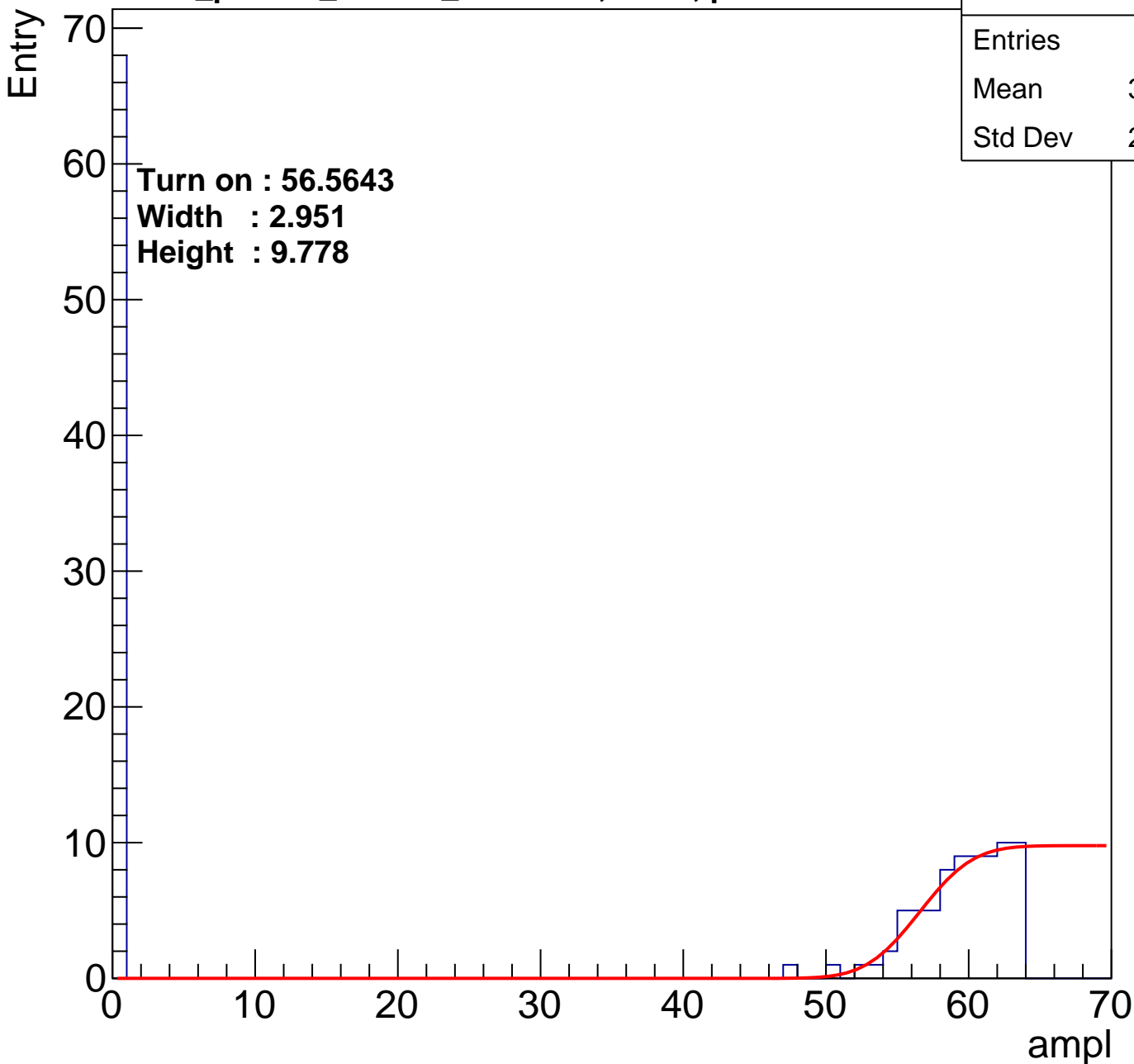


B1L104S, U25-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	144
Mean	31.14
Std Dev	29.55

Turn on : 56.5643
Width : 2.951
Height : 9.778



B1L104S, U25-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.94
Std Dev	28.94

Turn on : 54.0640

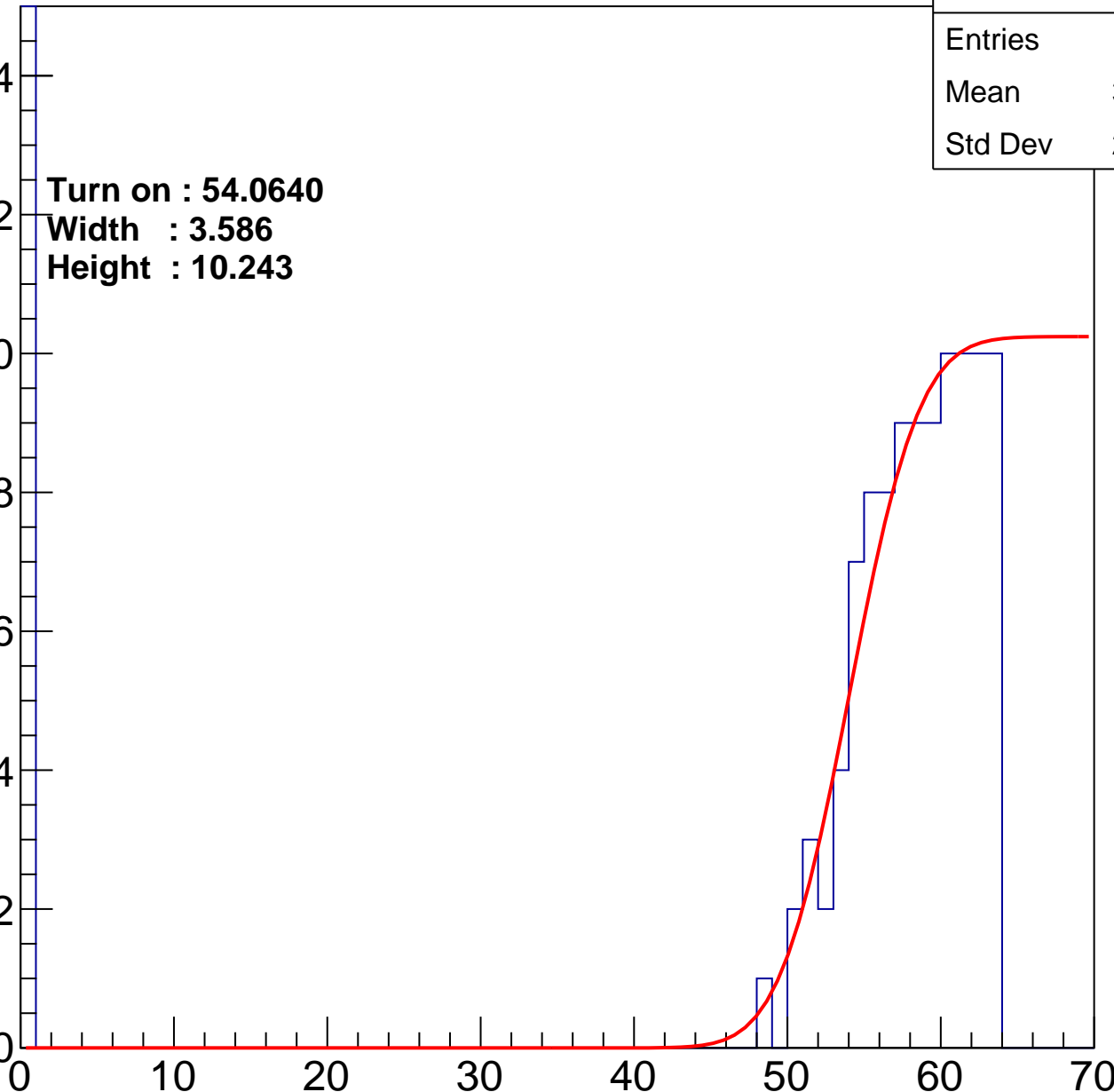
Width : 3.586

Height : 10.243

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	35.64
Std Dev	28.59

Turn on : 54.8506

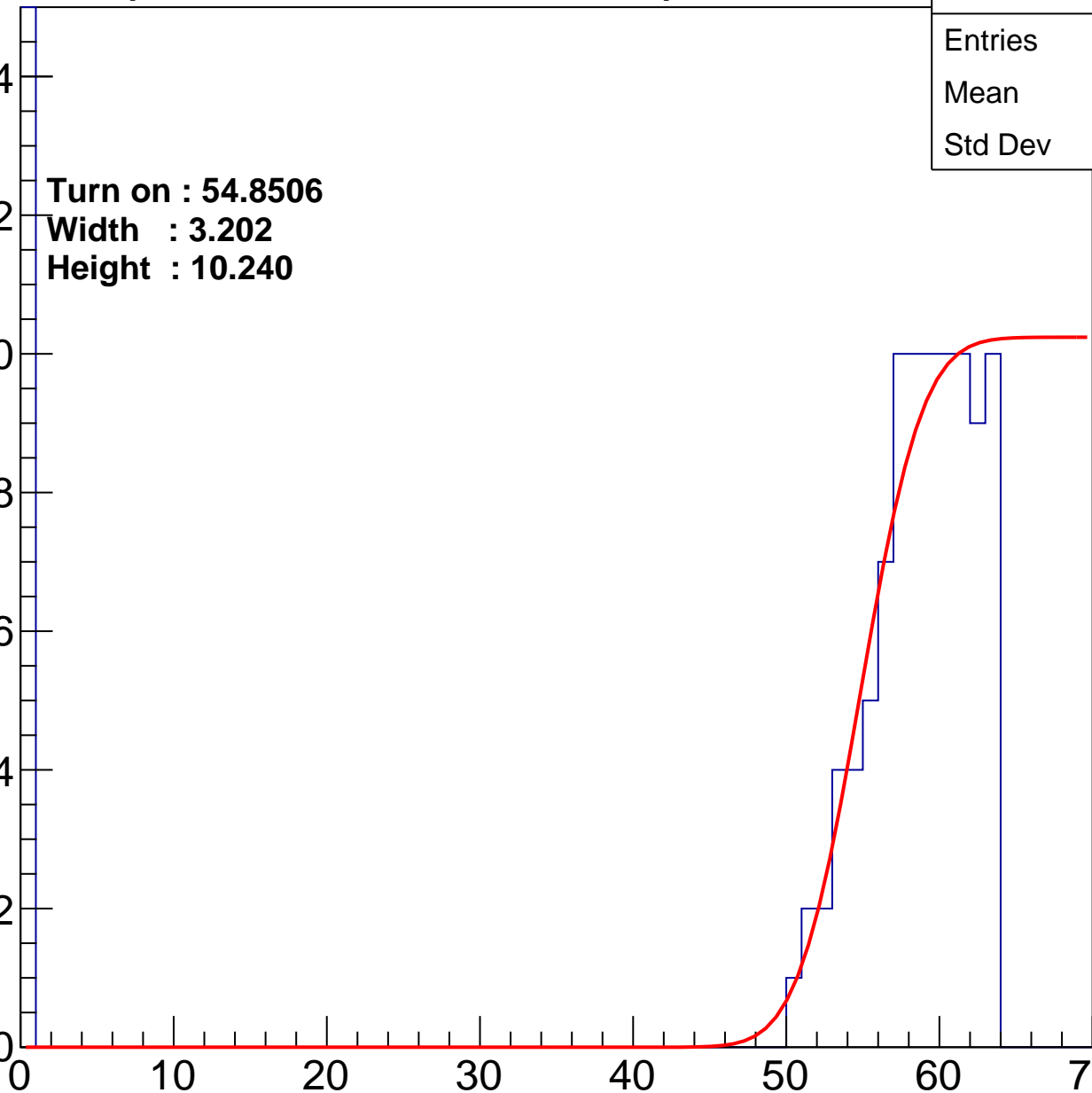
Width : 3.202

Height : 10.240

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	35.17
Std Dev	28.69

Turn on : 55.0881

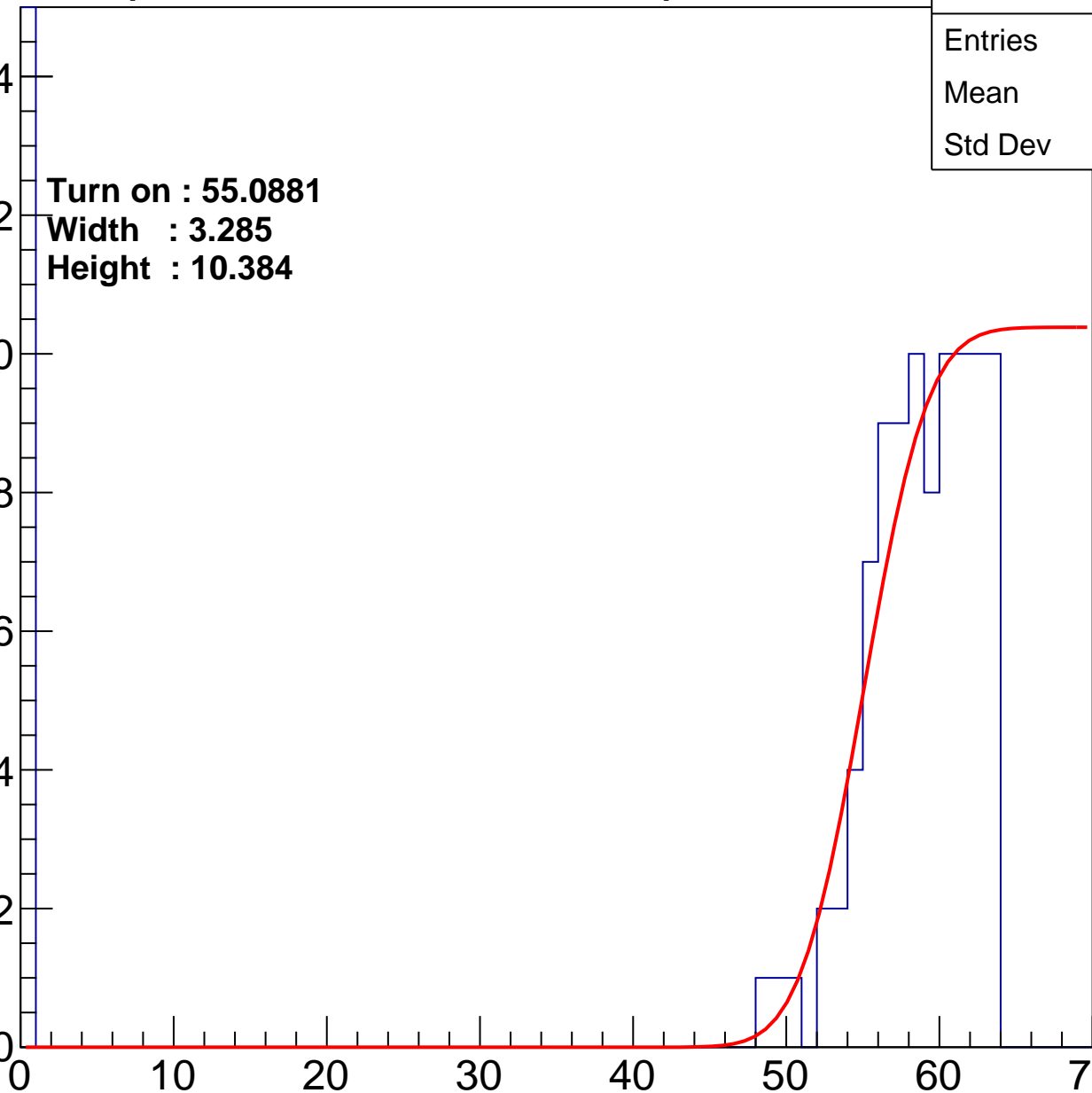
Width : 3.285

Height : 10.384

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	37.49
Std Dev	27.37

Turn on : 52.1299

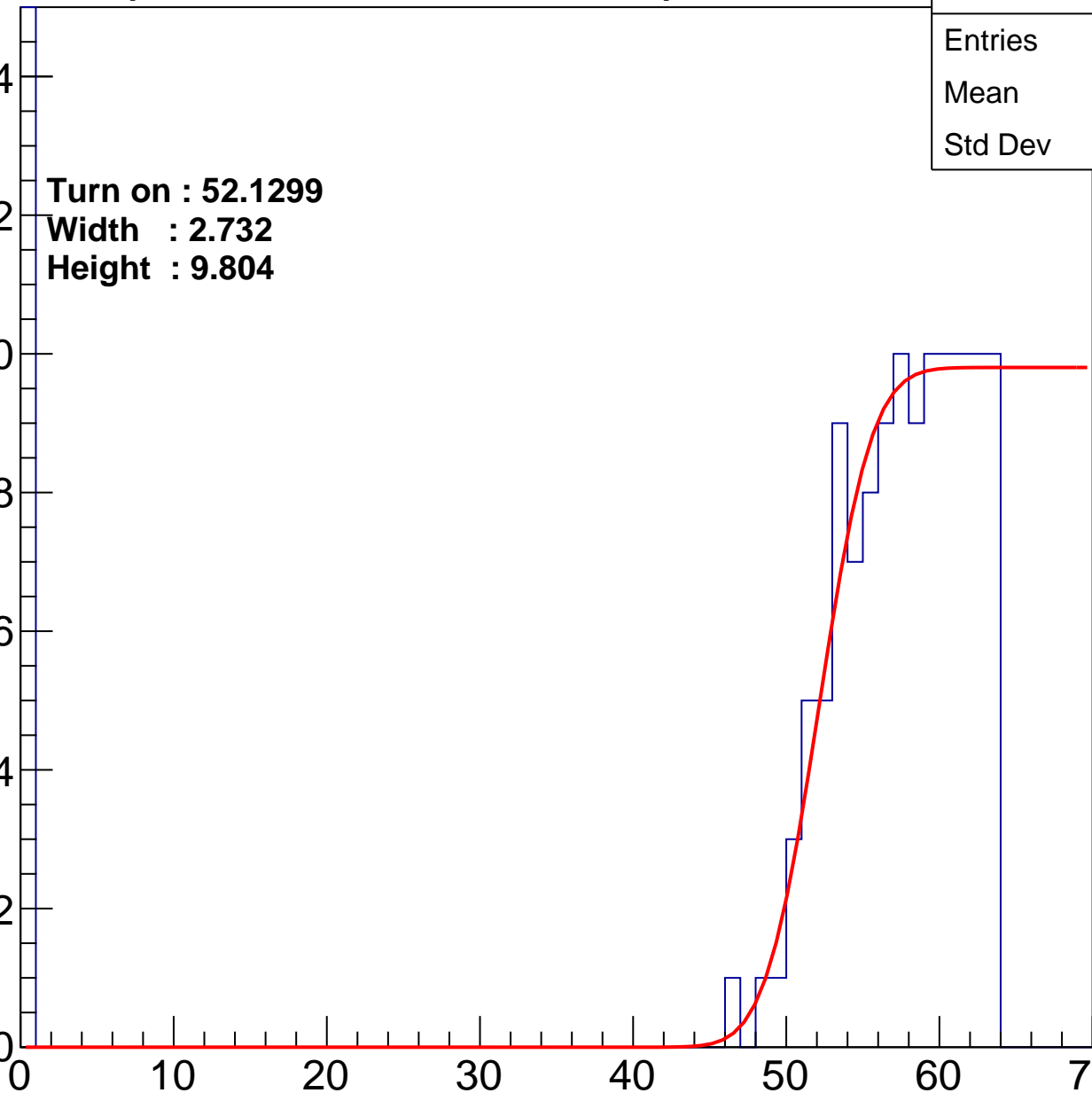
Width : 2.732

Height : 9.804

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	146
Mean	39.05
Std Dev	27.48

Turn on : 54.6361

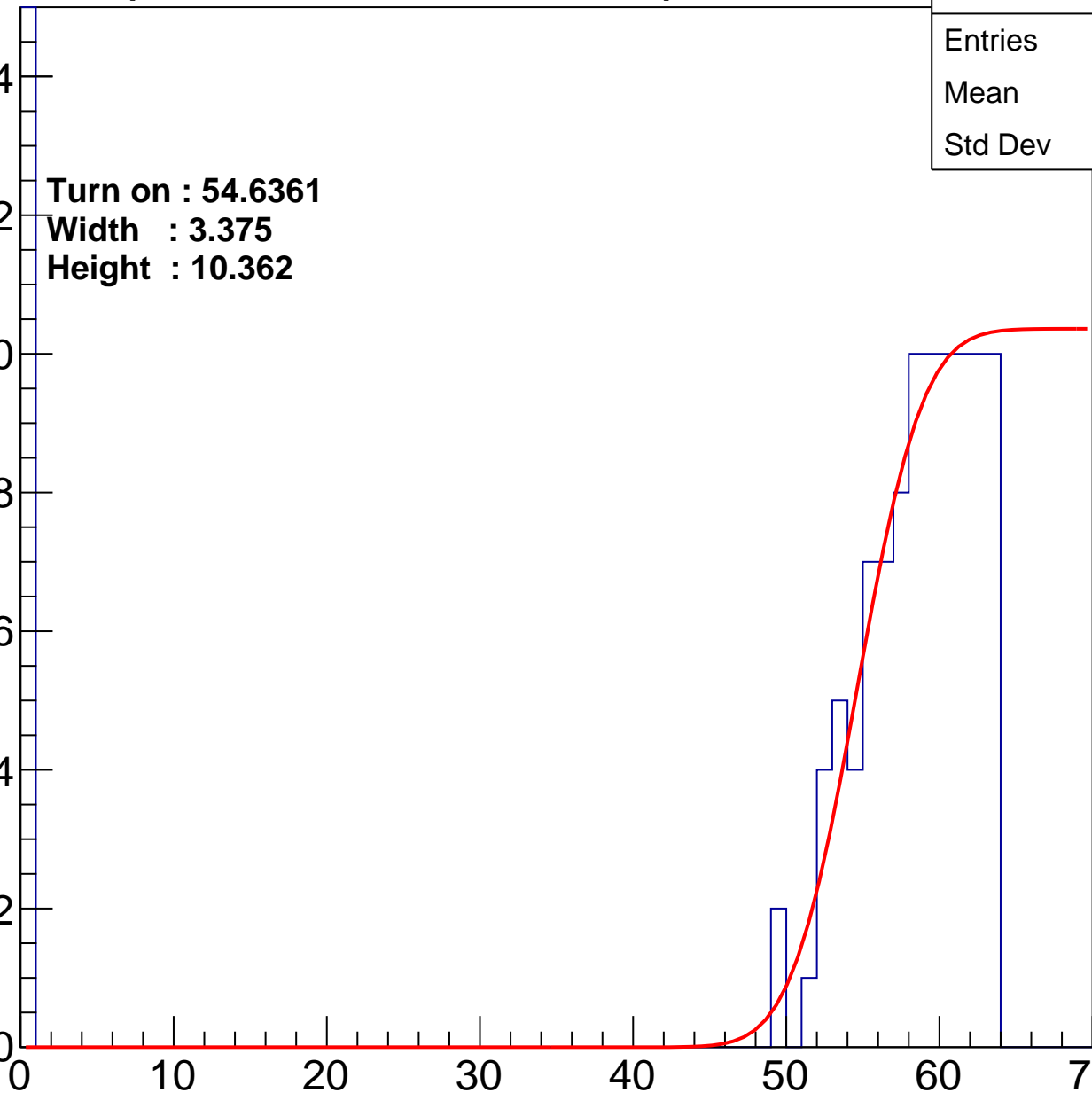
Width : 3.375

Height : 10.362

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch16

calib_packv5_033123_0516.root, FC#4, port A1

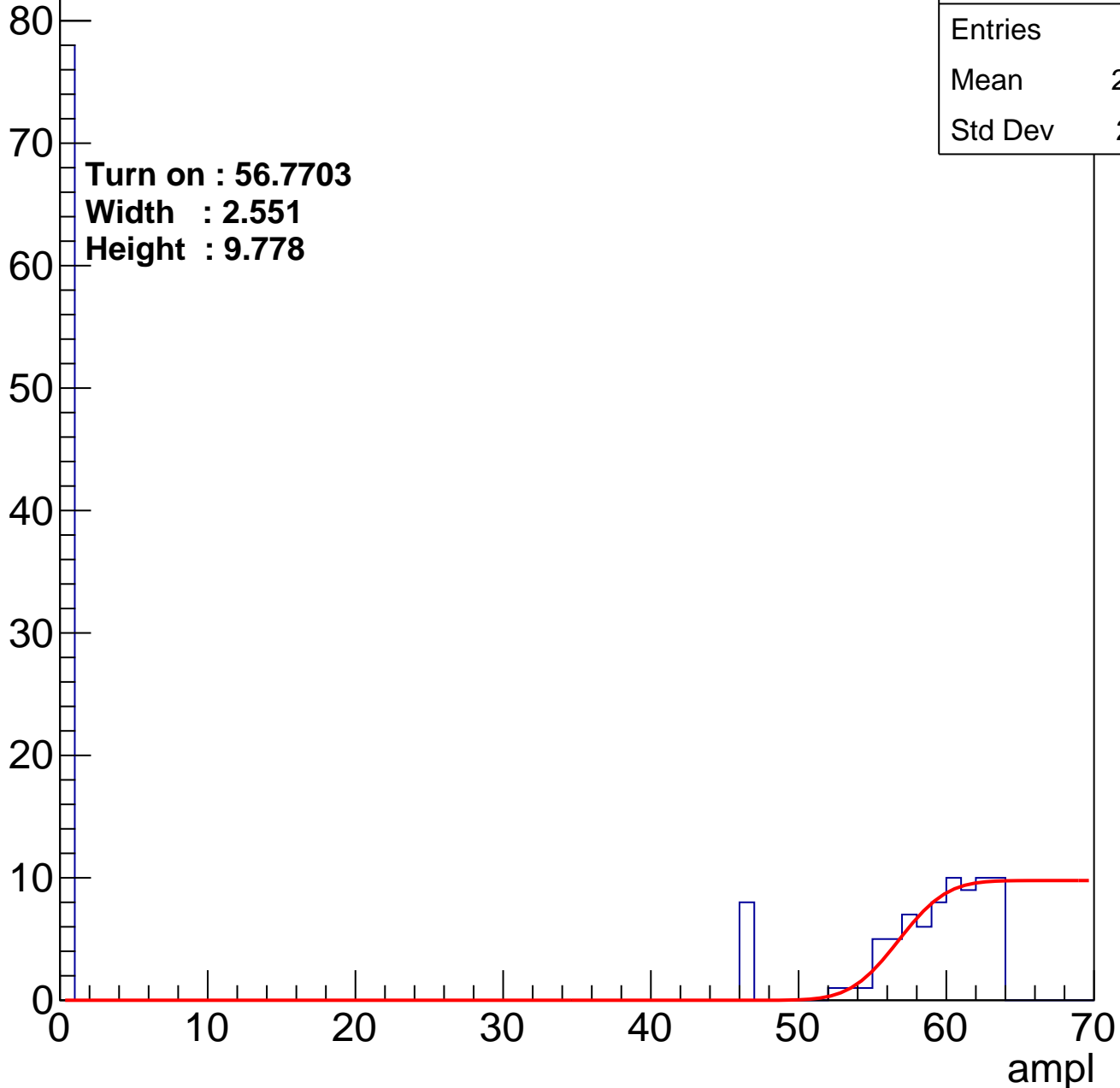
Entries	159
Mean	29.56
Std Dev	29.21

Turn on : 56.7703

Width : 2.551

Height : 9.778

Entry



B1L104S, U25-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	34.63
Std Dev	28.61

Turn on : 53.8389

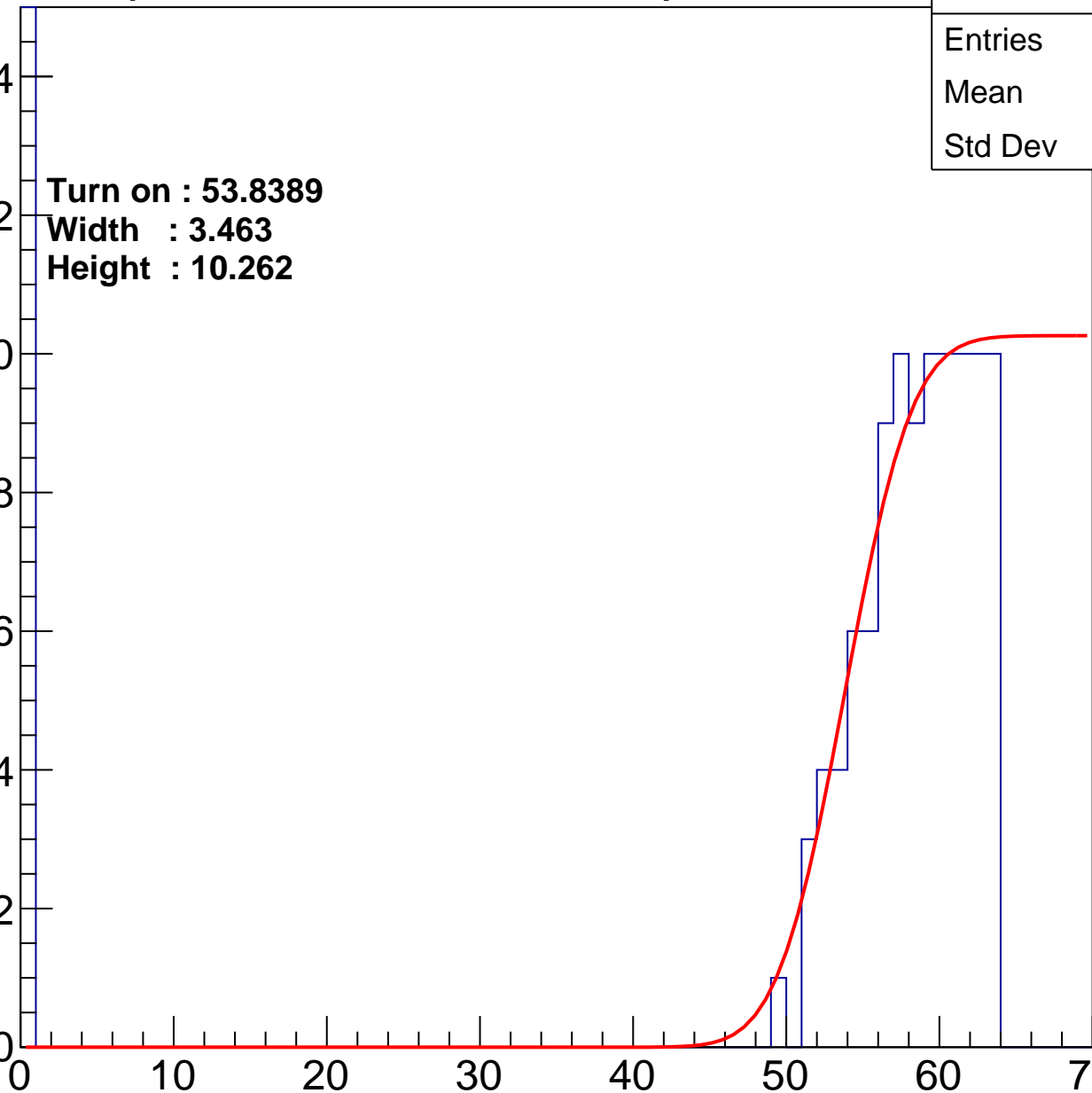
Width : 3.463

Height : 10.262

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	30.03
Std Dev	29.07

Turn on : 54.3666

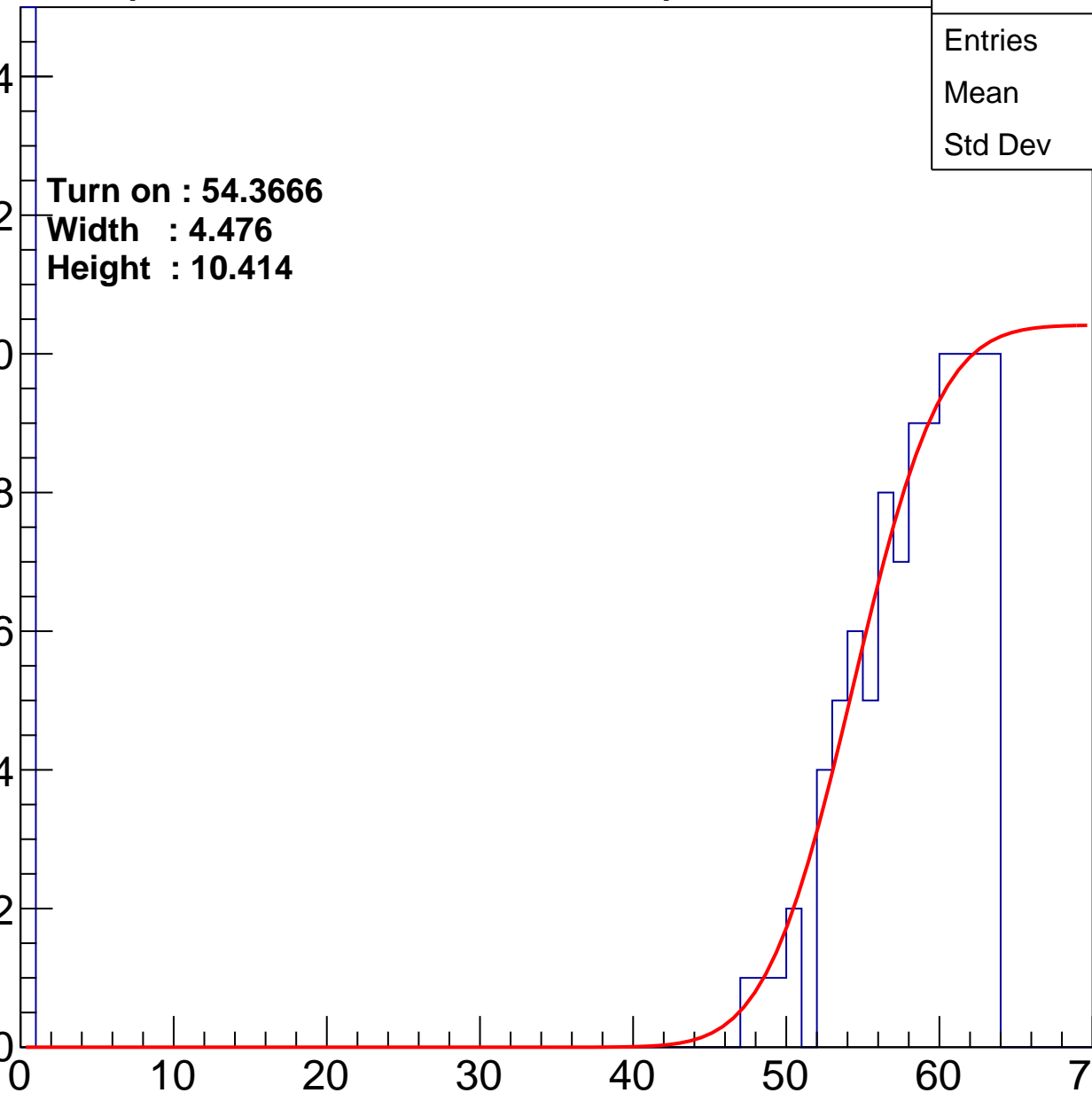
Width : 4.476

Height : 10.414

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	32.72
Std Dev	29.27

Turn on : 55.8297

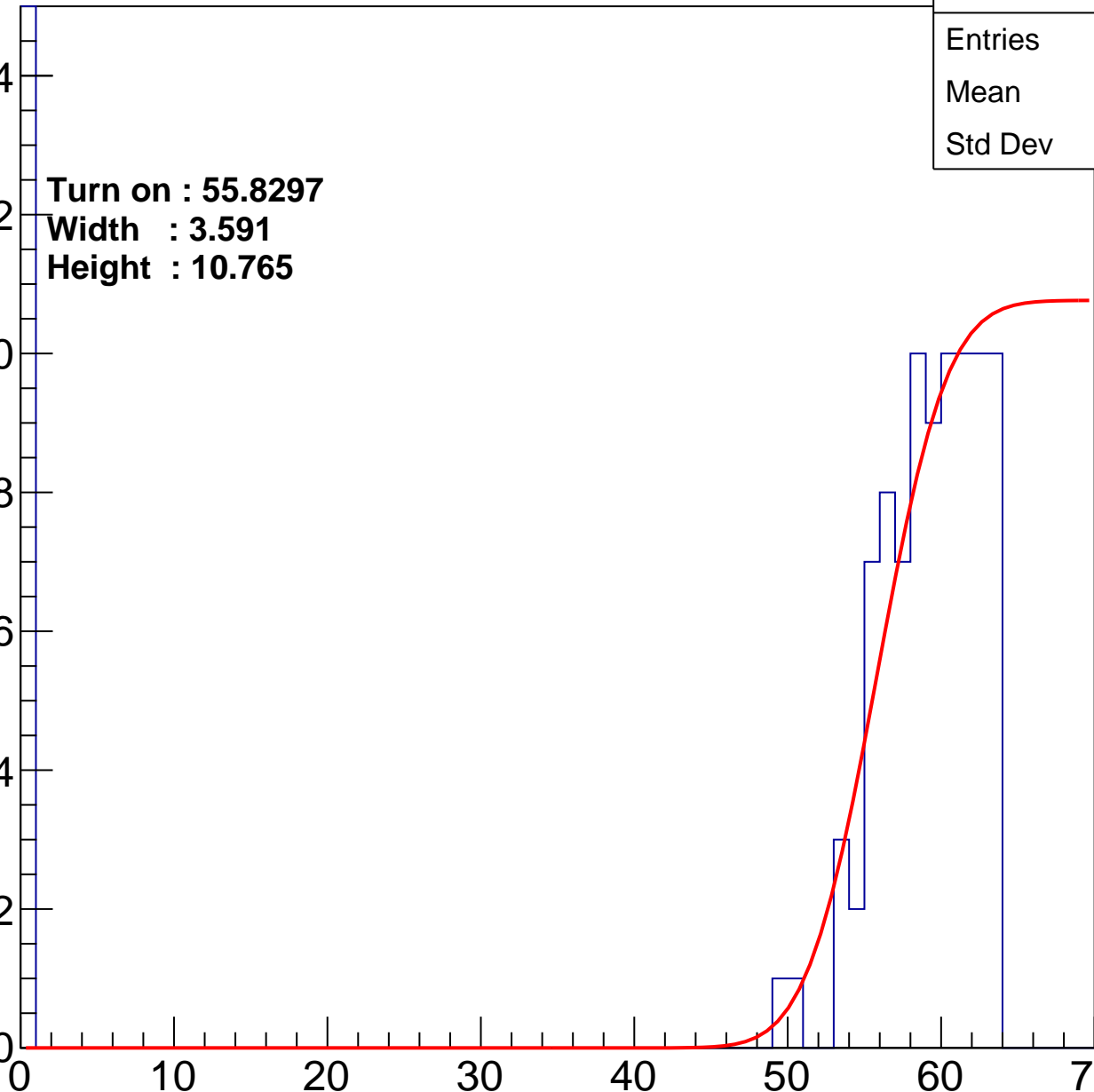
Width : 3.591

Height : 10.765

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	29.77
Std Dev	29.12

Turn on : 55.0475

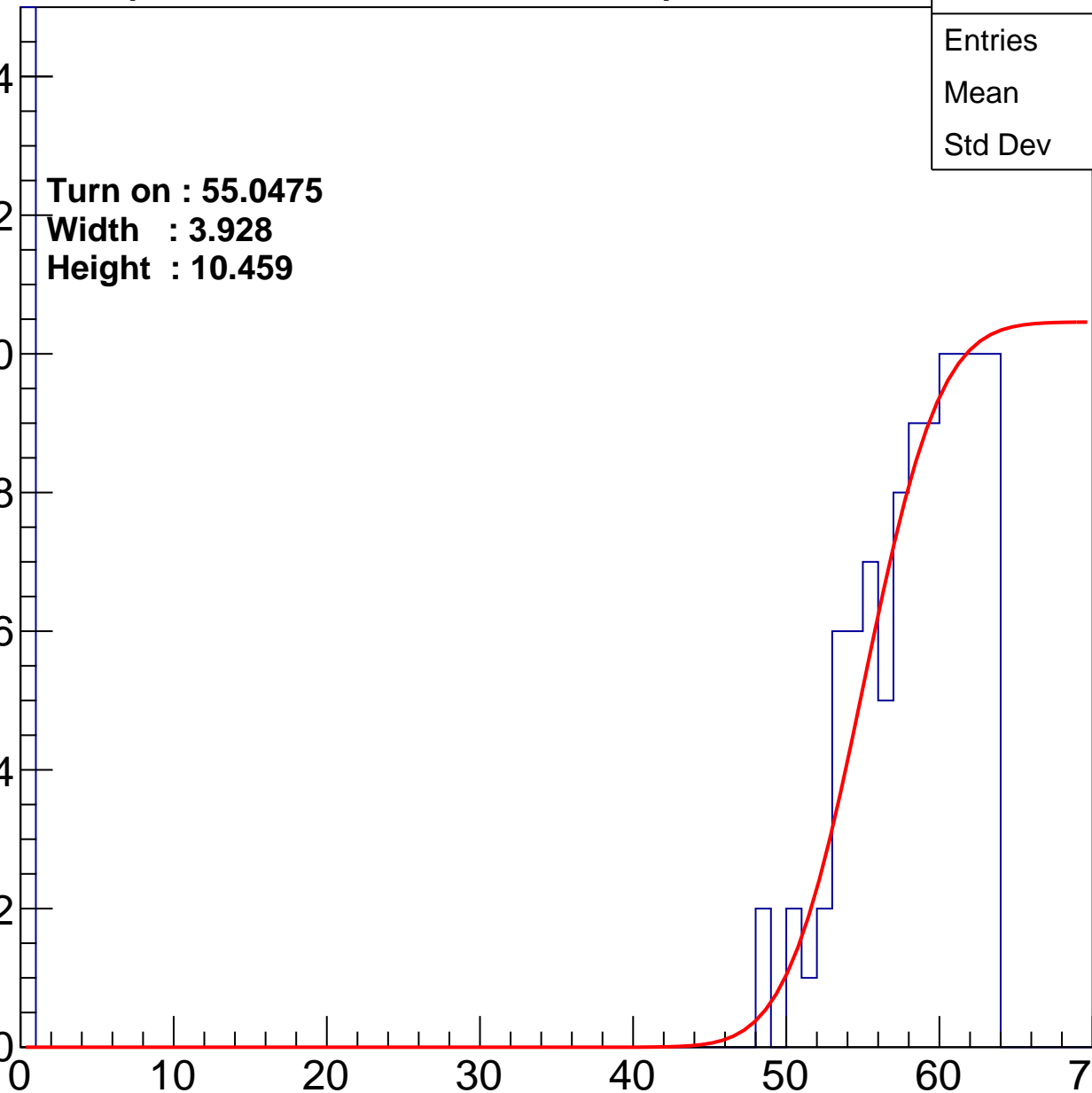
Width : 3.928

Height : 10.459

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	143
Mean	35.27
Std Dev	28.82

Turn on : 55.5982

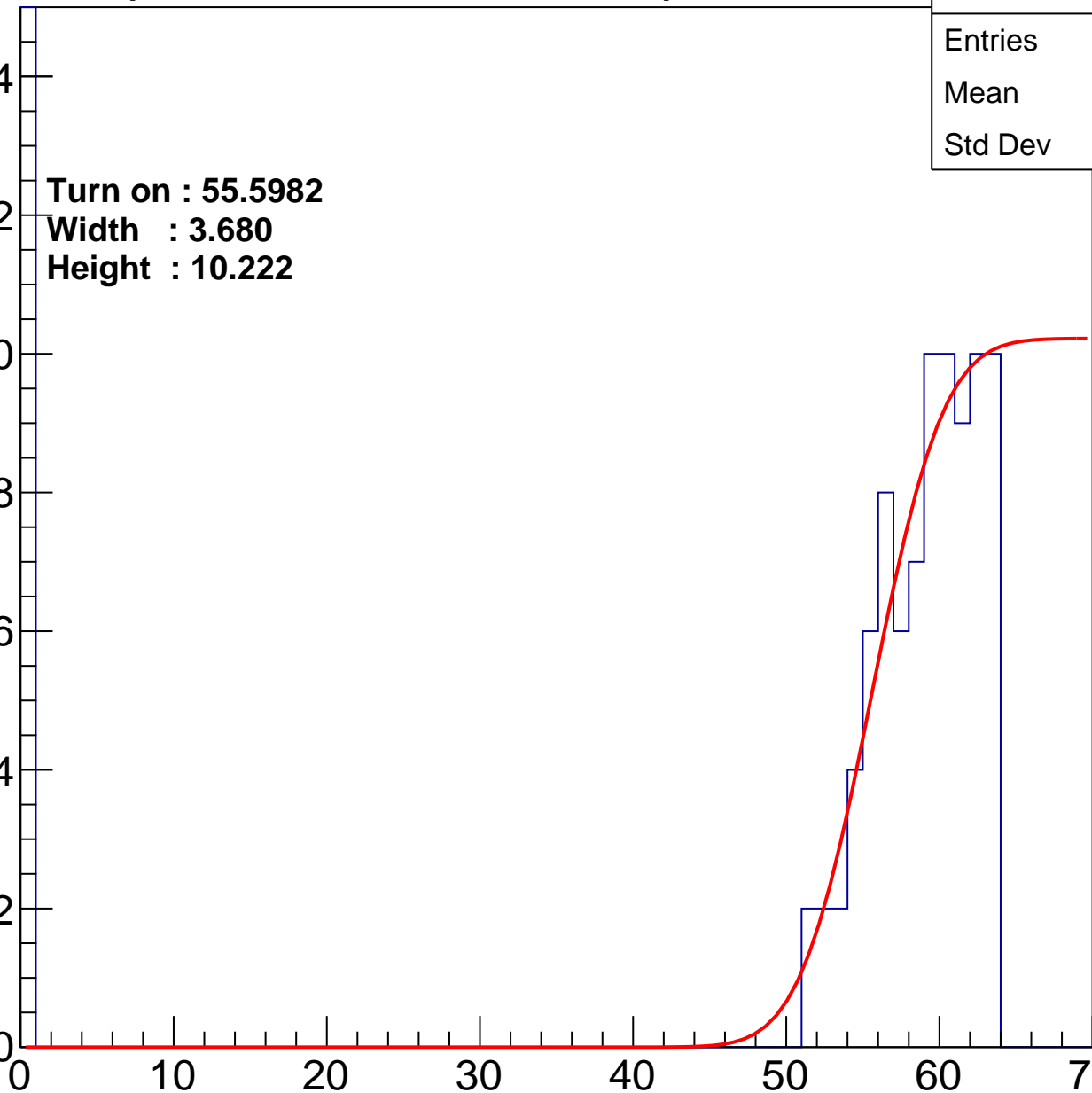
Width : 3.680

Height : 10.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	30.7
Std Dev	29.17

Turn on : 56.0499

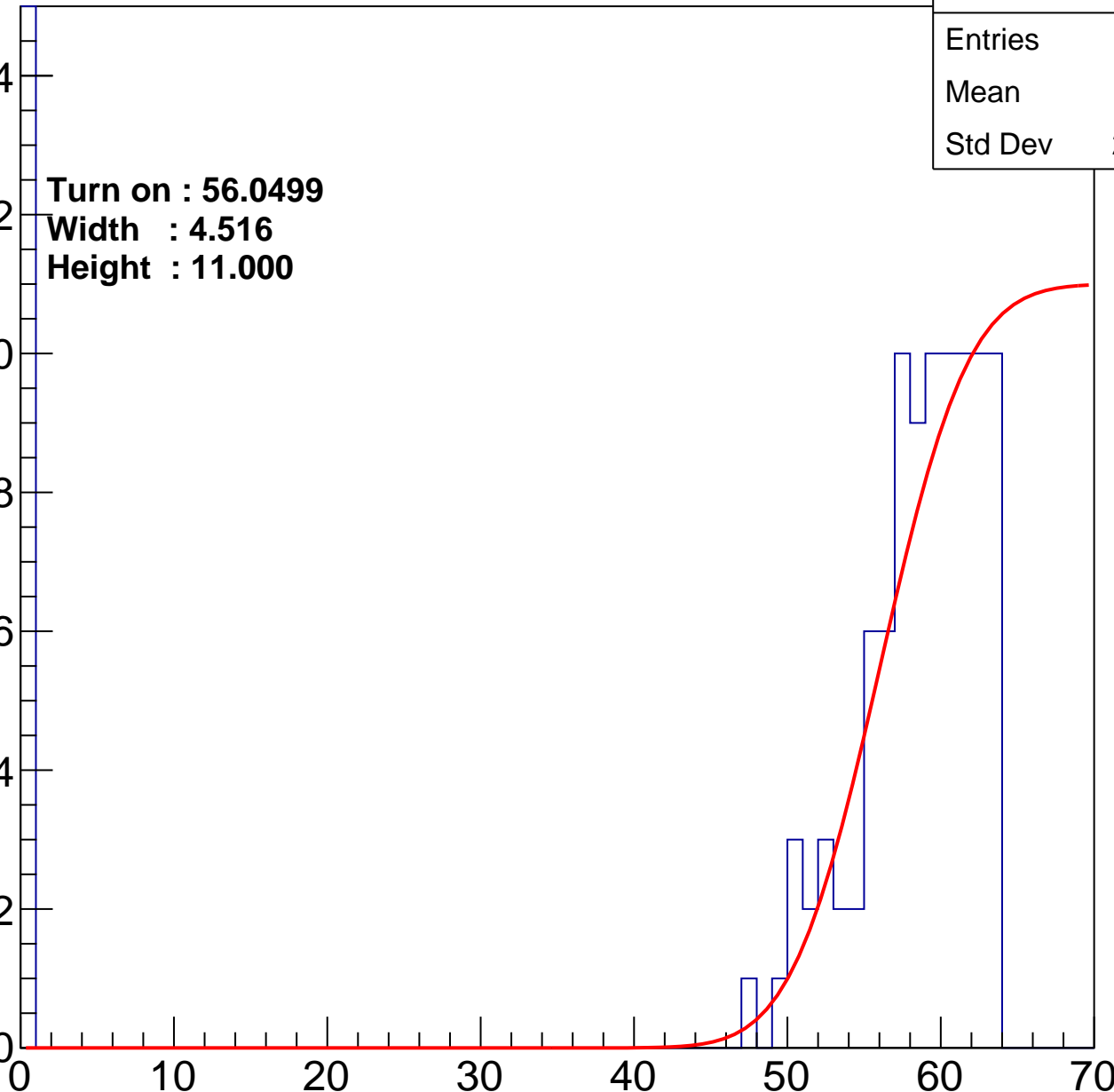
Width : 4.516

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	33.79
Std Dev	28.83

Turn on : 54.5819

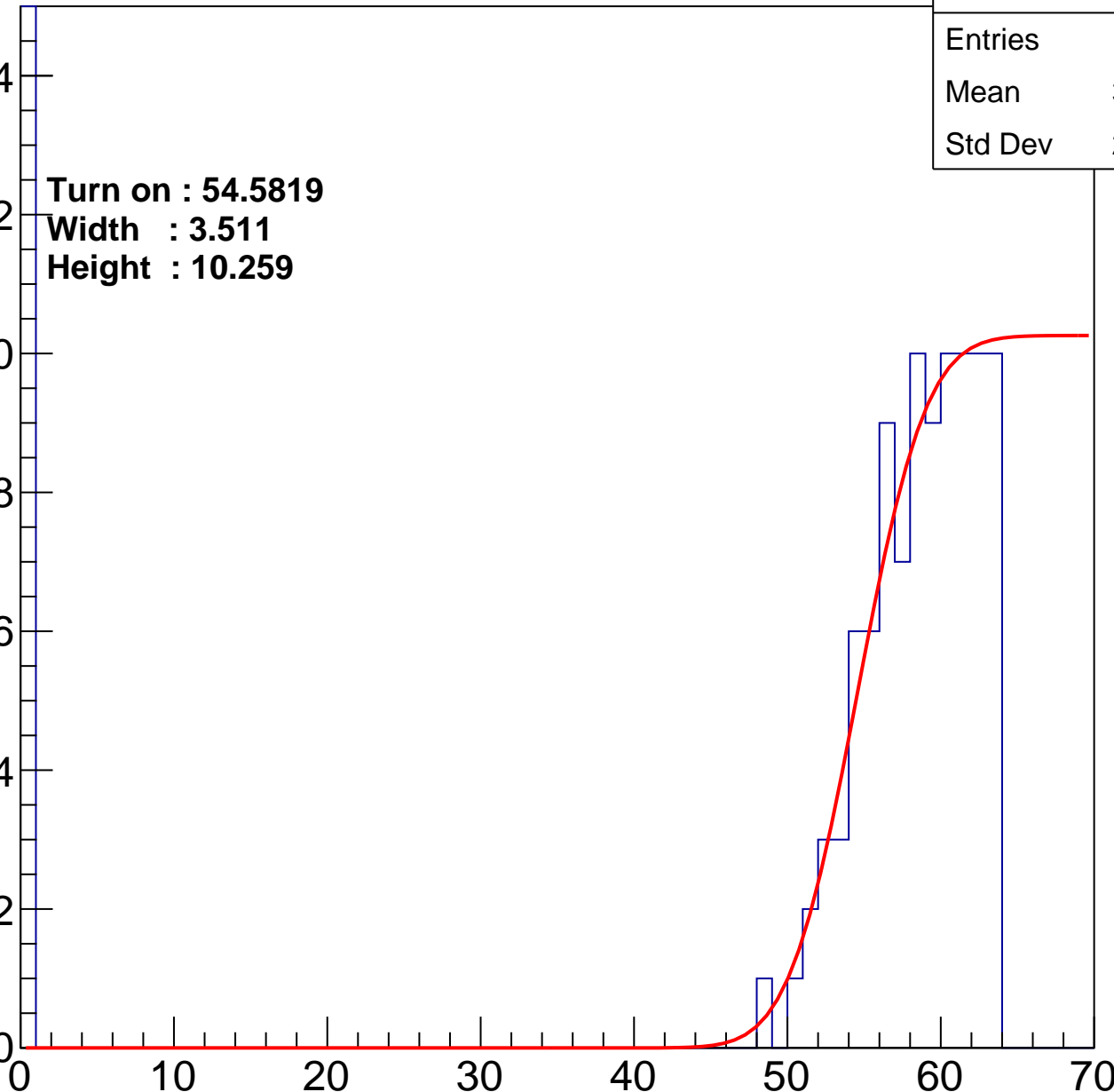
Width : 3.511

Height : 10.259

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	31.36
Std Dev	29.08

Turn on : 55.2231

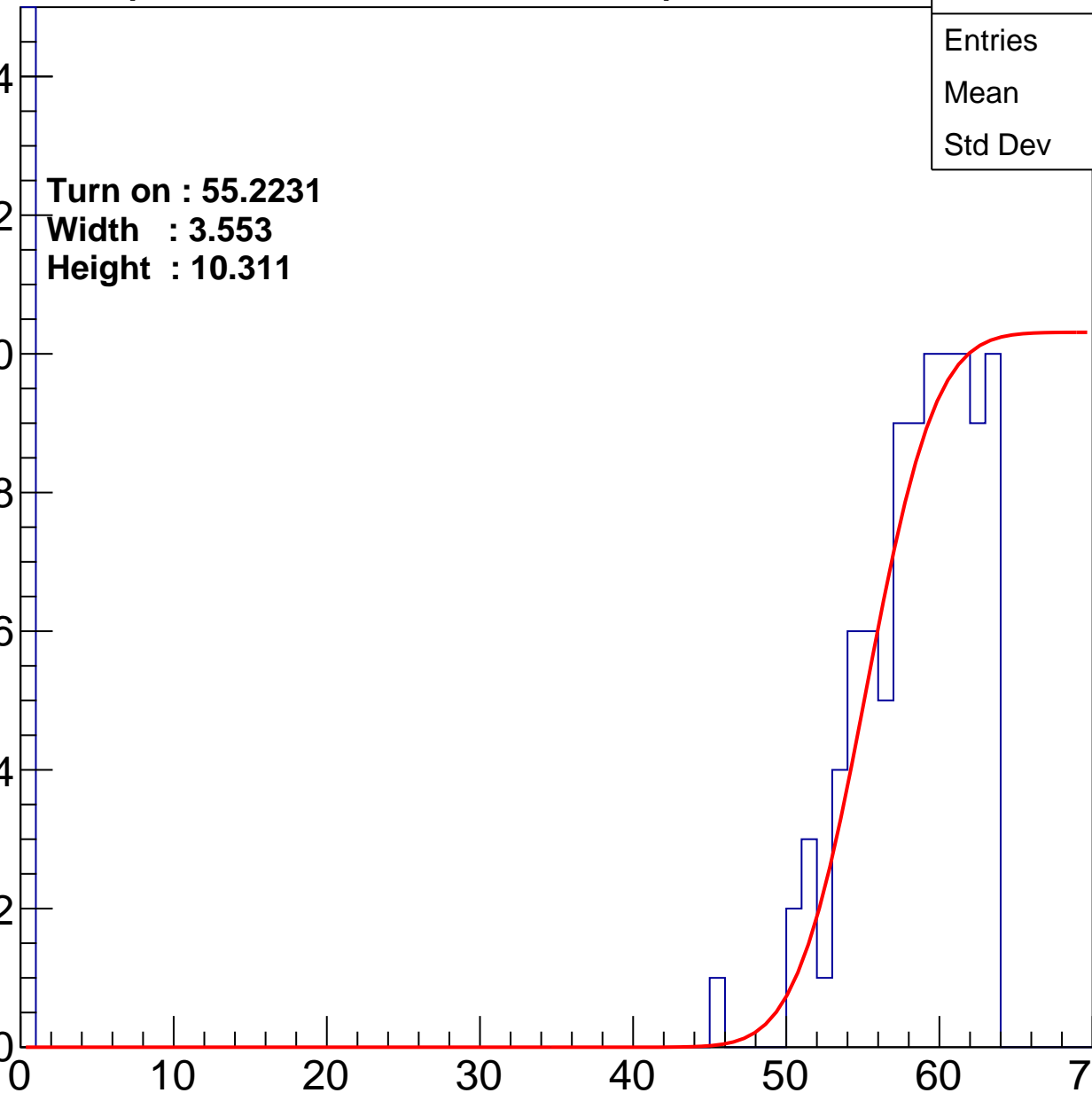
Width : 3.553

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	35.47
Std Dev	28.3

Turn on : 54.0857

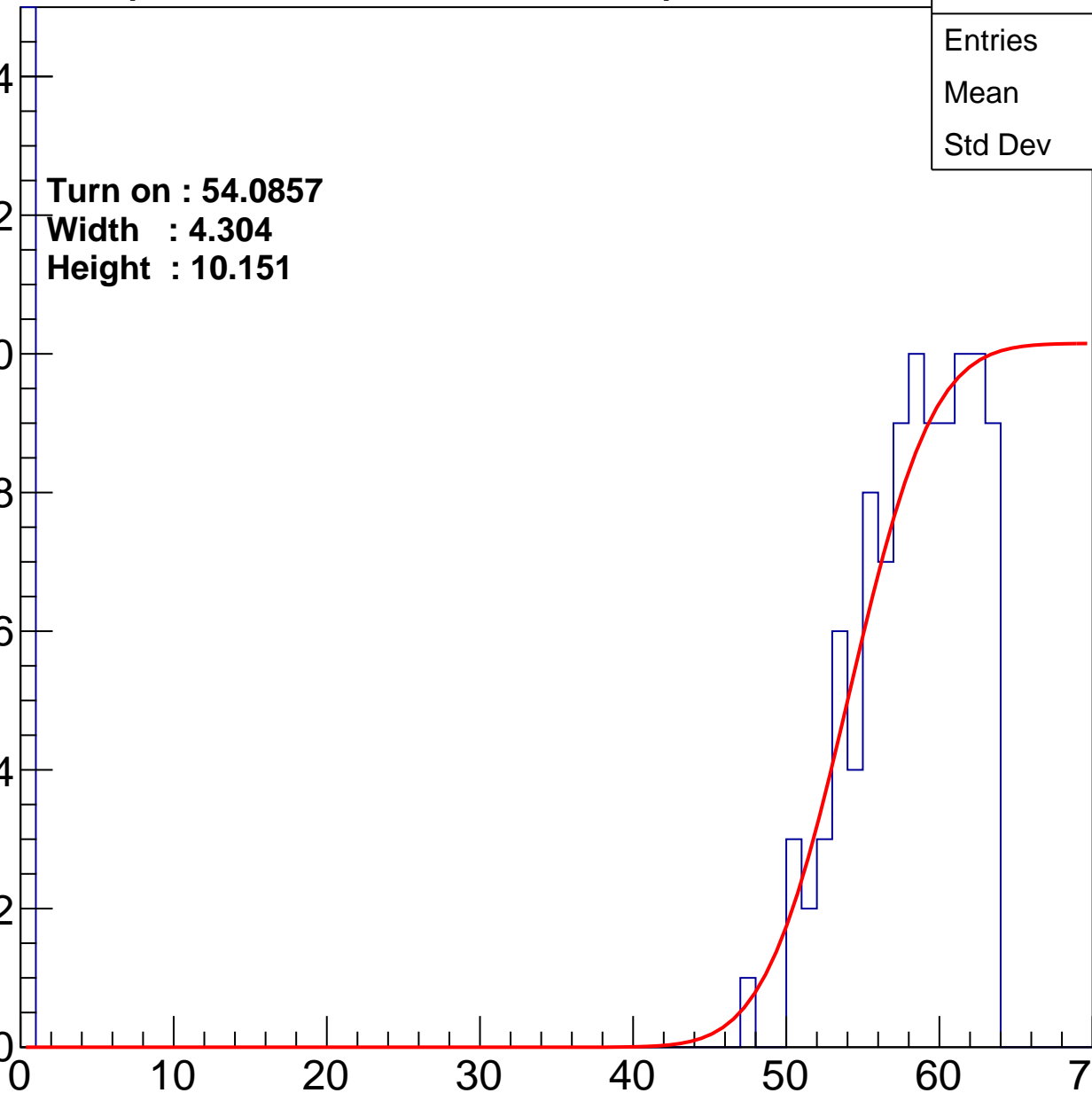
Width : 4.304

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	27.35
Std Dev	28.99

Turn on : 54.9258

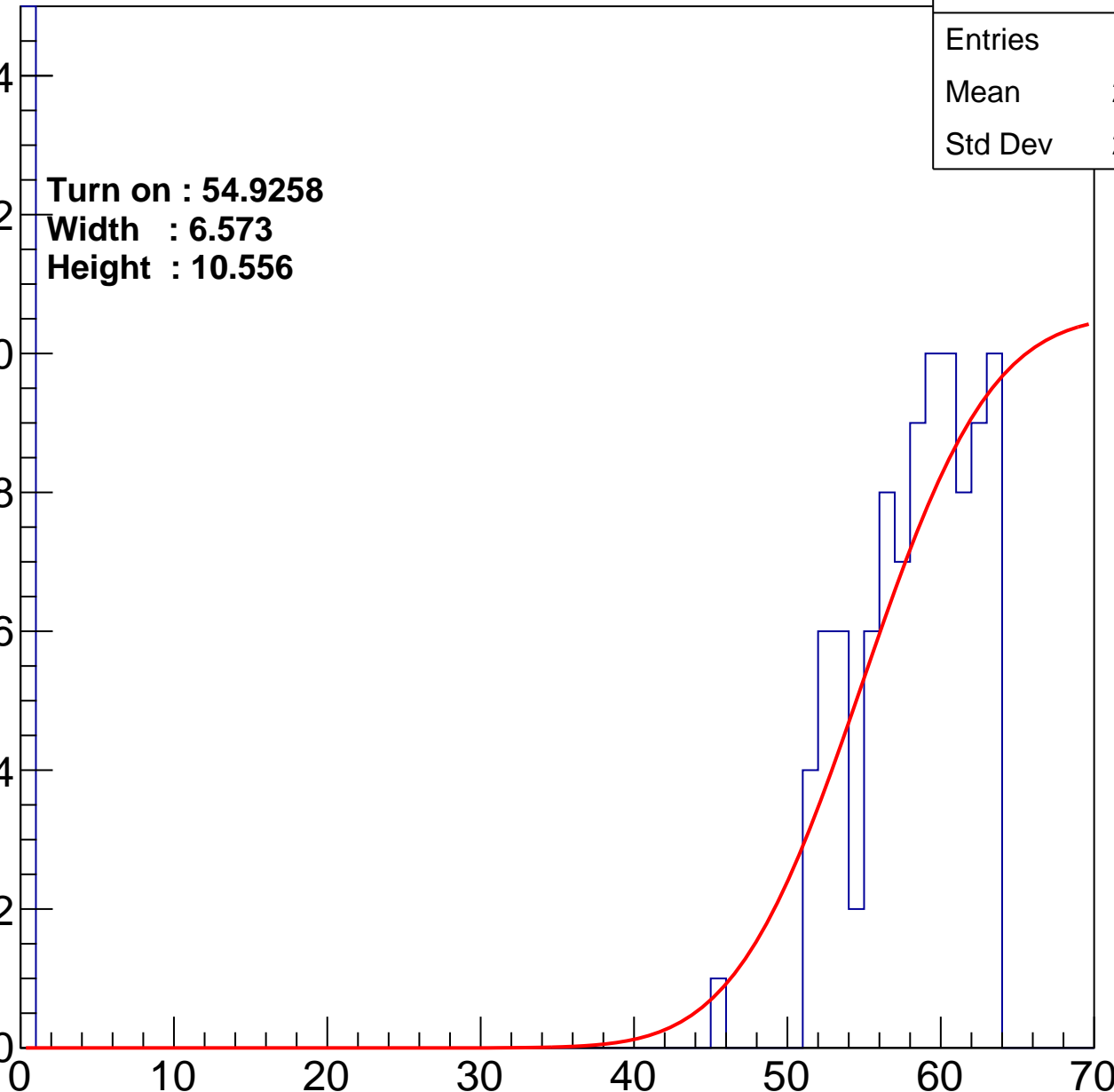
Width : 6.573

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	38.88
Std Dev	27.06

Turn on : 52.7583

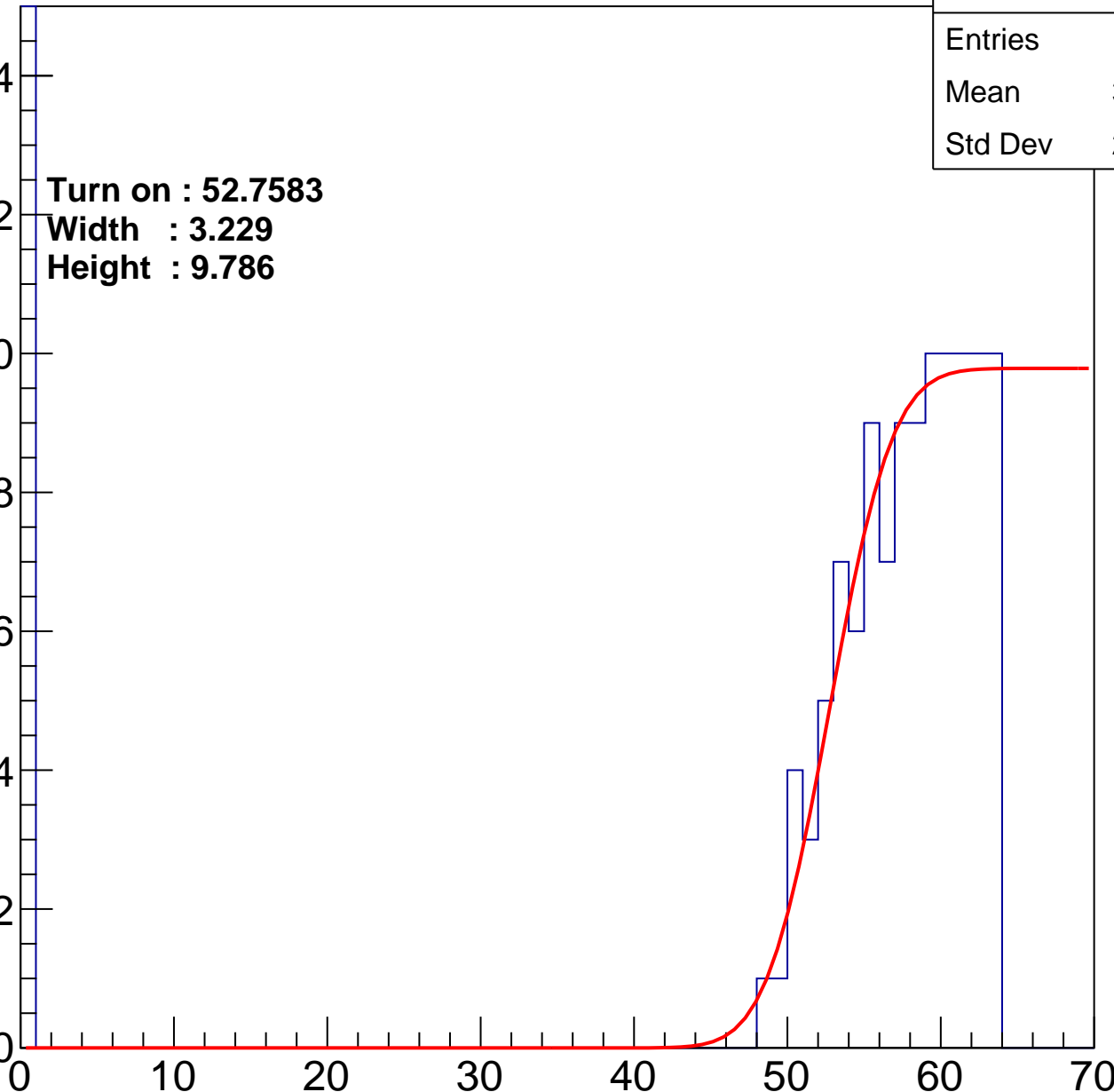
Width : 3.229

Height : 9.786

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	138
Mean	32.17
Std Dev	29.56

Turn on : 56.3754

Width : 2.950

Height : 9.778

Entry

60

50

40

30

20

10

0

0

10

20

30

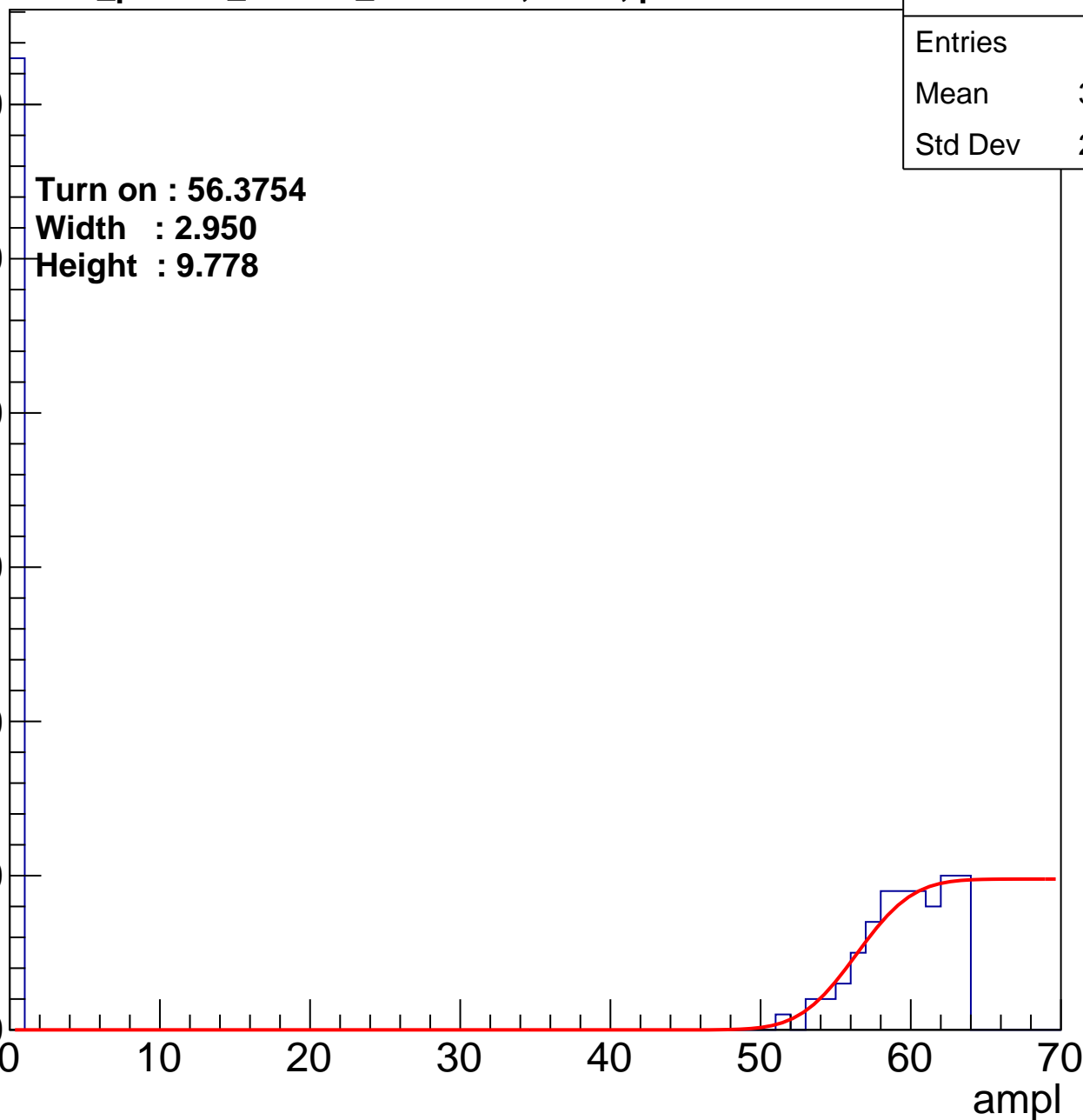
40

50

60

70

ampl



B1L104S, U25-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	33.76
Std Dev	28.88

Turn on : 54.2051

Width : 3.100

Height : 9.797

Entry

14

12

10

8

6

4

2

0

0

10

20

30

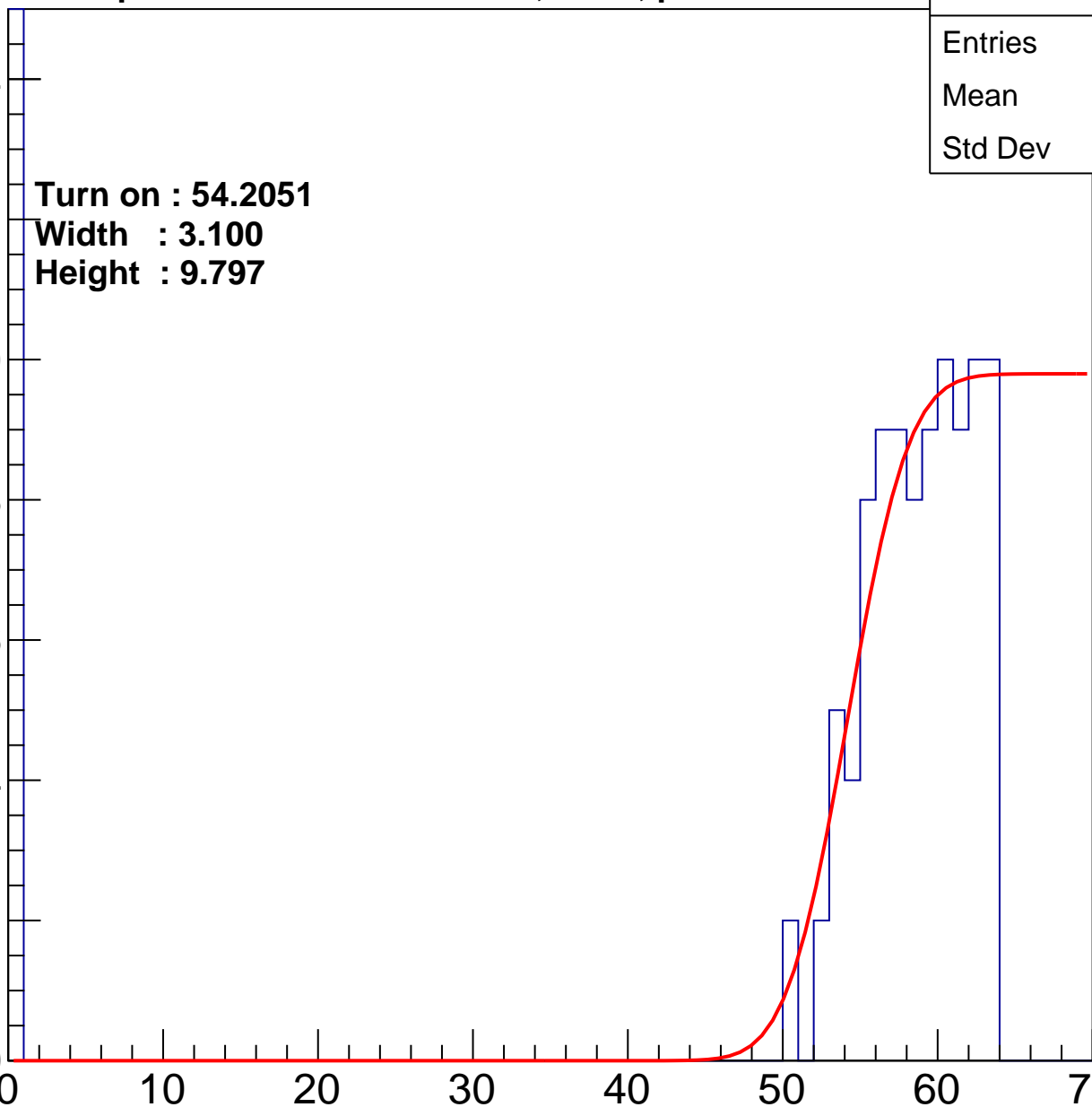
40

50

60

70

ampl



B1L104S, U25-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	36.47
Std Dev	28.21

Turn on : 55.2981

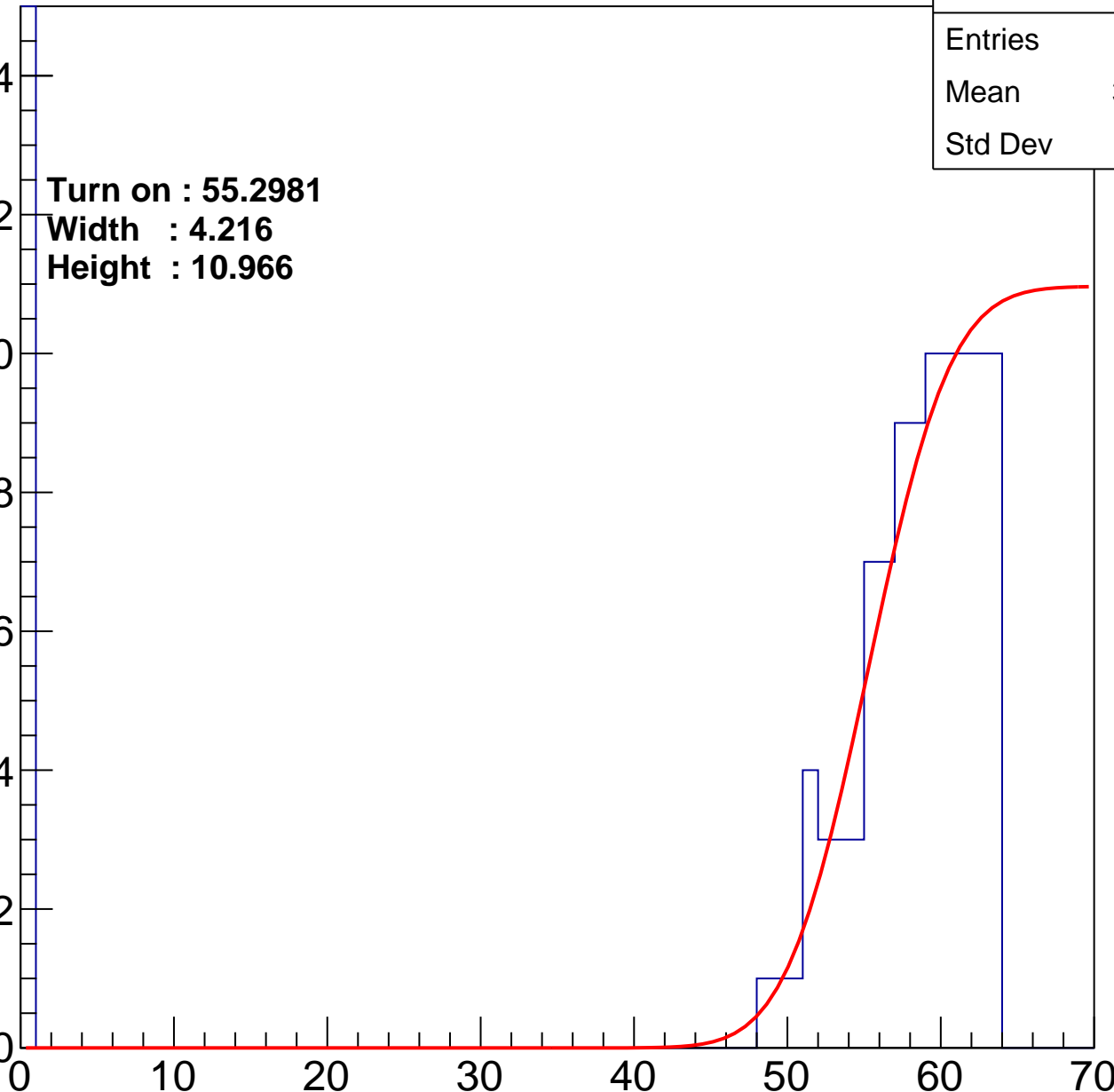
Width : 4.216

Height : 10.966

Entry

14
12
10
8
6
4
2
0

ampl



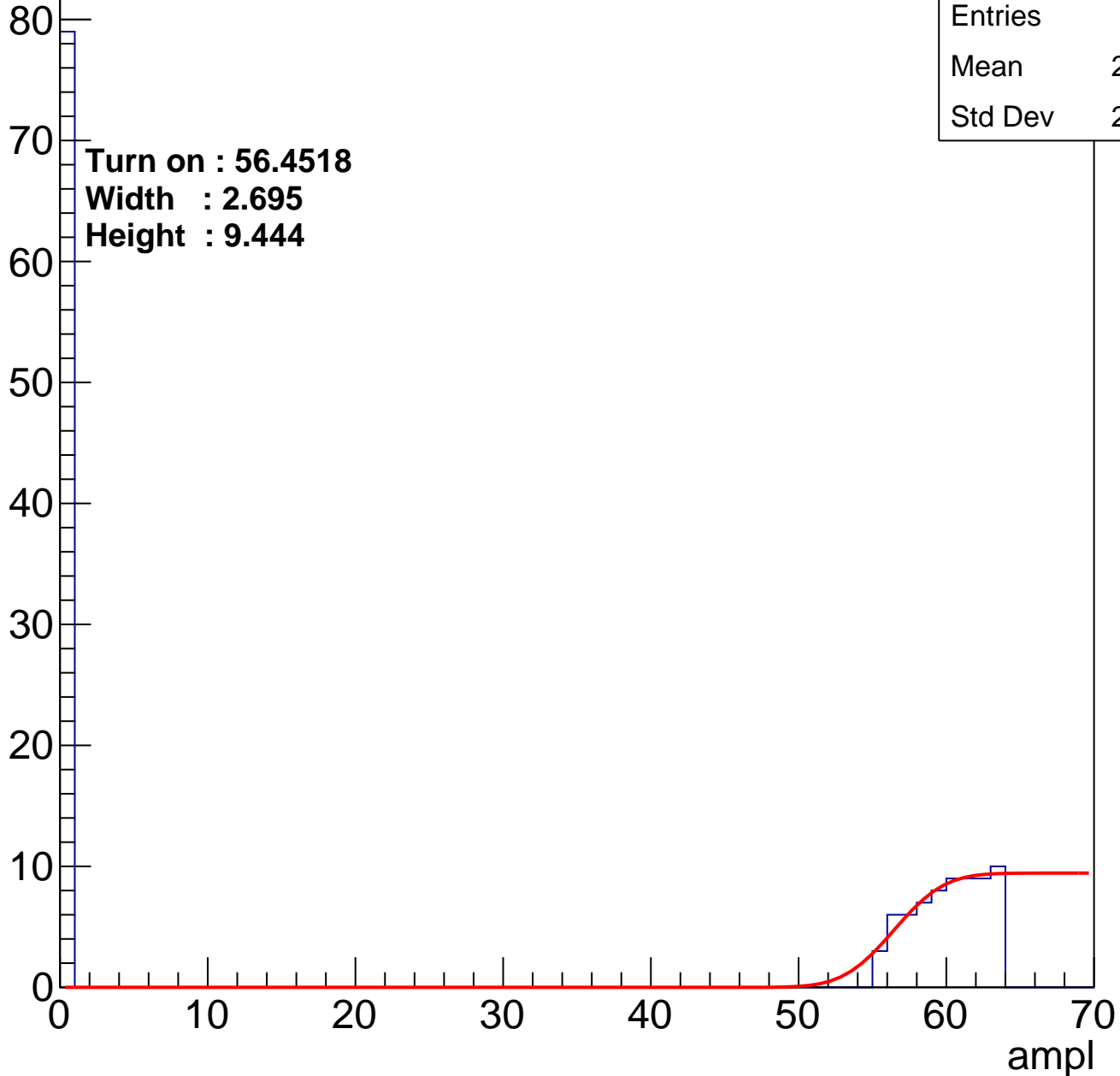
B1L104S, U25-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	146
Mean	27.38
Std Dev	29.78

Turn on : 56.4518
Width : 2.695
Height : 9.444

Entry



B1L104S, U25-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	29.19
Std Dev	29.29

Turn on : 55.4861

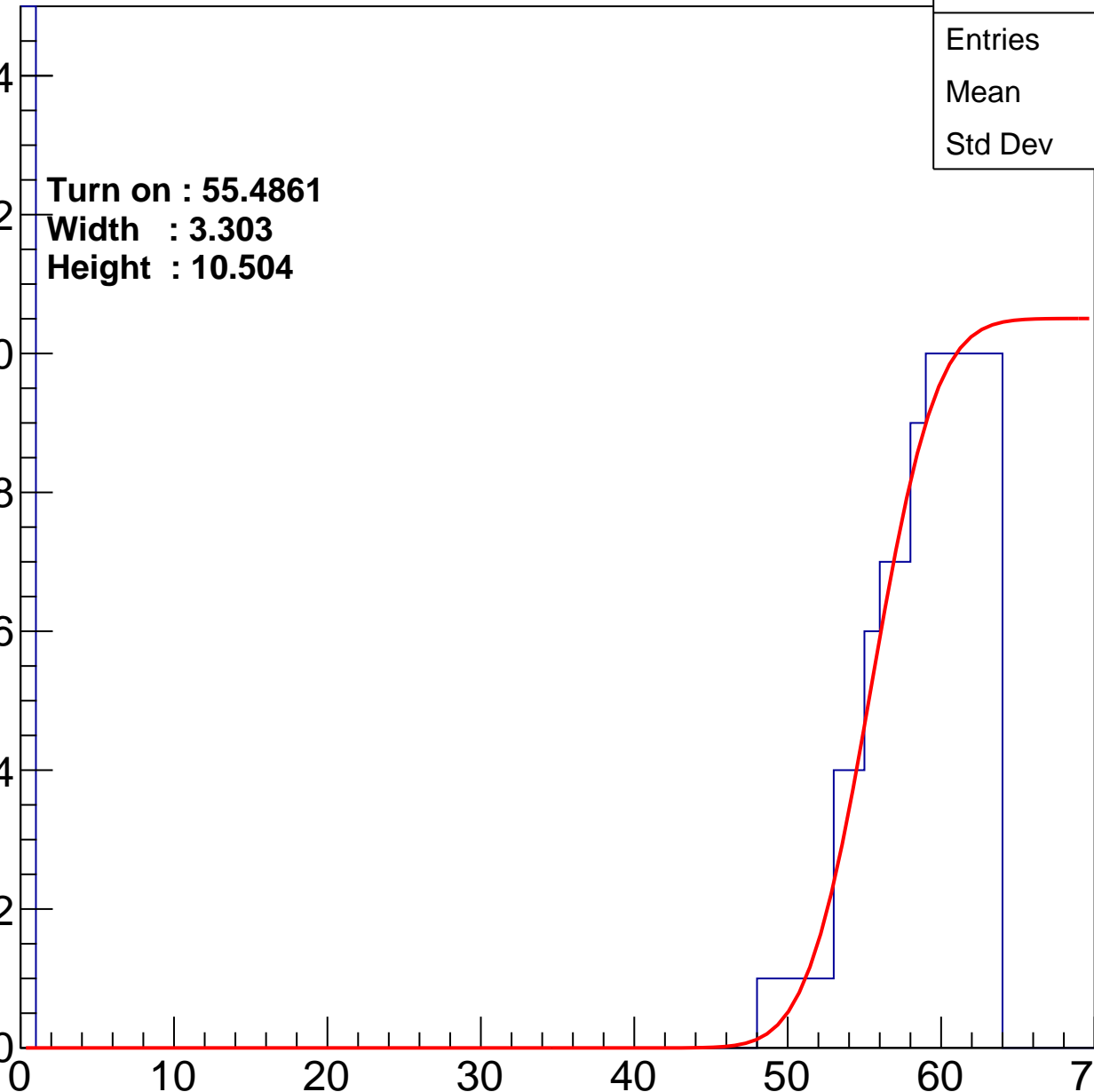
Width : 3.303

Height : 10.504

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	38.29
Std Dev	27.62

Turn on : 53.9697

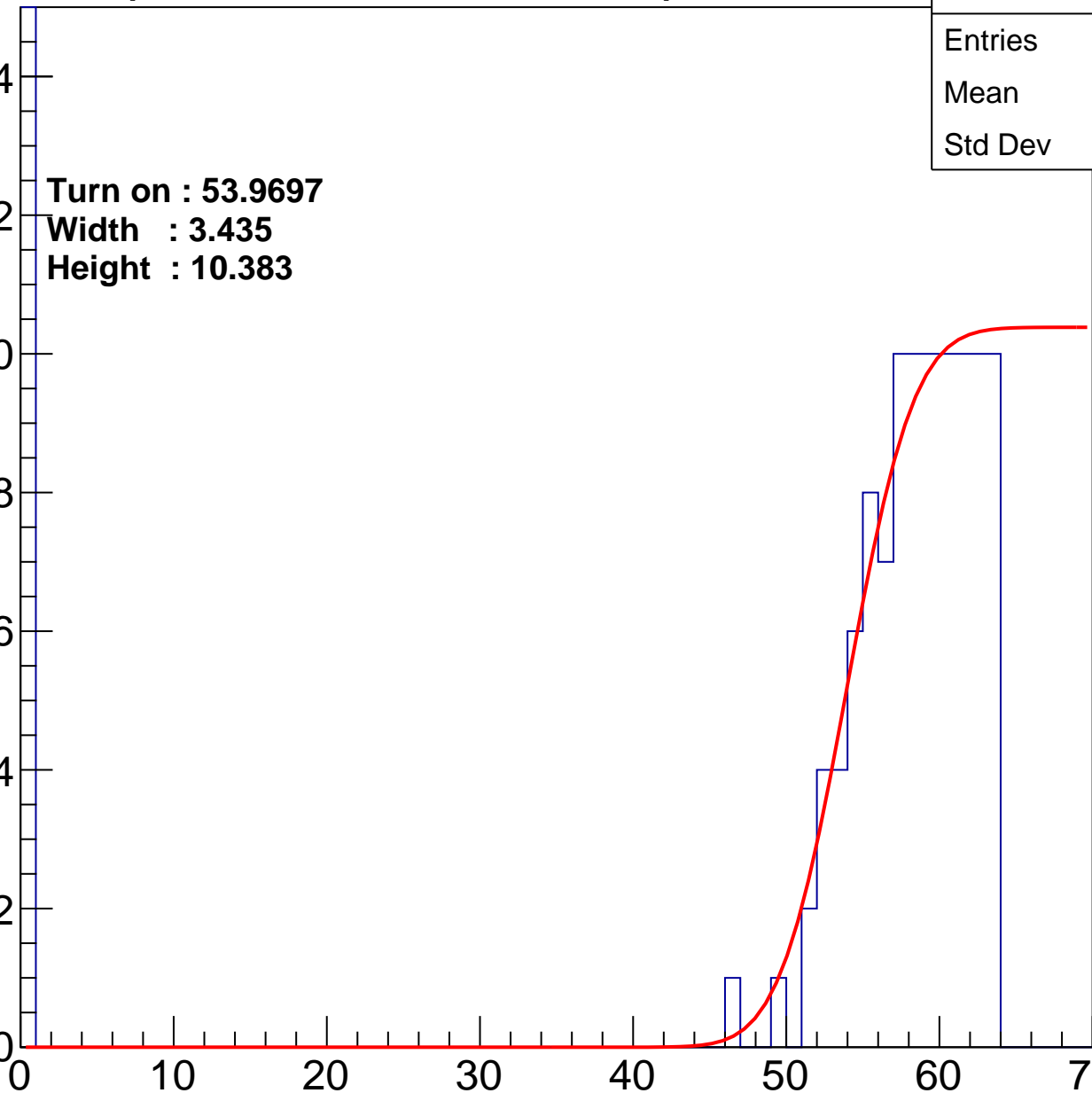
Width : 3.435

Height : 10.383

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	32.12
Std Dev	29.23

Turn on : 55.7543

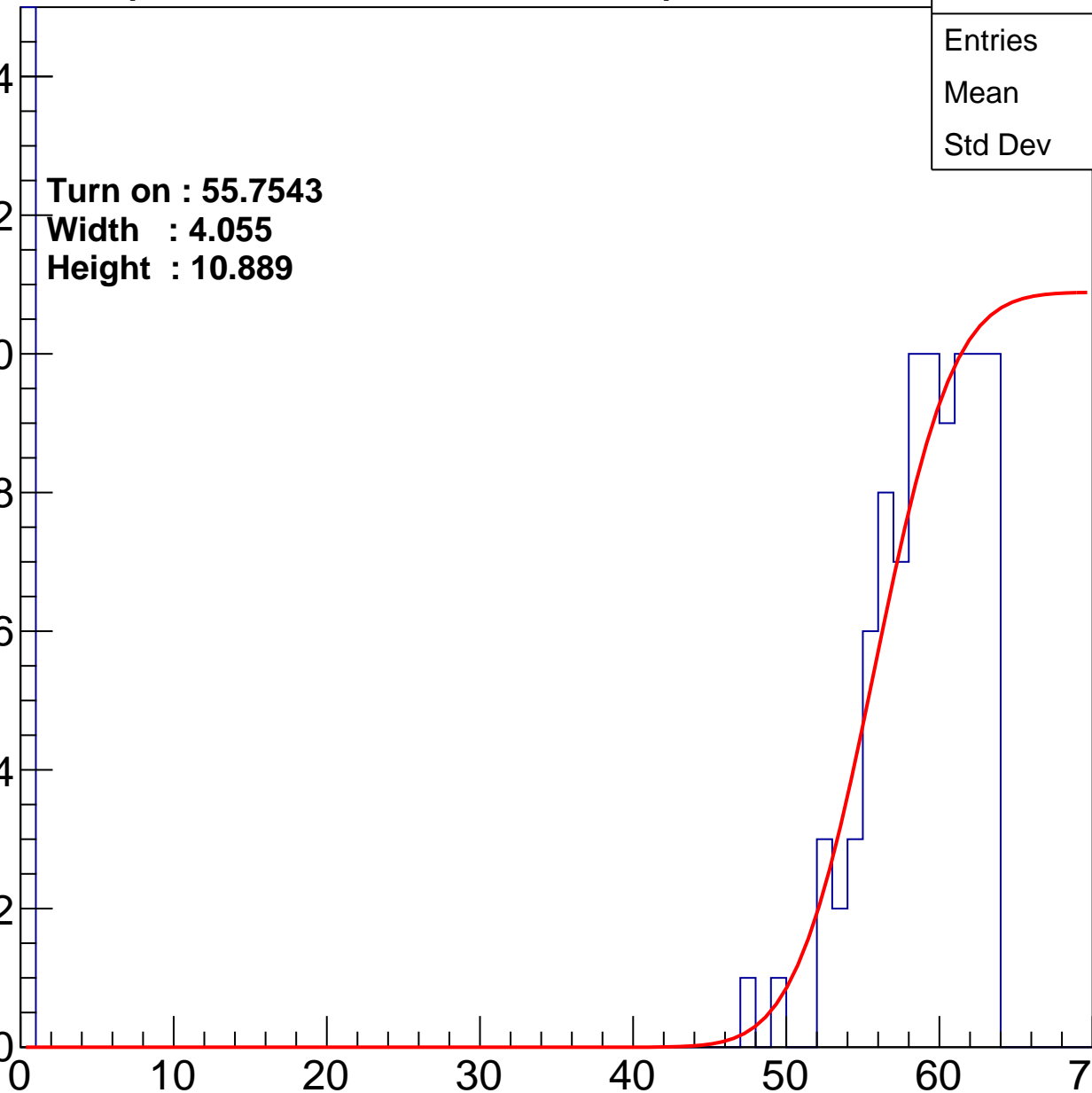
Width : 4.055

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	32.03
Std Dev	29.36

Turn on : 54.9458

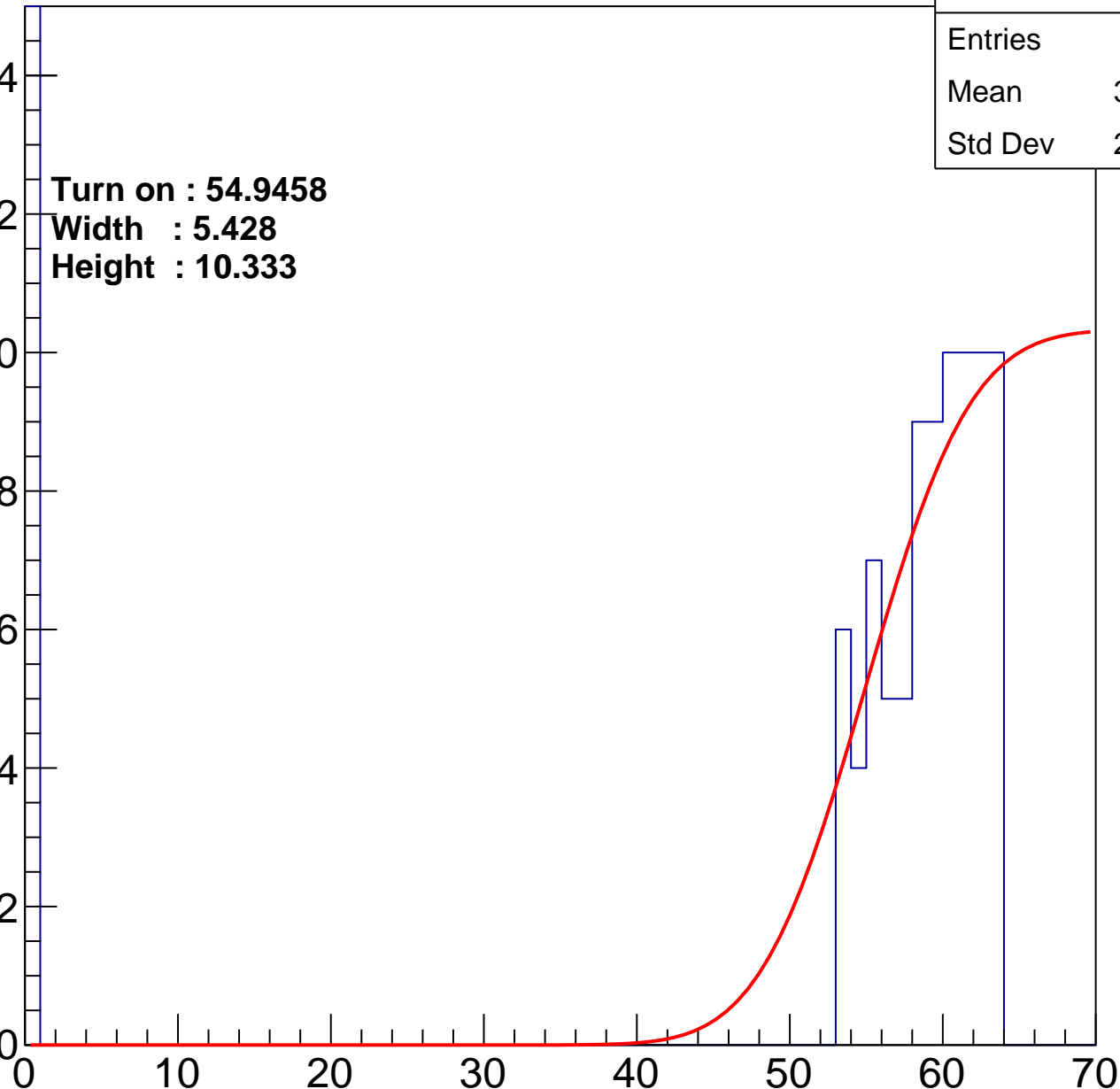
Width : 5.428

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	28.92
Std Dev	29.32

Turn on : 54.5348

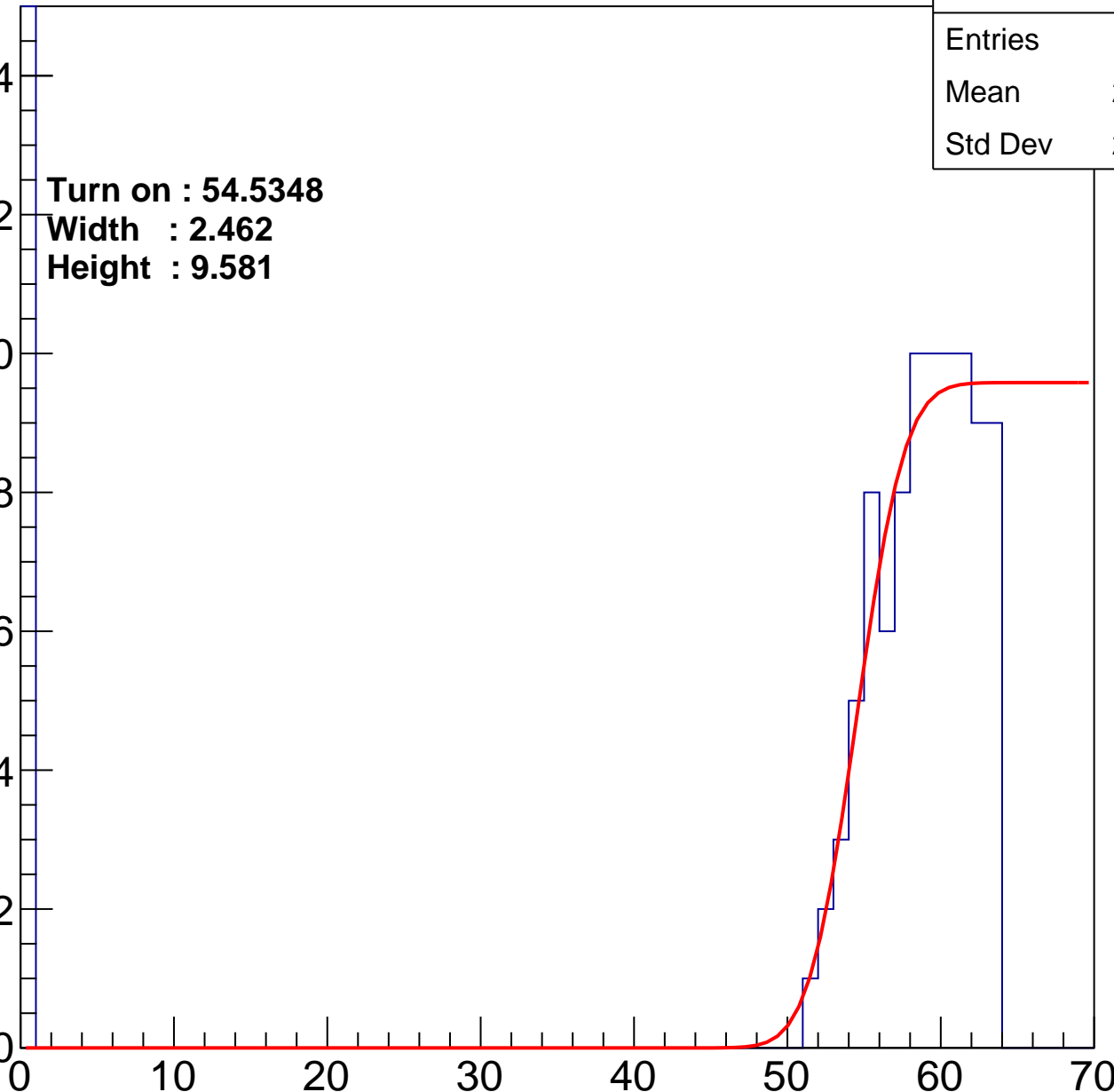
Width : 2.462

Height : 9.581

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	32.42
Std Dev	28.98

Turn on : 55.1038

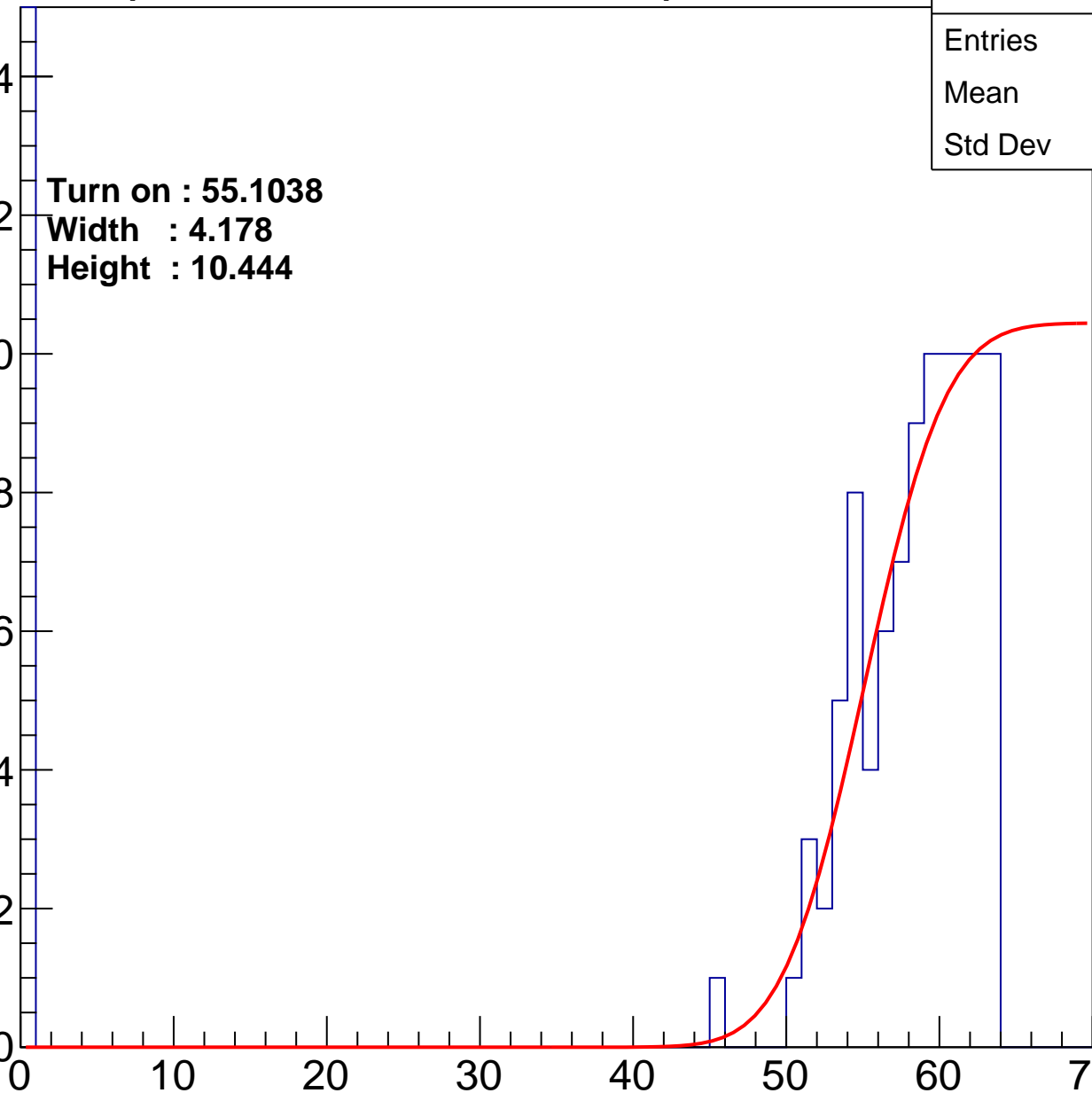
Width : 4.178

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



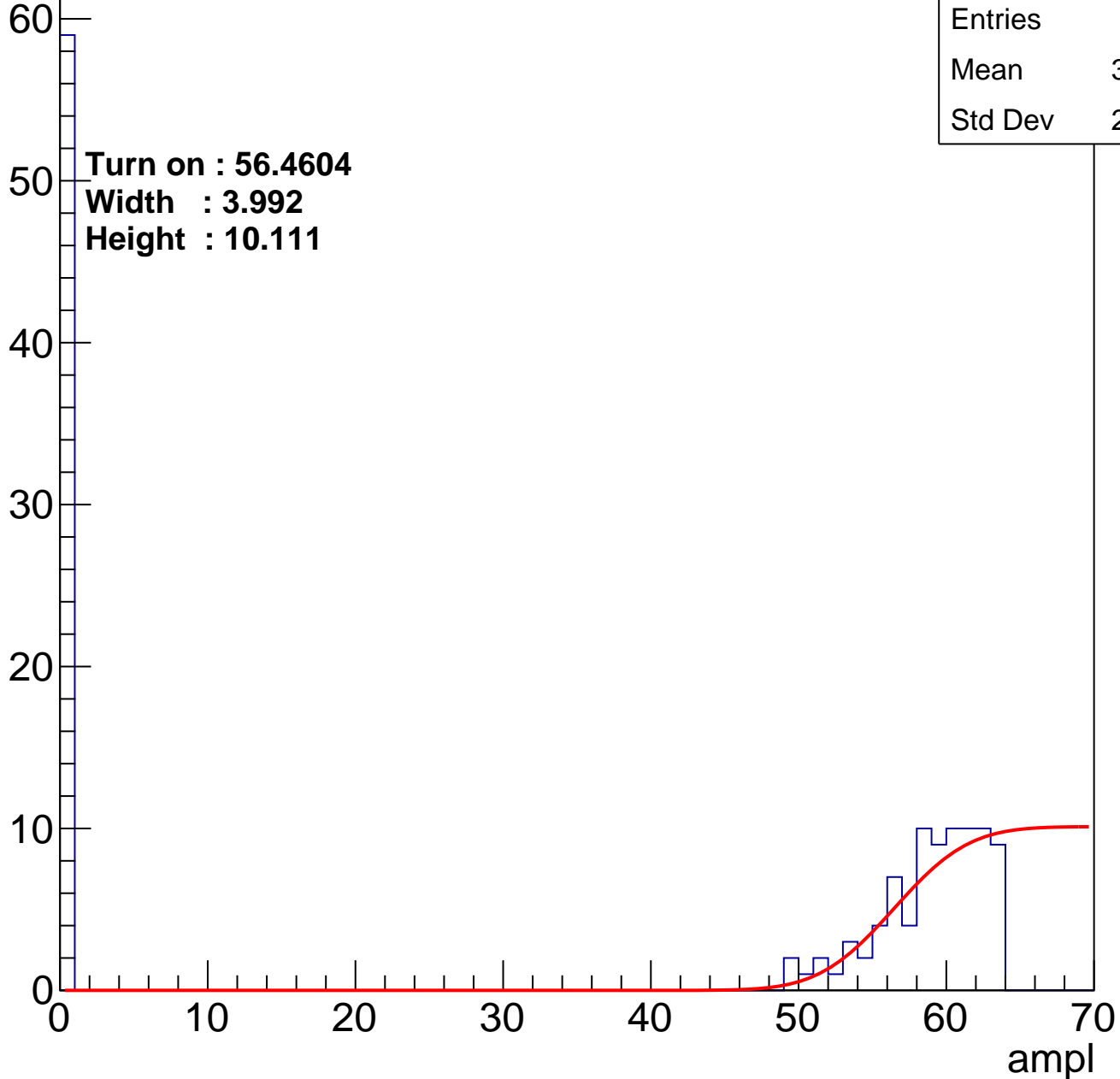
B1L104S, U25-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	143
Mean	34.38
Std Dev	28.94

Turn on : 56.4604
Width : 3.992
Height : 10.111

Entry



B1L104S, U25-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	36.14
Std Dev	27.65

Turn on : 52.7433

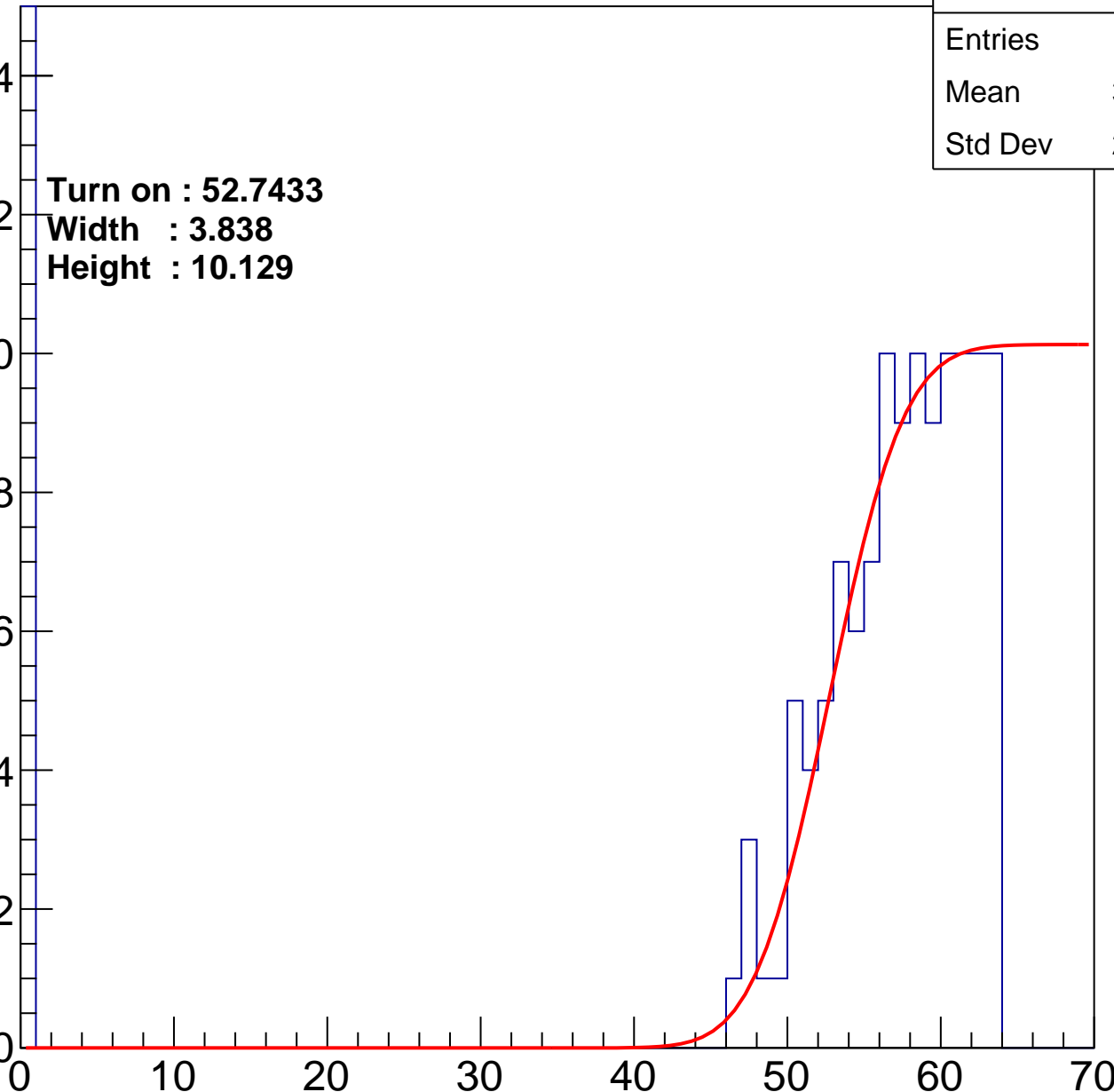
Width : 3.838

Height : 10.129

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	37.33
Std Dev	27.5

Turn on : 52.8001

Width : 4.338

Height : 10.352

Entry

14

12

10

8

6

4

2

0

ampl

0

10

20

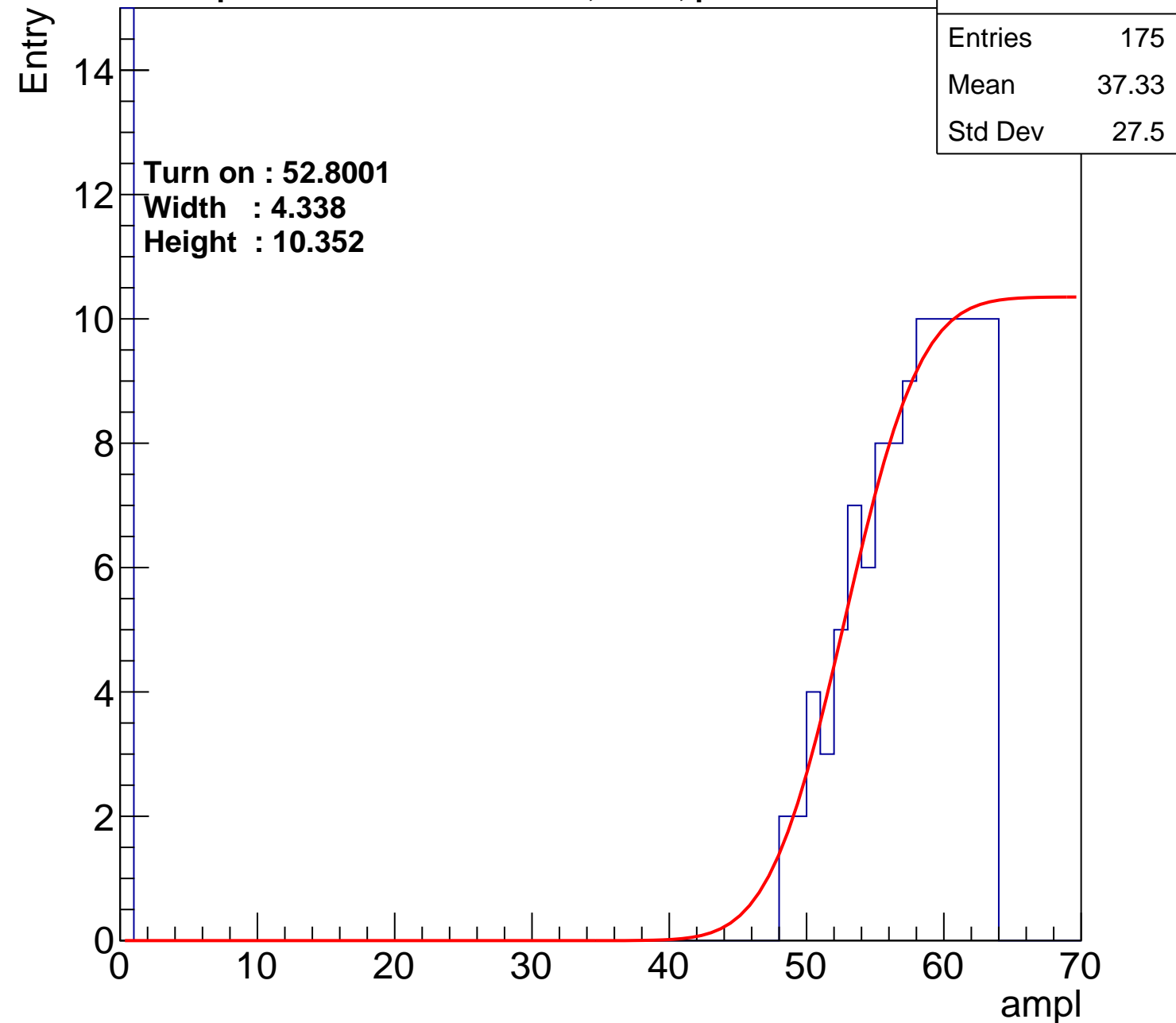
30

40

50

60

70



B1L104S, U25-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	33.55
Std Dev	29.1

Turn on : 55.4386

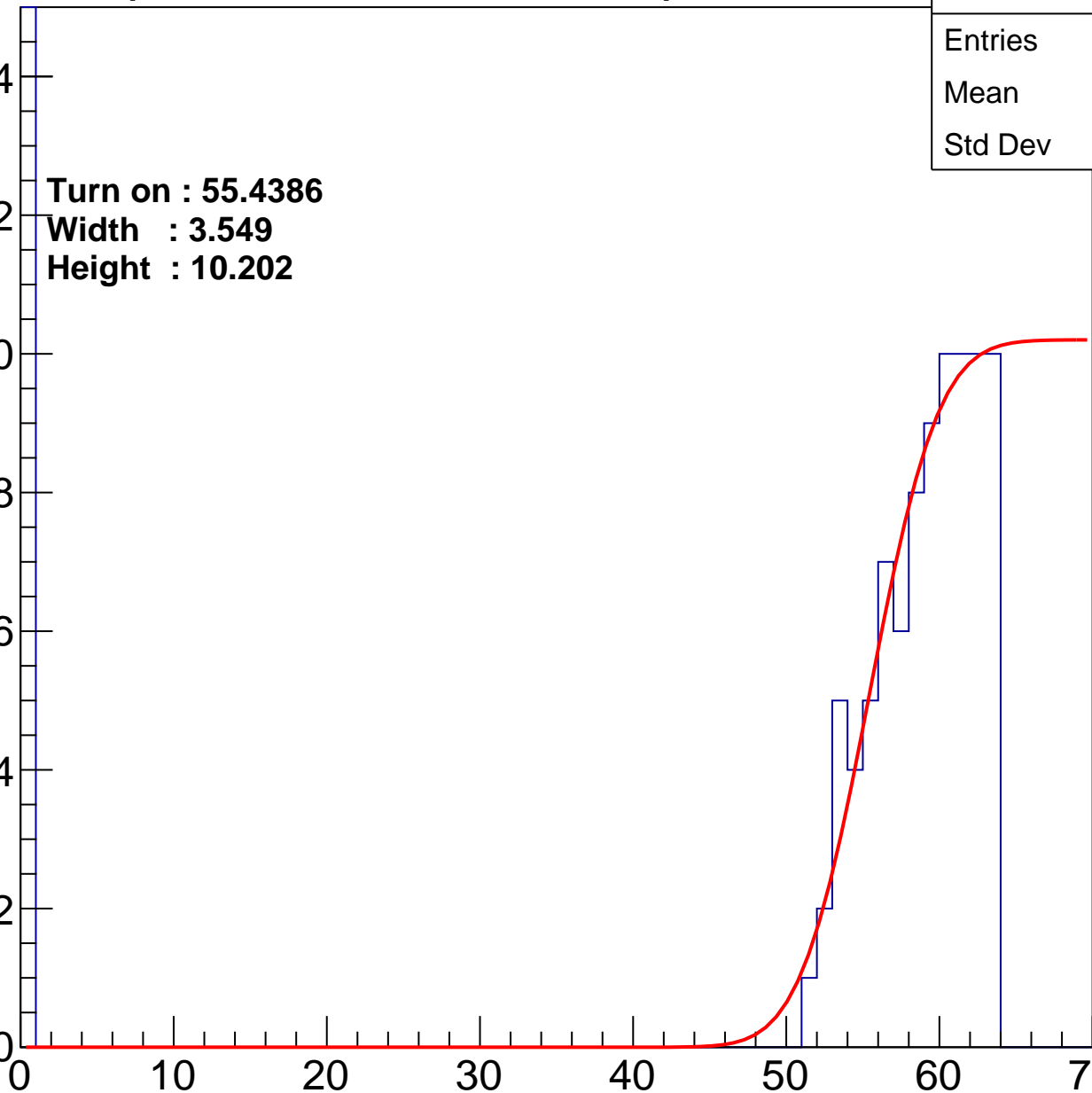
Width : 3.549

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	35.02
Std Dev	28.58

Turn on : 55.2253

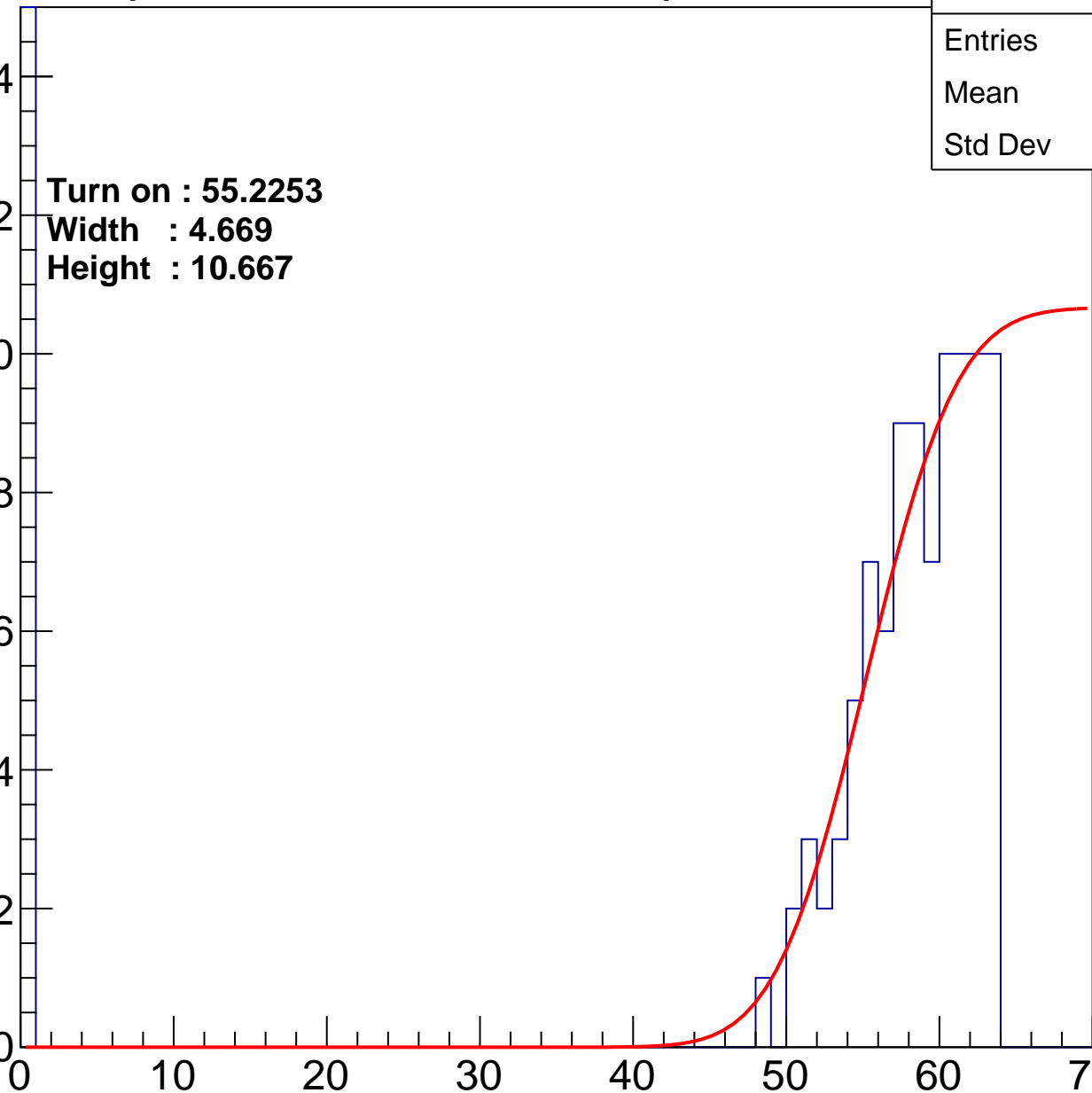
Width : 4.669

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	30.79
Std Dev	29.27

Turn on : 55.0920

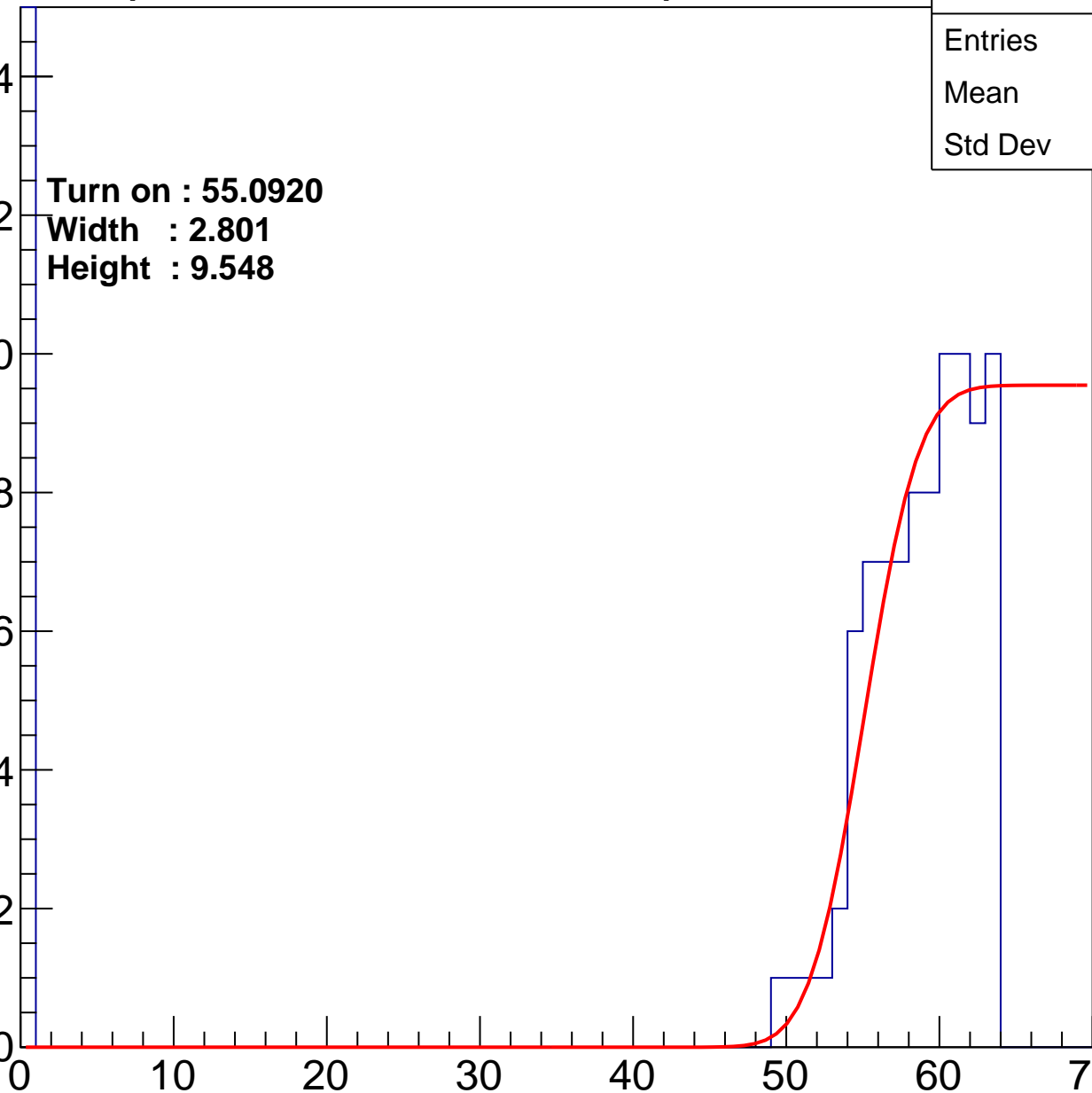
Width : 2.801

Height : 9.548

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	33.1
Std Dev	28.98

Turn on : 56.7453

Width : 3.894

Height : 10.111

Entry

60

50

40

30

20

10

0

0

10

20

30

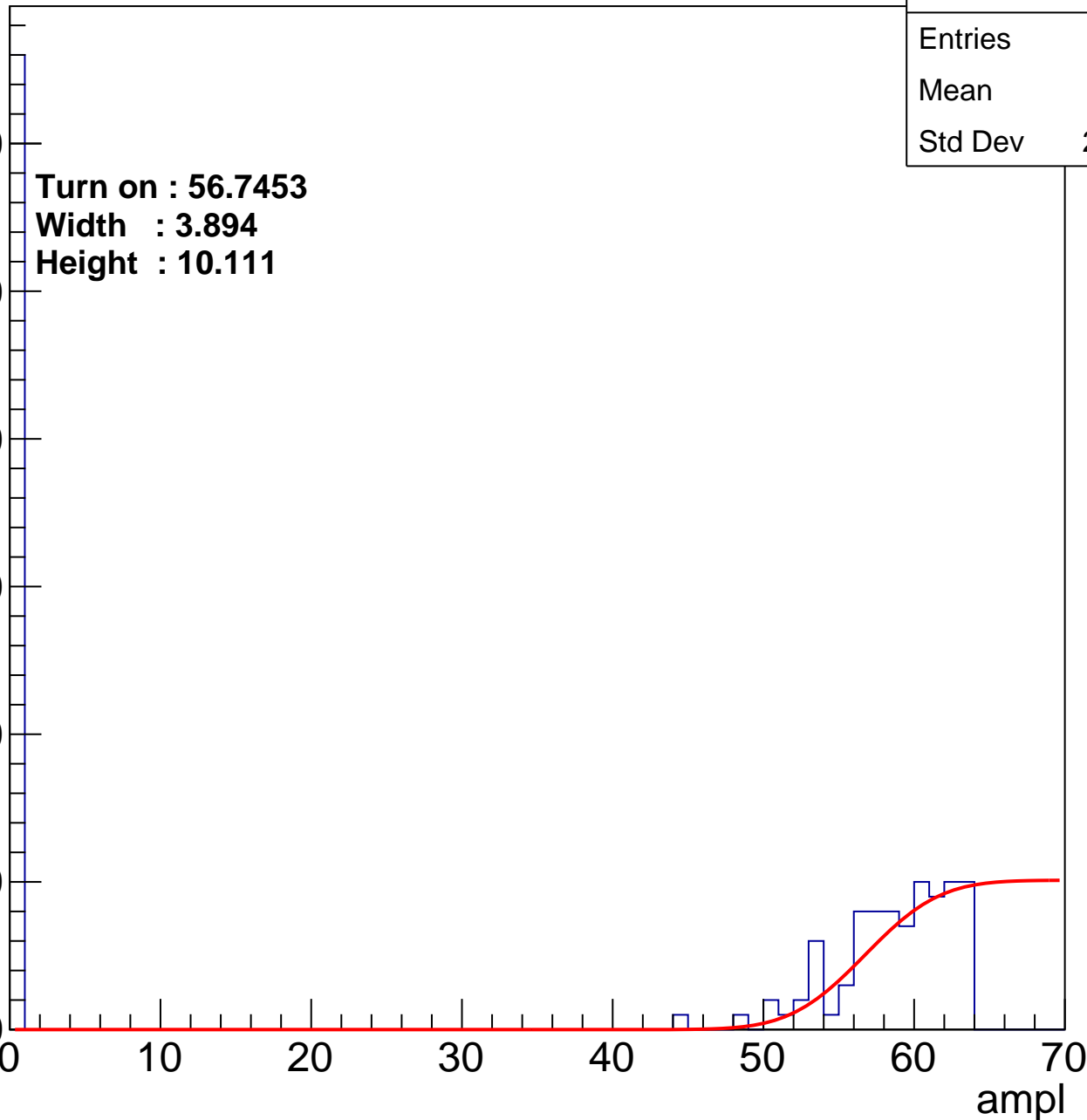
40

50

60

70

ampl



B1L104S, U25-ch45

calib_packv5_033123_0516.root, FC#4, port A1

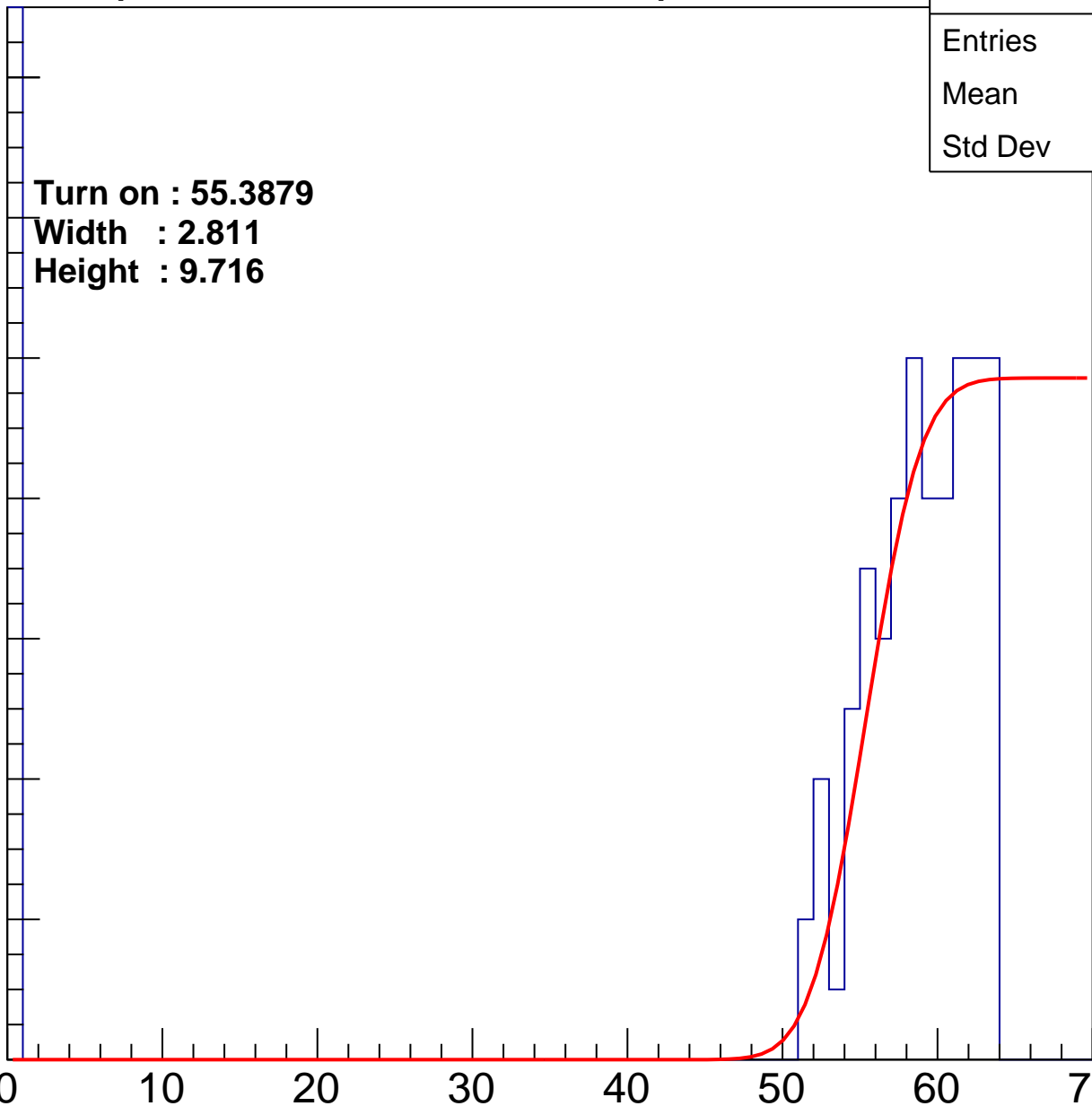
Entry

14
12
10
8
6
4
2
0

Turn on : 55.3879
Width : 2.811
Height : 9.716

Entries	158
Mean	32.92
Std Dev	29.09

ampl



B1L104S, U25-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	31.84
Std Dev	29.14

Turn on : 54.9910

Width : 4.275

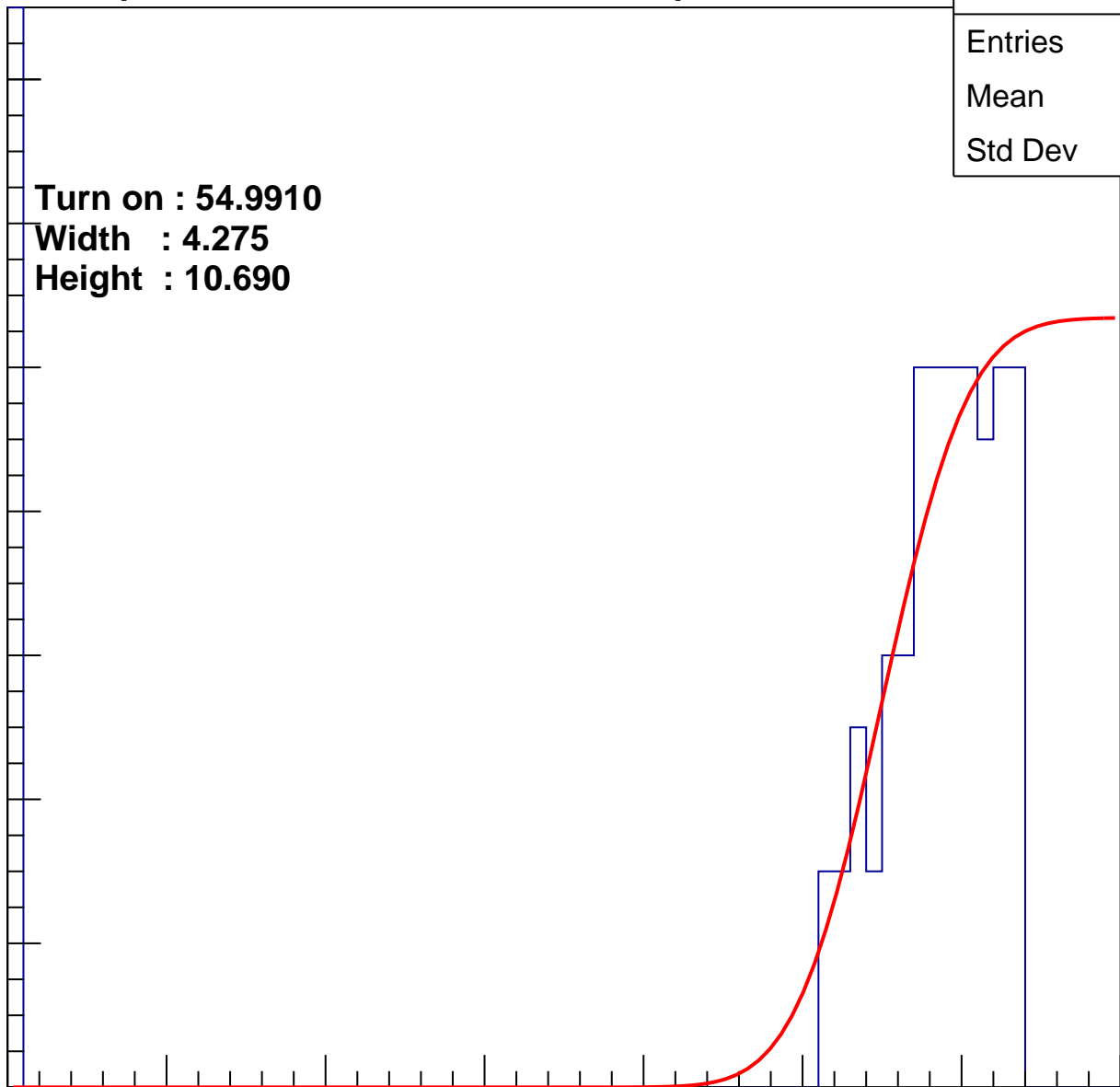
Height : 10.690

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U25-ch47

calib_packv5_033123_0516.root, FC#4, port A1

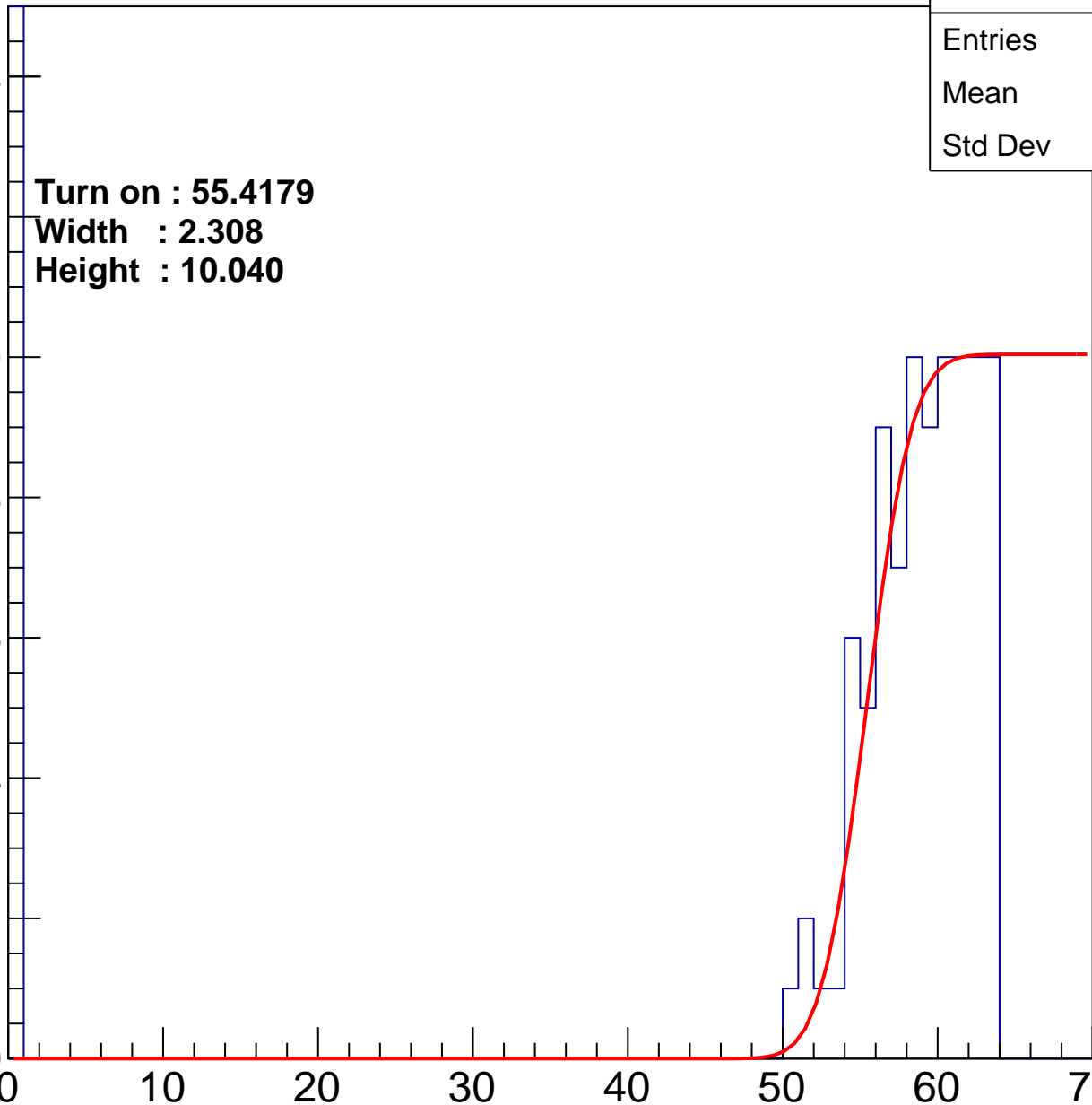
Entry

14
12
10
8
6
4
2
0

Turn on : 55.4179
Width : 2.308
Height : 10.040

Entries	152
Mean	35.07
Std Dev	28.82

ampl



B1L104S, U25-ch48

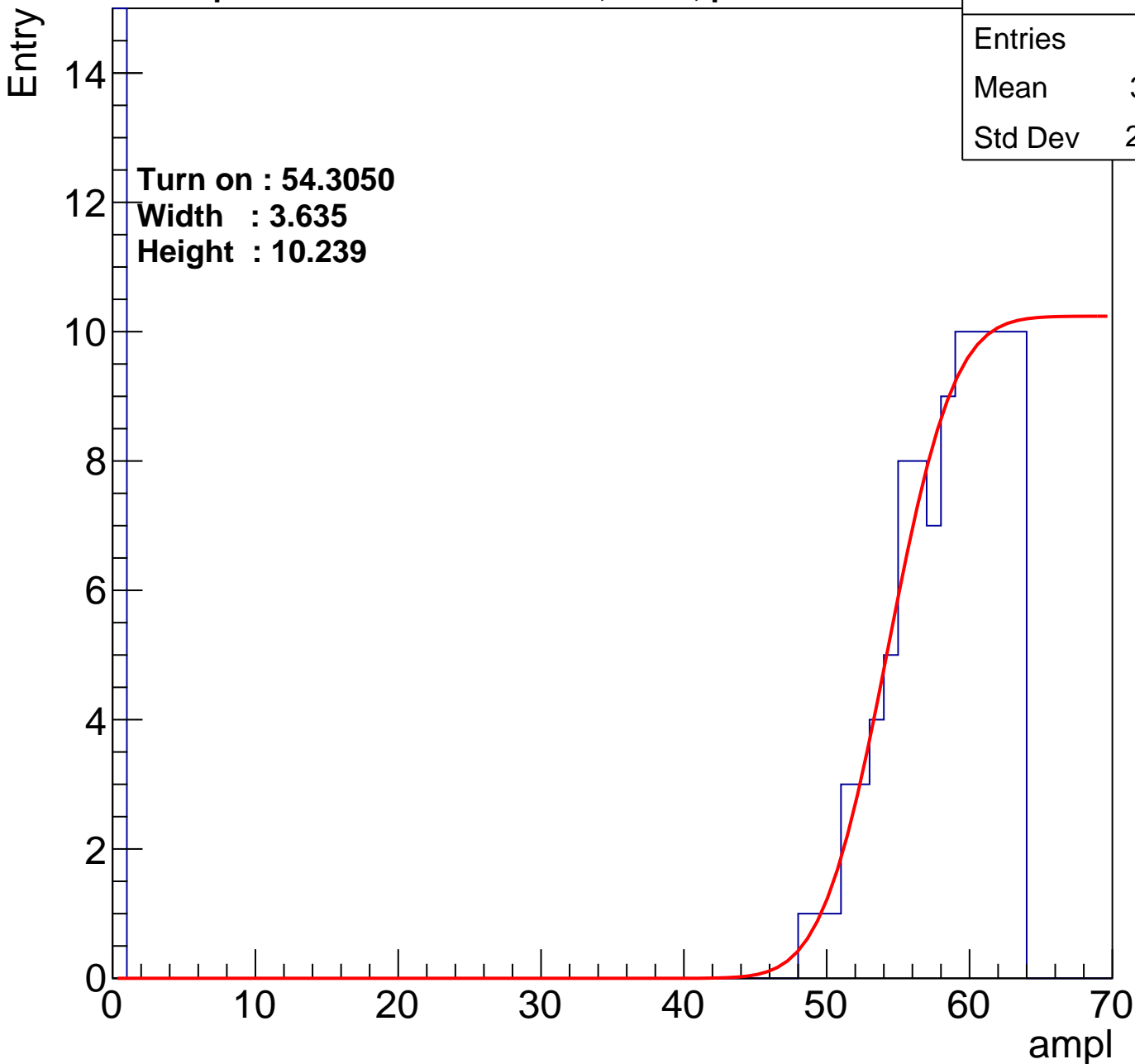
calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	36.01
Std Dev	28.27

Turn on : 54.3050

Width : 3.635

Height : 10.239



B1L104S, U25-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	34
Std Dev	28.65

Turn on : 55.3013

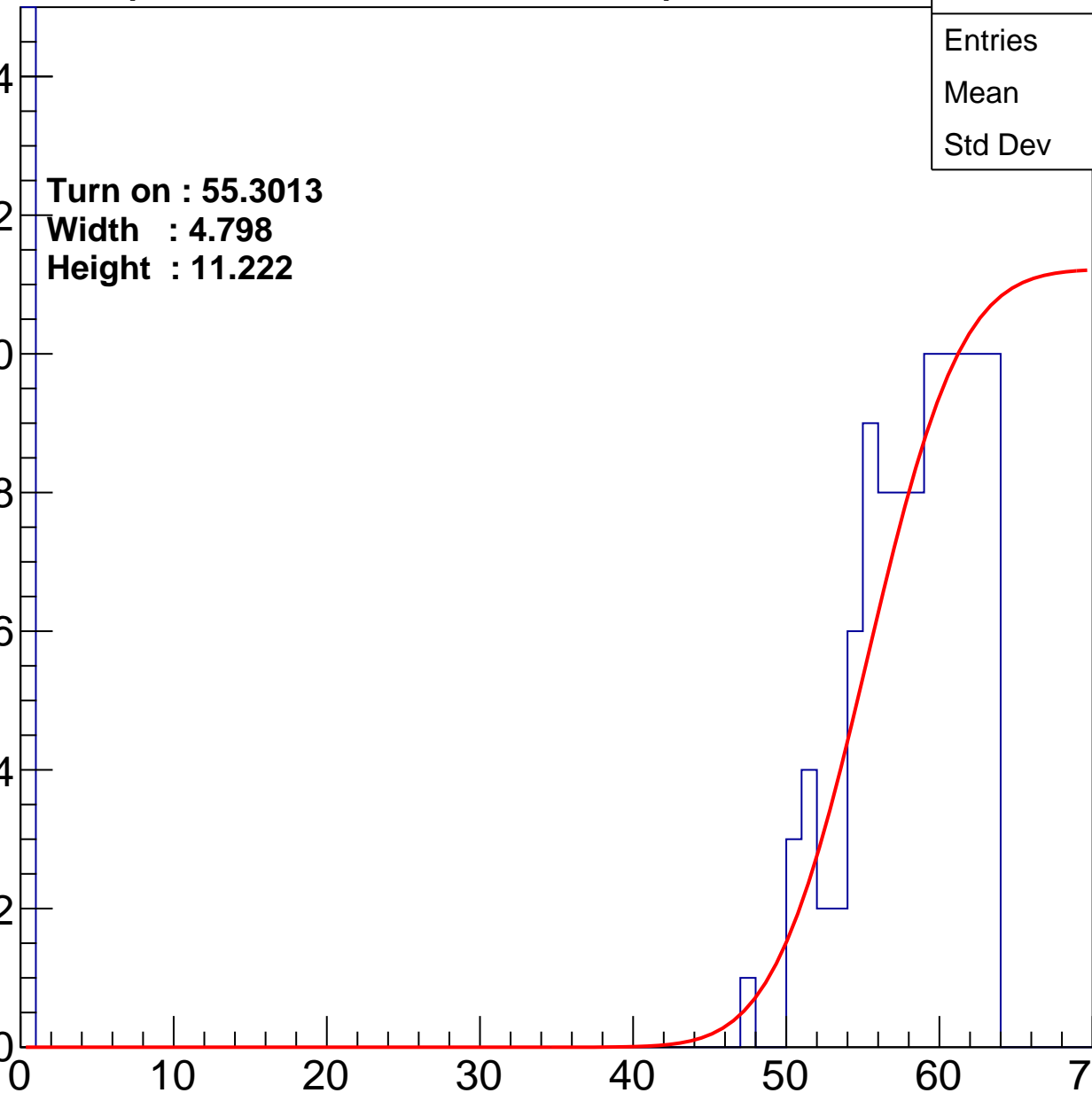
Width : 4.798

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	144
Mean	43.54
Std Dev	24.92

Turn on : 53.5240

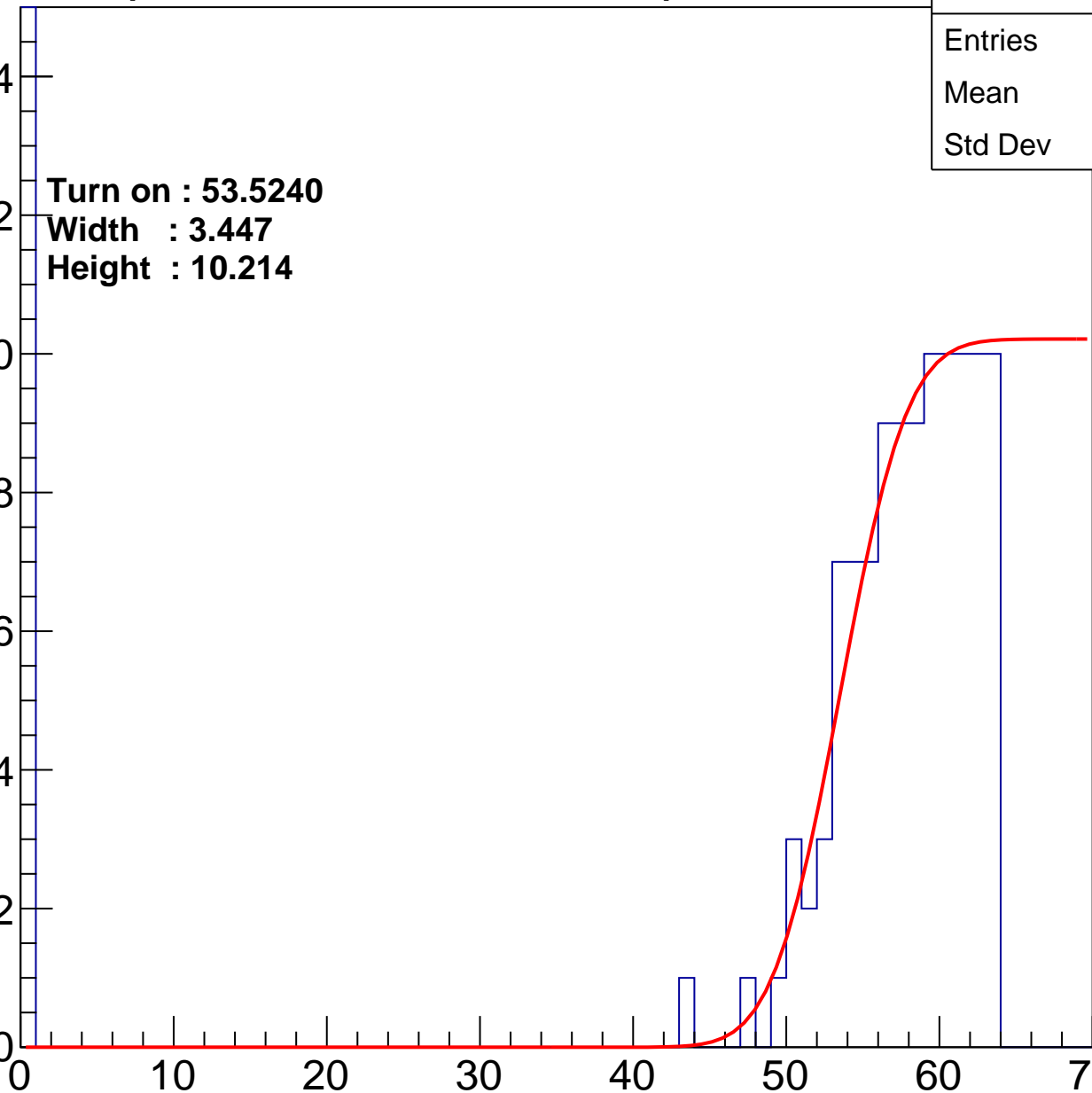
Width : 3.447

Height : 10.214

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	36.23
Std Dev	28.53

Turn on : 55.7421

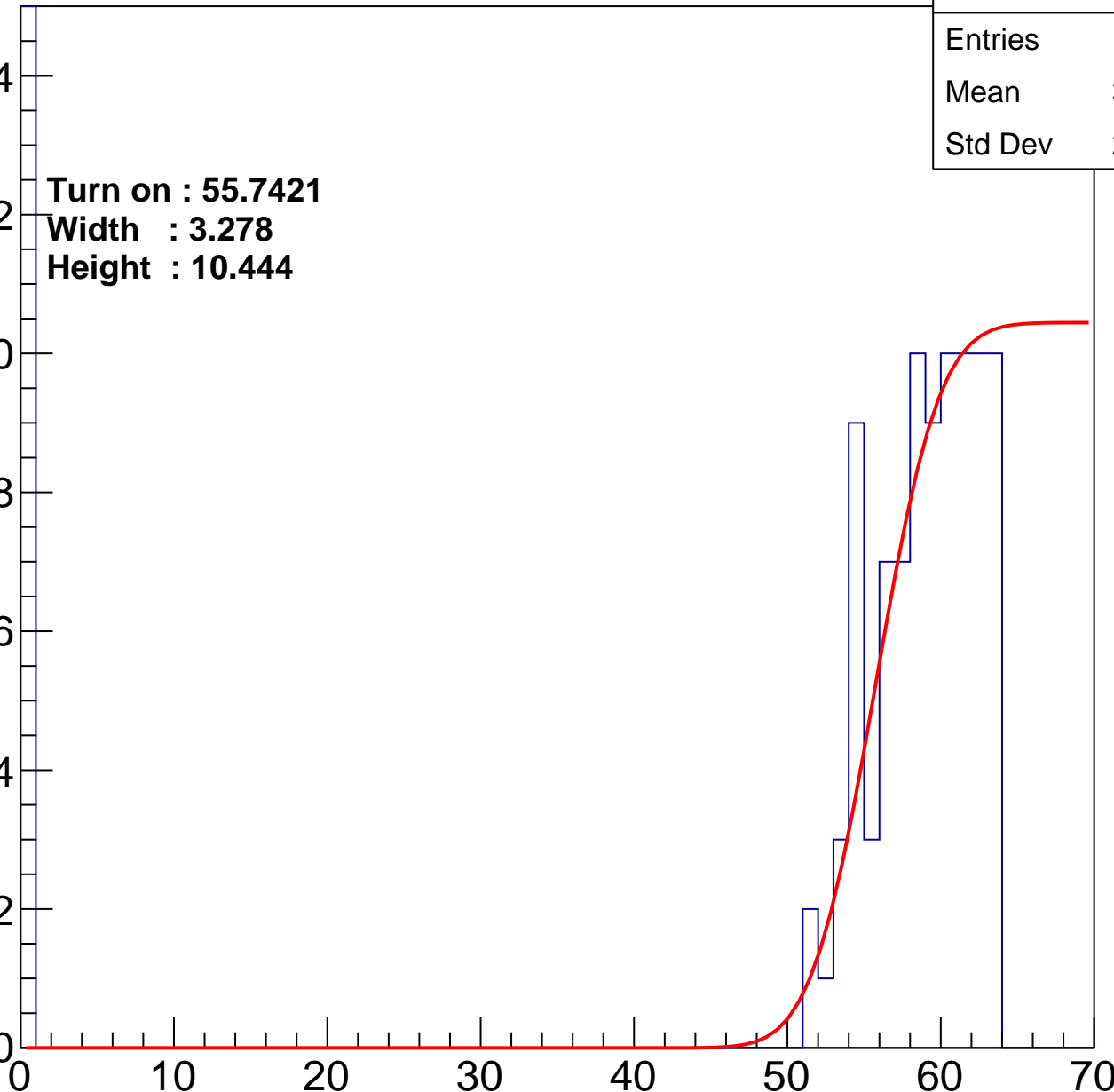
Width : 3.278

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch52

calib_packv5_033123_0516.root, FC#4, port A1

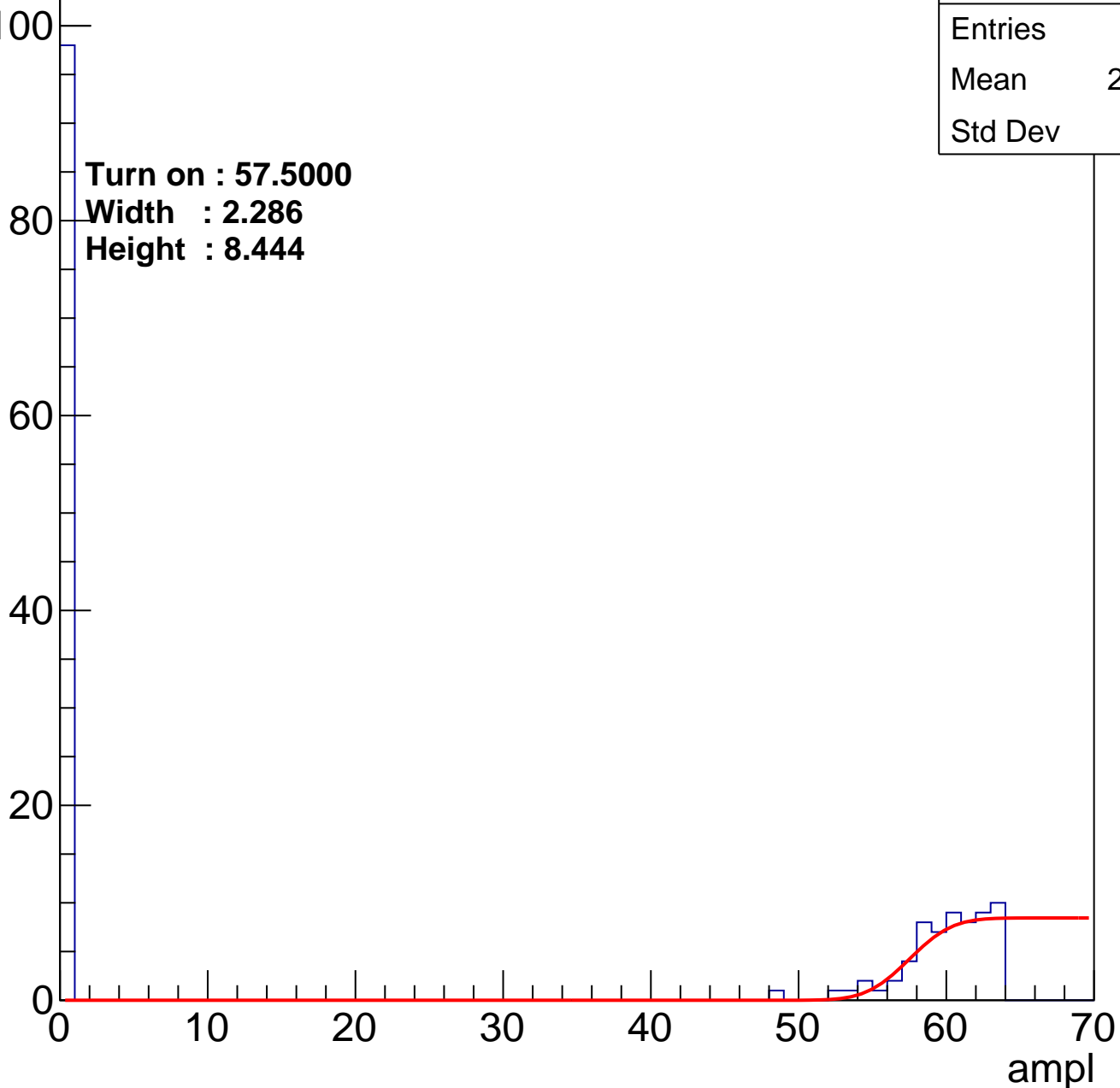
Entry

Entries	161
Mean	23.29
Std Dev	29.1

Turn on : 57.5000

Width : 2.286

Height : 8.444



B1L104S, U25-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	37.74
Std Dev	27.89

Turn on : 55.0855

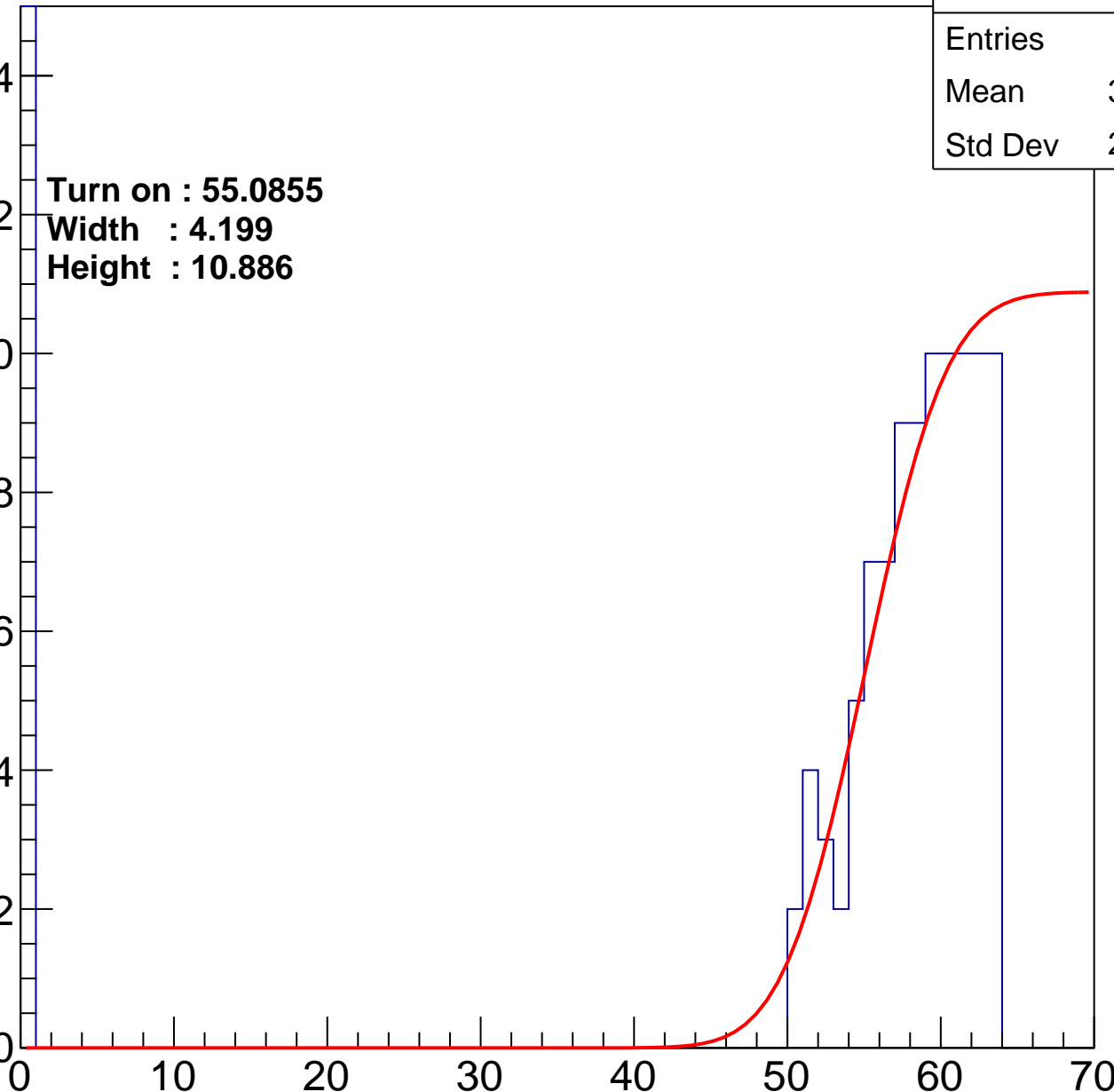
Width : 4.199

Height : 10.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch54

calib_packv5_033123_0516.root, FC#4, port A1

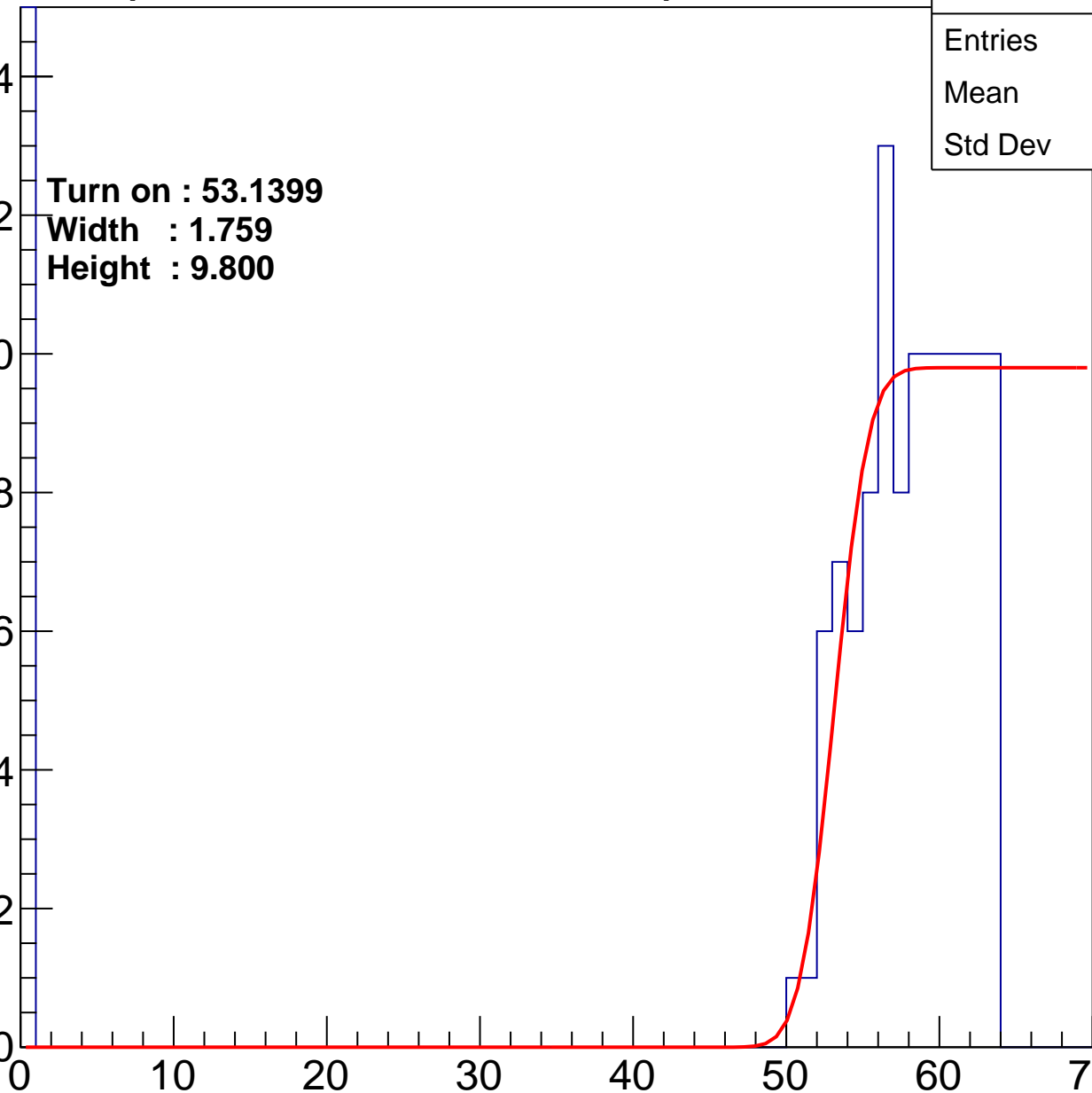
Entry

14
12
10
8
6
4
2
0

Turn on : 53.1399
Width : 1.759
Height : 9.800

Entries	166
Mean	38.33
Std Dev	27.49

ampl



B1L104S, U25-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	36.28
Std Dev	28.44

Turn on : 55.3391

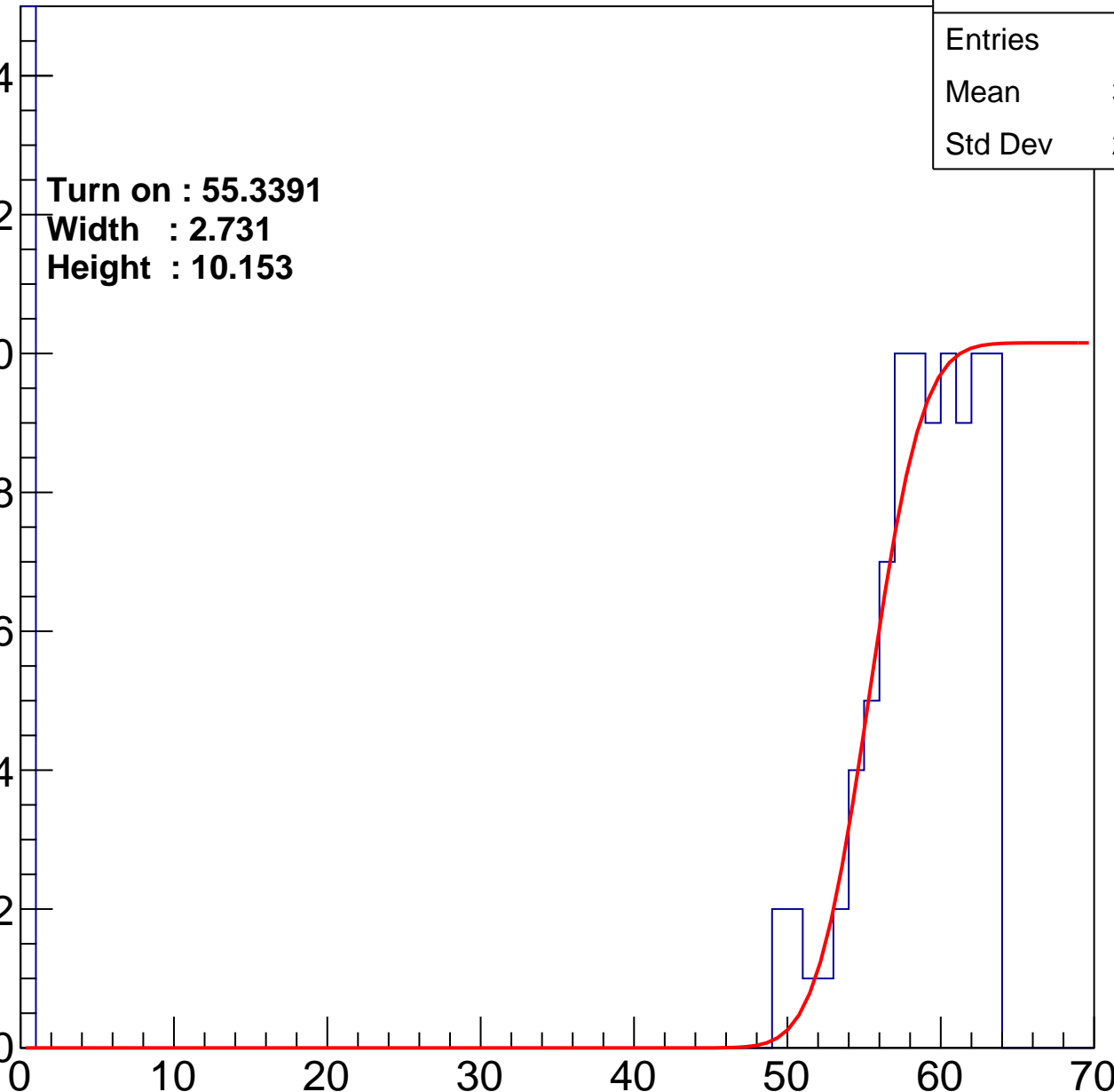
Width : 2.731

Height : 10.153

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch56

calib_packv5_033123_0516.root, FC#4, port A1

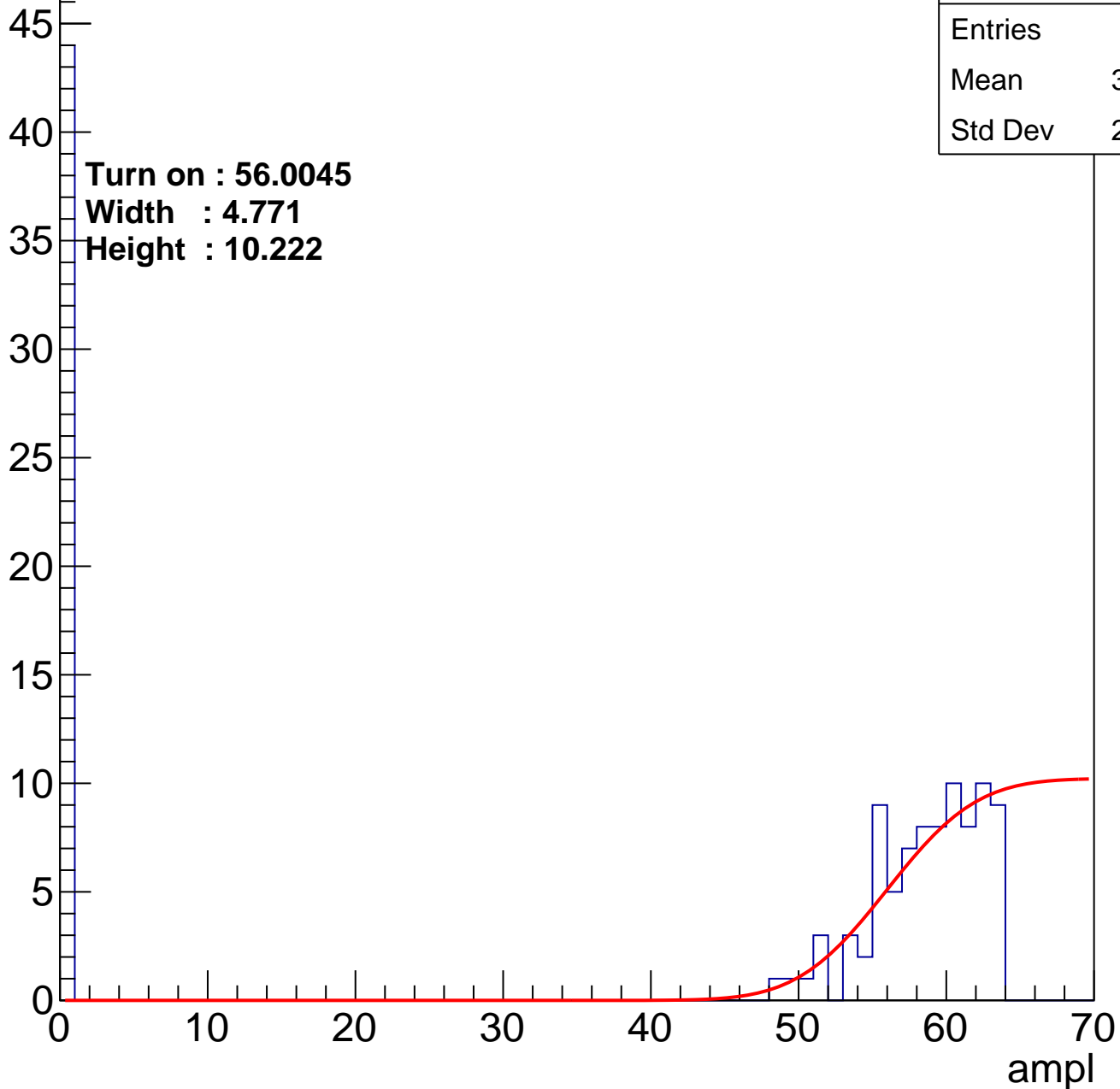
Entries	129
Mean	38.39
Std Dev	27.77

Turn on : 56.0045

Width : 4.771

Height : 10.222

Entry



B1L104S, U25-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	141
Mean	37.03
Std Dev	28.41

Turn on : 55.4220

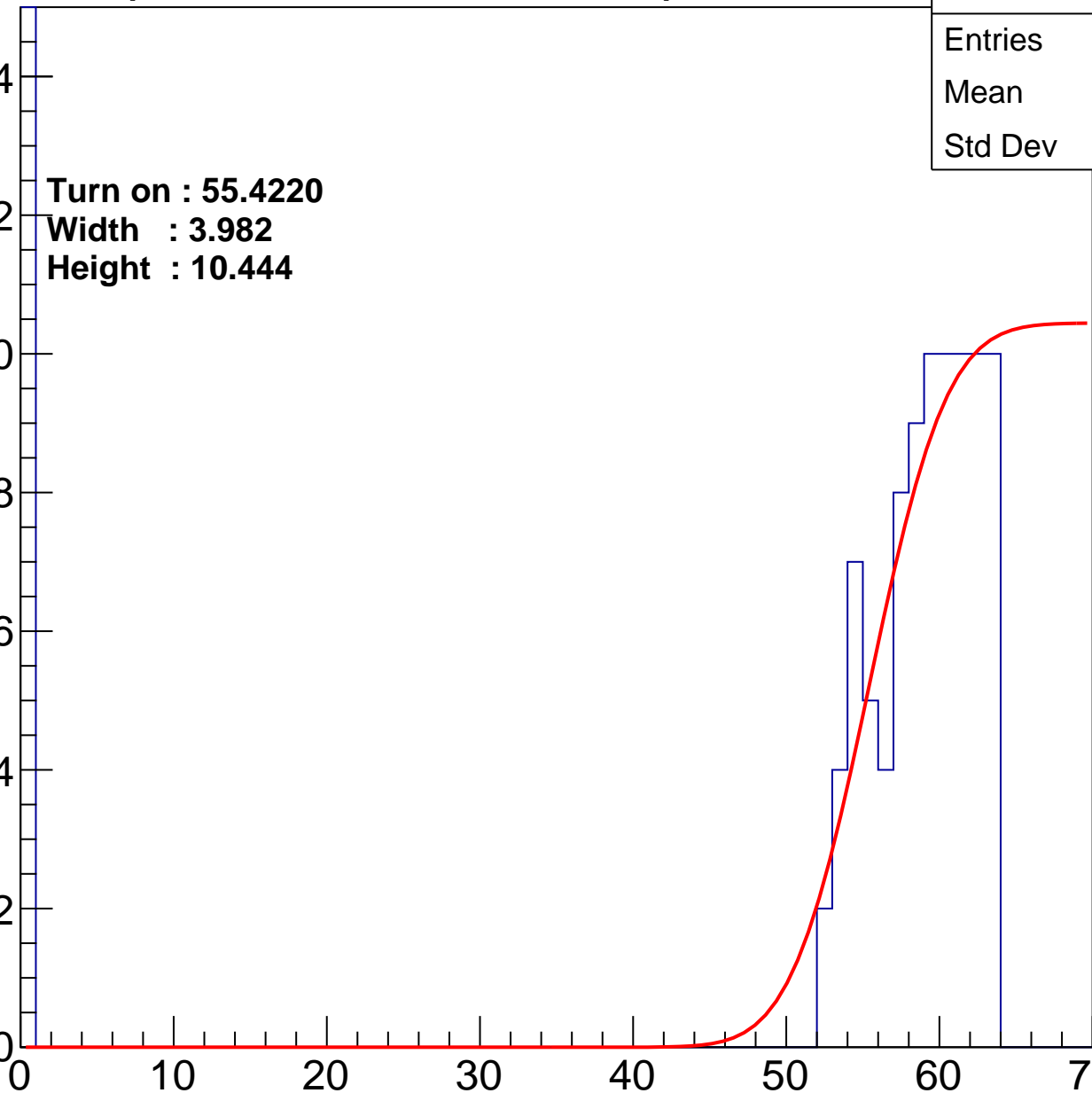
Width : 3.982

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch58

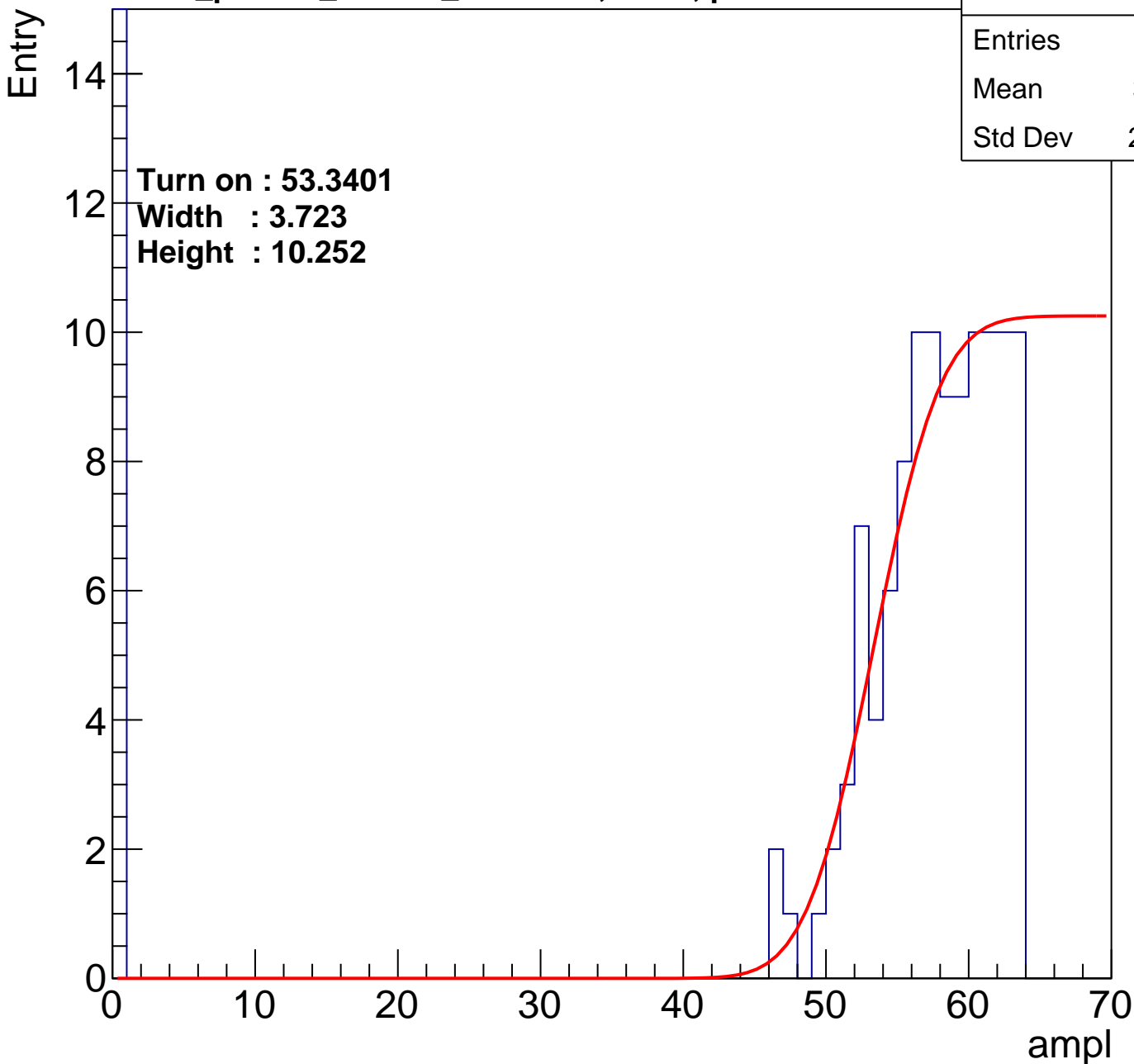
calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	33.81
Std Dev	28.39

Turn on : 53.3401

Width : 3.723

Height : 10.252



B1L104S, U25-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	33.68
Std Dev	29.01

Turn on : 55.3073

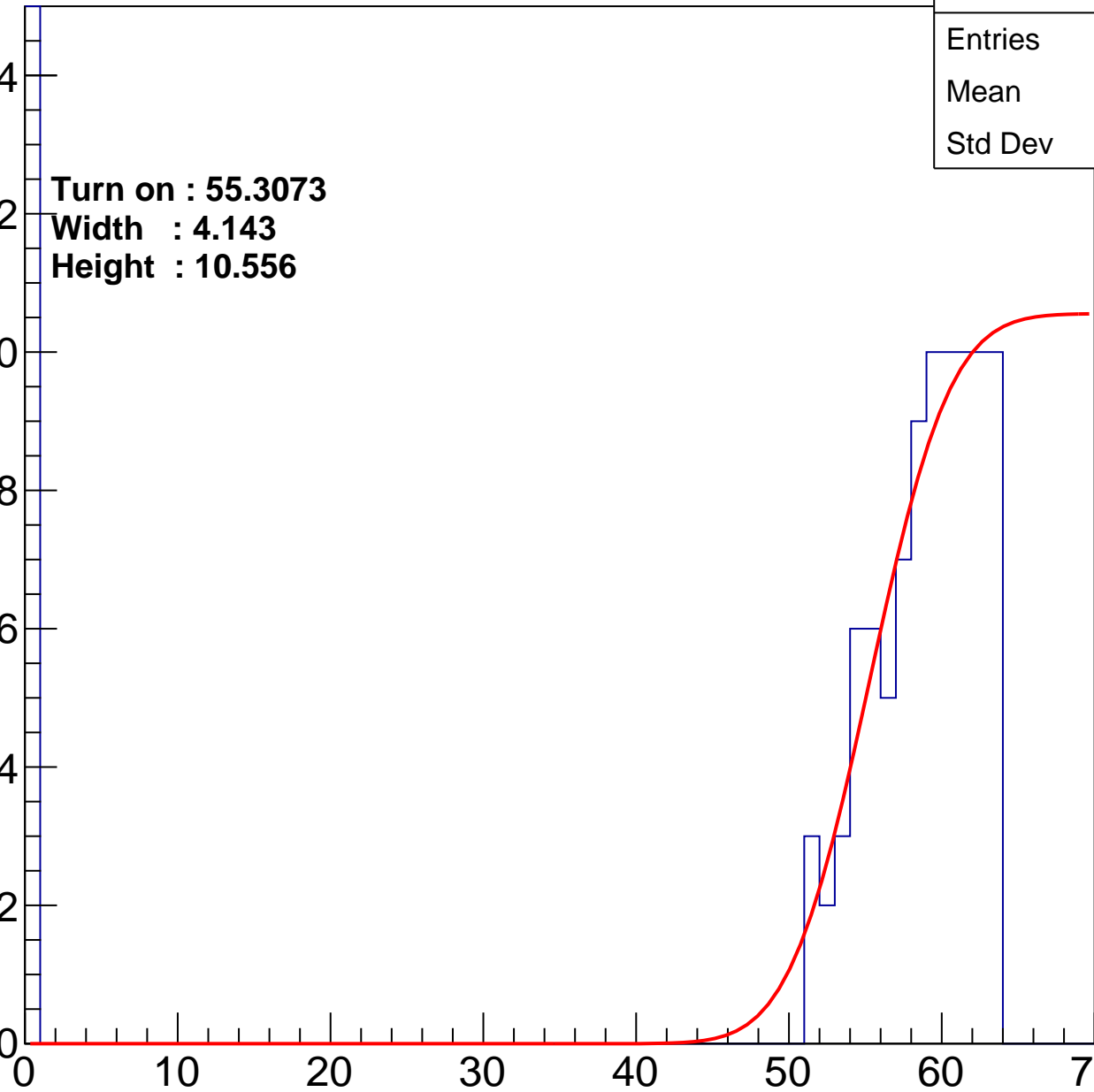
Width : 4.143

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	39.3
Std Dev	27.26

Turn on : 54.3579

Width : 3.714

Height : 10.560

Entry

14

12

10

8

6

4

2

0

0

10

20

30

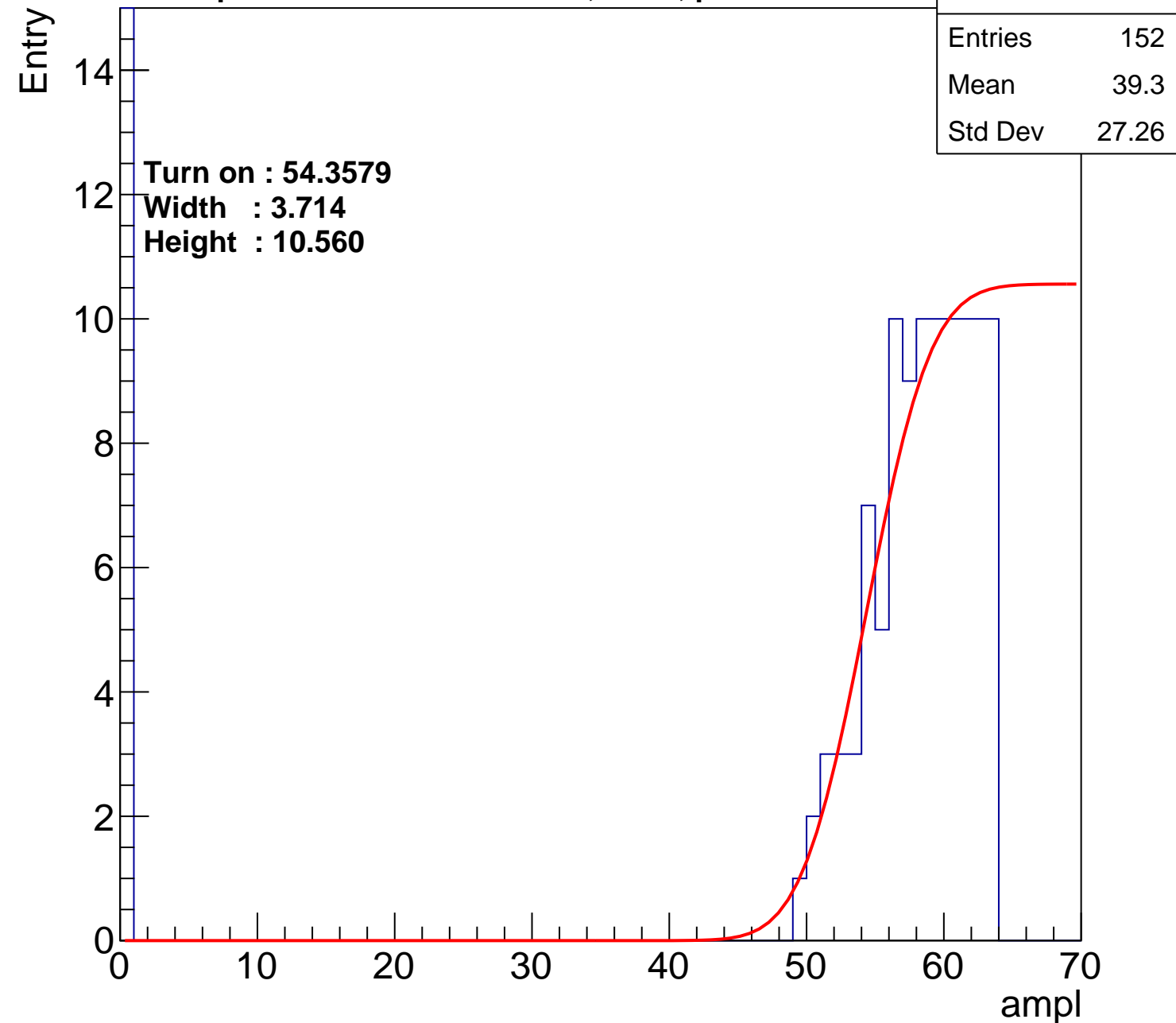
40

50

60

70

ampl



B1L104S, U25-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	33.91
Std Dev	28.62

Turn on : 53.2648

Width : 2.686

Height : 9.374

Entry

14

12

10

8

6

4

2

0

0

10

20

30

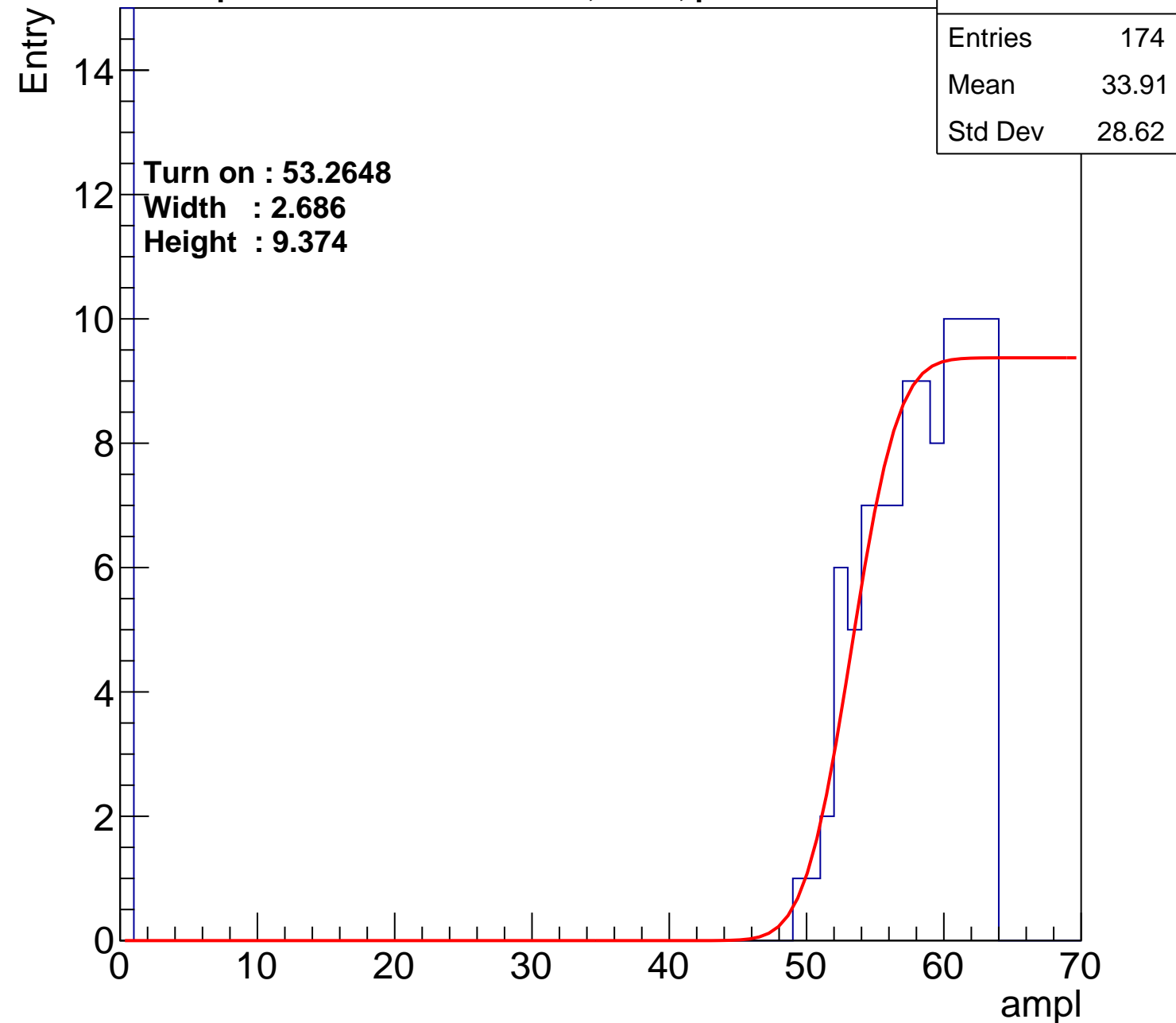
40

50

60

70

ampl



B1L104S, U25-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	32.08
Std Dev	29.19

Turn on : 55.2542

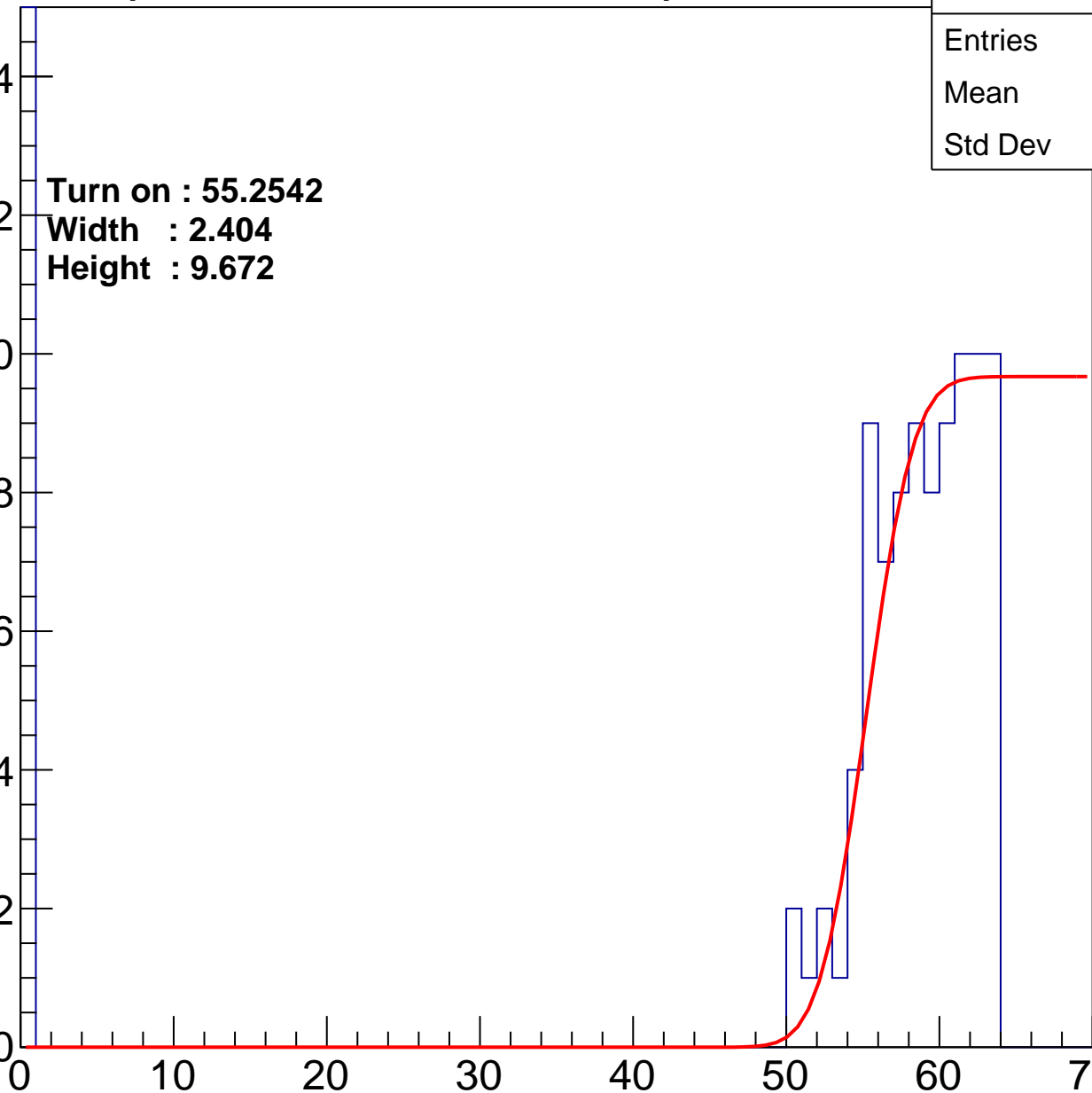
Width : 2.404

Height : 9.672

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	35.7
Std Dev	28.39

Turn on : 54.6302

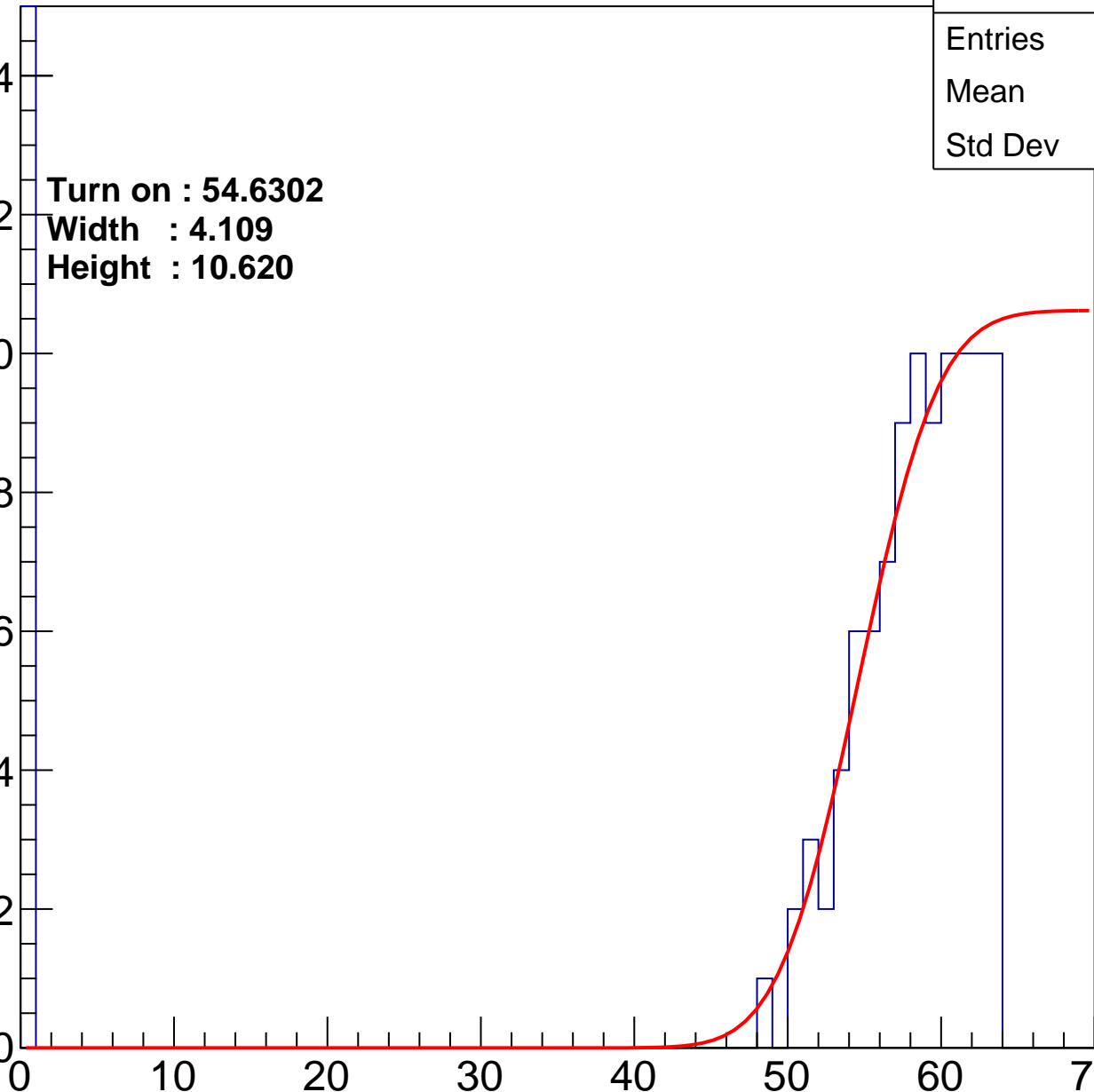
Width : 4.109

Height : 10.620

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	28.17
Std Dev	29.18

Turn on : 54.7561

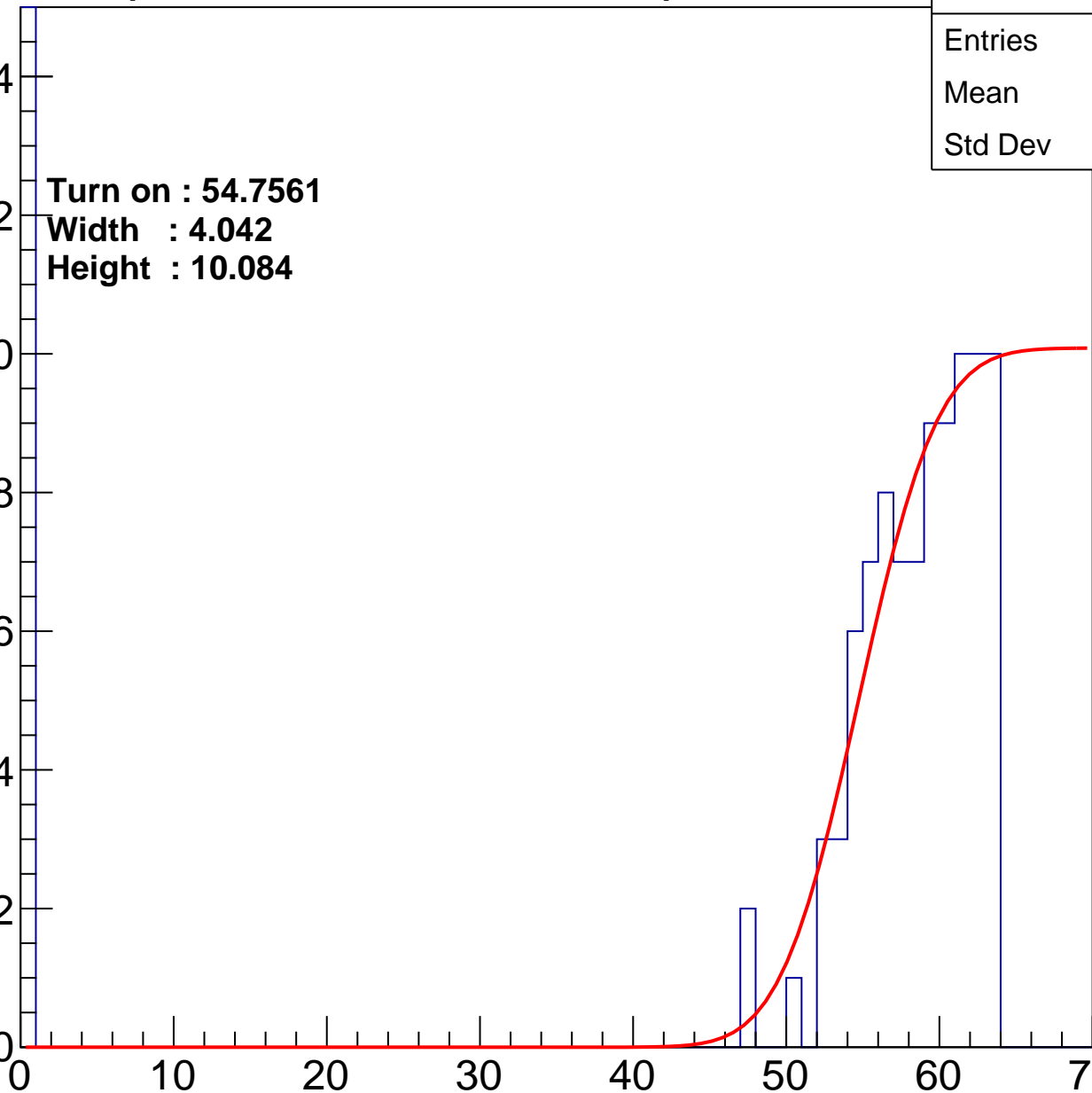
Width : 4.042

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	33.91
Std Dev	28.94

Turn on : 55.8041

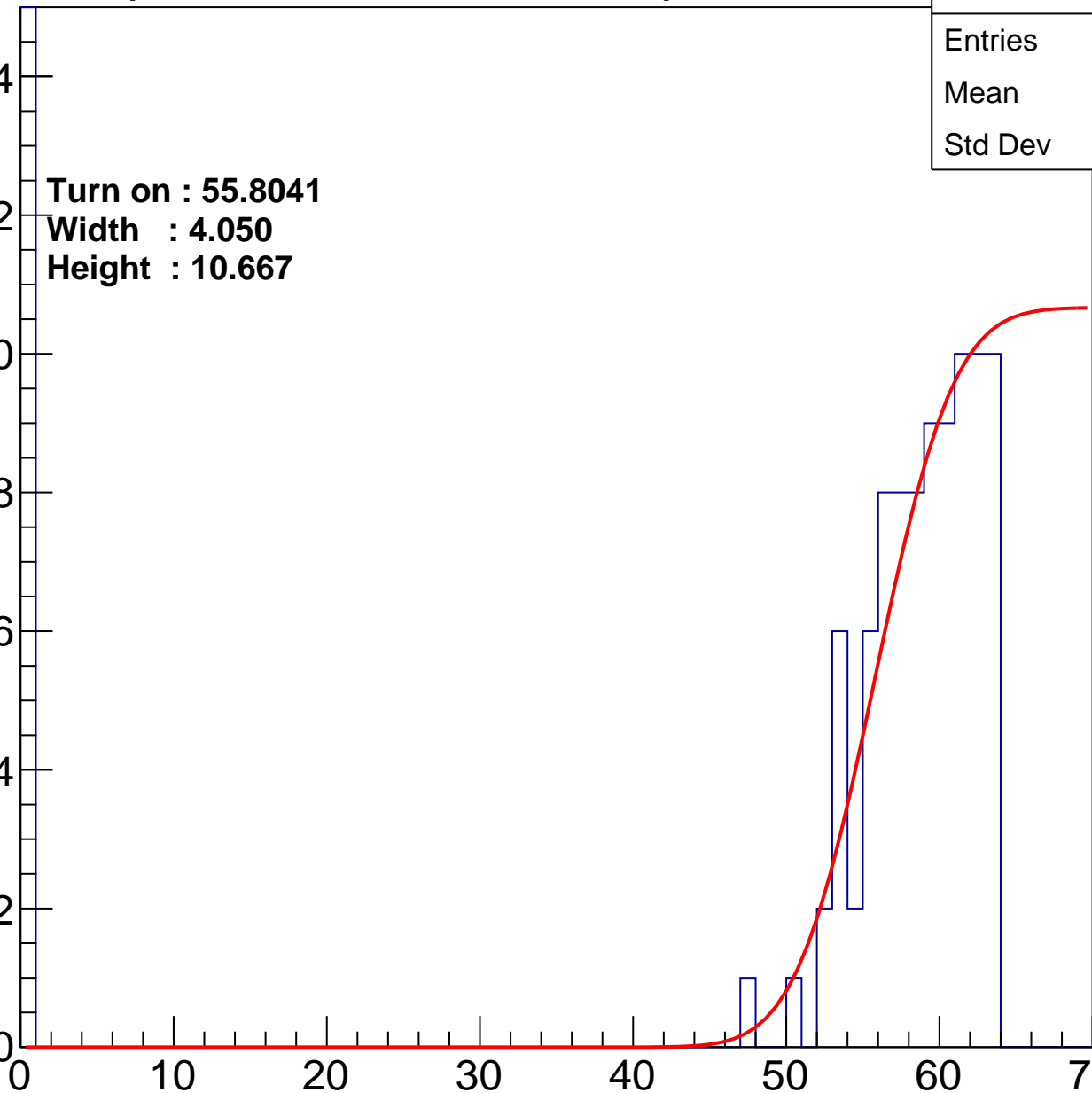
Width : 4.050

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch66

calib_packv5_033123_0516.root, FC#4, port A1

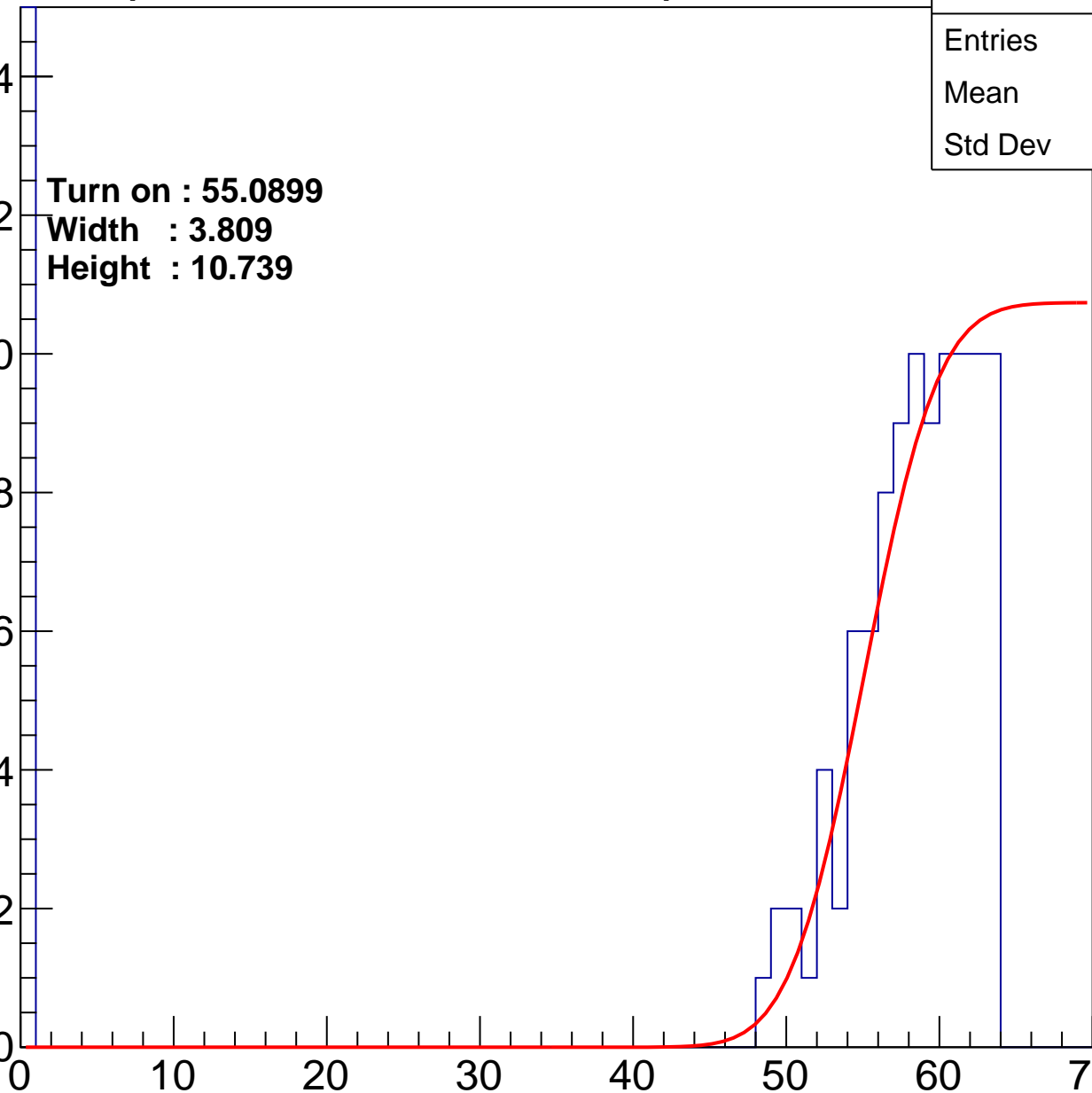
Entry

14
12
10
8
6
4
2
0

Turn on : 55.0899
Width : 3.809
Height : 10.739

Entries	156
Mean	37.16
Std Dev	27.97

ampl



B1L104S, U25-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.24
Std Dev	28.89

Turn on : 53.8722

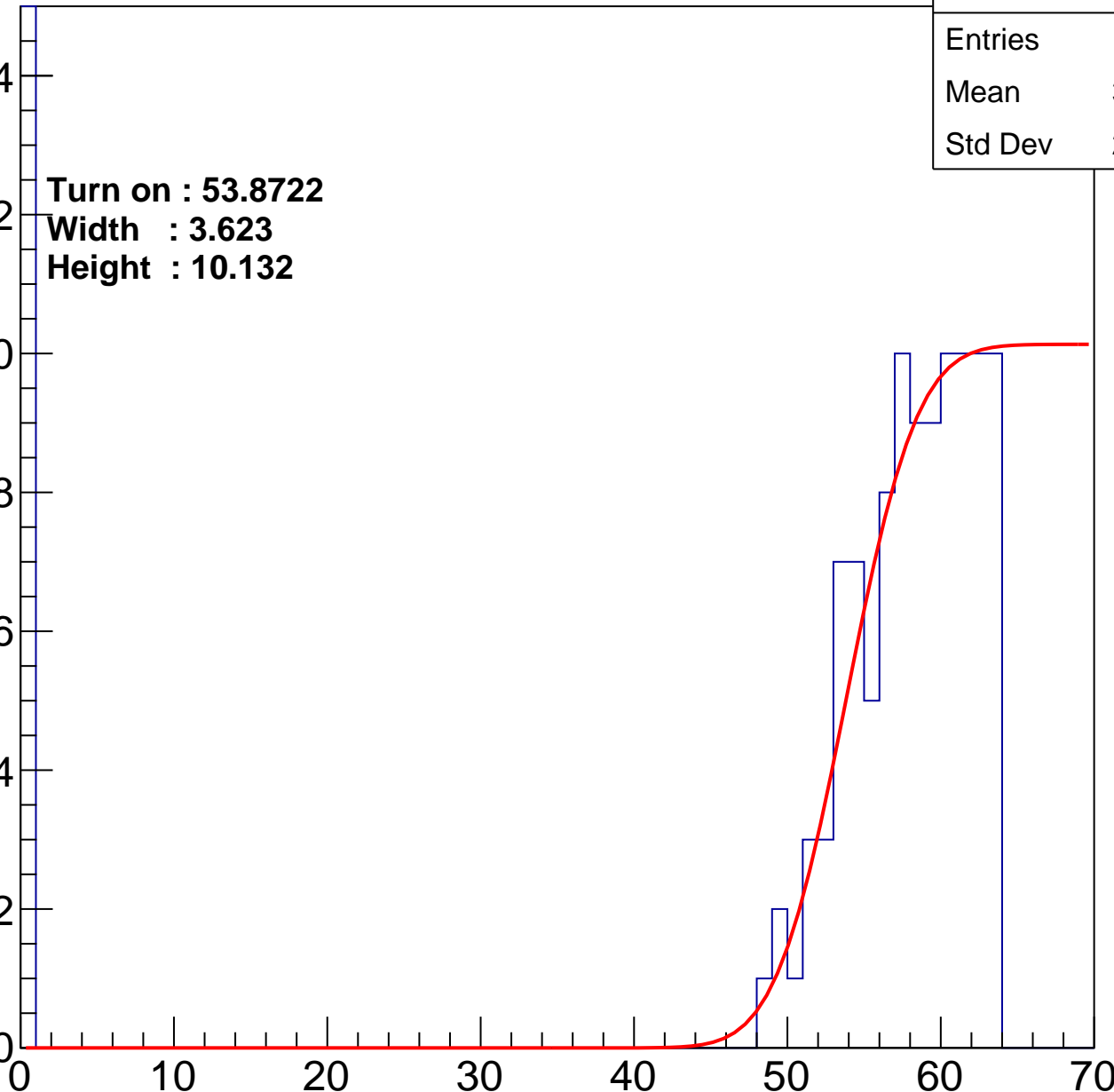
Width : 3.623

Height : 10.132

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	30.71
Std Dev	29.47

Turn on : 56.1308

Width : 3.118

Height : 10.222

Entry

70

50

40

30

20

10

0

0

10

20

30

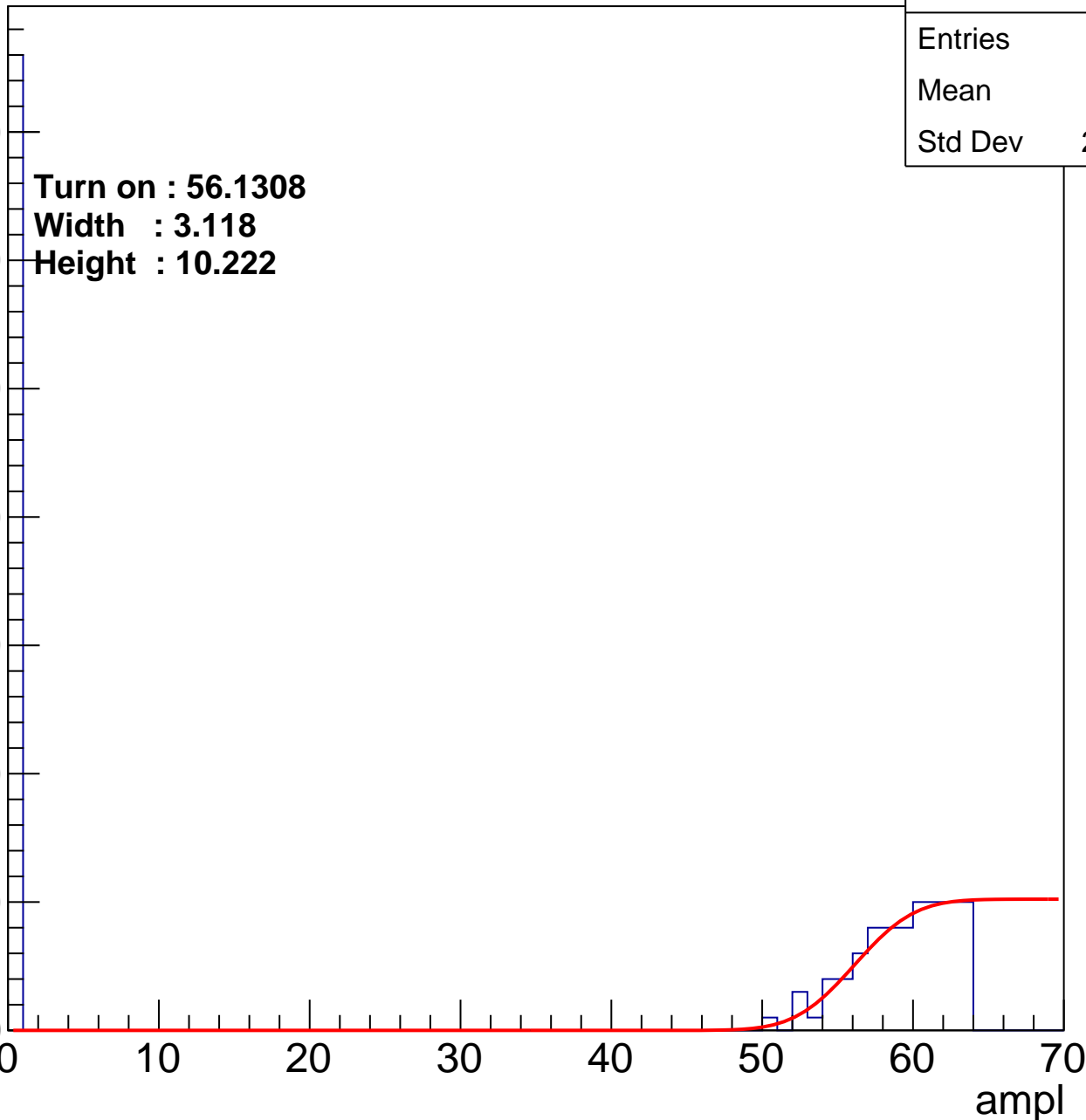
40

50

60

70

ampl



B1L104S, U25-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	33.79
Std Dev	29.23

Turn on : 56.0363

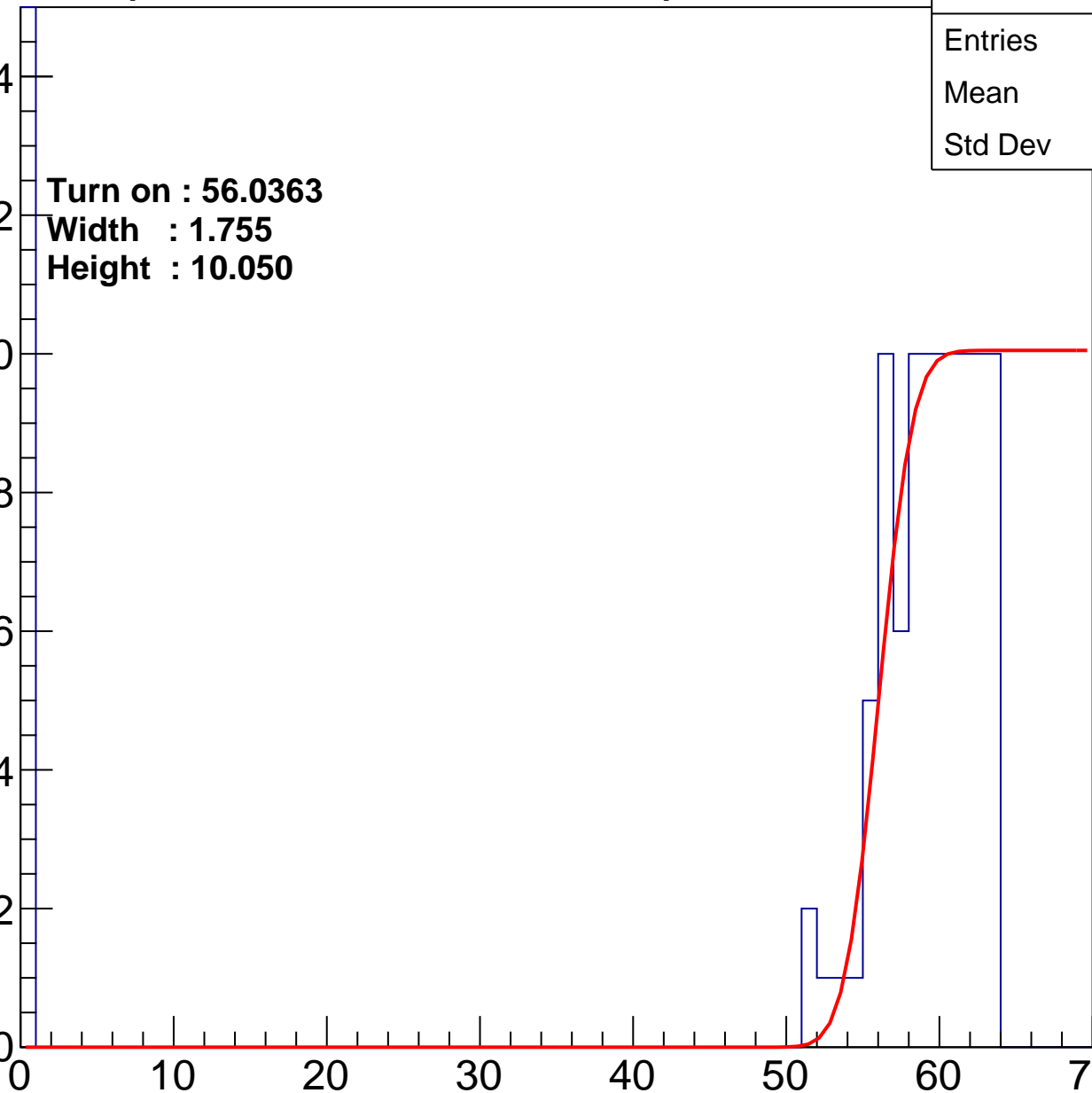
Width : 1.755

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	33.54
Std Dev	29.01

Turn on : 55.6421

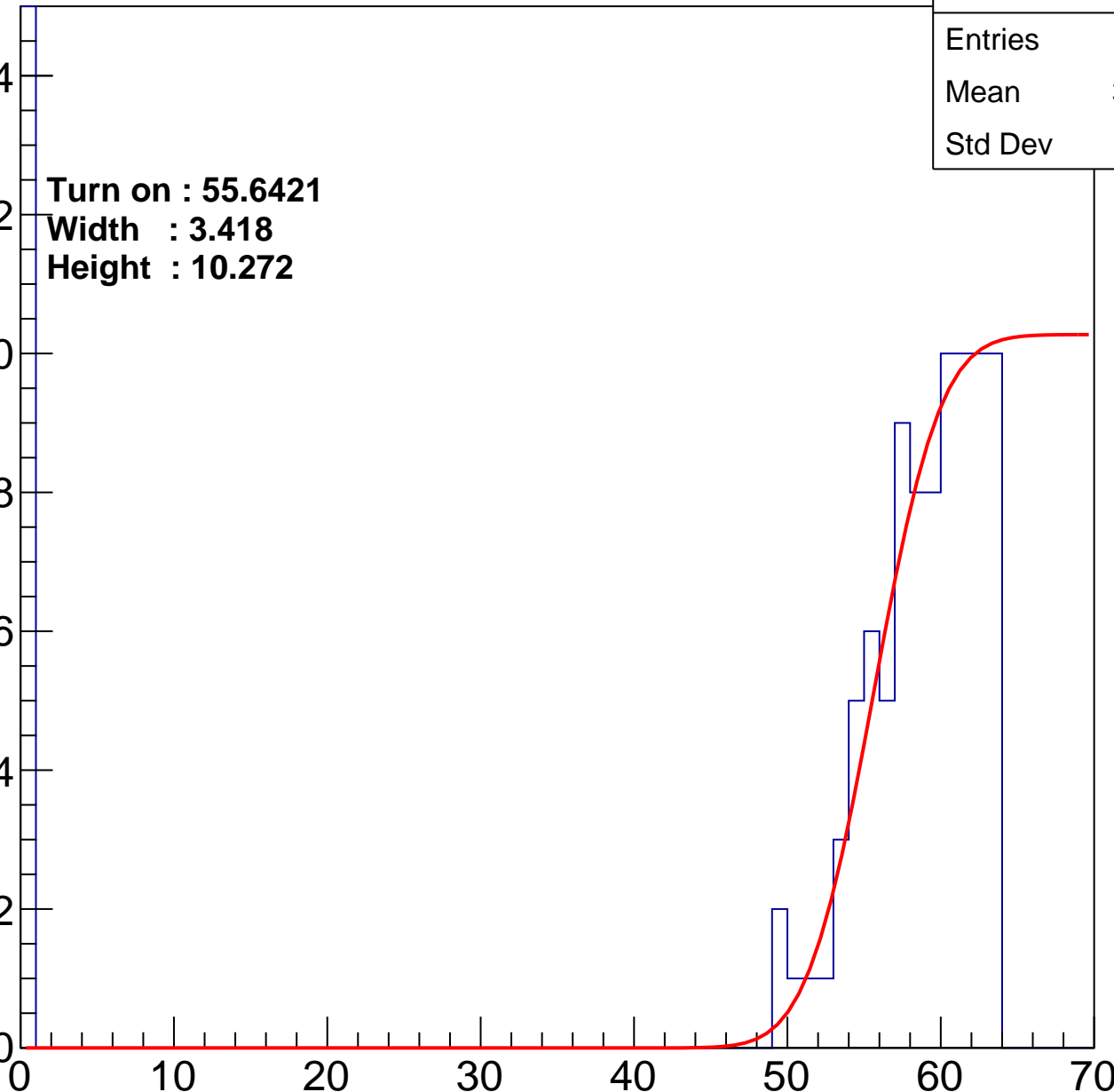
Width : 3.418

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	31.19
Std Dev	29.31

Turn on : 56.0778

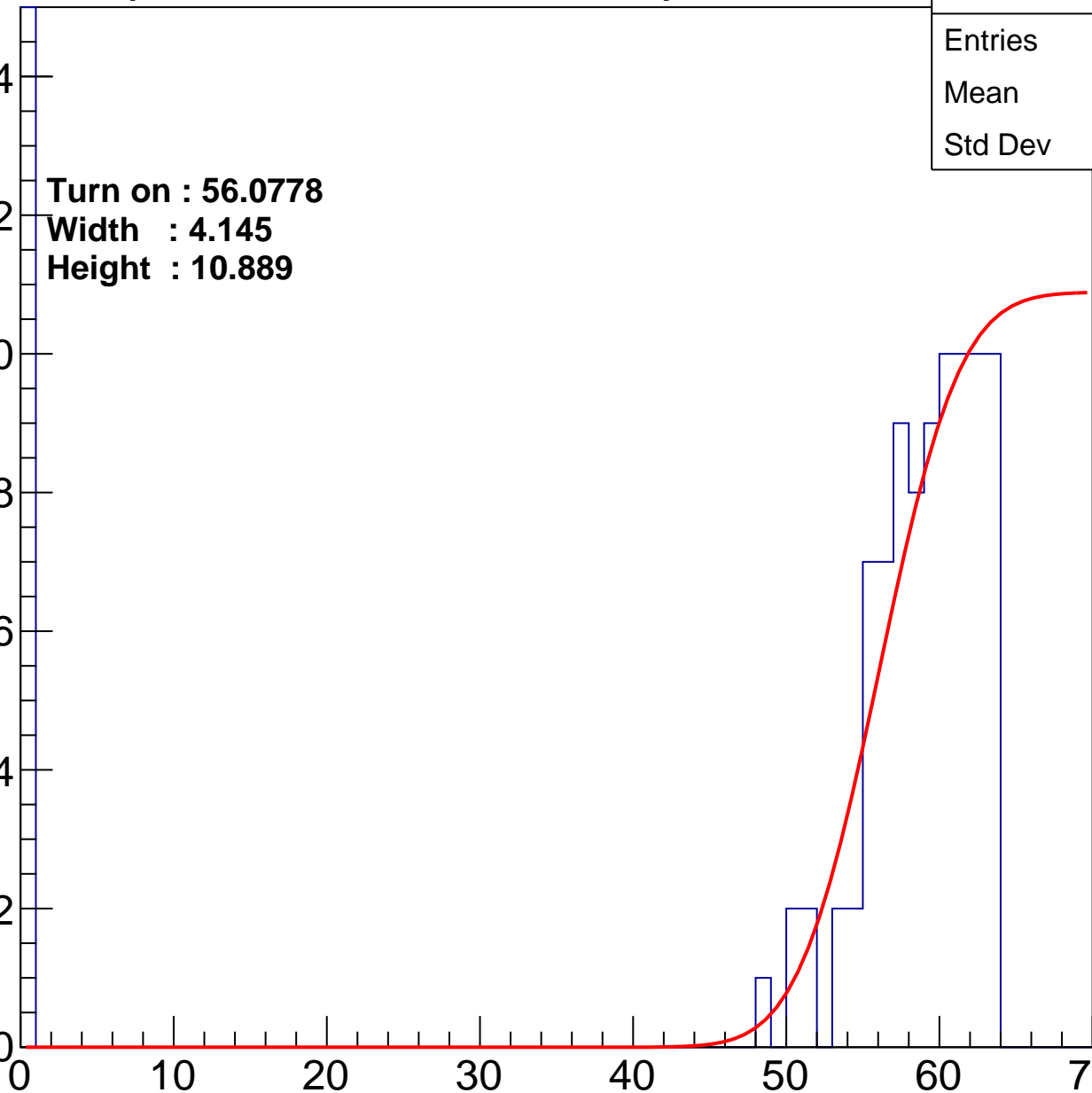
Width : 4.145

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch72

calib_packv5_033123_0516.root, FC#4, port A1

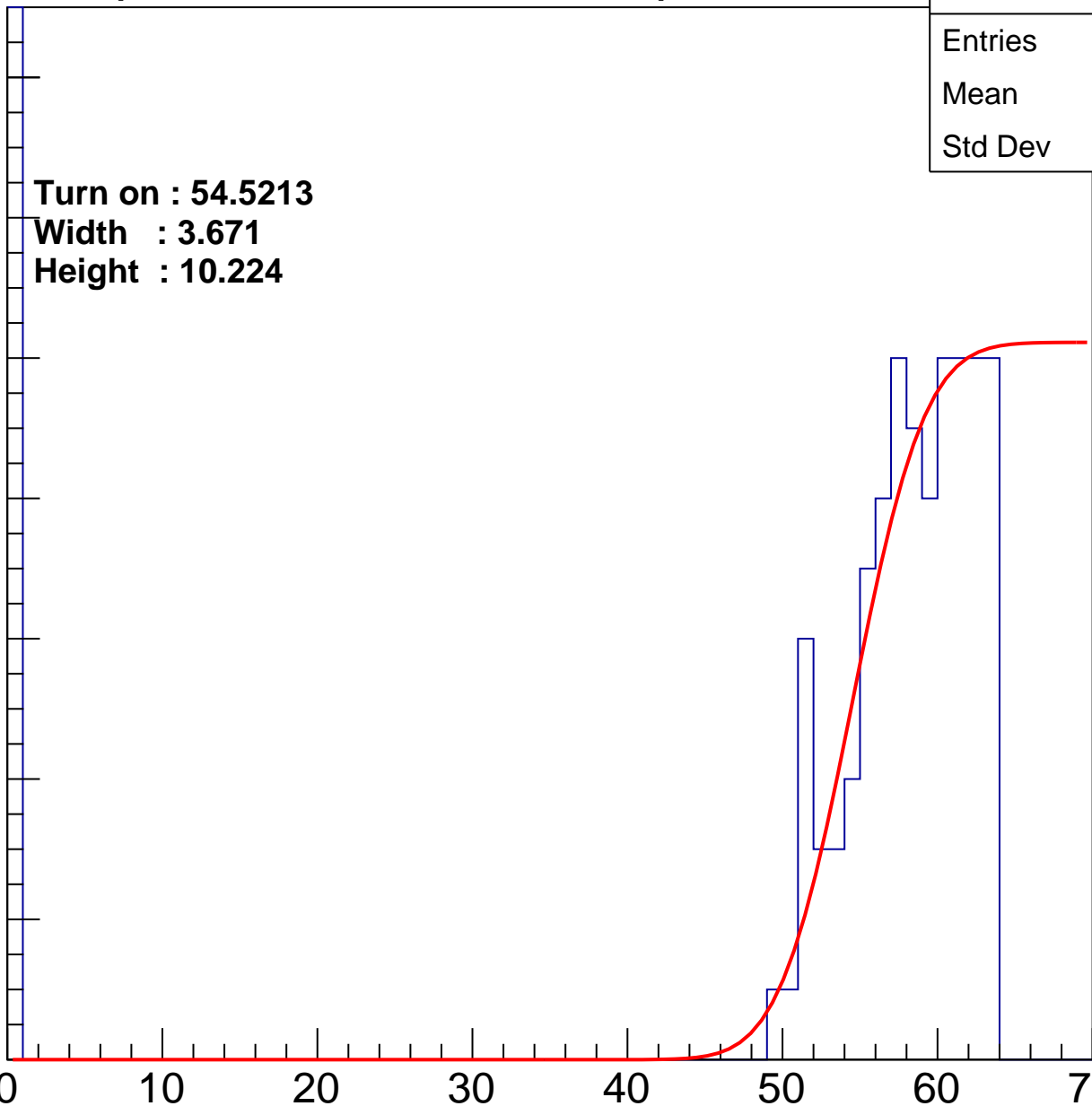
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5213
Width : 3.671
Height : 10.224

Entries	153
Mean	37.86
Std Dev	27.72

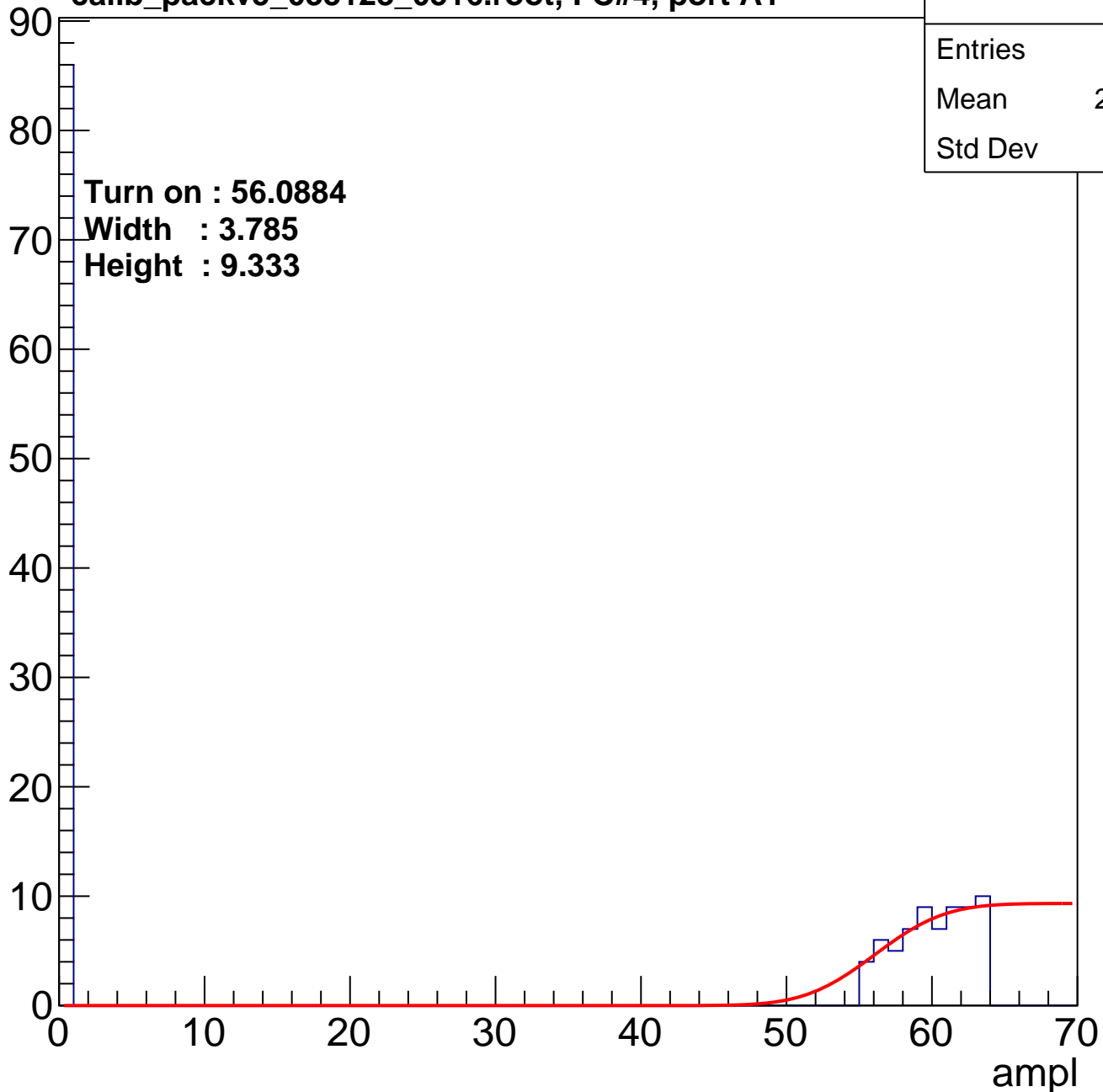
ampl



B1L104S, U25-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entry



B1L104S, U25-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	32.21
Std Dev	29.05

Turn on : 55.3643

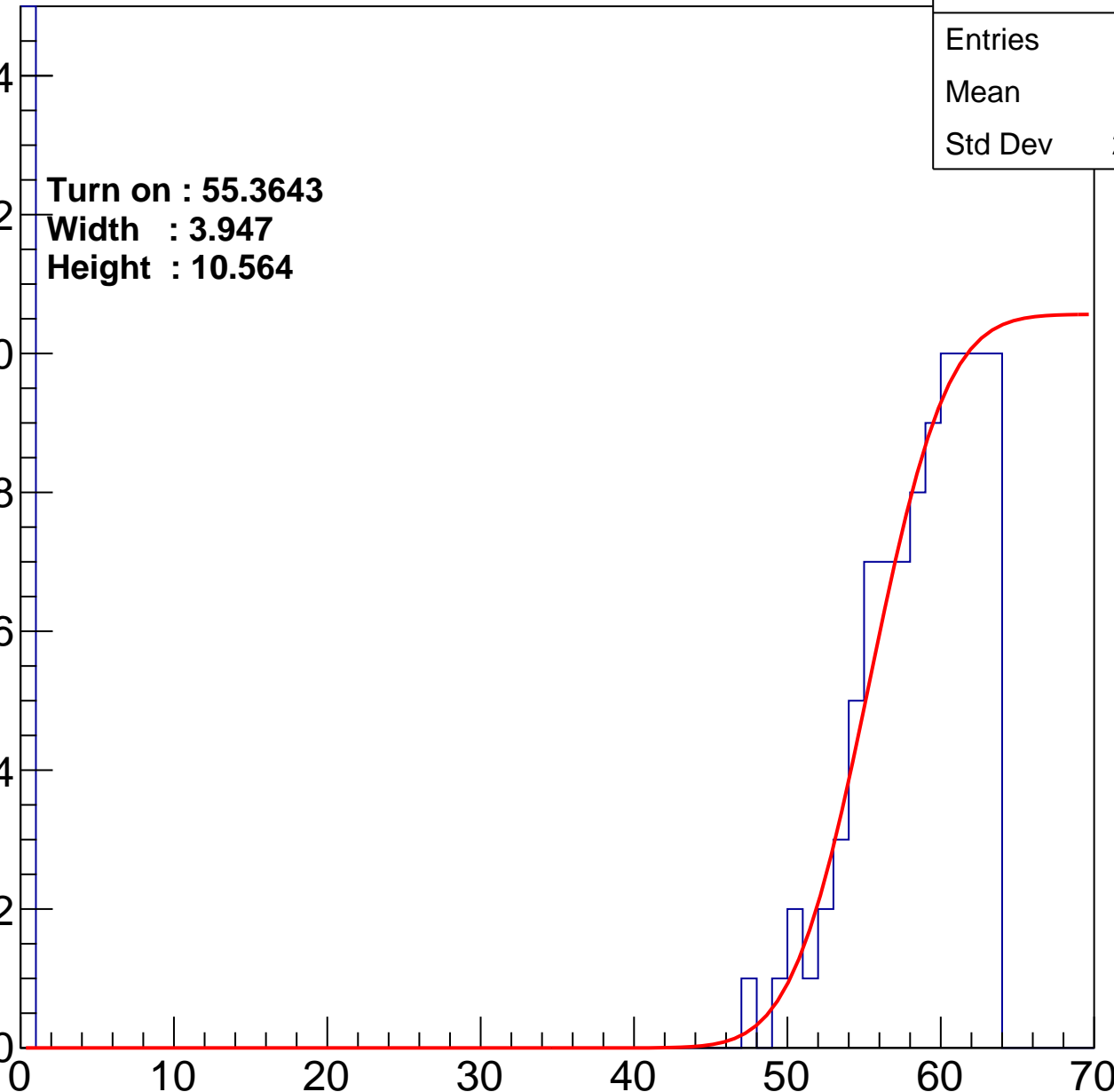
Width : 3.947

Height : 10.564

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	131
Mean	43.02
Std Dev	25.65

Turn on : 53.9673

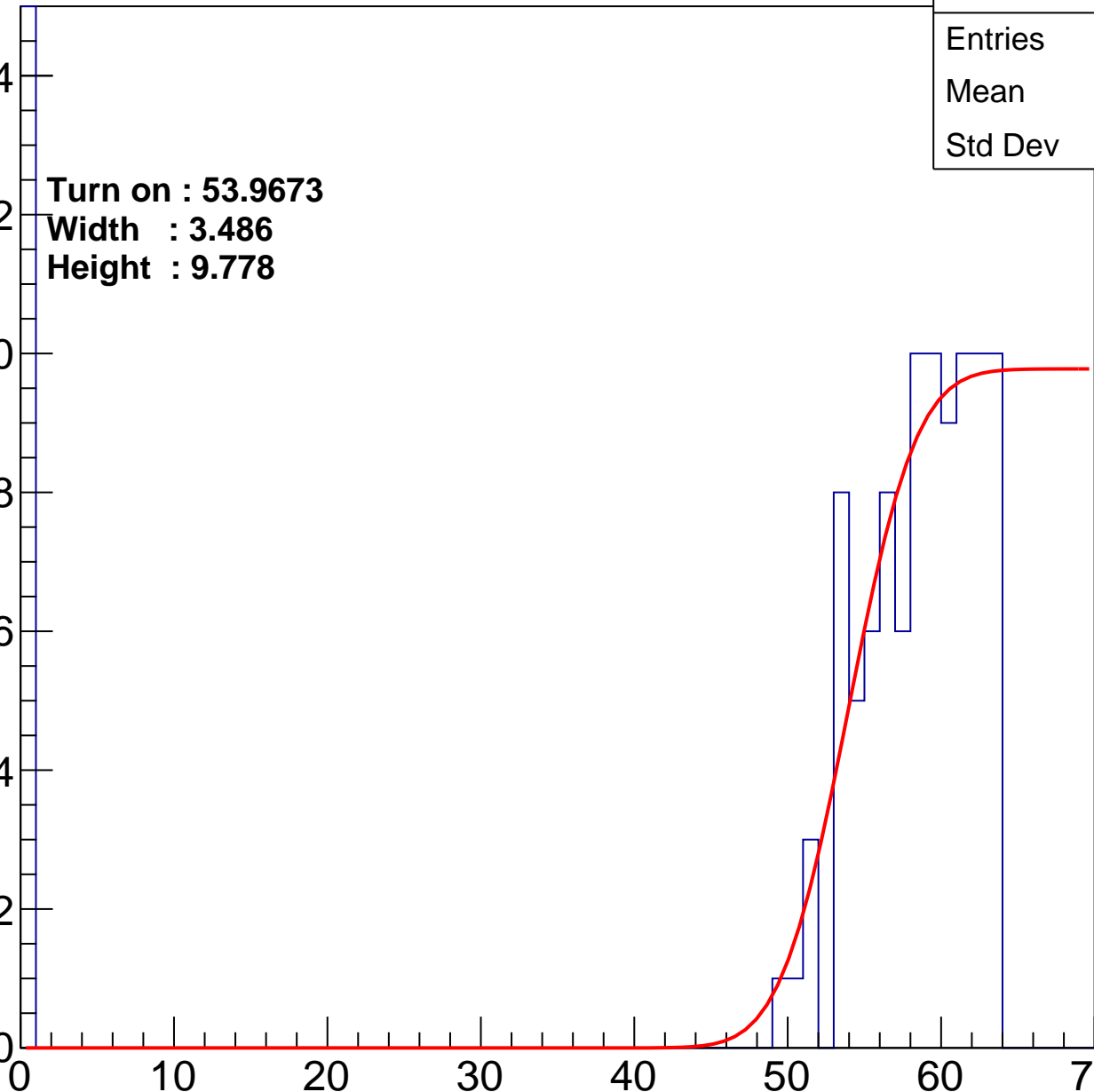
Width : 3.486

Height : 9.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch76

calib_packv5_033123_0516.root, FC#4, port A1

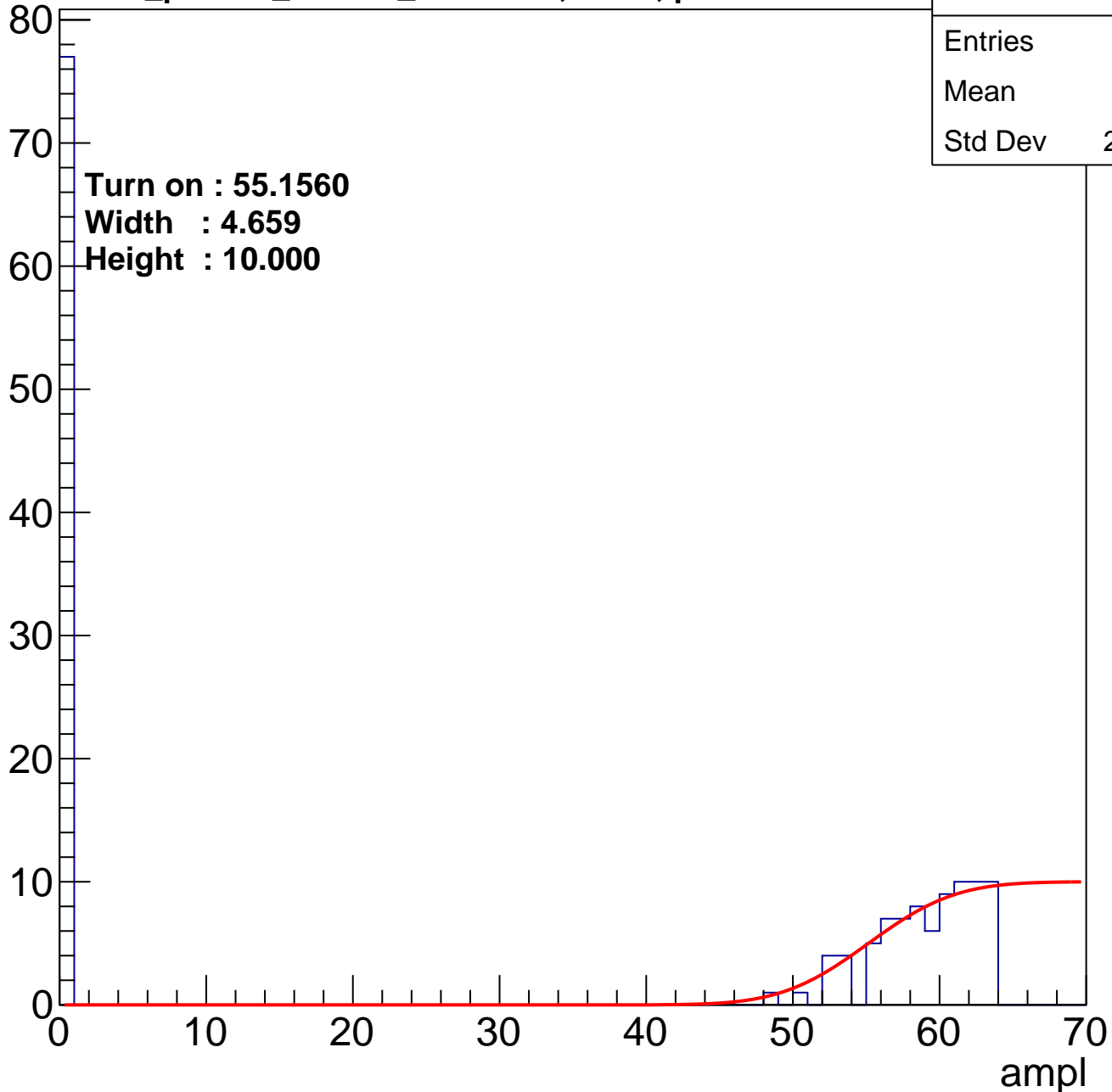
Entries	159
Mean	30.2
Std Dev	29.37

Turn on : 55.1560

Width : 4.659

Height : 10.000

Entry



B1L104S, U25-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	38.02
Std Dev	27.86

Turn on : 55.2223

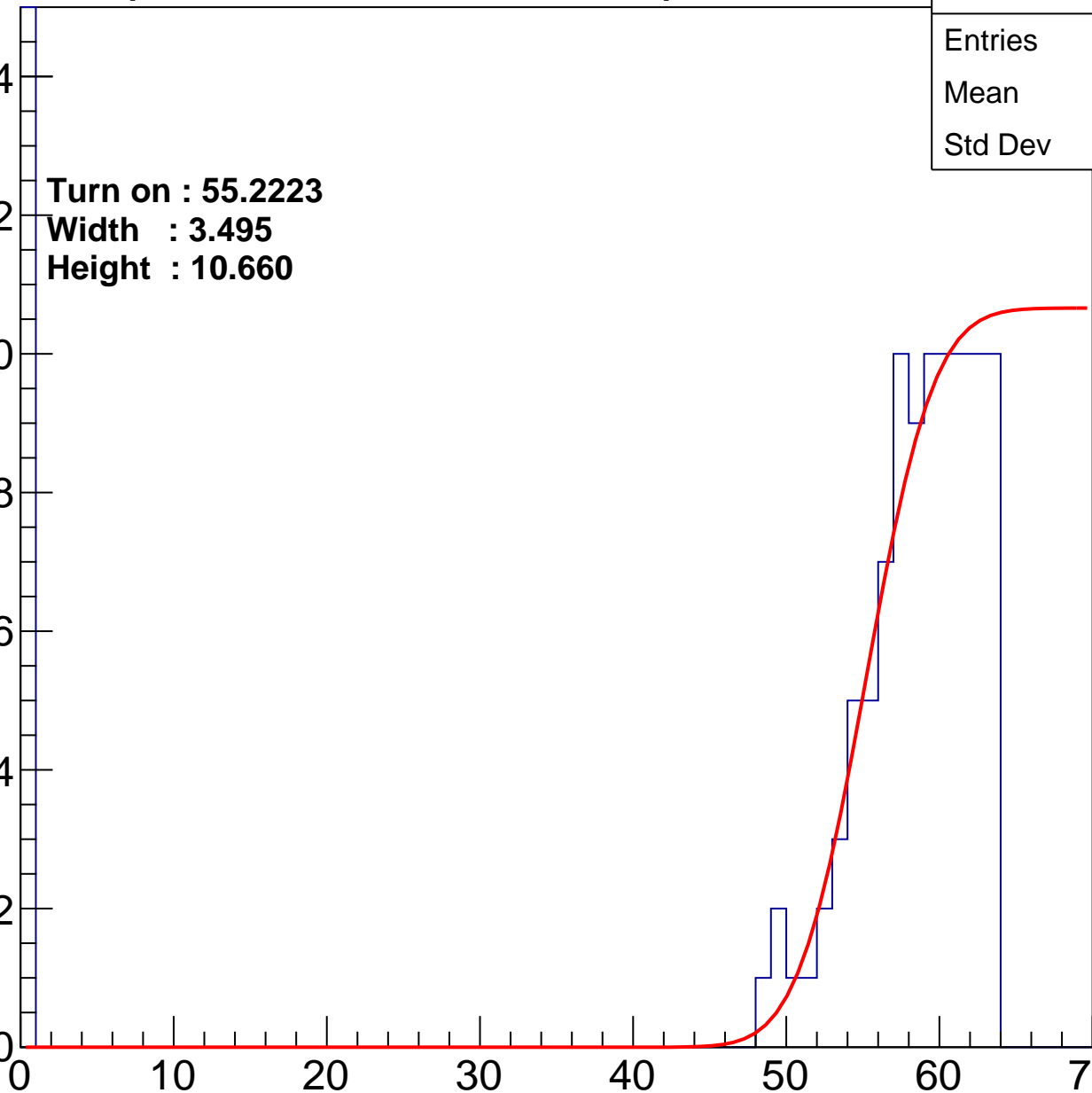
Width : 3.495

Height : 10.660

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	34.43
Std Dev	28.54

Turn on : 54.6924

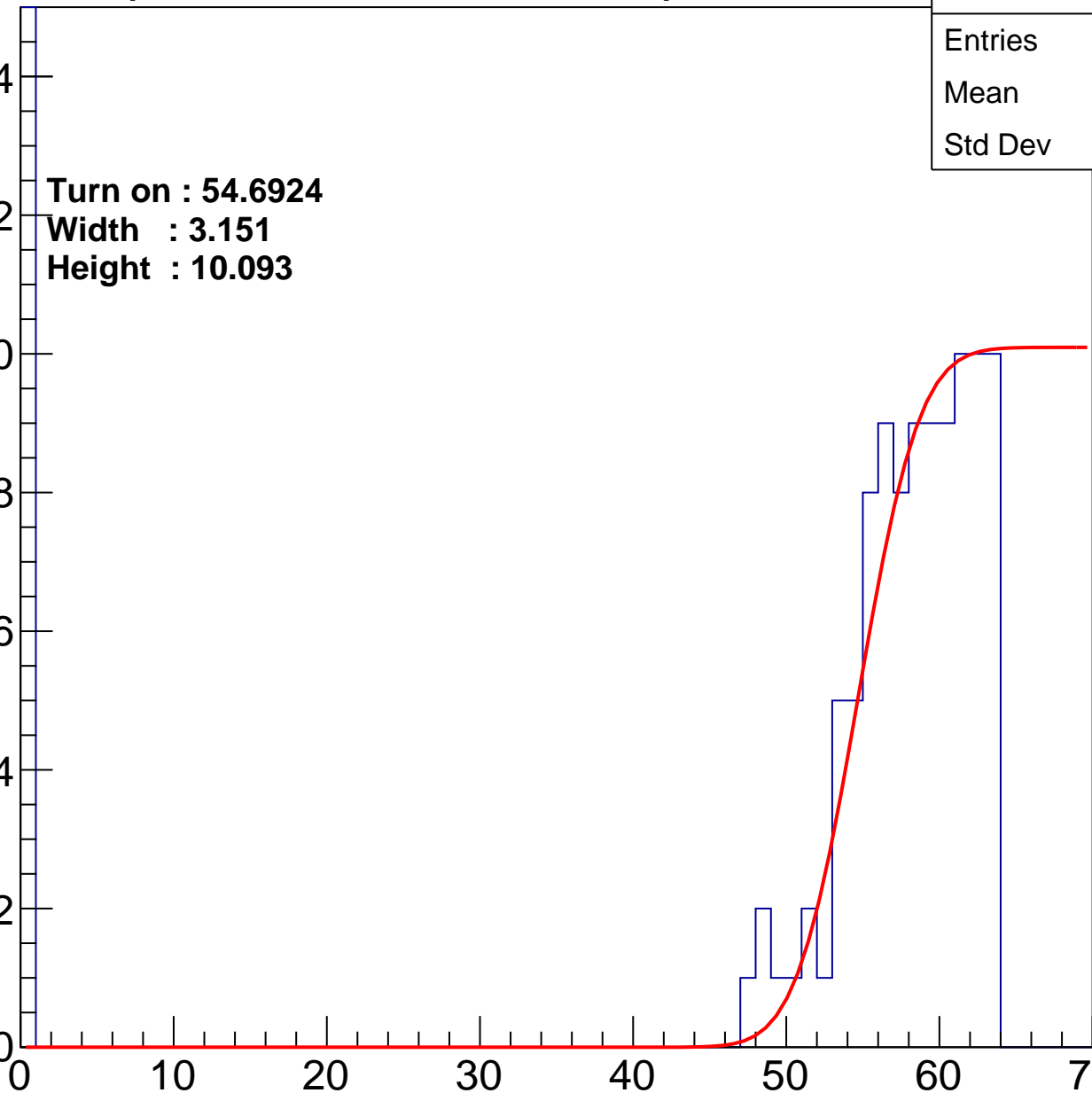
Width : 3.151

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch79

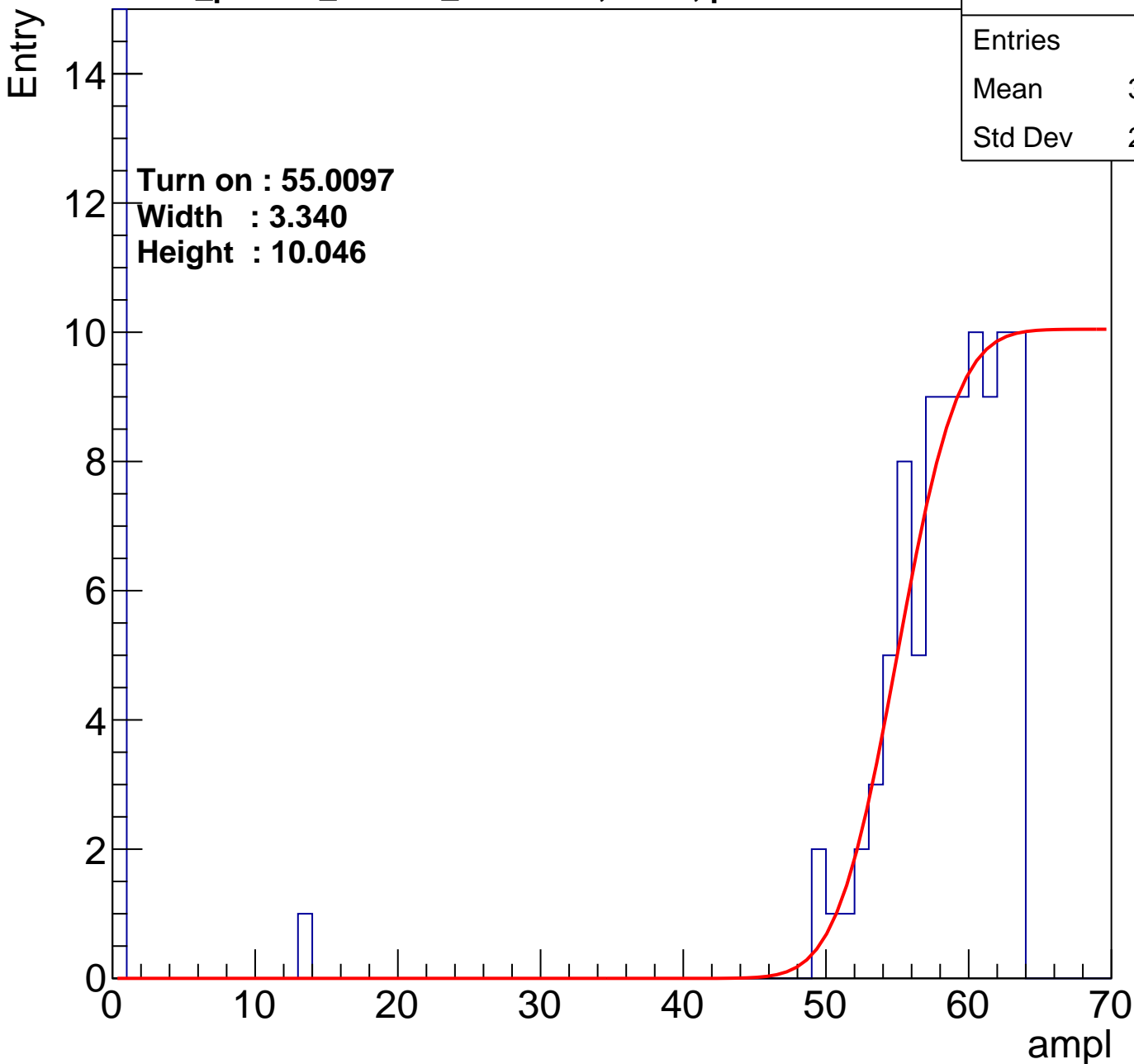
calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	31.39
Std Dev	29.09

Turn on : 55.0097

Width : 3.340

Height : 10.046



B1L104S, U25-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.87
Std Dev	28.66

Turn on : 53.2127

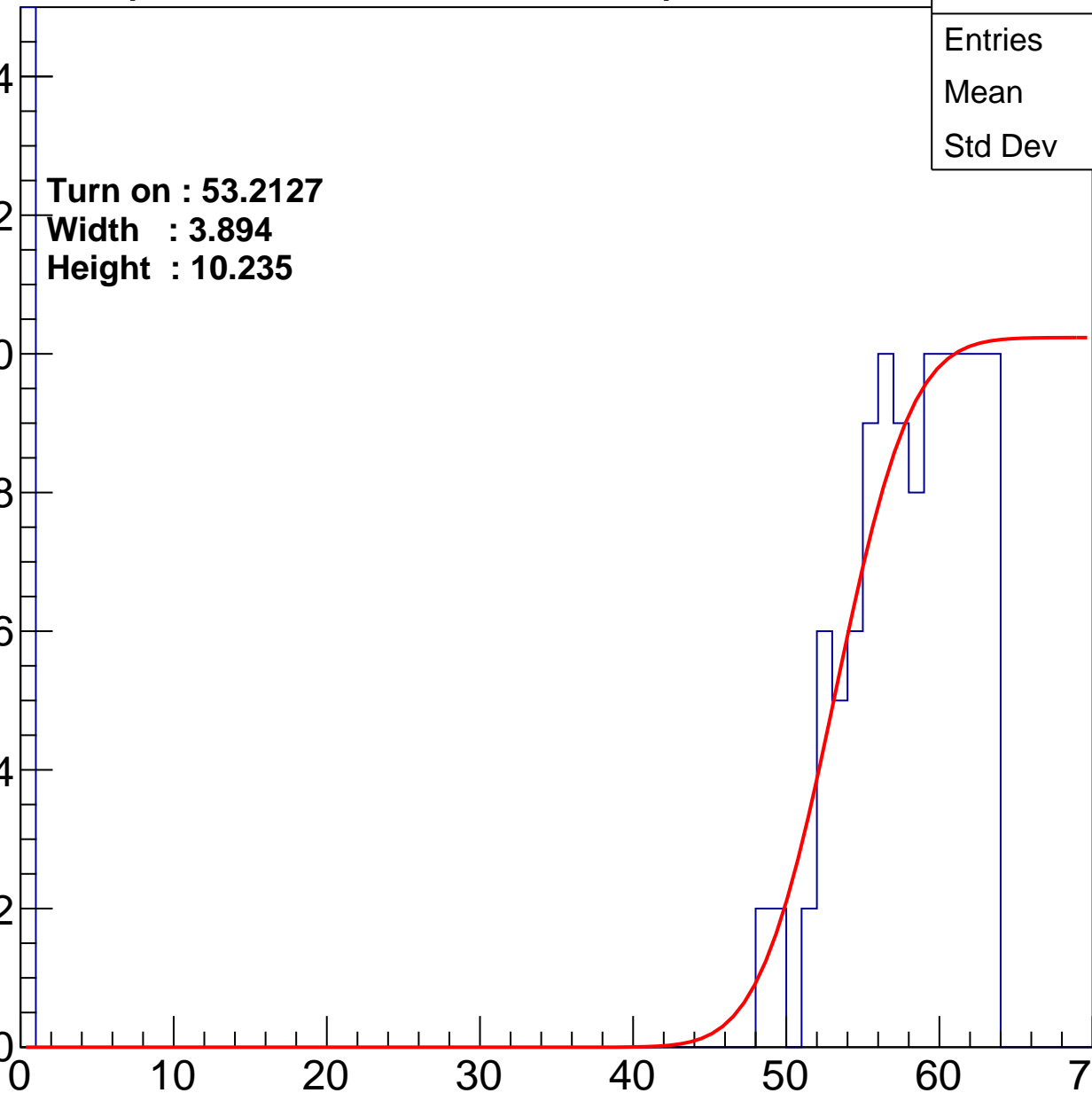
Width : 3.894

Height : 10.235

Entry

14
12
10
8
6
4
2
0

ampl

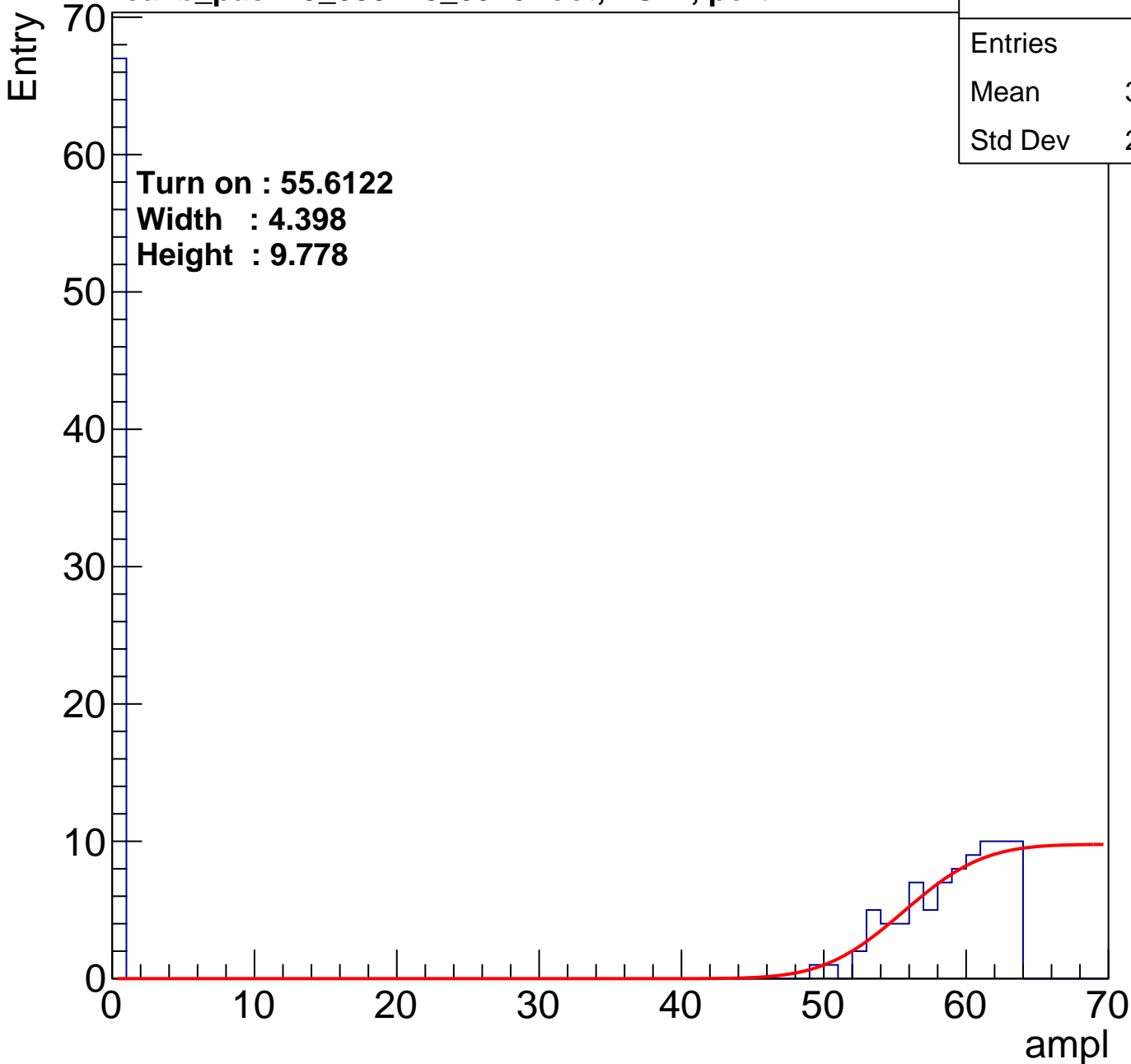


B1L104S, U25-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	32.39
Std Dev	29.22

Turn on : 55.6122
Width : 4.398
Height : 9.778



B1L104S, U25-ch82

calib_packv5_033123_0516.root, FC#4, port A1

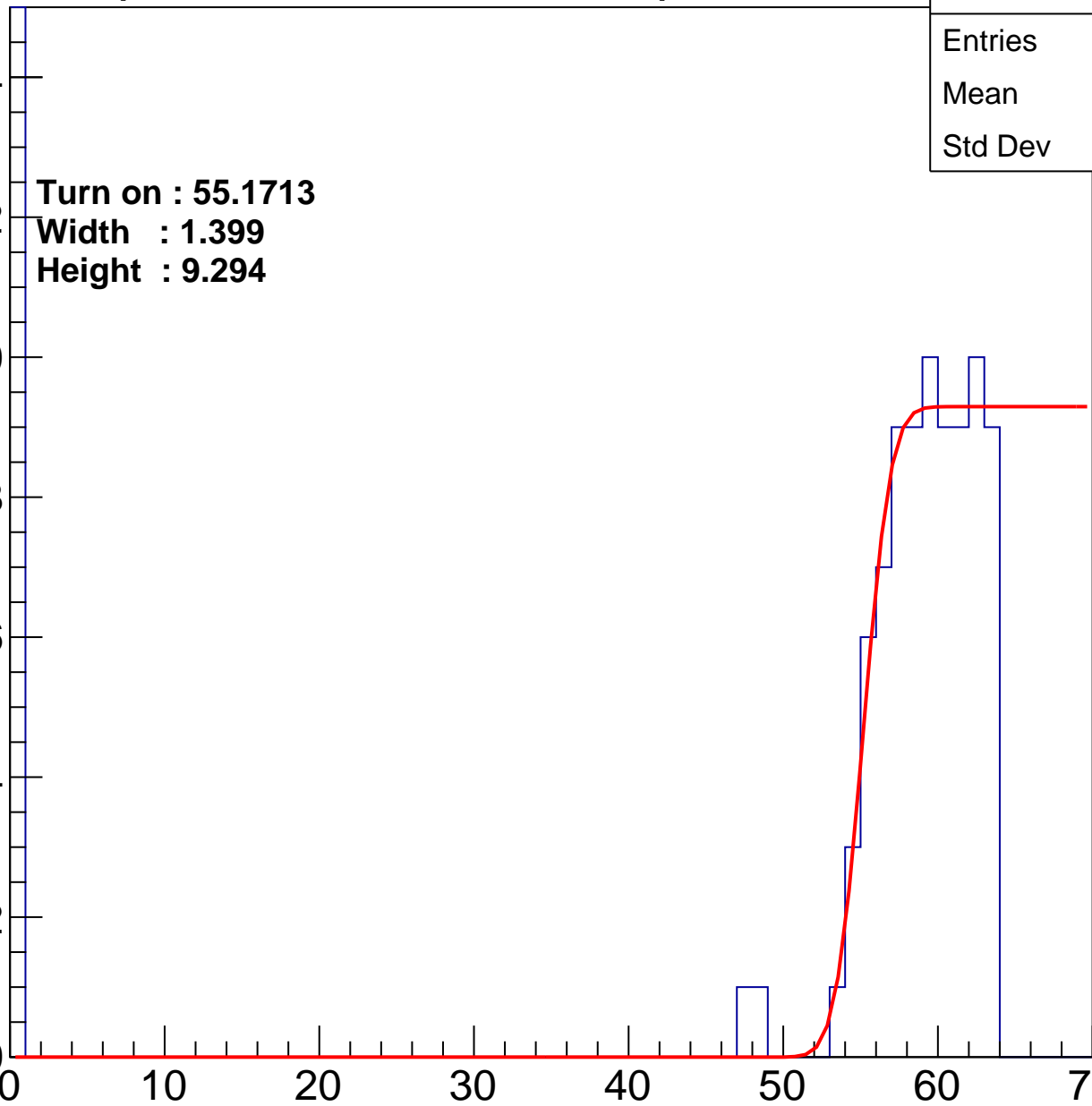
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1713
Width : 1.399
Height : 9.294

Entries	164
Mean	30.08
Std Dev	29.44

ampl



B1L104S, U25-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	34.42
Std Dev	28.35

Turn on : 54.5698

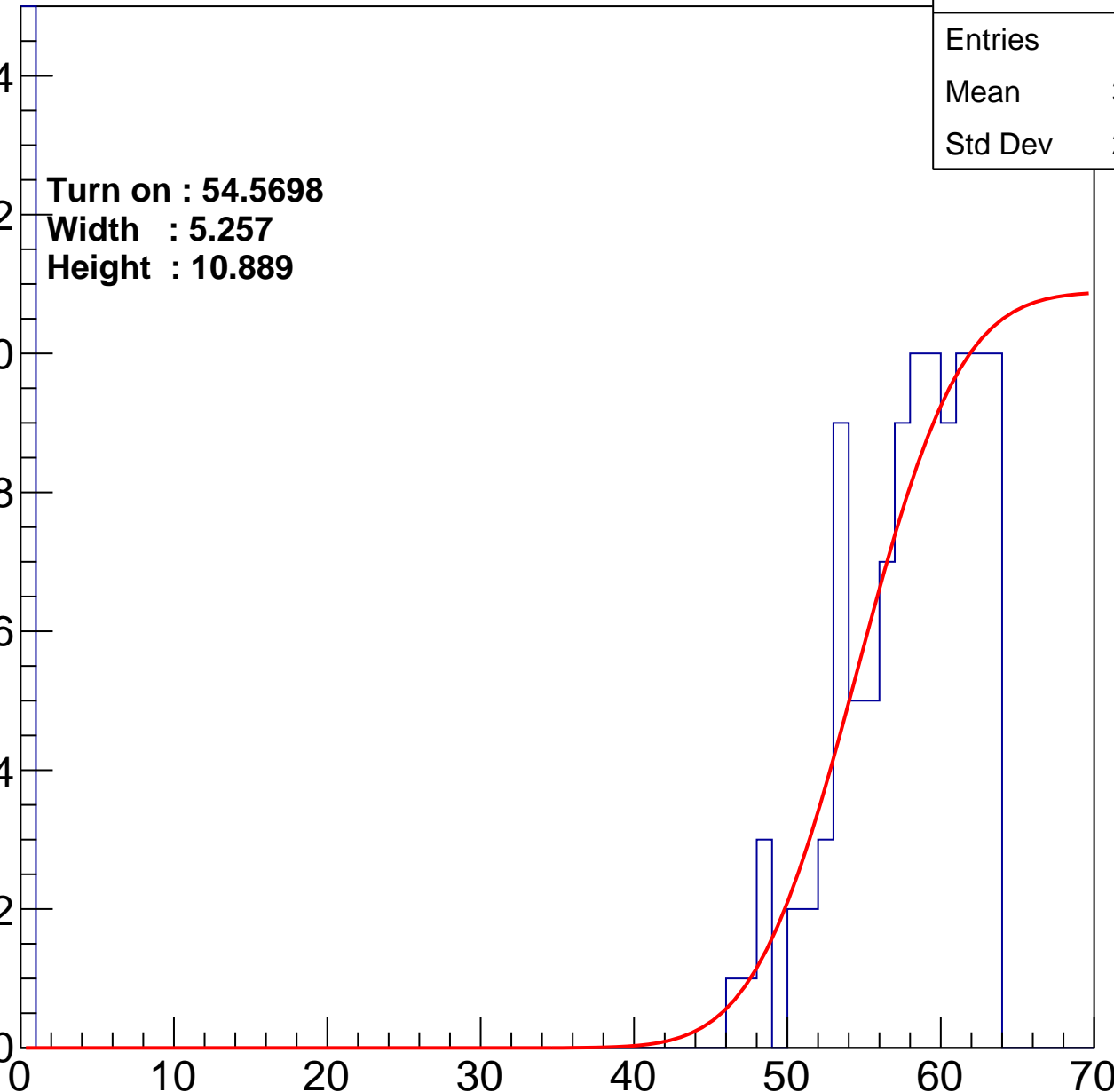
Width : 5.257

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	34.49
Std Dev	28.63

Turn on : 54.8409

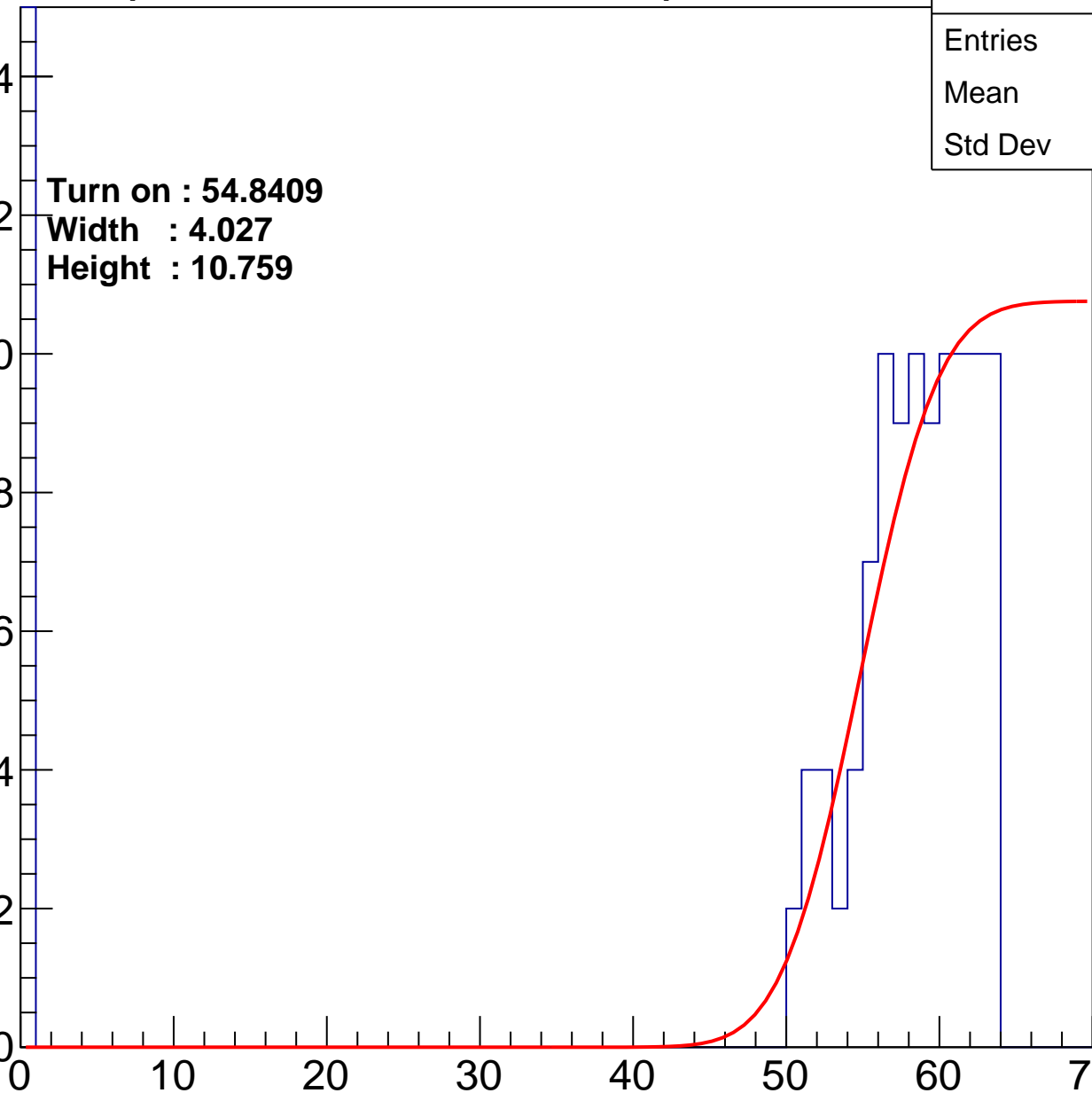
Width : 4.027

Height : 10.759

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	29.46
Std Dev	29.01

Turn on : 53.7947

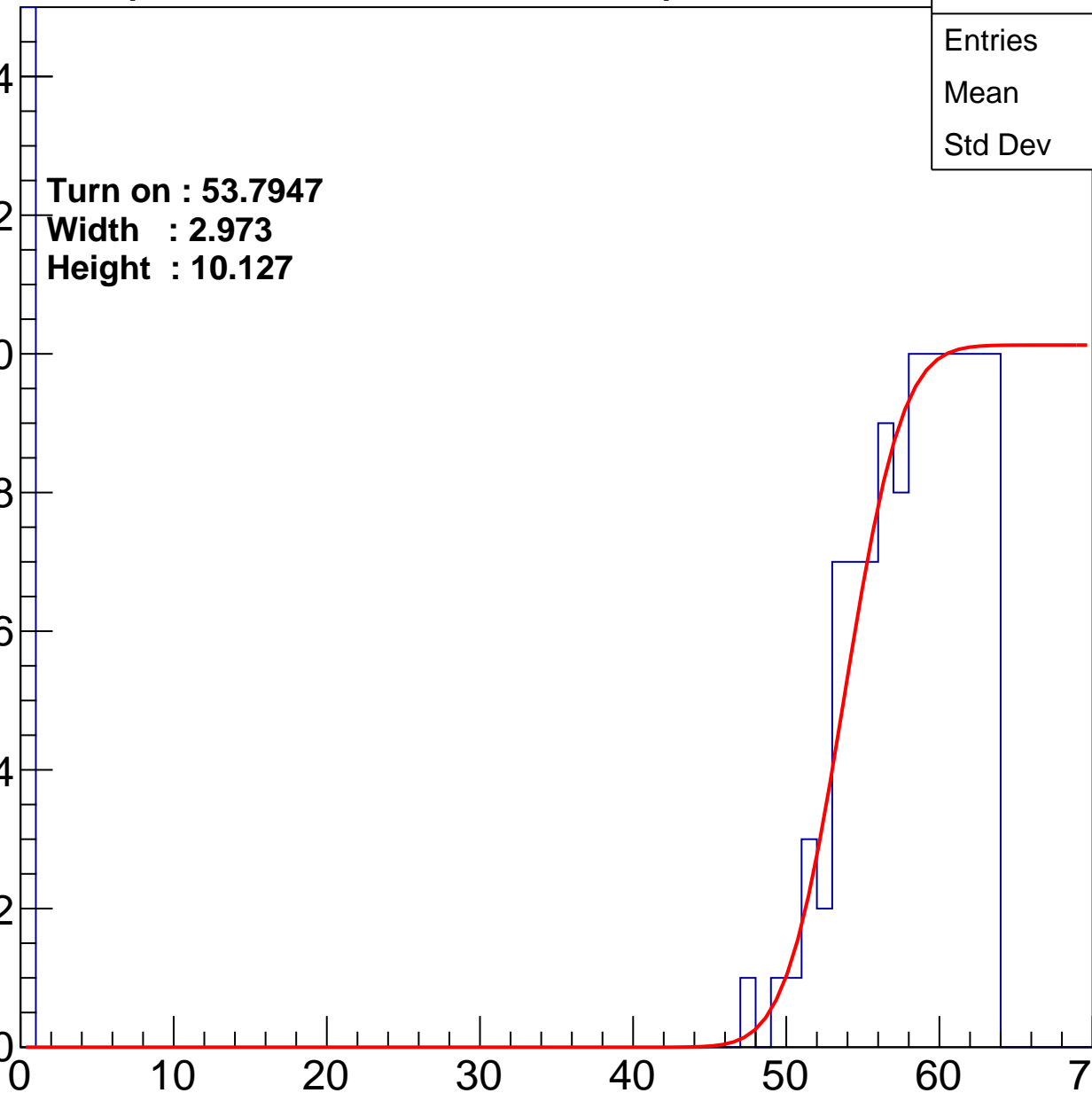
Width : 2.973

Height : 10.127

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	32.08
Std Dev	28.46

Turn on : 52.5390

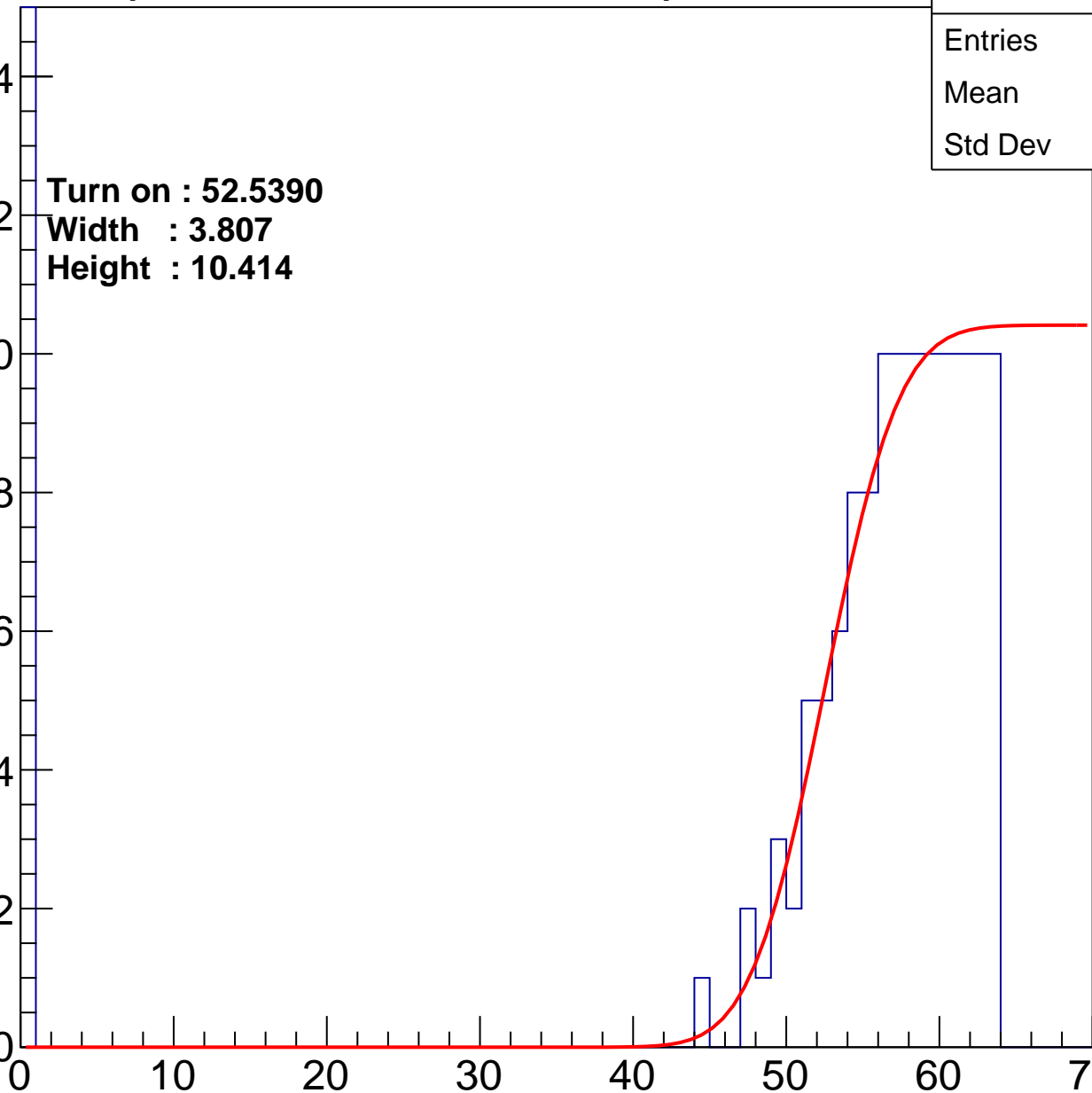
Width : 3.807

Height : 10.414

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch87

calib_packv5_033123_0516.root, FC#4, port A1

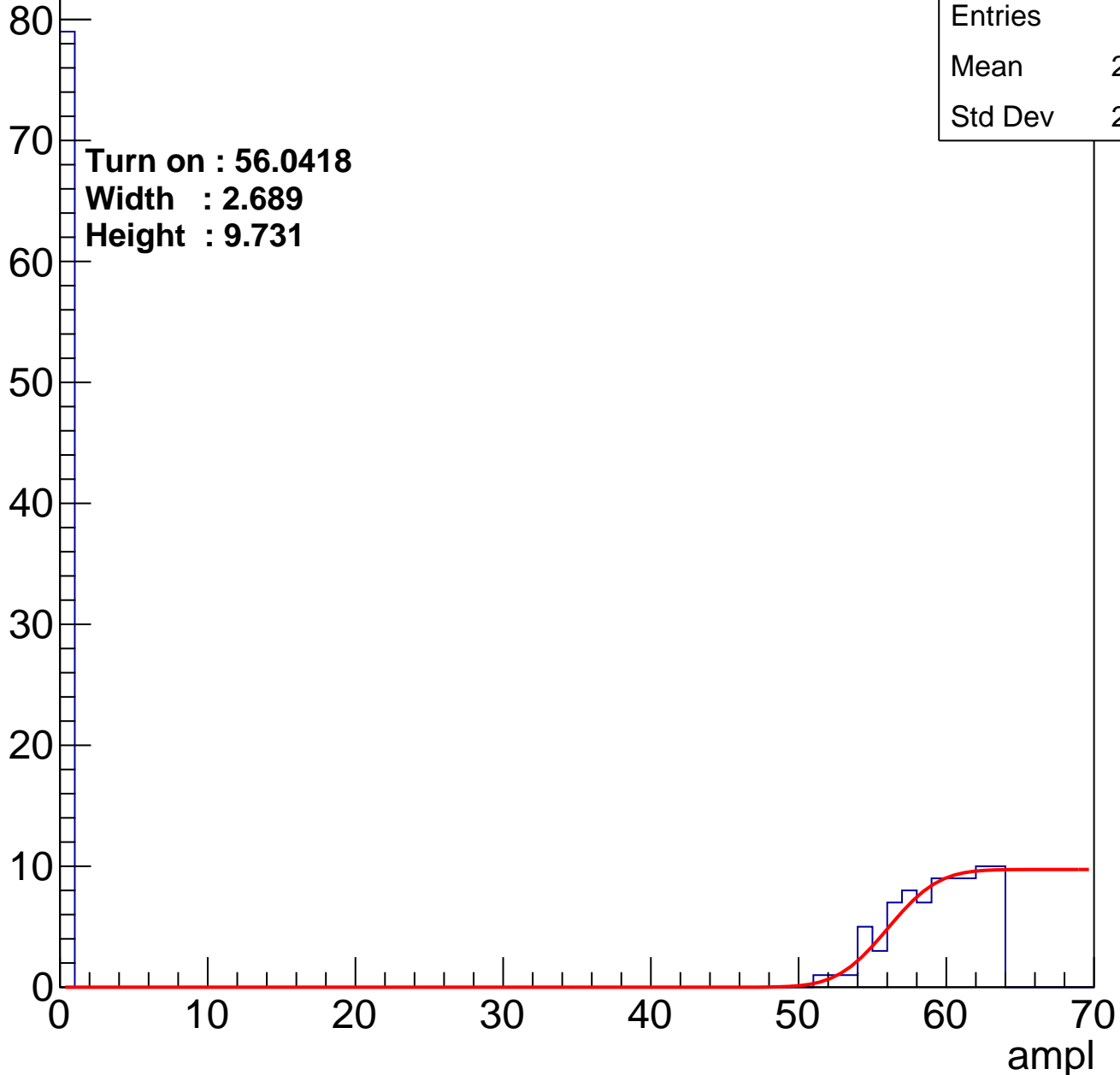
Entries	159
Mean	29.65
Std Dev	29.54

Turn on : 56.0418

Width : 2.689

Height : 9.731

Entry



B1L104S, U25-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	30.76
Std Dev	29.3

Turn on : 55.4585

Width : 3.050

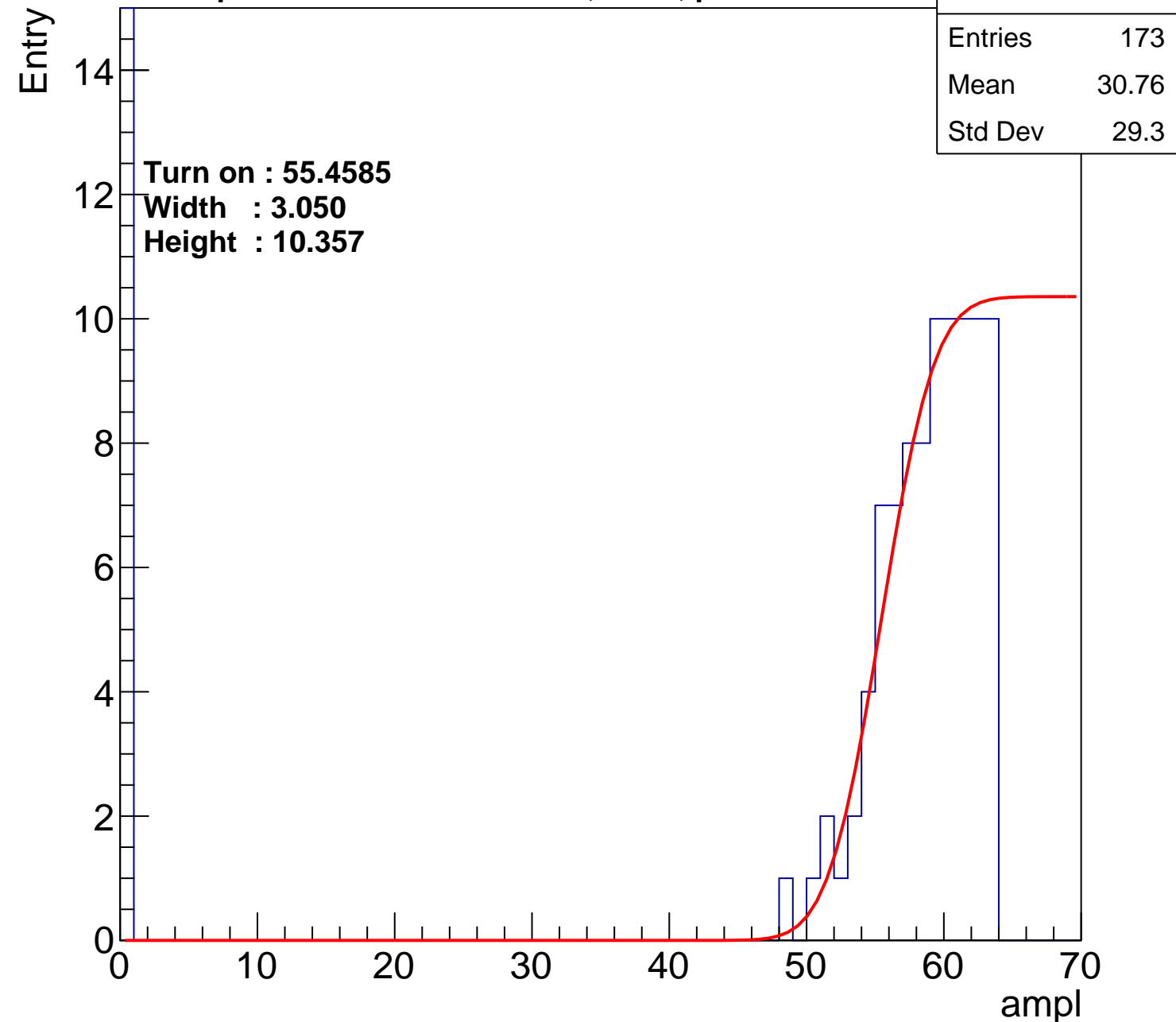
Height : 10.357

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U25-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entry

70

60

50

40

30

20

10

0

0

10

20

30

40

50

60

70

ampl

Turn on : 56.1066

Width : 3.999

Height : 9.444

Entries	152
Mean	29.77
Std Dev	29.47

B1L104S, U25-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	30.83
Std Dev	29.19

Turn on : 55.6930

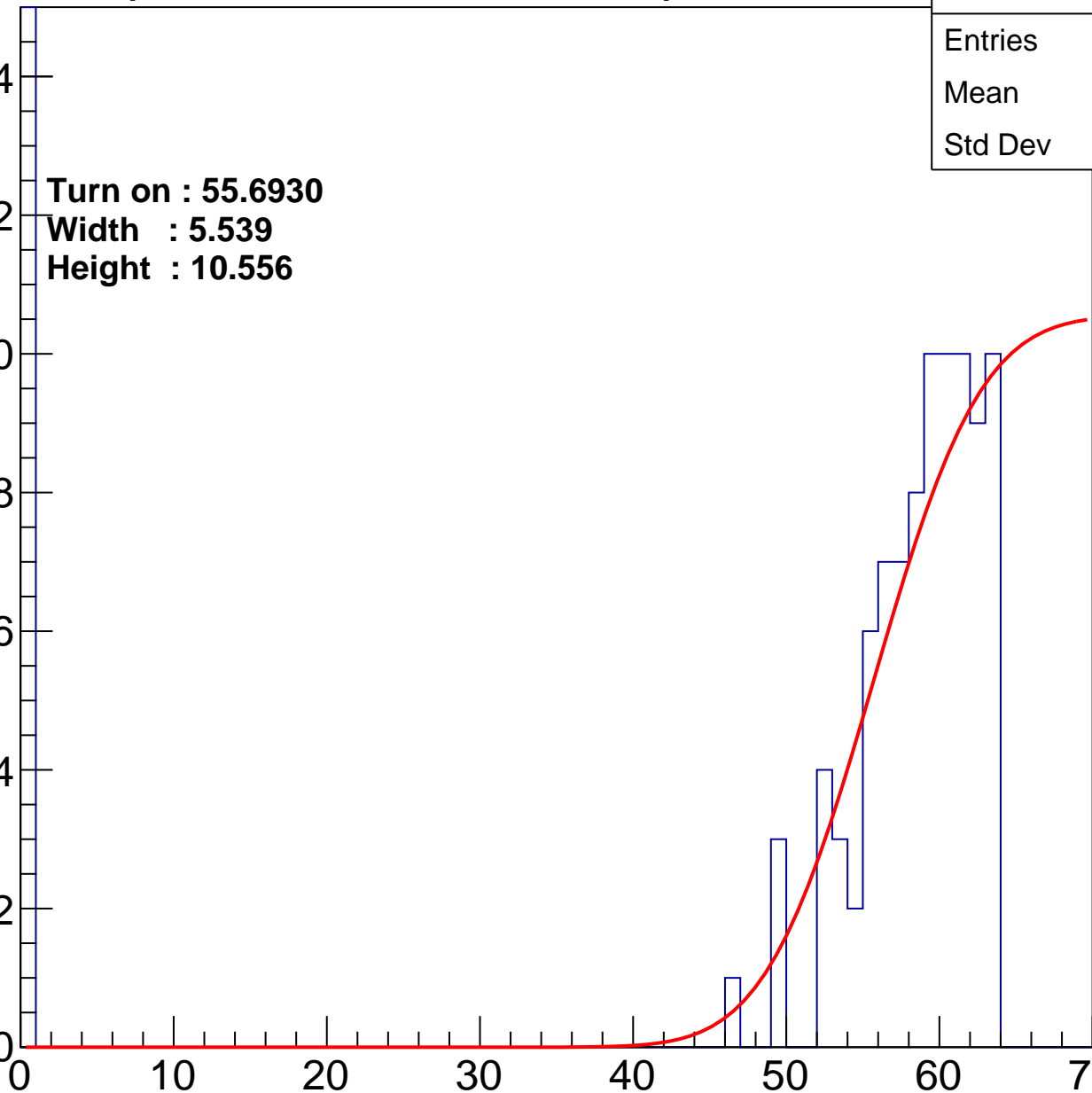
Width : 5.539

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	29.37
Std Dev	29.45

Turn on : 55.4051

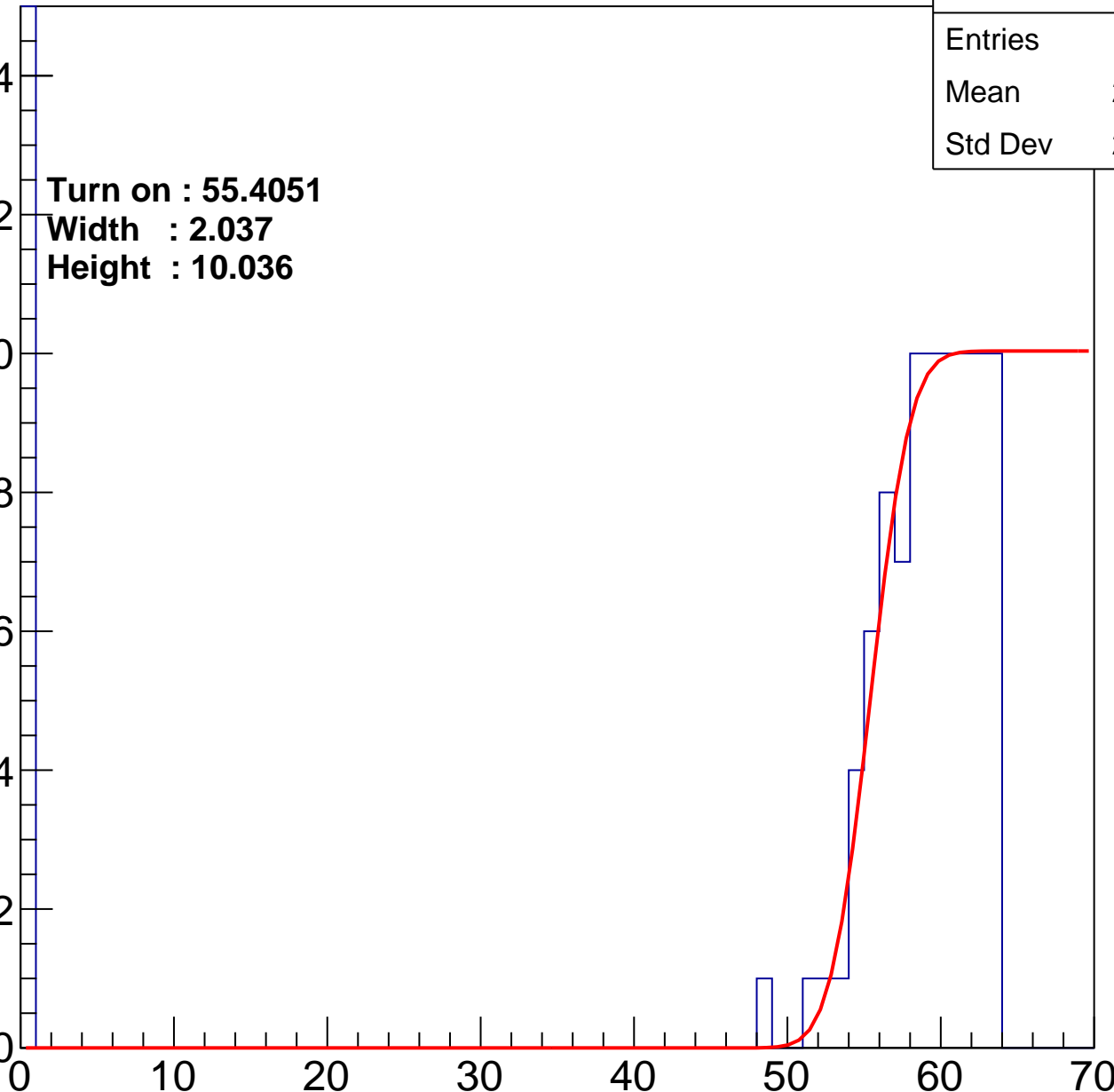
Width : 2.037

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch92

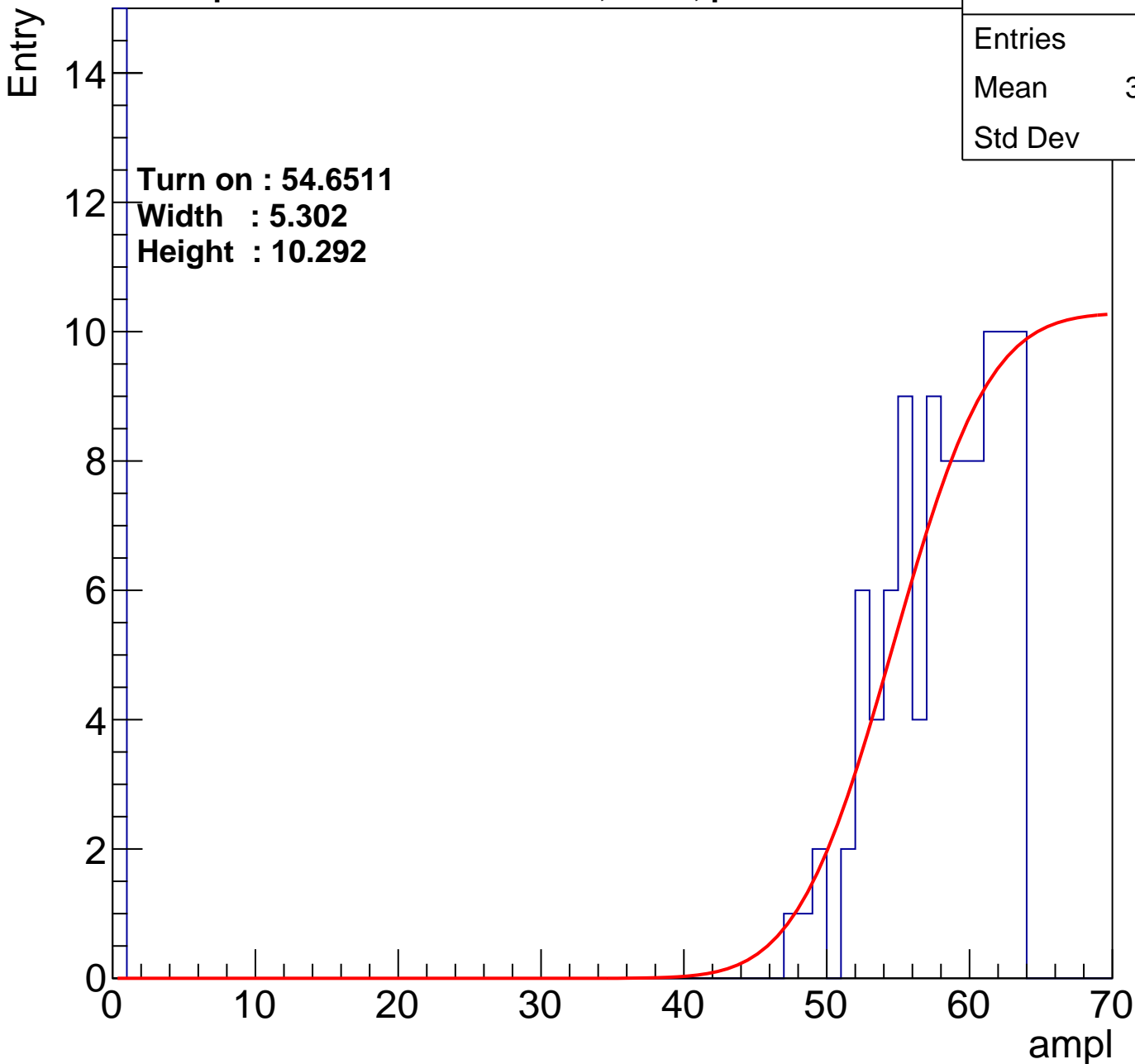
calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	37.42
Std Dev	27.7

Turn on : 54.6511

Width : 5.302

Height : 10.292



B1L104S, U25-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	31.04
Std Dev	29.36

Turn on : 55.5844

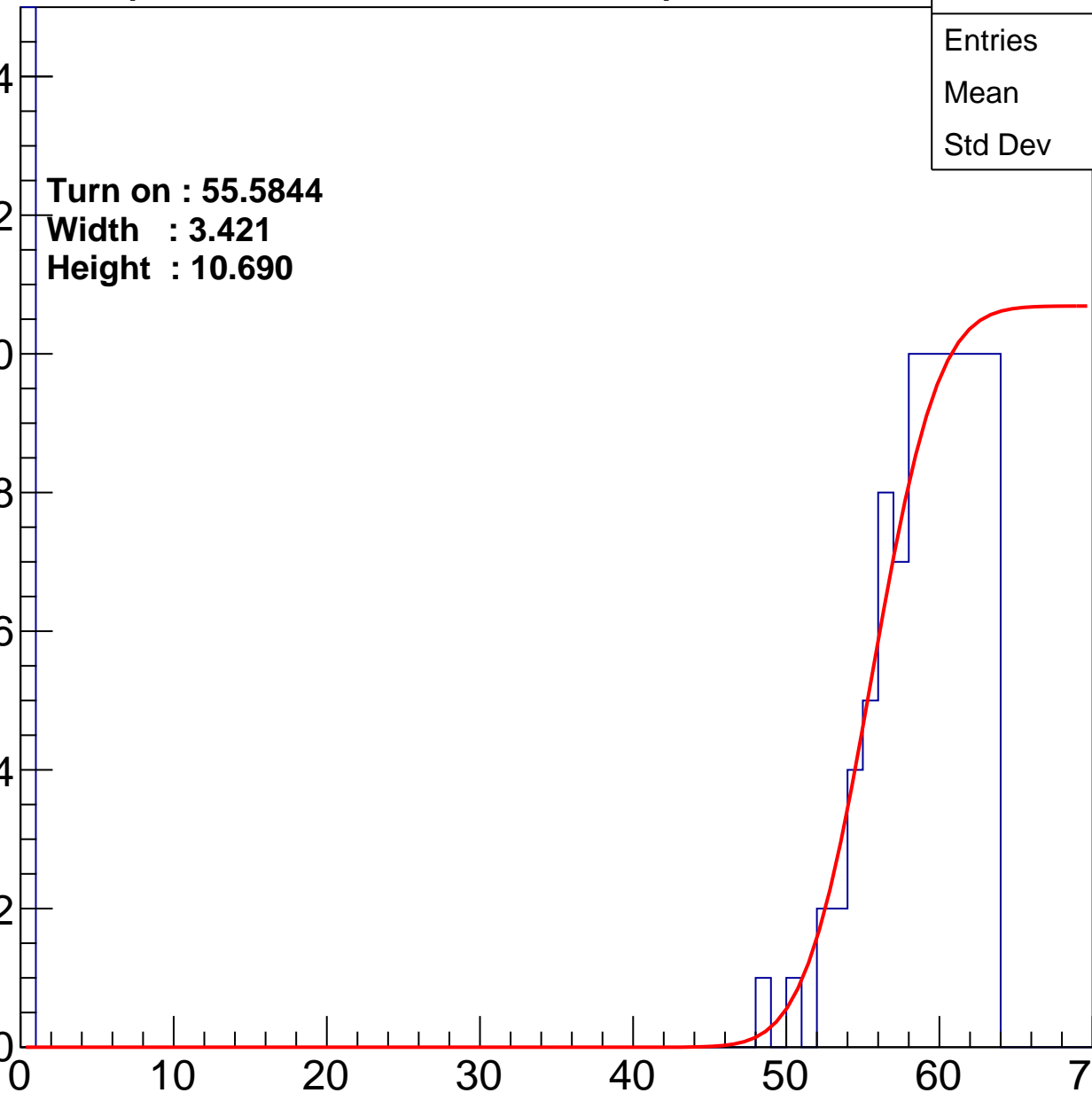
Width : 3.421

Height : 10.690

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	34.75
Std Dev	28.81

Turn on : 55.7565

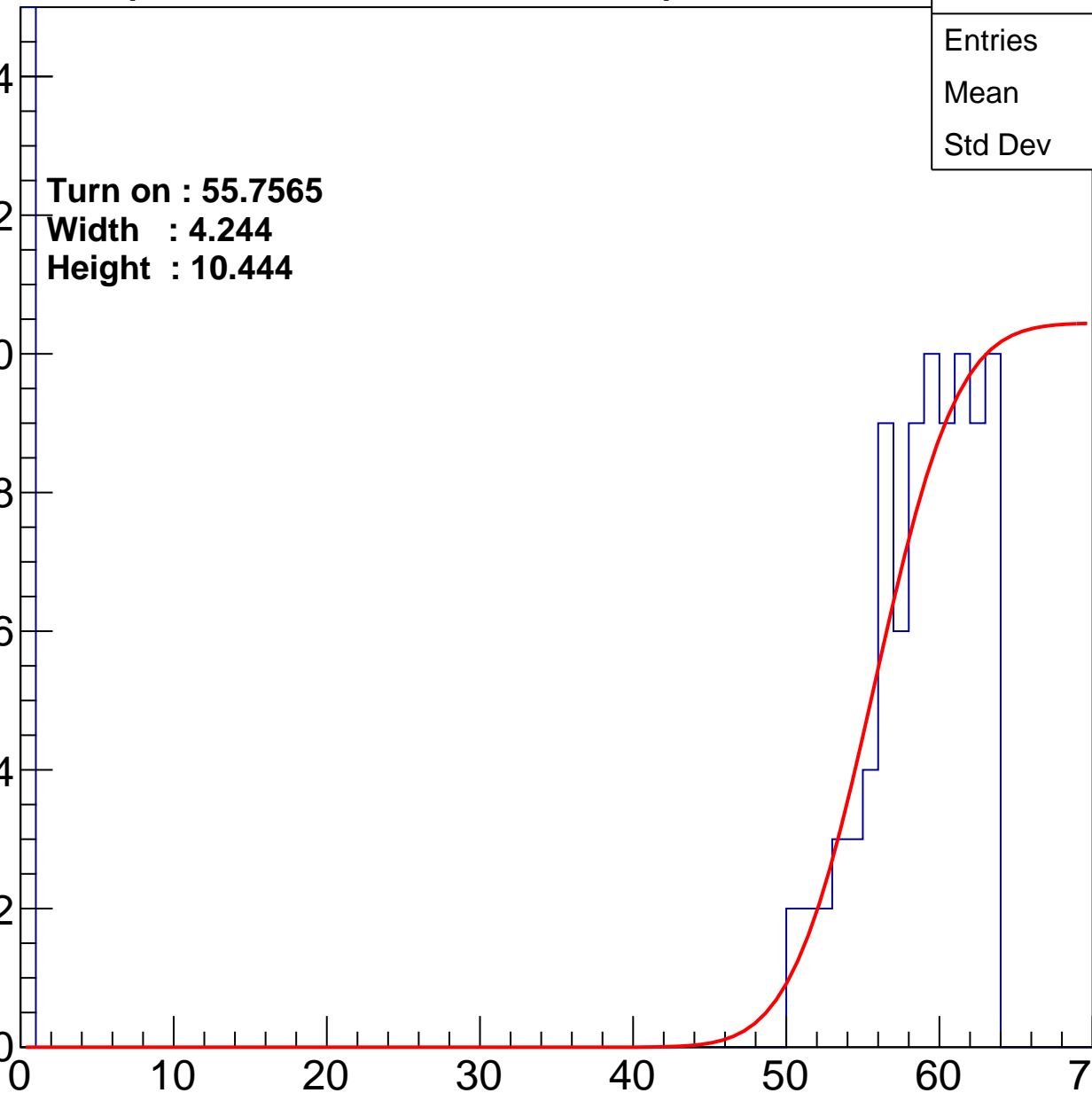
Width : 4.244

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	36.92
Std Dev	27.82

Turn on : 53.5137

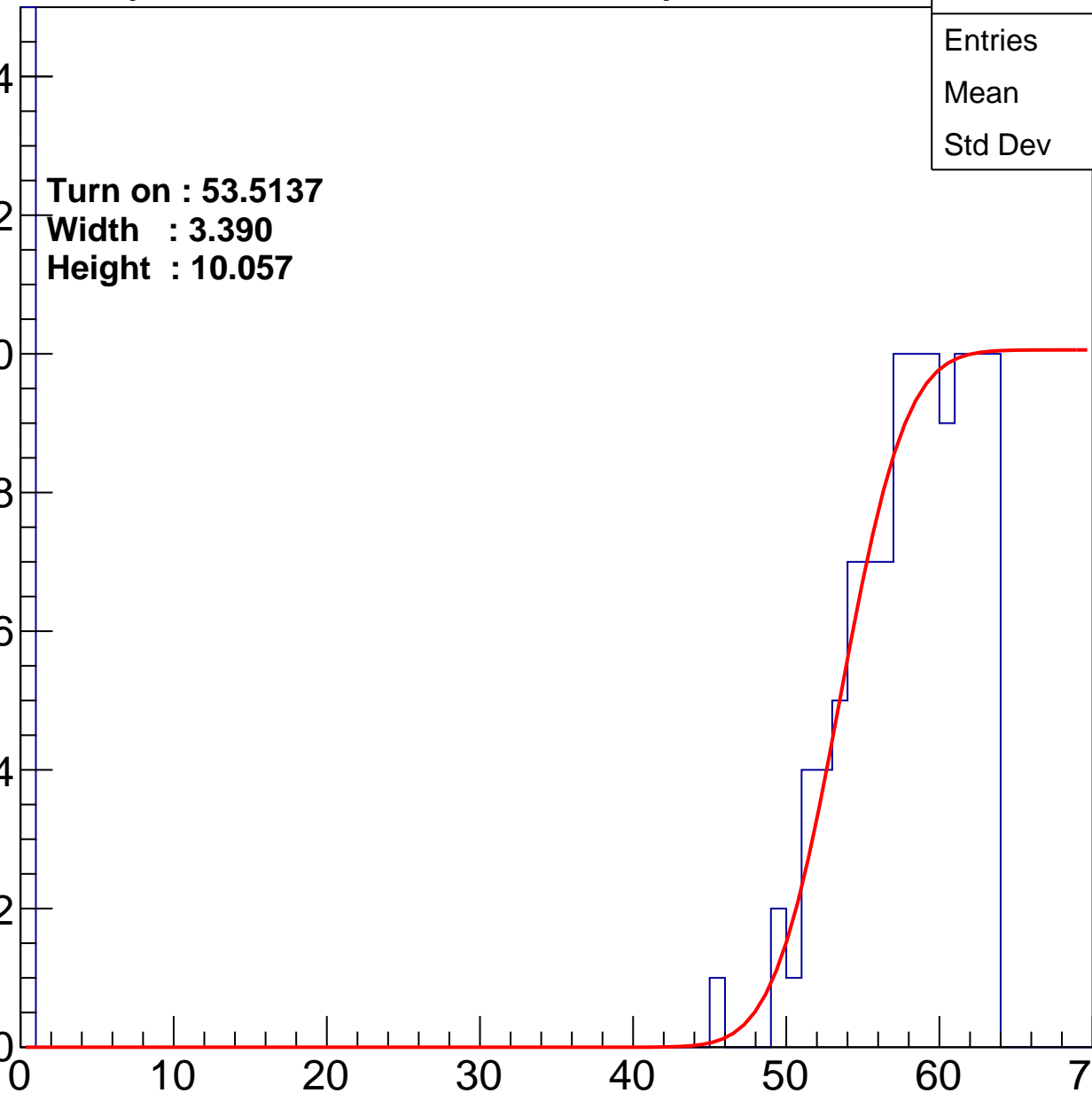
Width : 3.390

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	30.08
Std Dev	29.22

Turn on : 55.1922

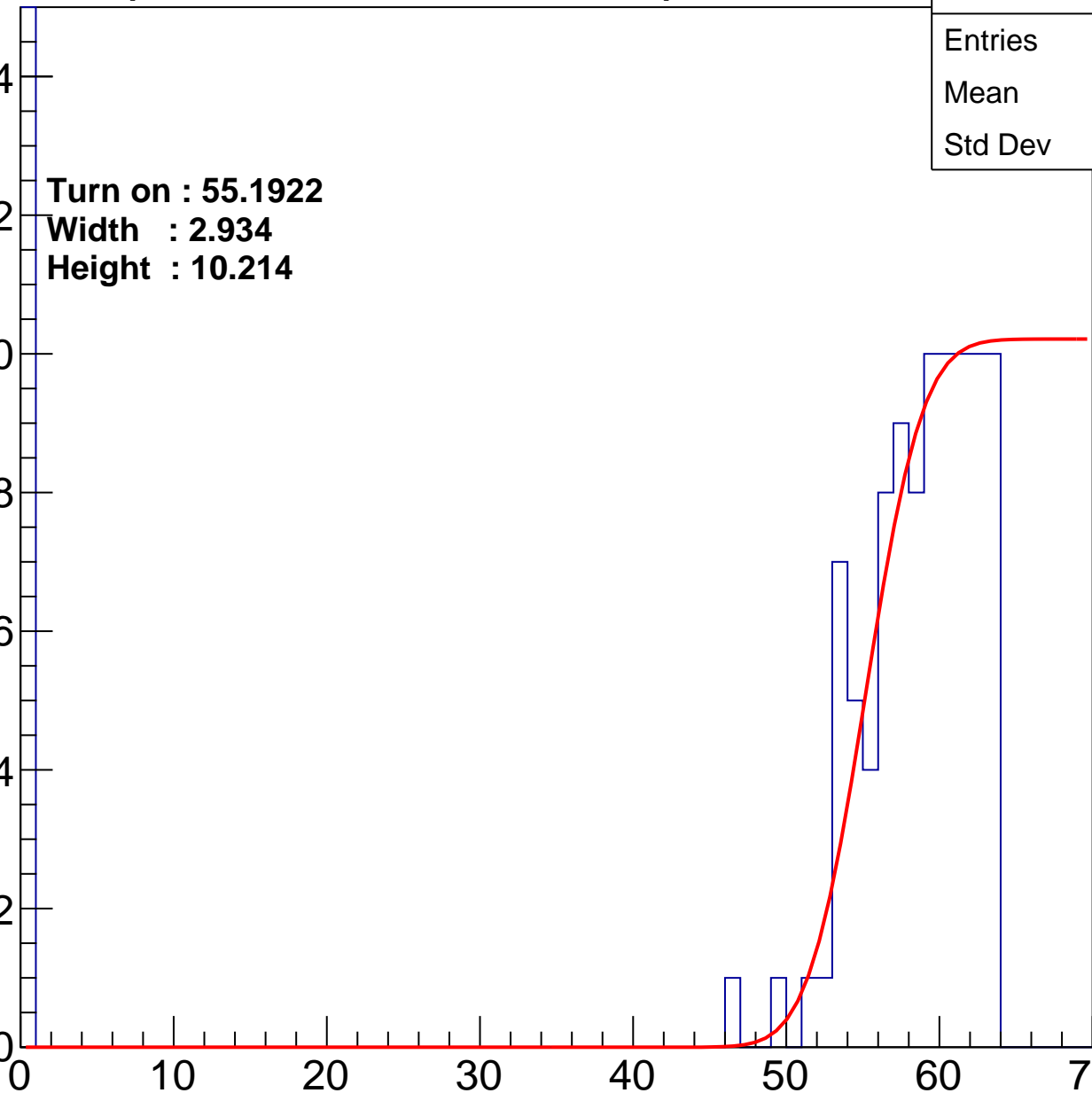
Width : 2.934

Height : 10.214

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	32.4
Std Dev	28.66

Turn on : 52.8148

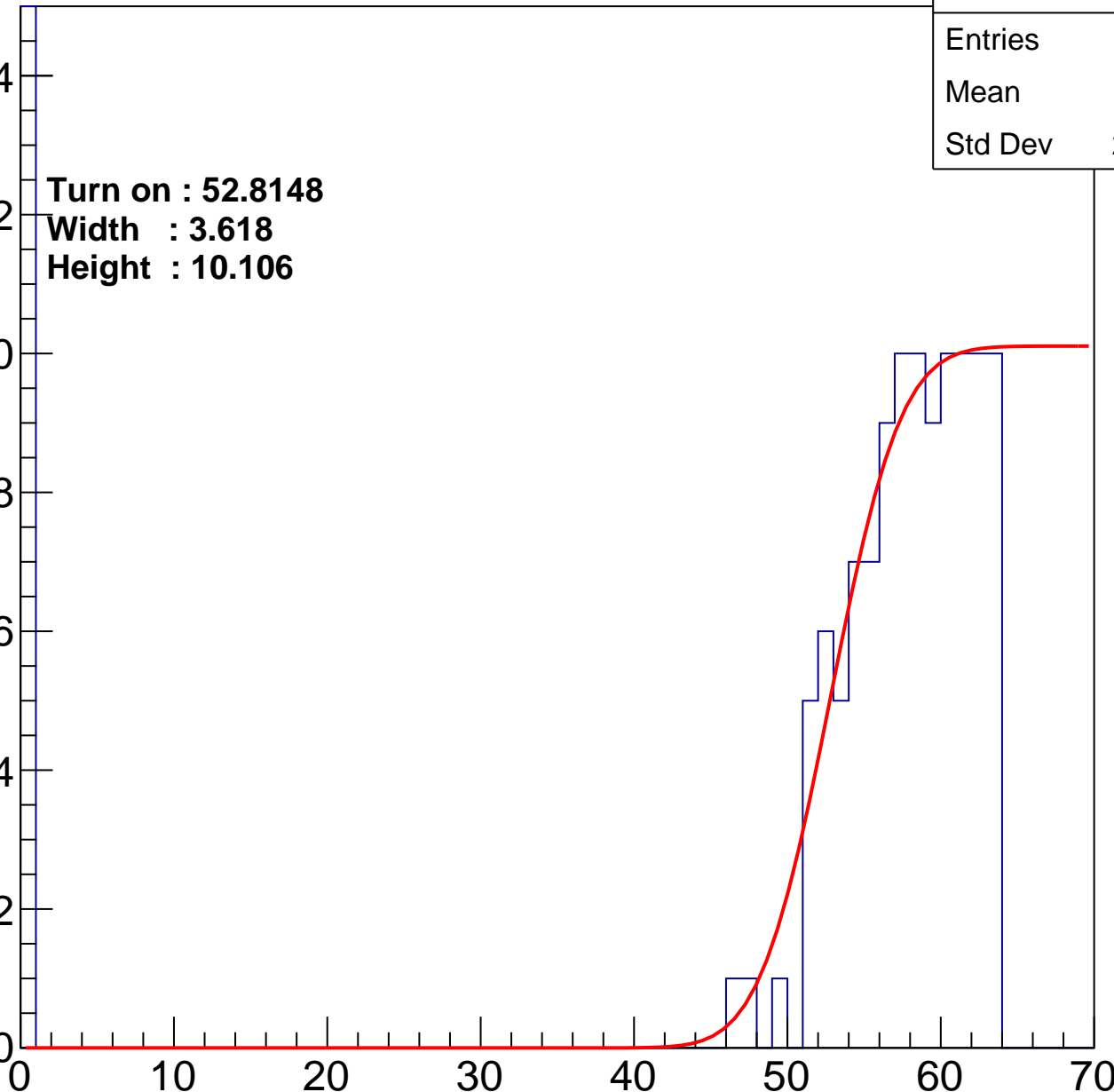
Width : 3.618

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	36.29
Std Dev	27.99

Turn on : 54.8173

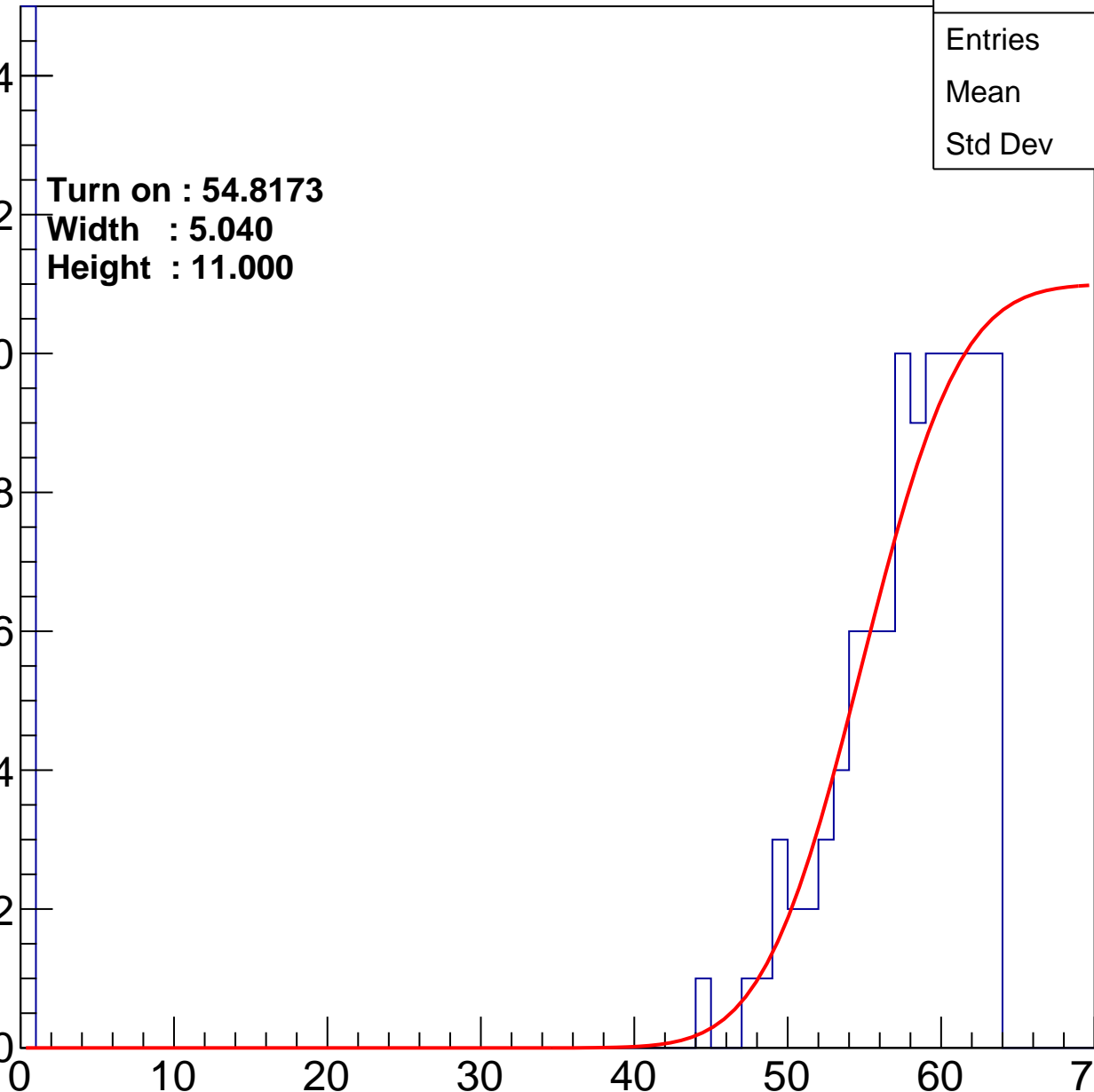
Width : 5.040

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	35.2
Std Dev	28.57

Turn on : 54.7665

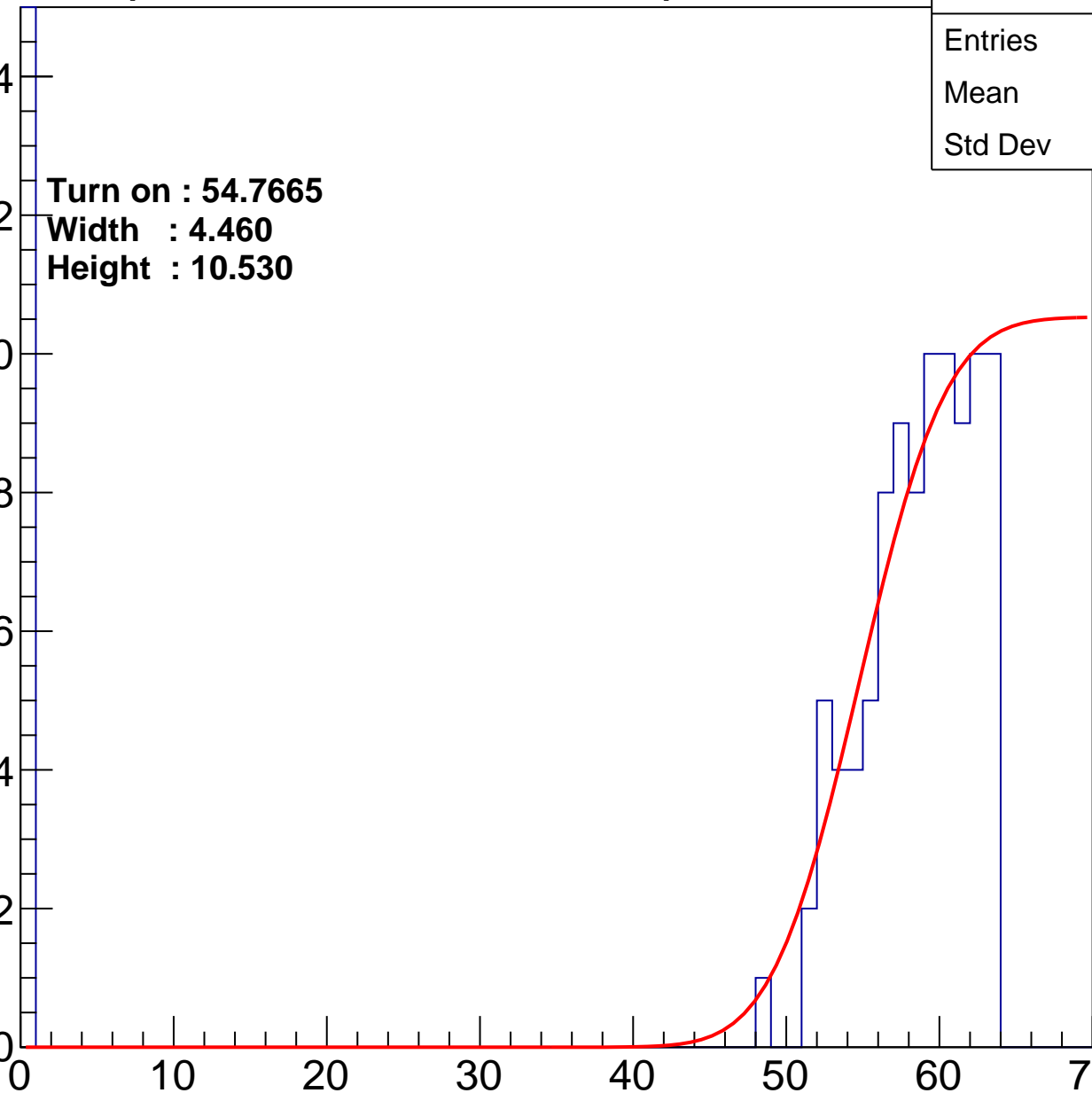
Width : 4.460

Height : 10.530

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch100

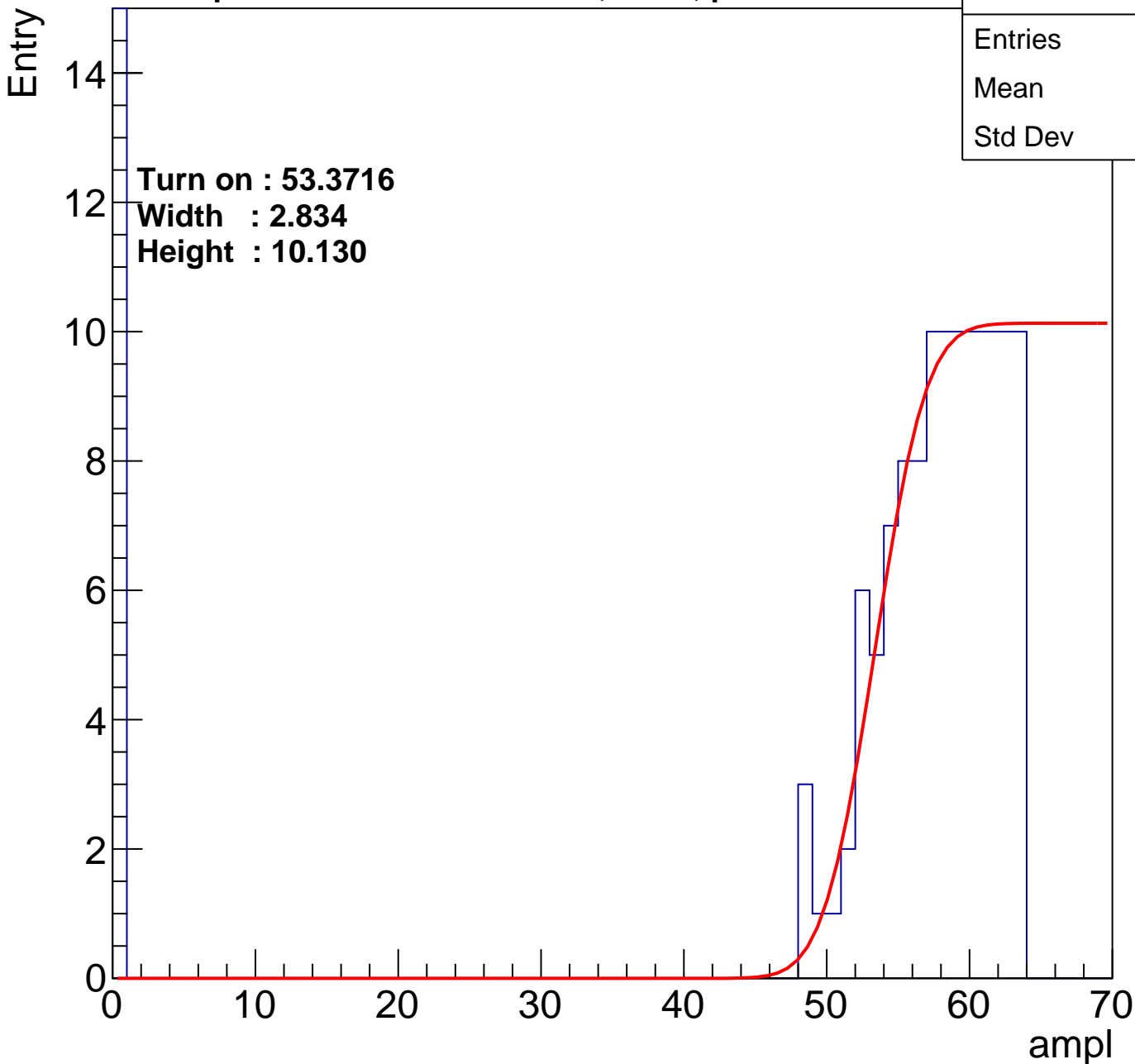
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.1
Std Dev	28.6

Turn on : 53.3716

Width : 2.834

Height : 10.130



B1L104S, U25-ch101

calib_packv5_033123_0516.root, FC#4, port A1

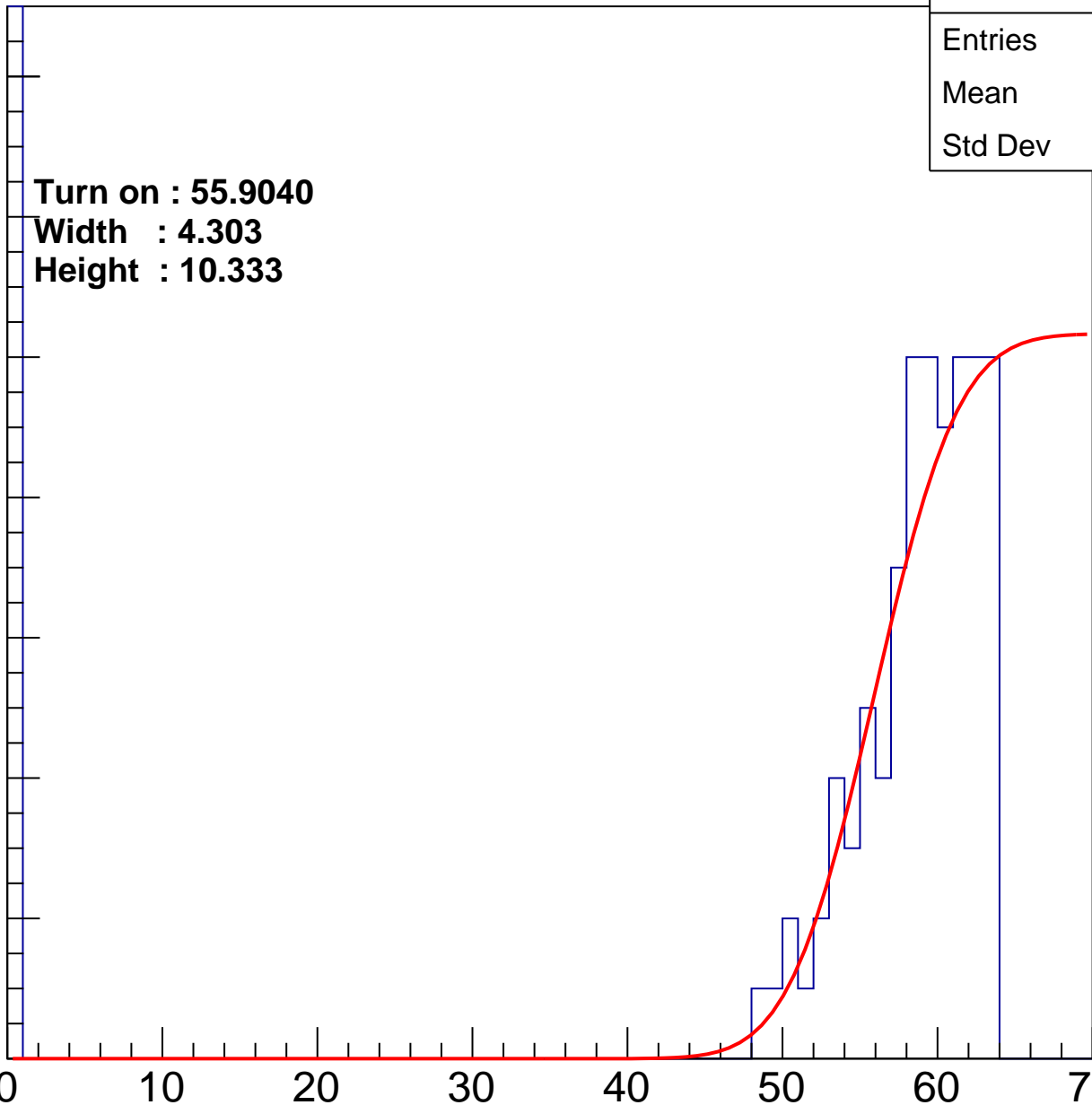
Entry

14
12
10
8
6
4
2
0

Turn on : 55.9040
Width : 4.303
Height : 10.333

Entries	158
Mean	32.87
Std Dev	29.07

ampl



B1L104S, U25-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	32.3
Std Dev	29.01

Turn on : 55.1284

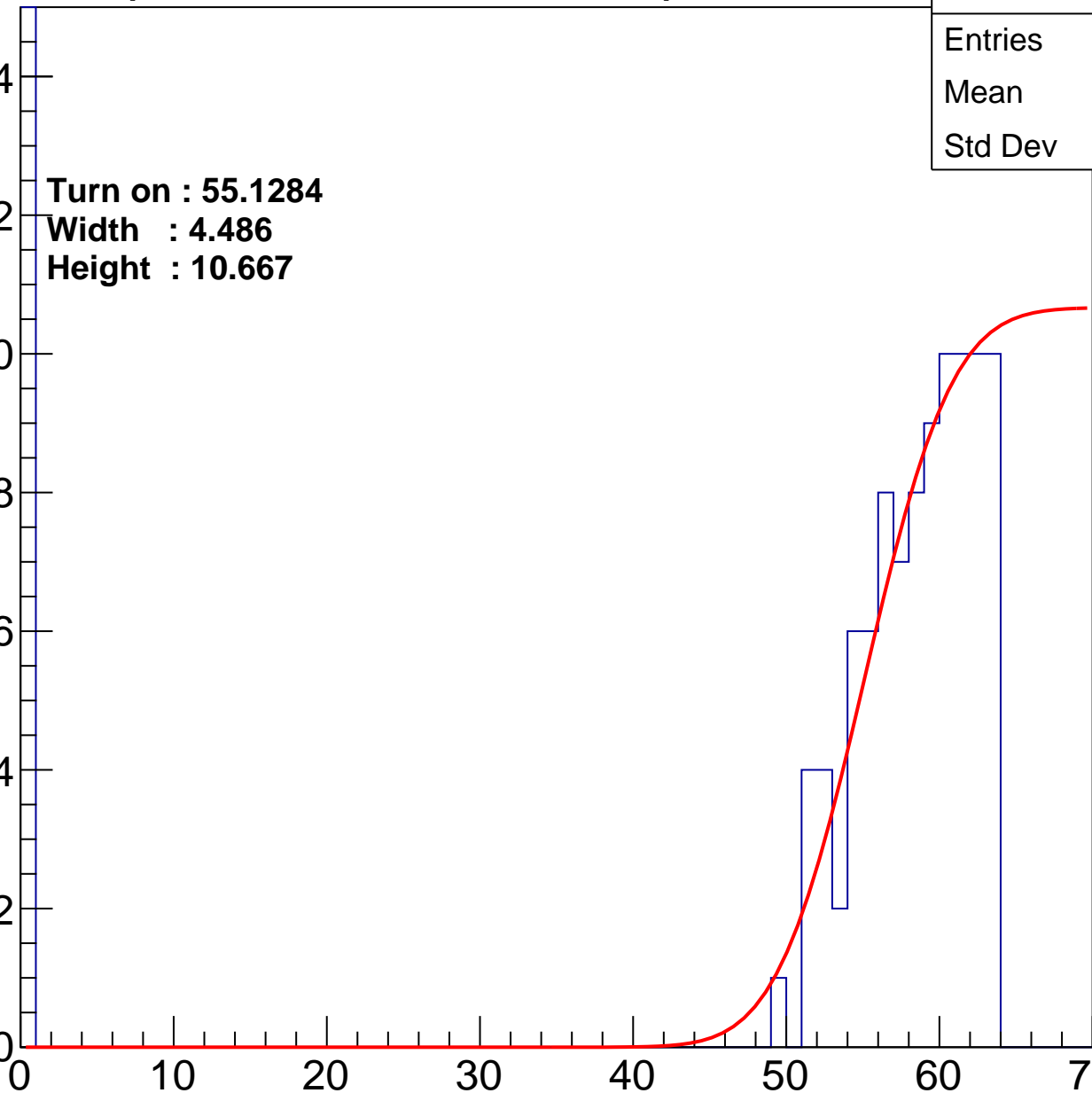
Width : 4.486

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	27.25
Std Dev	29.57

Turn on : 56.3101

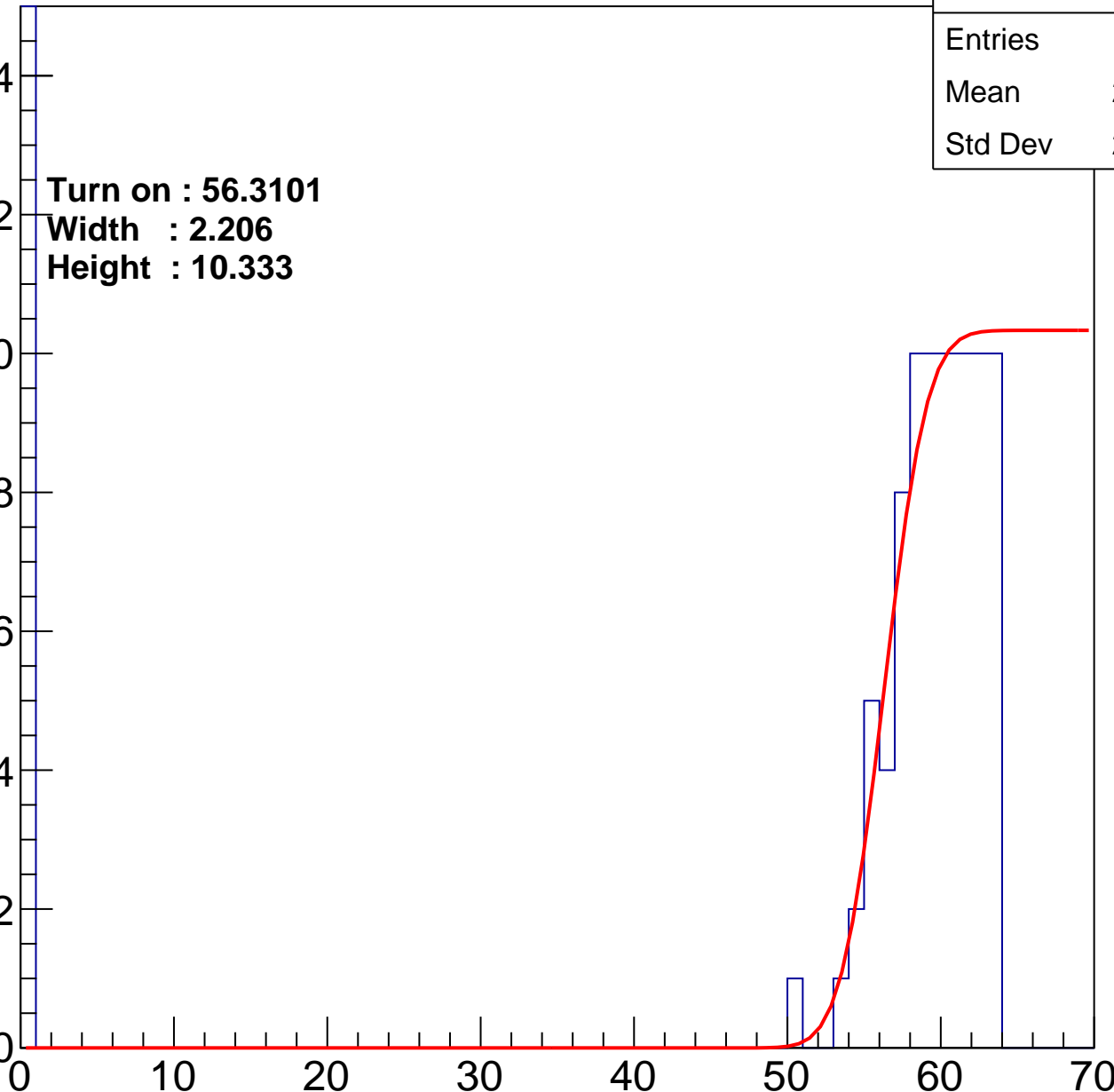
Width : 2.206

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	34.76
Std Dev	28.58

Turn on : 55.2042

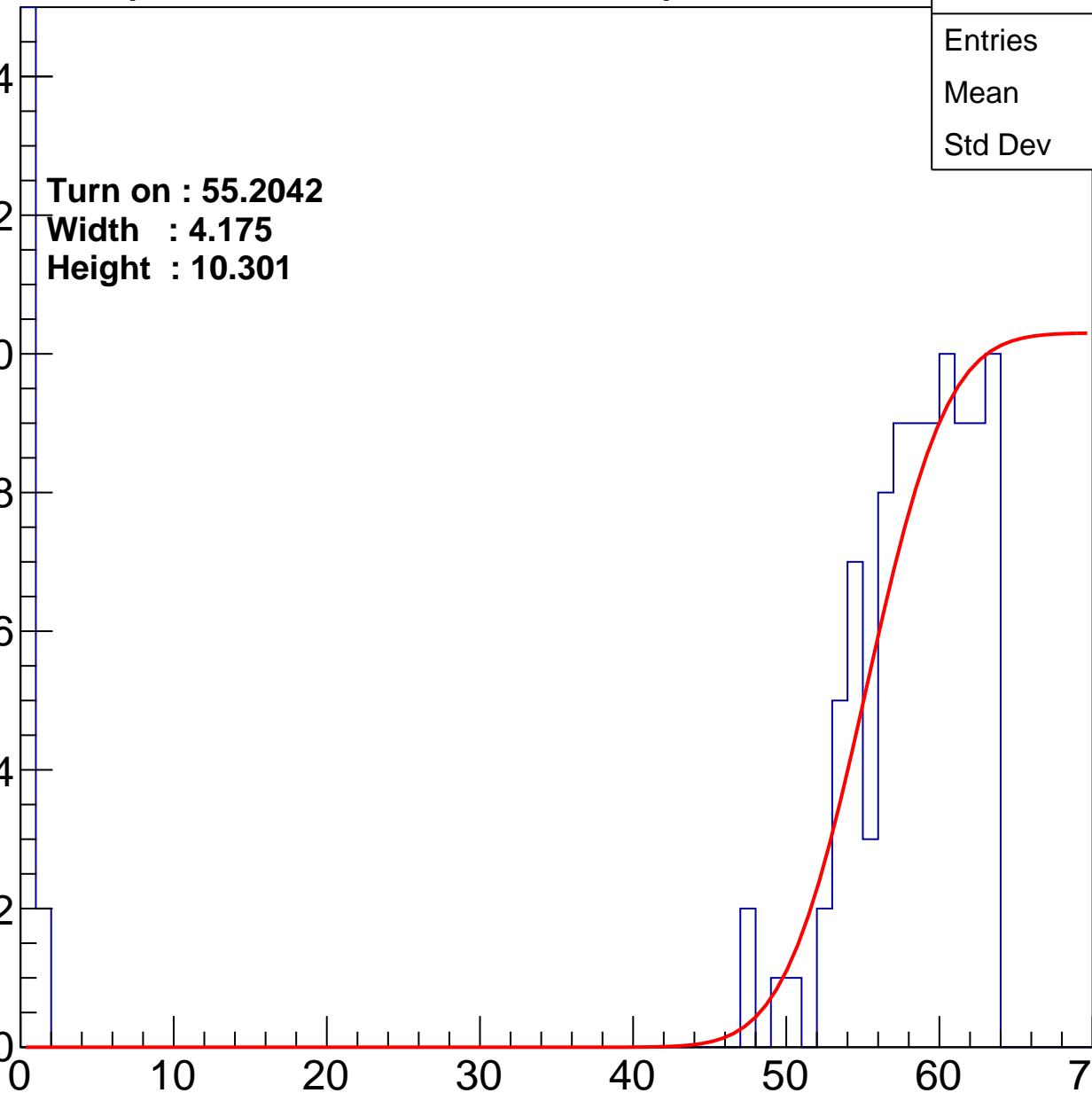
Width : 4.175

Height : 10.301

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	27.79
Std Dev	29.43

Turn on : 55.8248

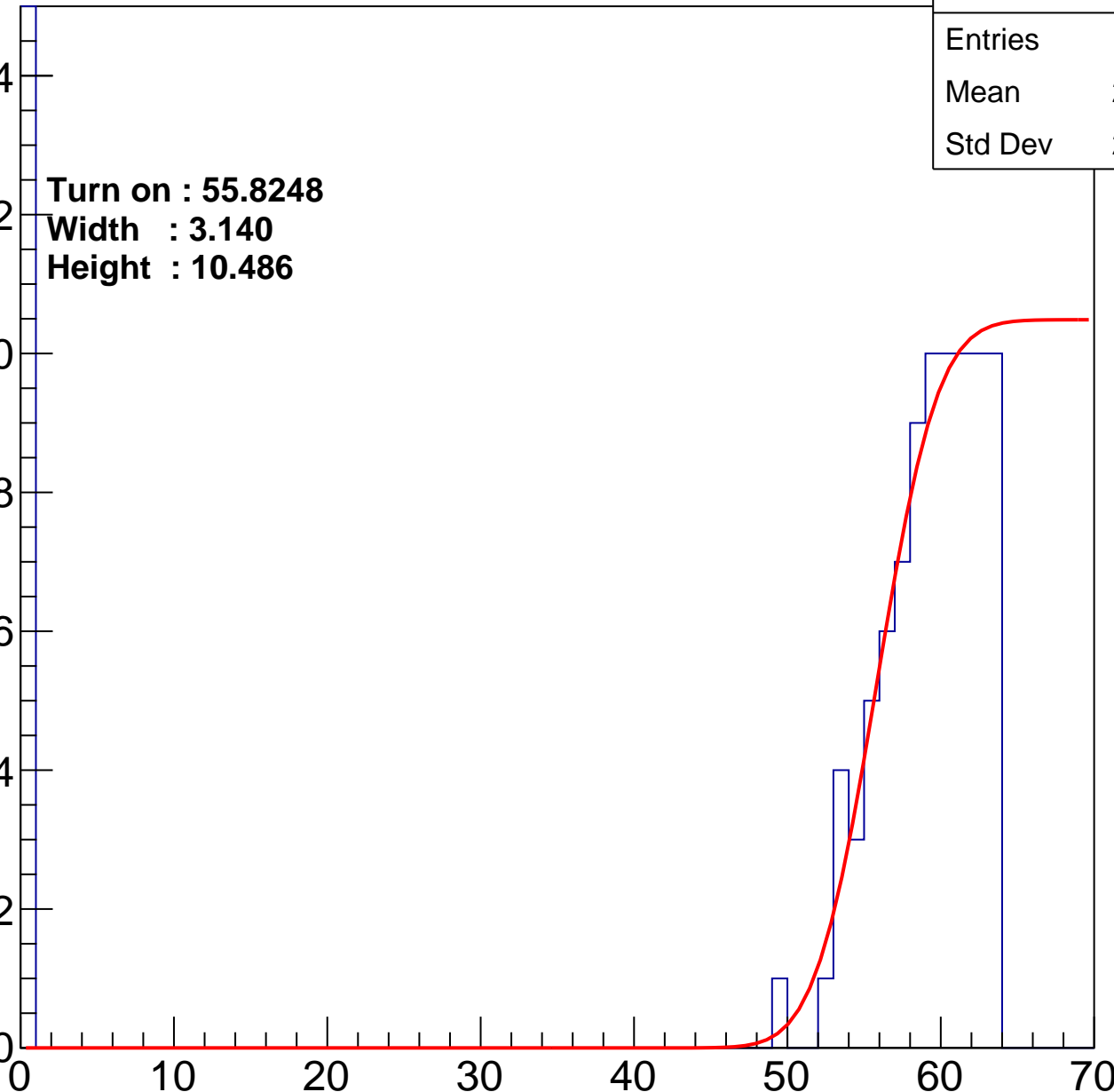
Width : 3.140

Height : 10.486

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	29.88
Std Dev	29.18

Turn on : 55.9617

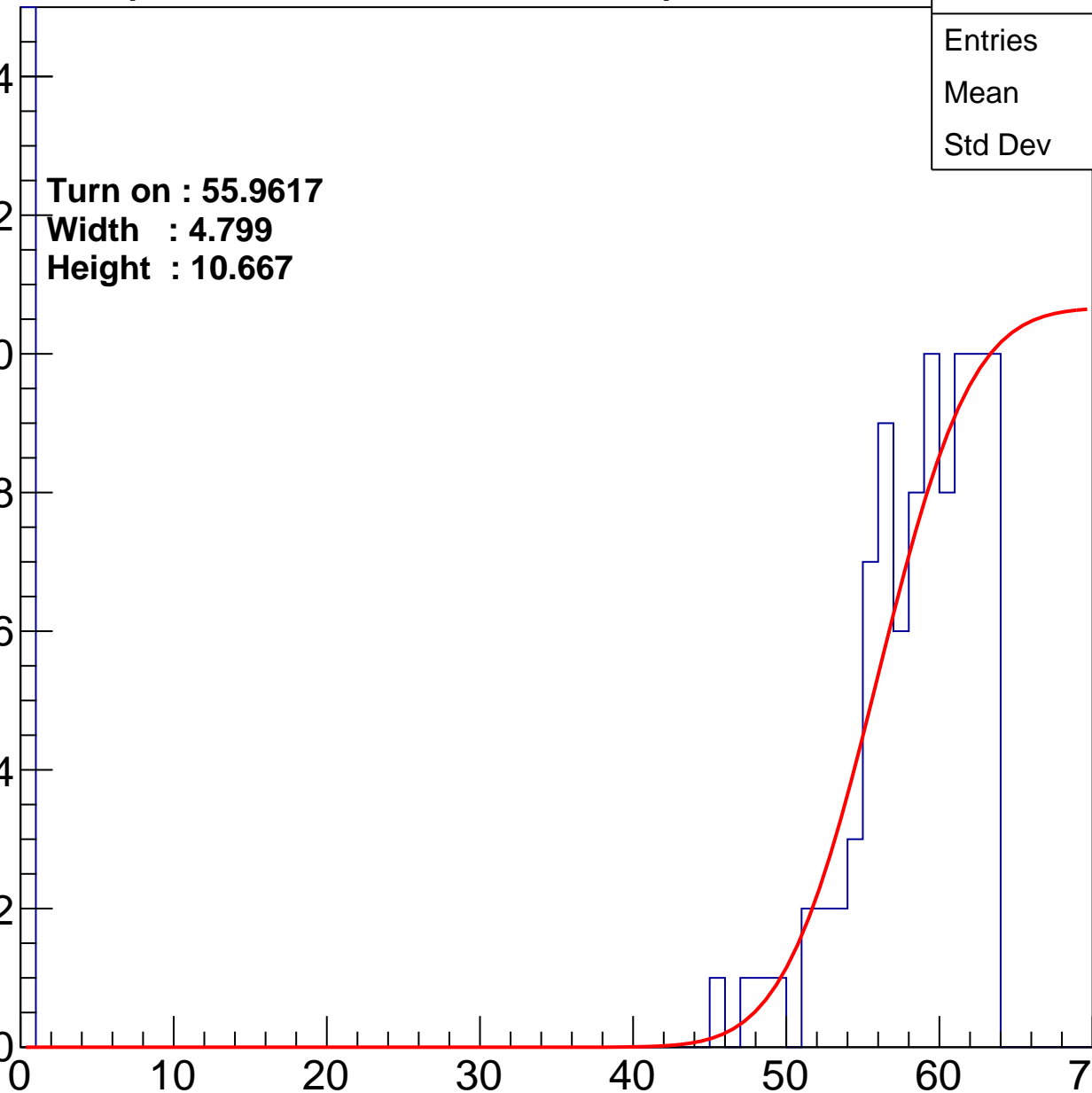
Width : 4.799

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch107

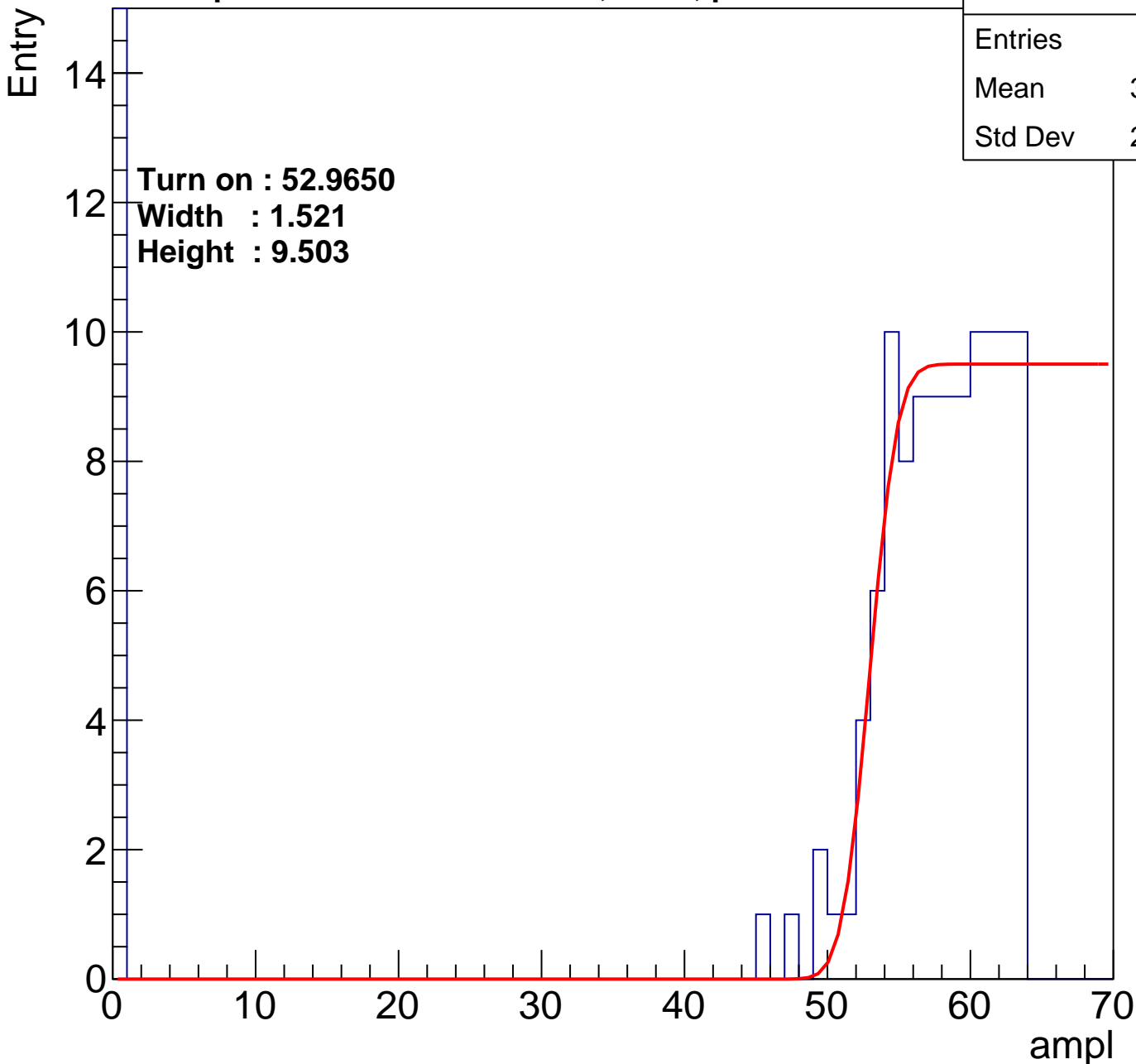
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.65
Std Dev	28.49

Turn on : 52.9650

Width : 1.521

Height : 9.503



B1L104S, U25-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	30.82
Std Dev	29.2

Turn on : 54.7268

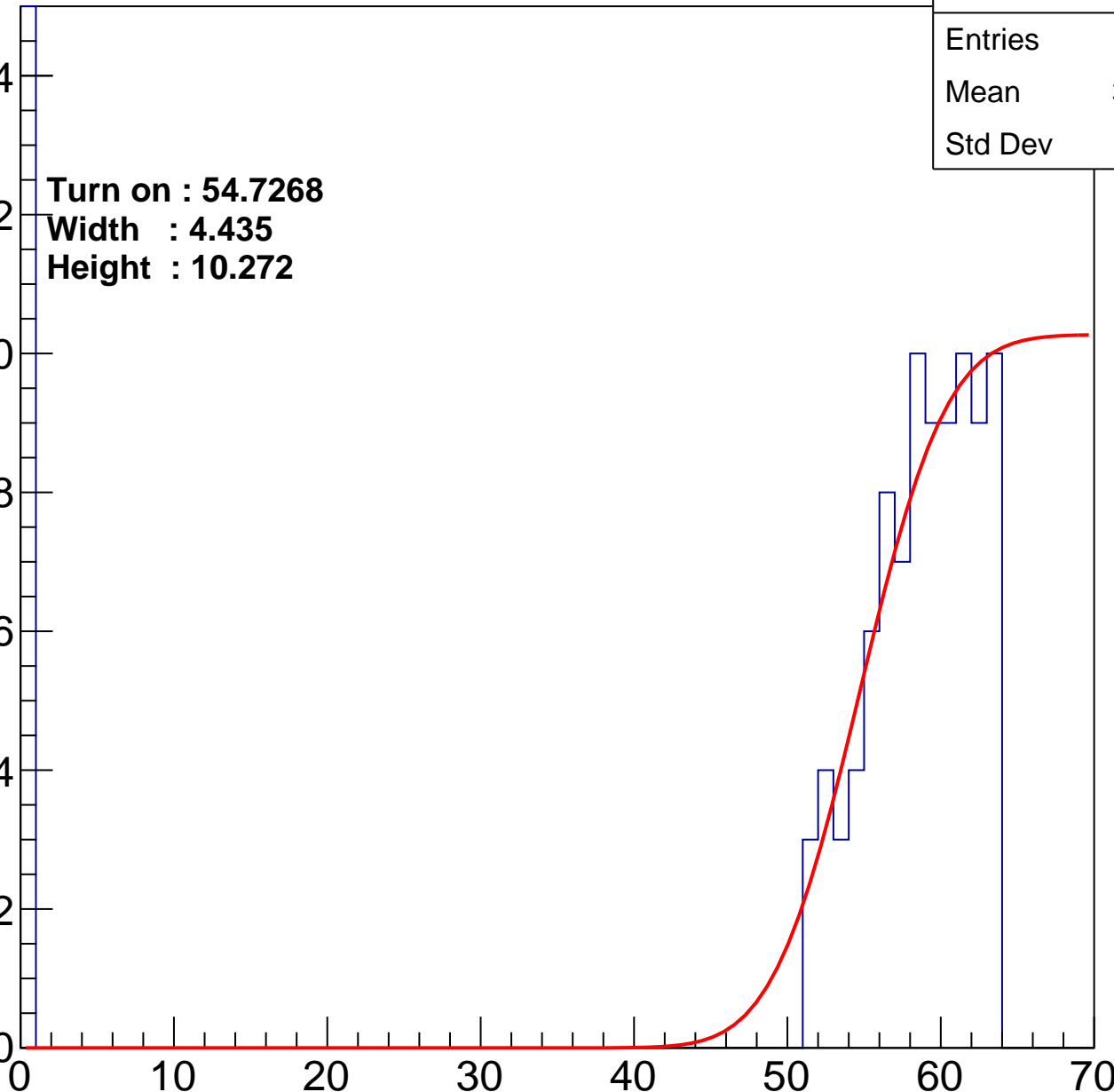
Width : 4.435

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch109

calib_packv5_033123_0516.root, FC#4, port A1

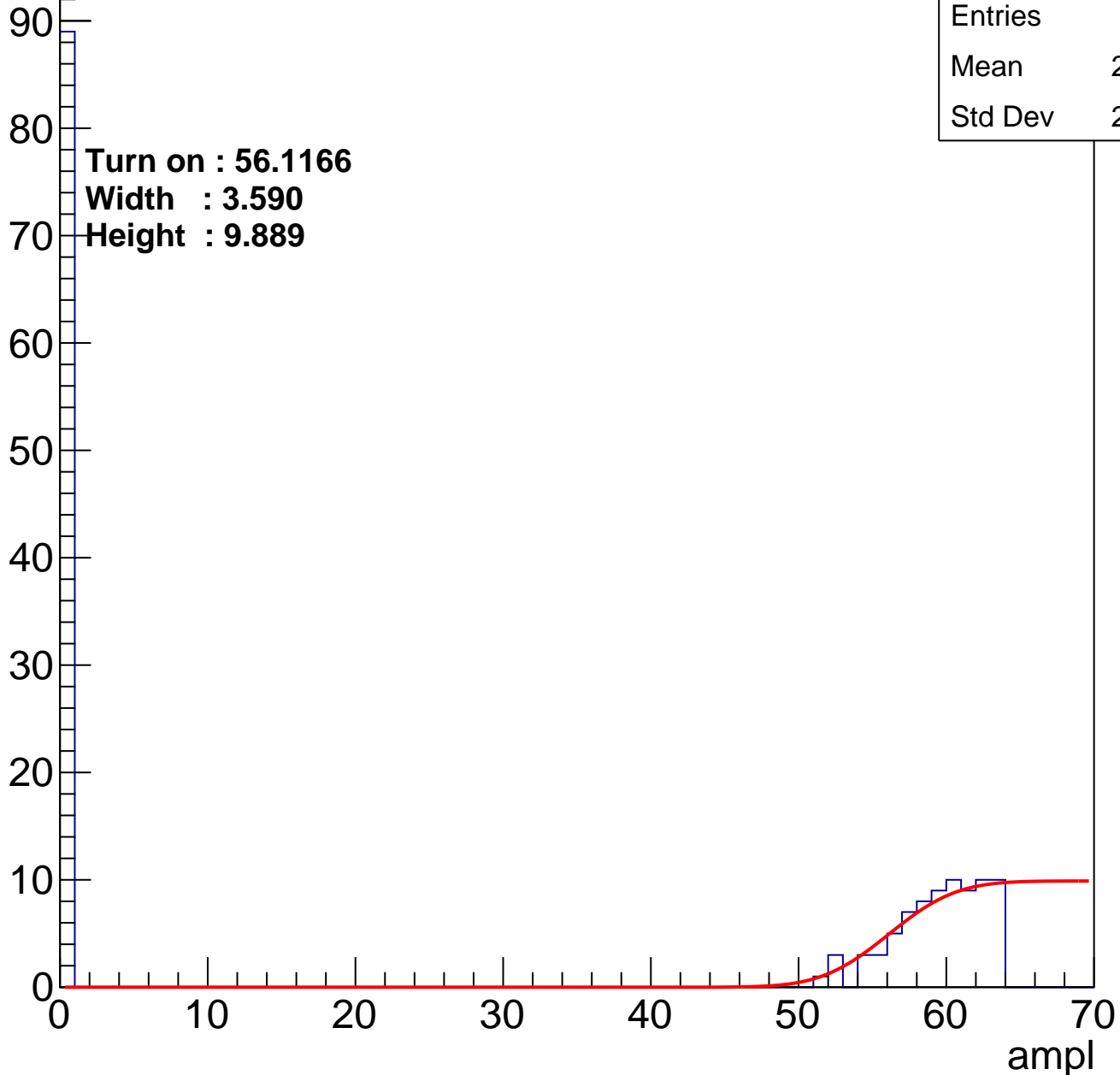
Entries	167
Mean	27.59
Std Dev	29.54

Turn on : 56.1166

Width : 3.590

Height : 9.889

Entry



B1L104S, U25-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	37.95
Std Dev	27.49

Turn on : 52.7975

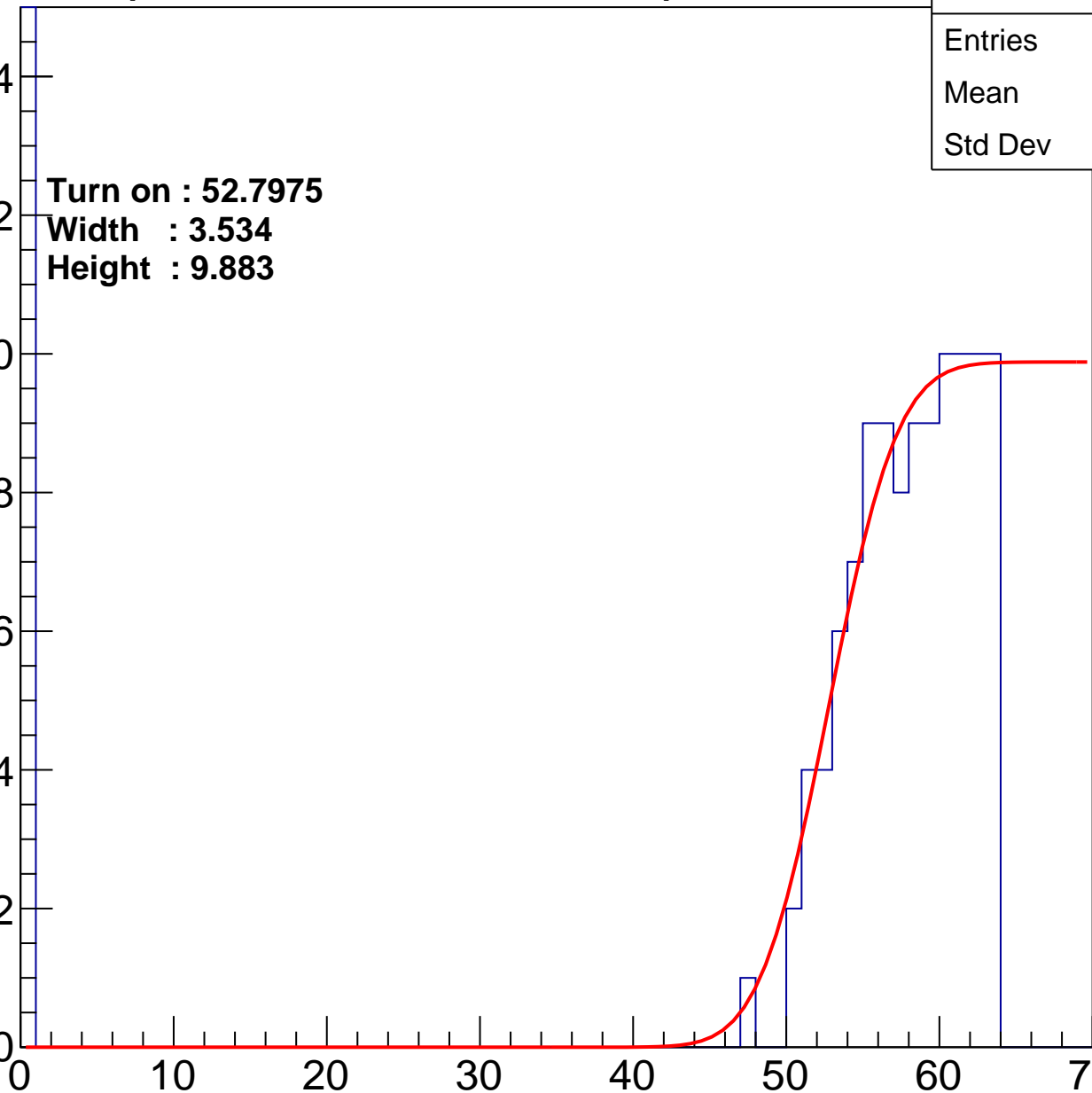
Width : 3.534

Height : 9.883

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	31.3
Std Dev	29.22

Turn on : 55.3389

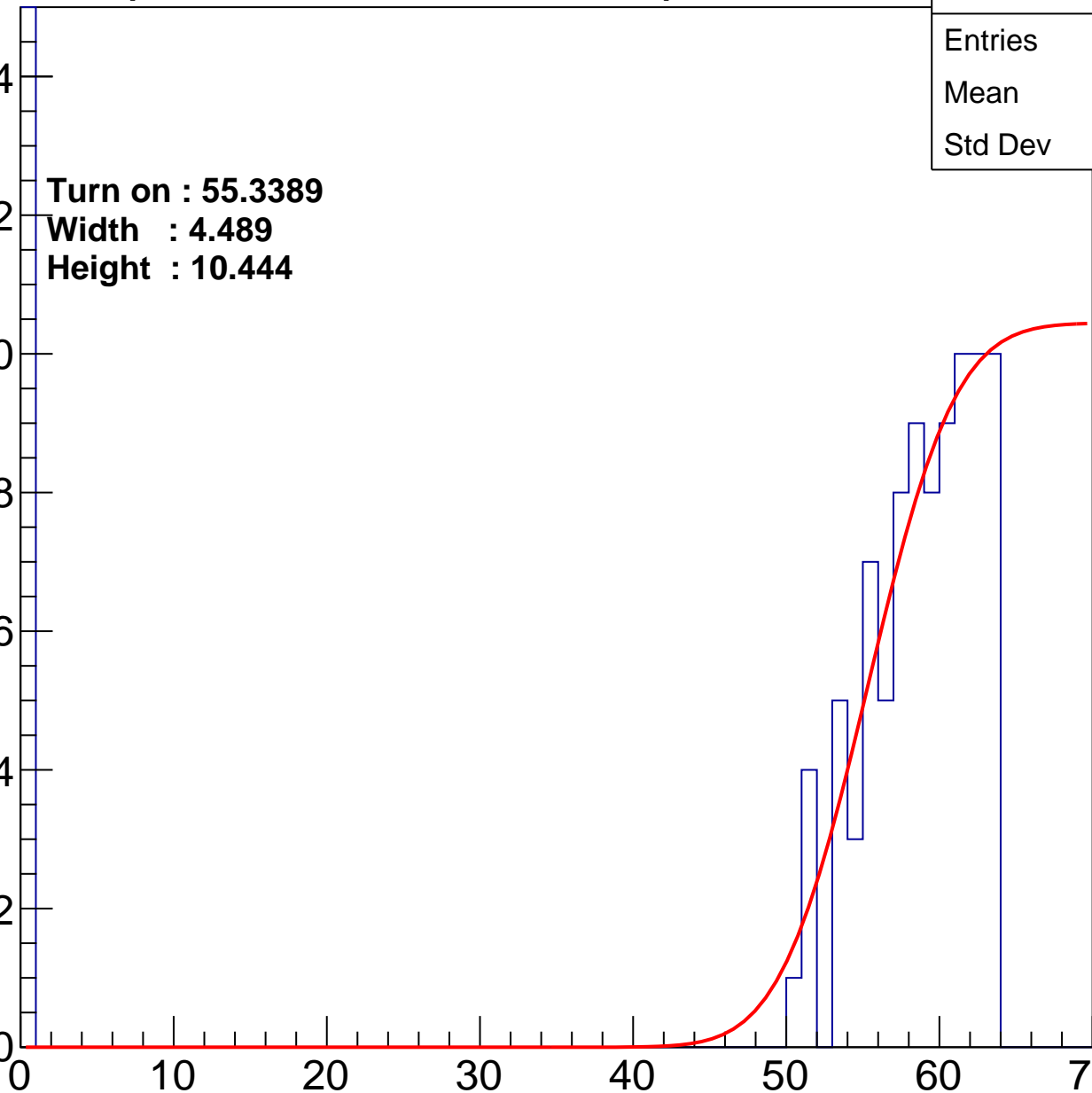
Width : 4.489

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch112

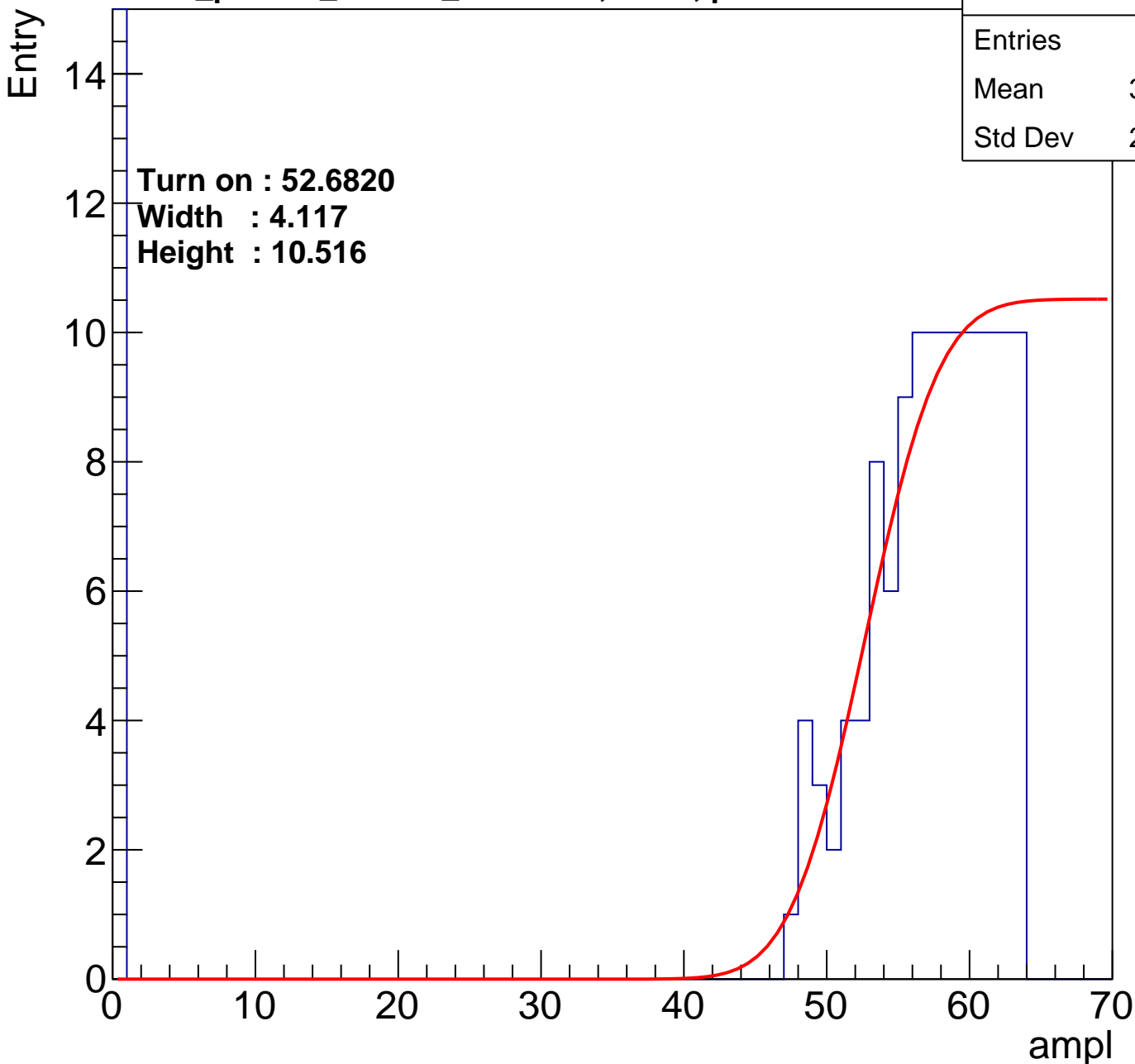
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	38.34
Std Dev	26.99

Turn on : 52.6820

Width : 4.117

Height : 10.516



B1L104S, U25-ch113

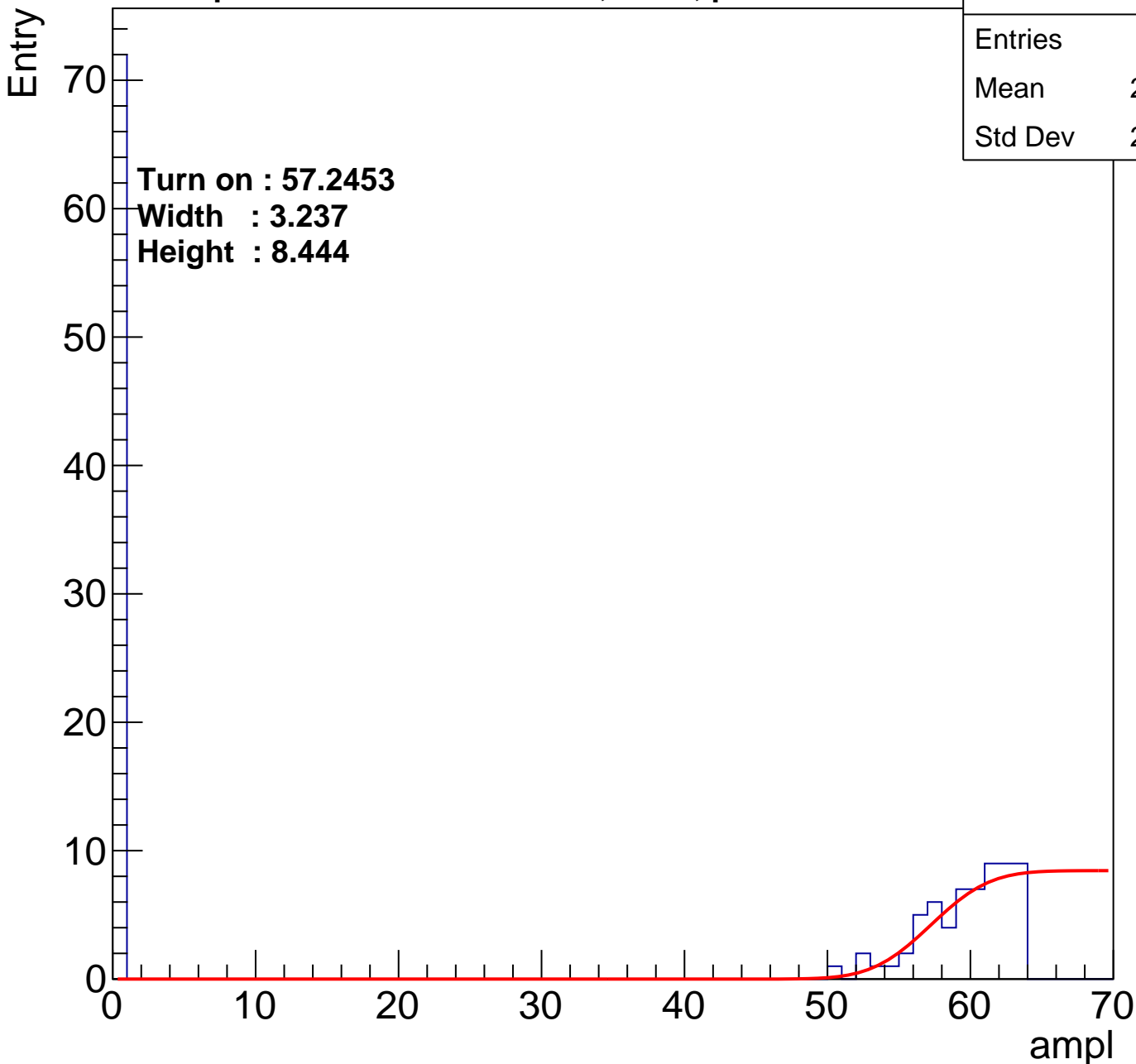
calib_packv5_033123_0516.root, FC#4, port A1

Entries	135
Mean	27.64
Std Dev	29.63

Turn on : 57.2453

Width : 3.237

Height : 8.444



B1L104S, U25-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	31.8
Std Dev	29

Turn on : 54.4045

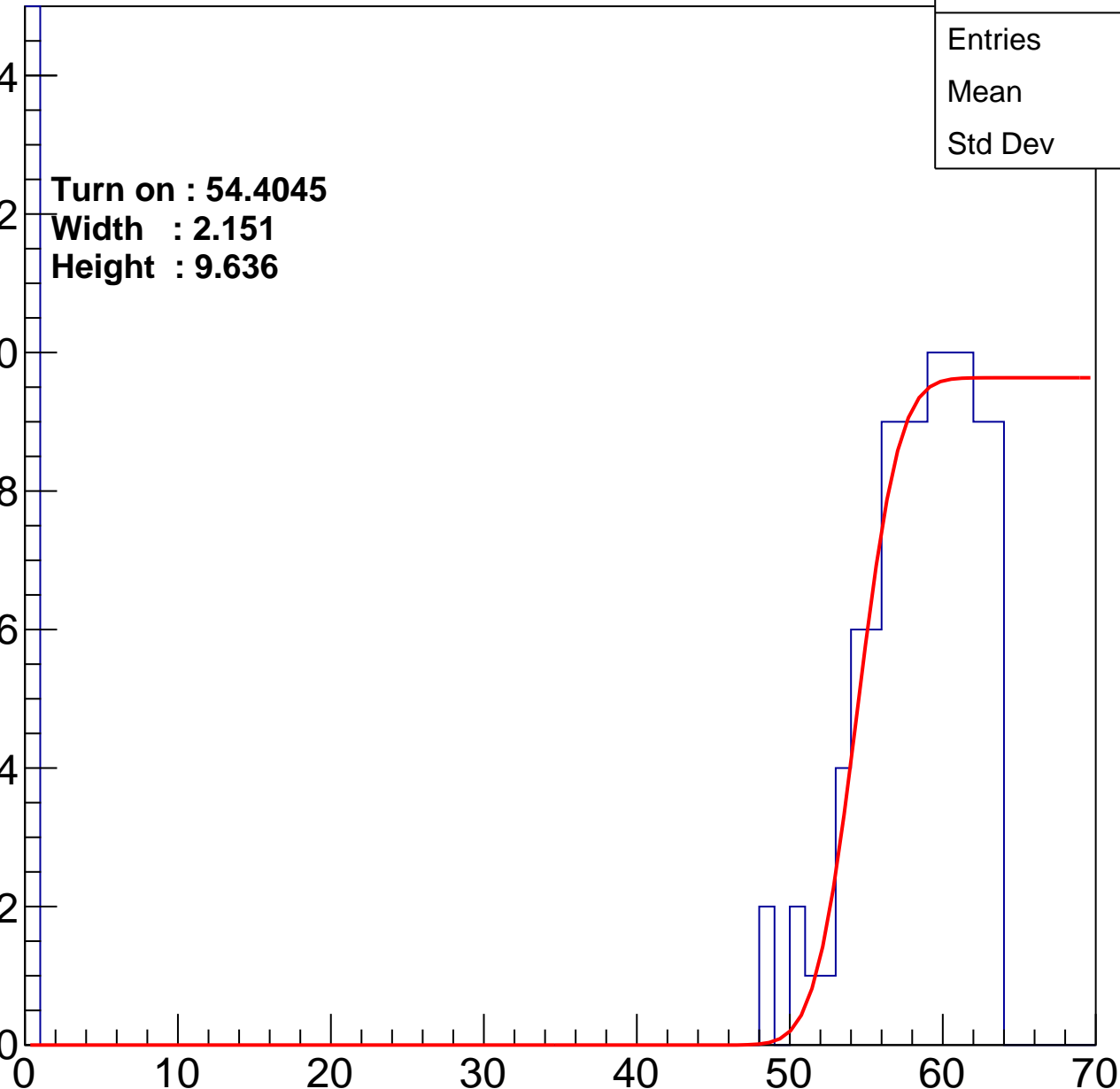
Width : 2.151

Height : 9.636

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	29.56
Std Dev	28.97

Turn on : 54.0500

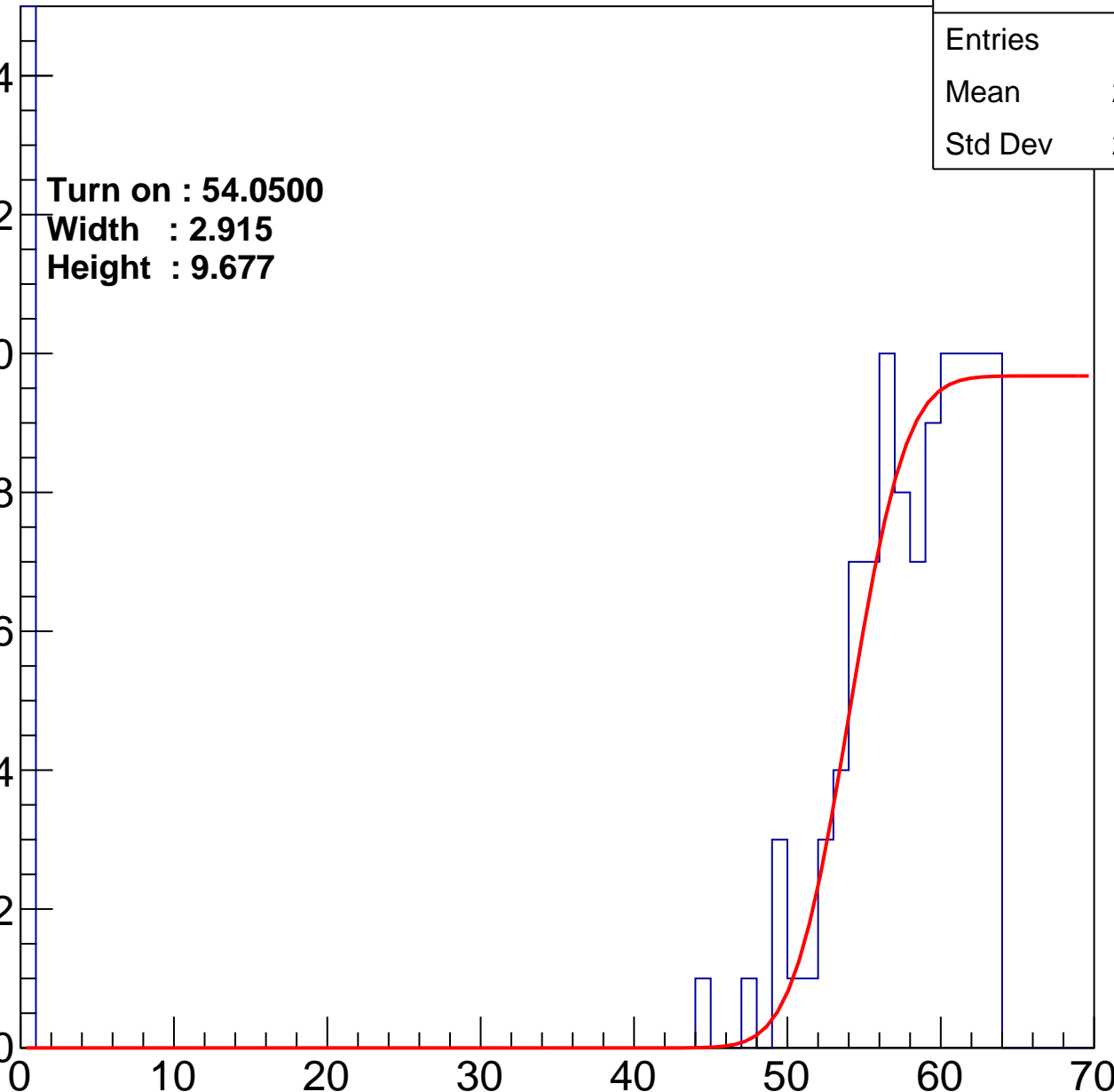
Width : 2.915

Height : 9.677

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	30.37
Std Dev	29.17

Turn on : 55.6757

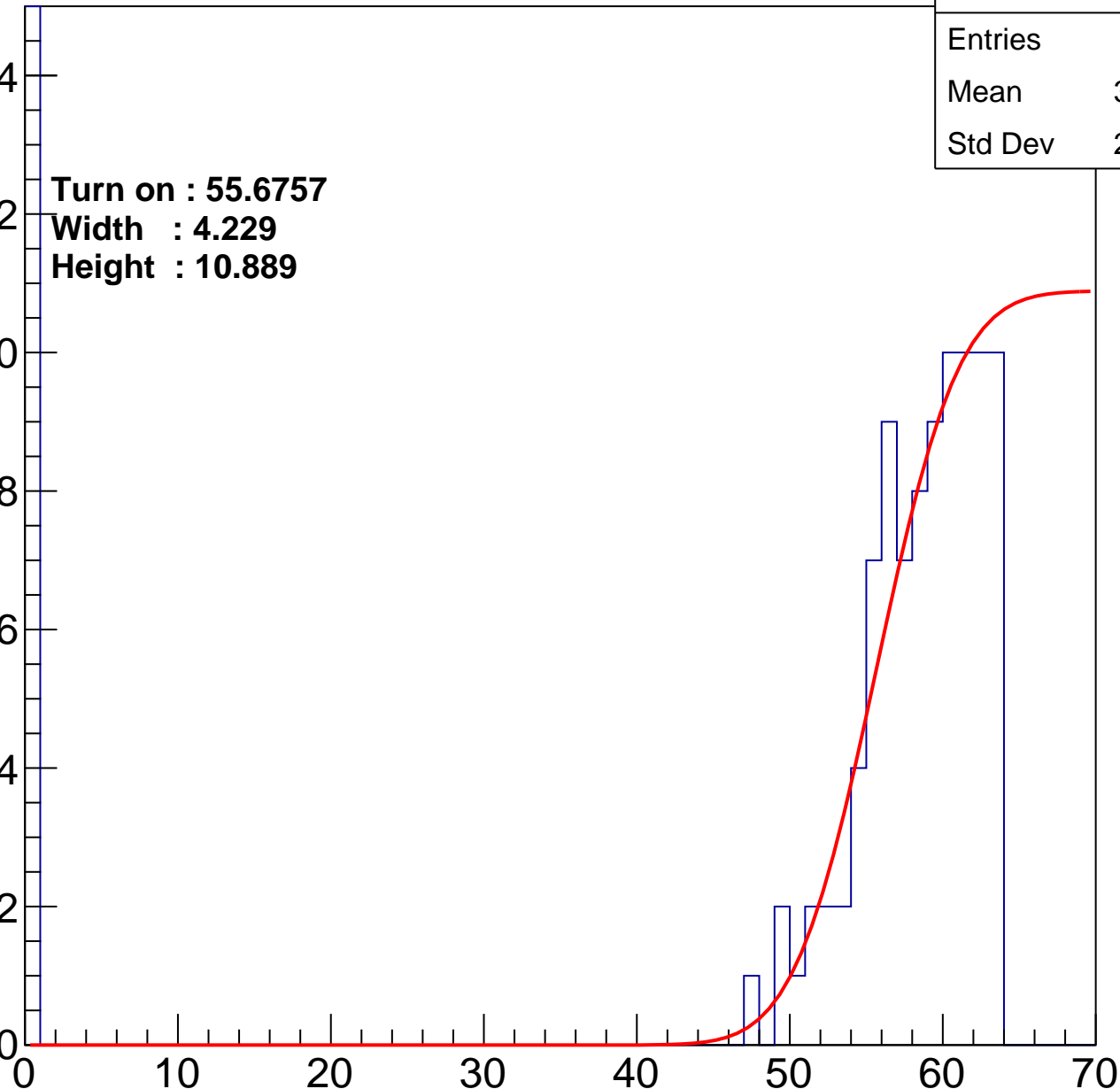
Width : 4.229

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	30.13
Std Dev	29.06

Turn on : 54.1718

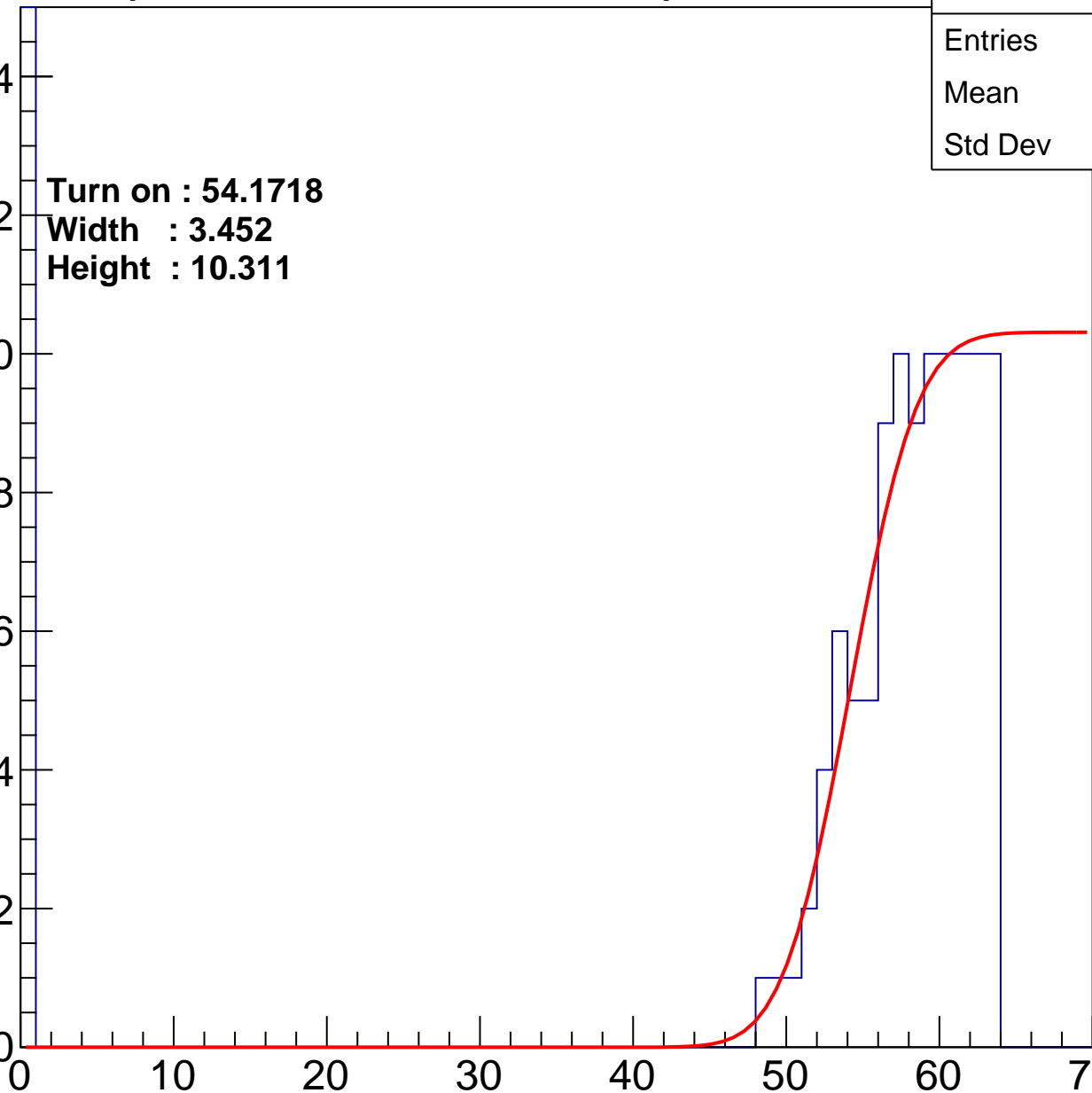
Width : 3.452

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.29
Std Dev	28.69

Turn on : 53.8107

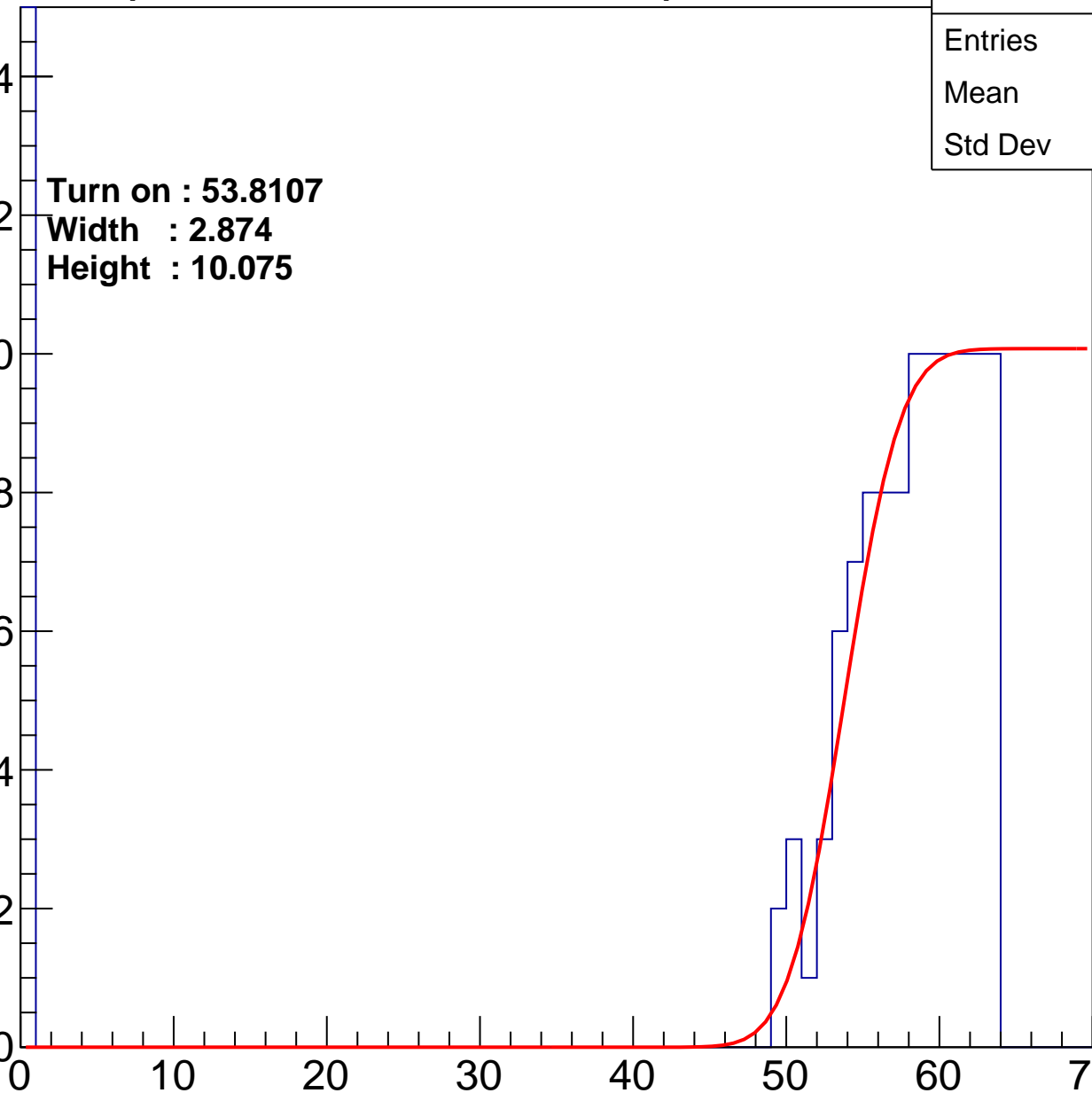
Width : 2.874

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	34.18
Std Dev	28.91

Turn on : 55.7995

Width : 4.492

Height : 10.111

Entry

60
50
40
30
20
10
0

ampl

0 10 20 30 40 50 60 70

B1L104S, U25-ch120

calib_packv5_033123_0516.root, FC#4, port A1

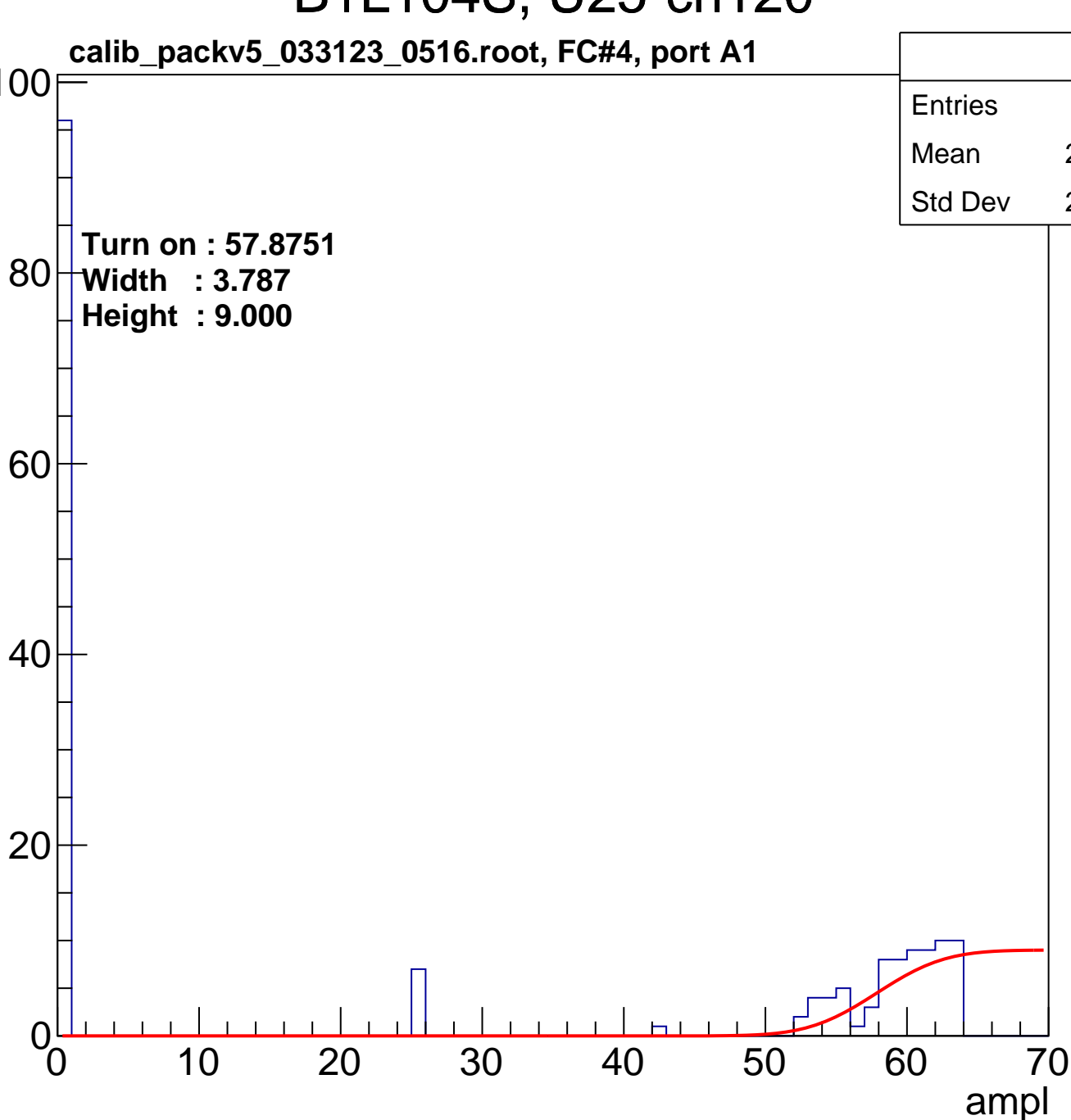
Entry

Entries	177
Mean	25.57
Std Dev	28.67

Turn on : 57.8751

Width : 3.787

Height : 9.000



B1L104S, U25-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	33.57
Std Dev	29.02

Turn on : 55.6780

Width : 4.790

Height : 10.667

Entry

14

12

10

8

6

4

2

0

0

10

20

30

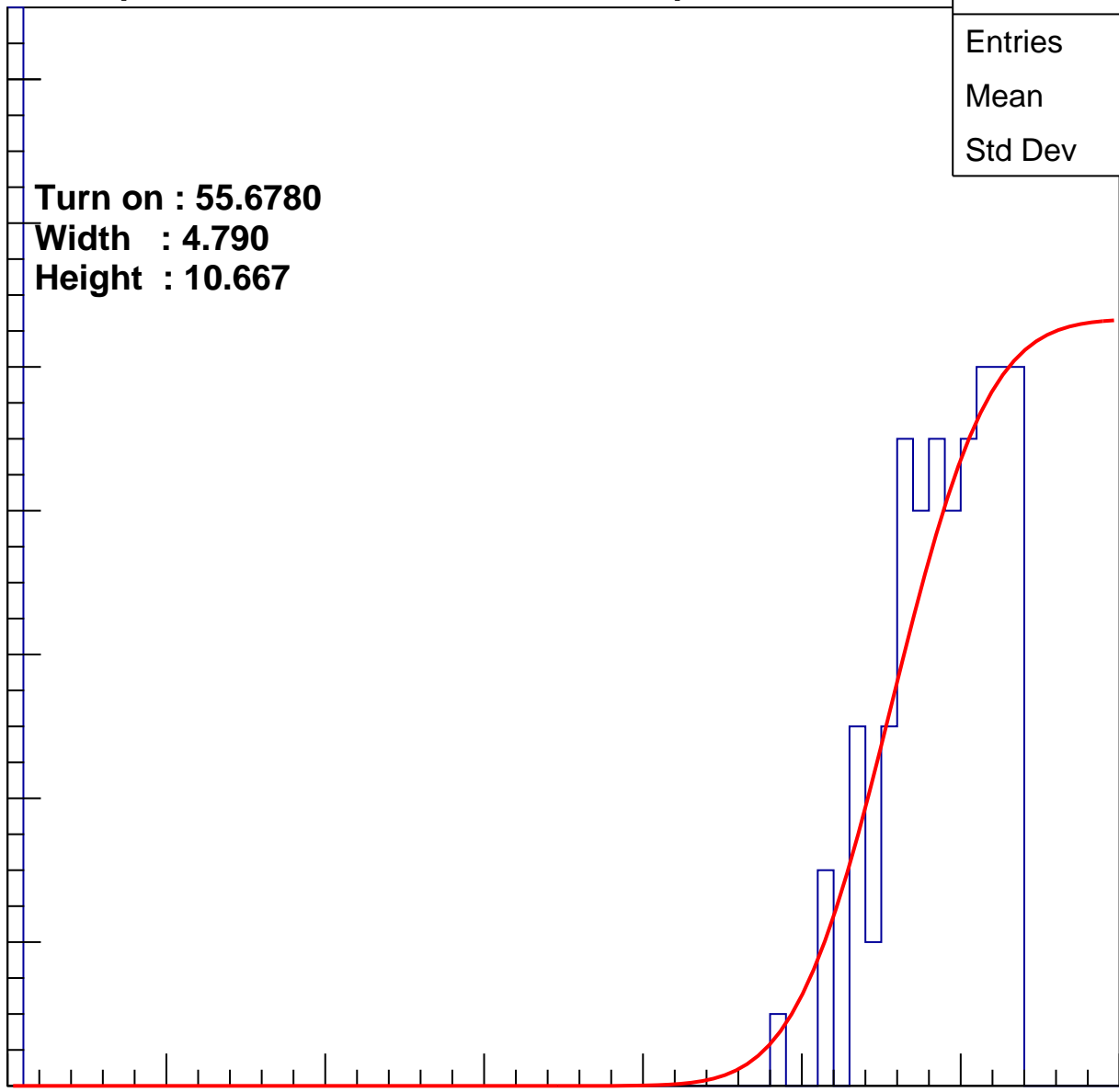
40

50

60

70

ampl



B1L104S, U25-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	145
Mean	33.04
Std Dev	29.08

Turn on : 56.1051

Width : 3.750

Height : 9.556

Entry

60

50

40

30

20

10

0

0

10

20

30

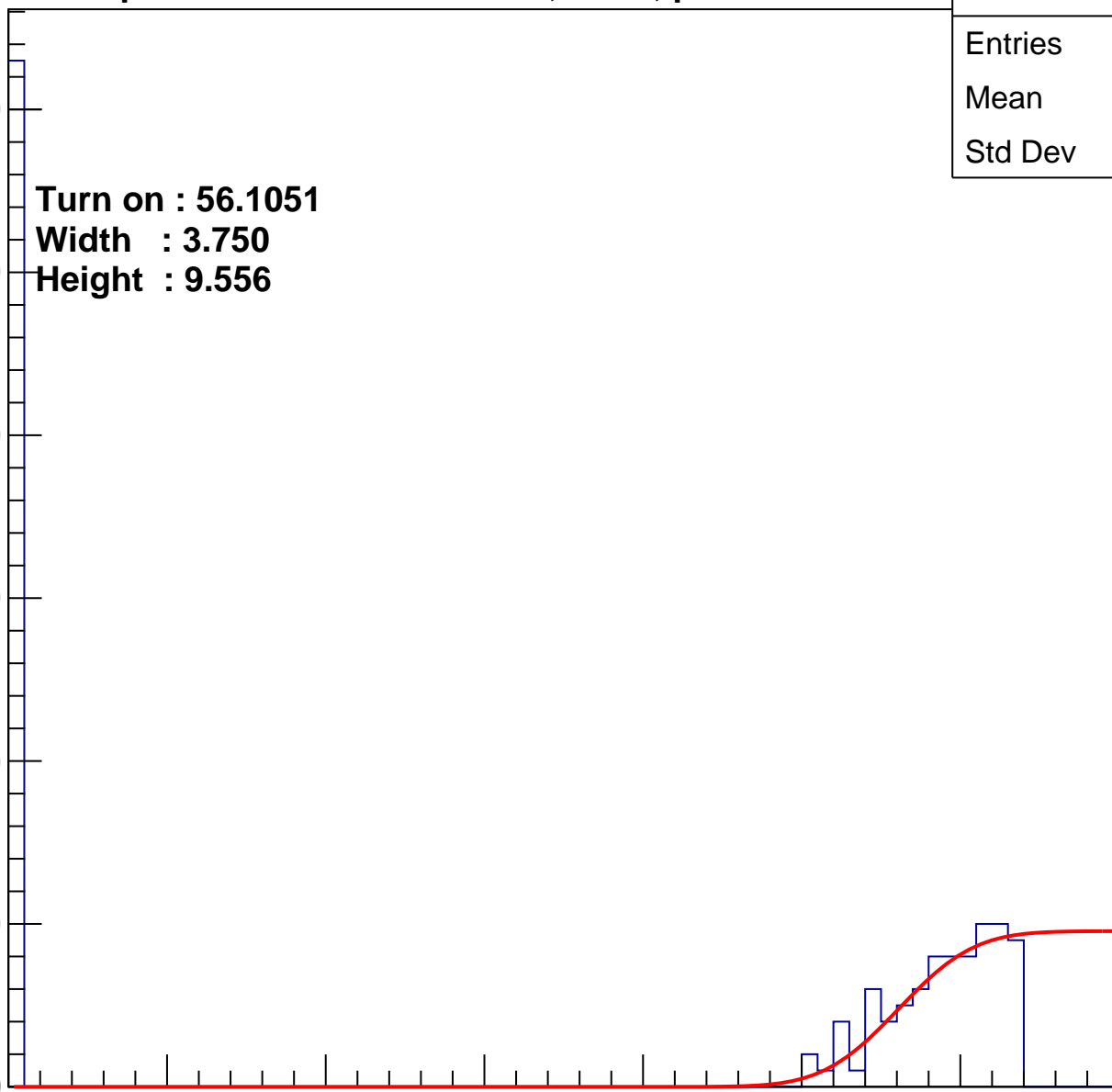
40

50

60

70

ampl



B1L104S, U25-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	34.6
Std Dev	28.47

Turn on : 53.6930

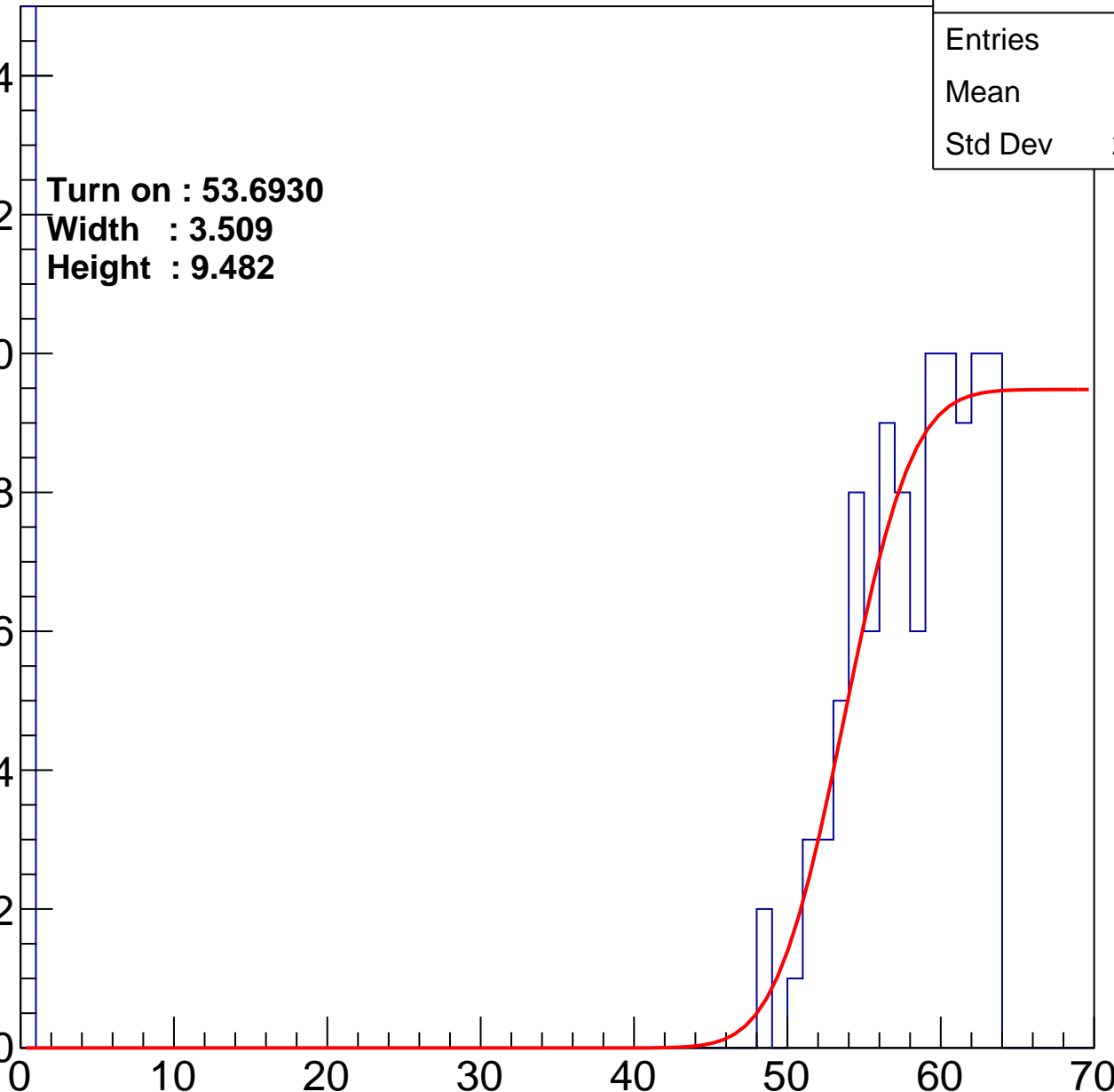
Width : 3.509

Height : 9.482

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	32.81
Std Dev	28.88

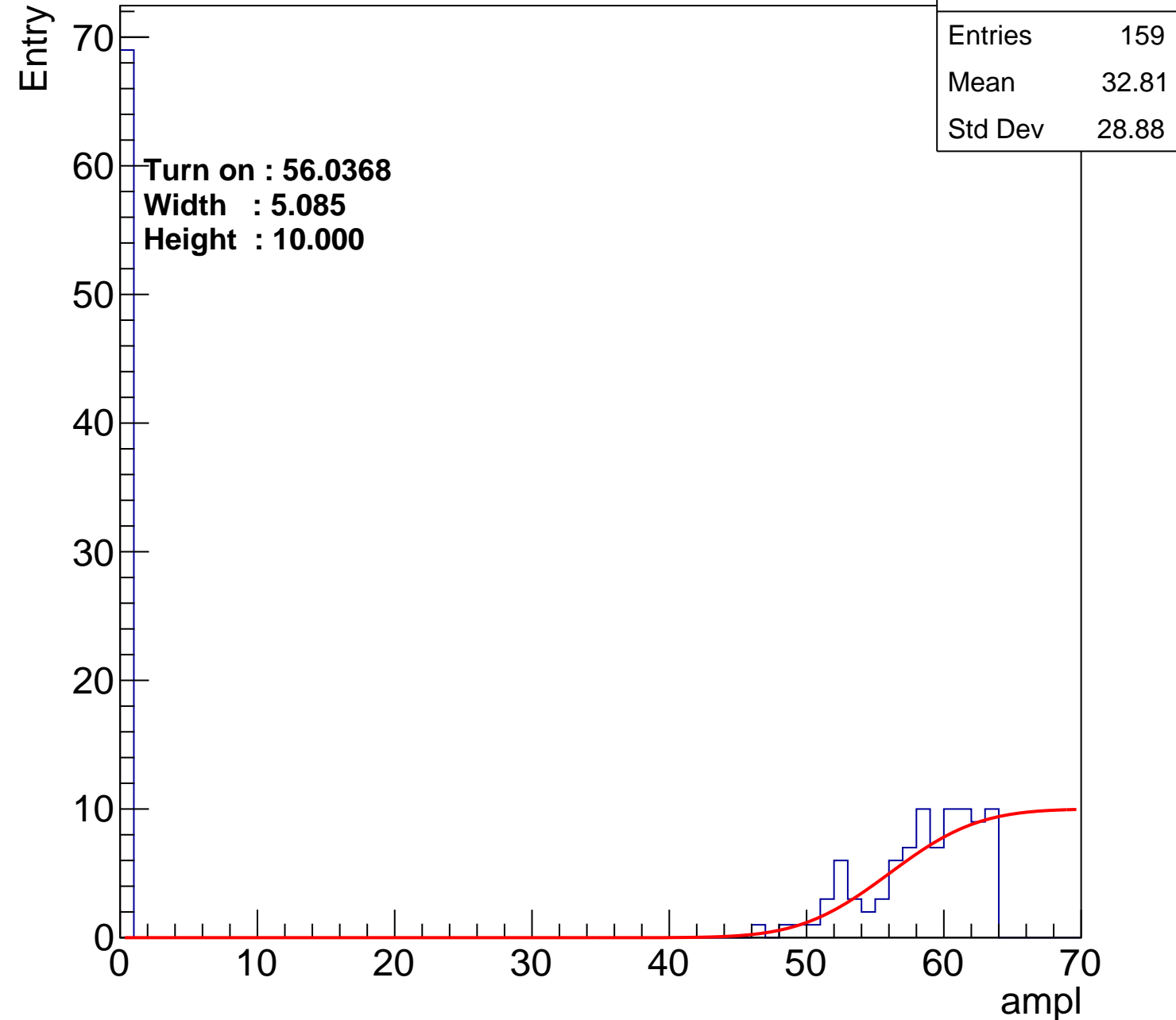
Entry

70
60
50
40
30
20
10
0

Turn on : 56.0368
Width : 5.085
Height : 10.000

ampl

0 10 20 30 40 50 60 70



B1L104S, U25-ch125

calib_packv5_033123_0516.root, FC#4, port A1

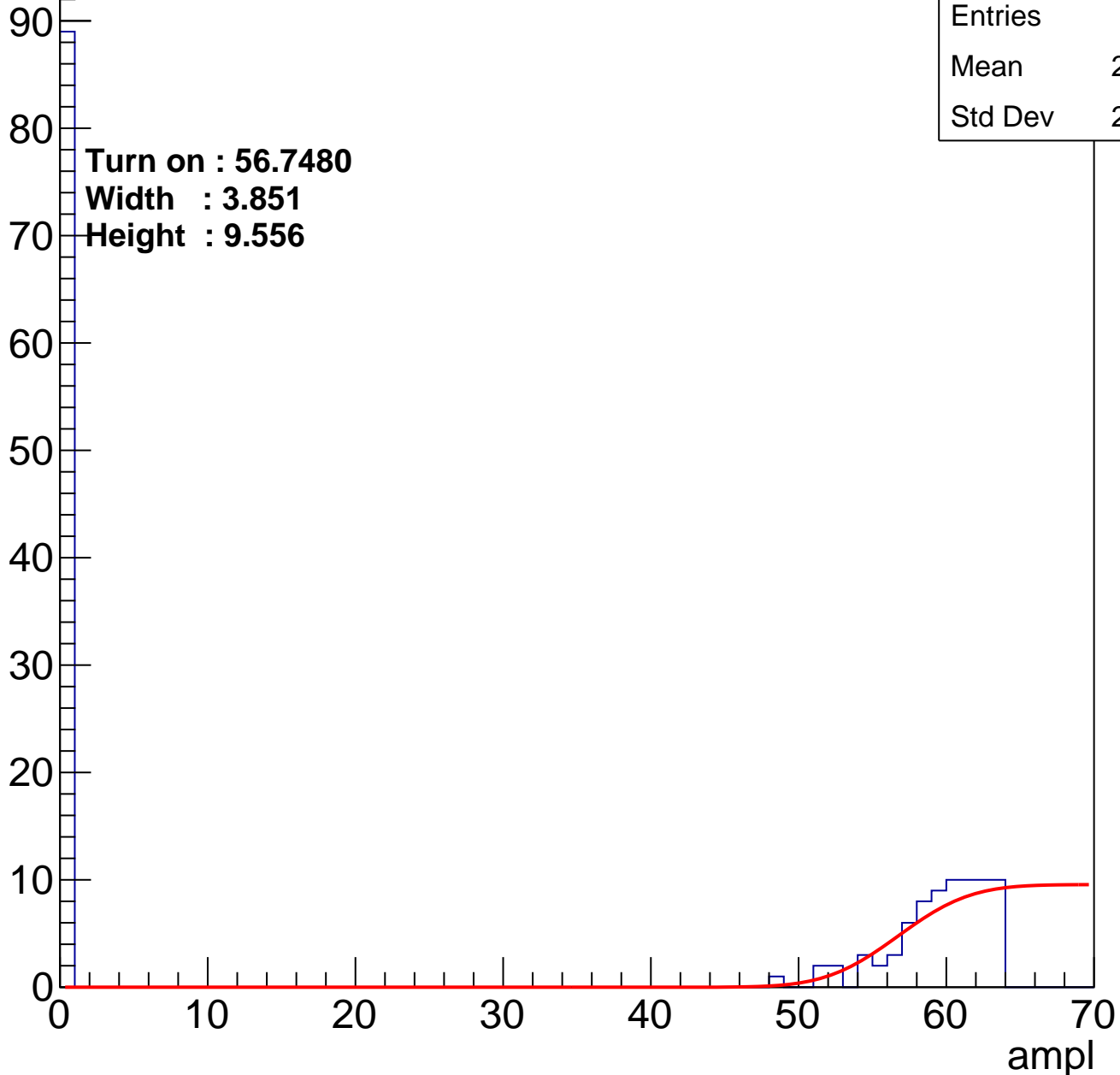
Entries	165
Mean	27.22
Std Dev	29.54

Turn on : 56.7480

Width : 3.851

Height : 9.556

Entry



B1L104S, U25-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.12
Std Dev	28.85

Turn on : 54.9528

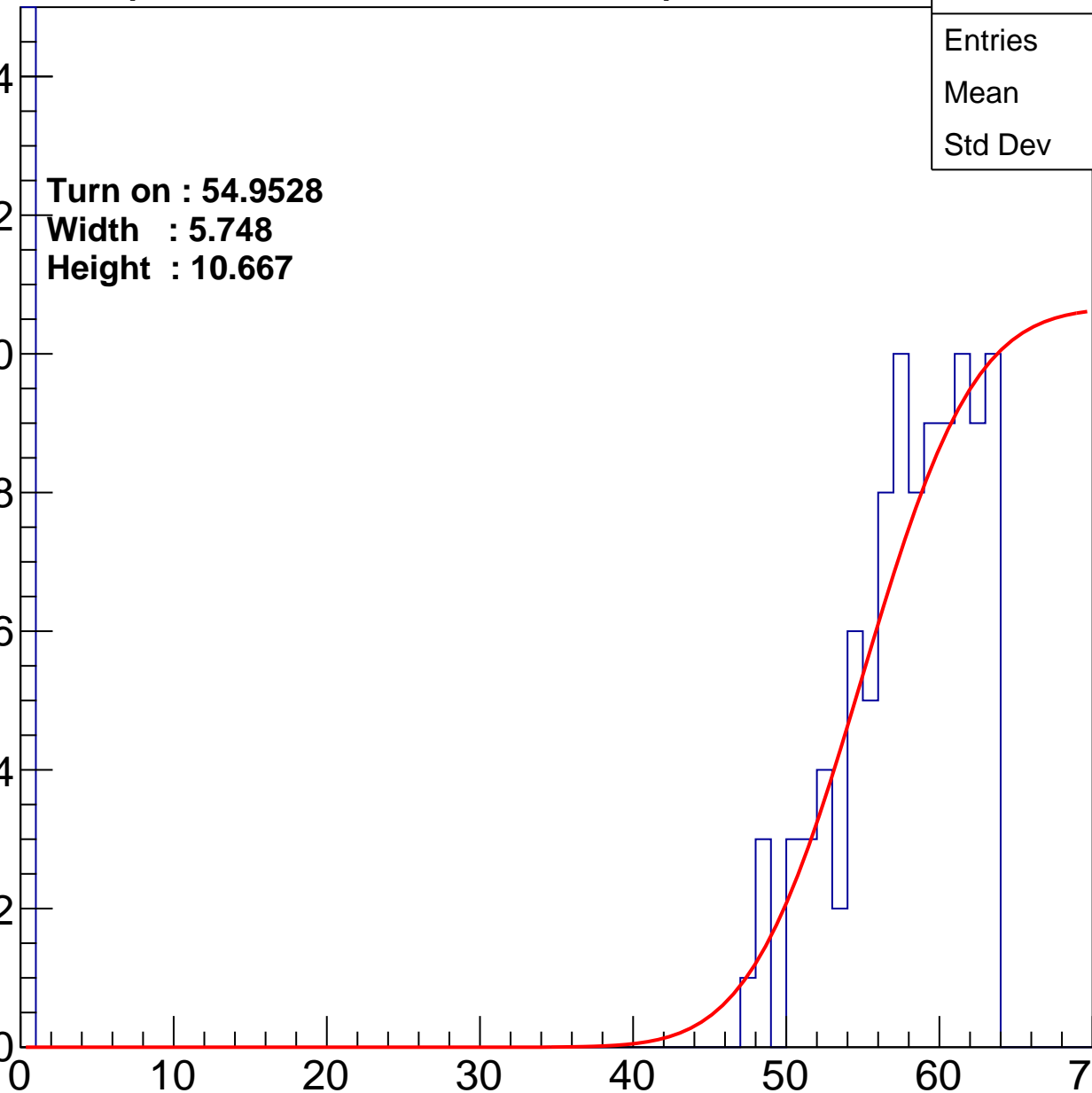
Width : 5.748

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	26.52
Std Dev	29.13

Turn on : 56.8853

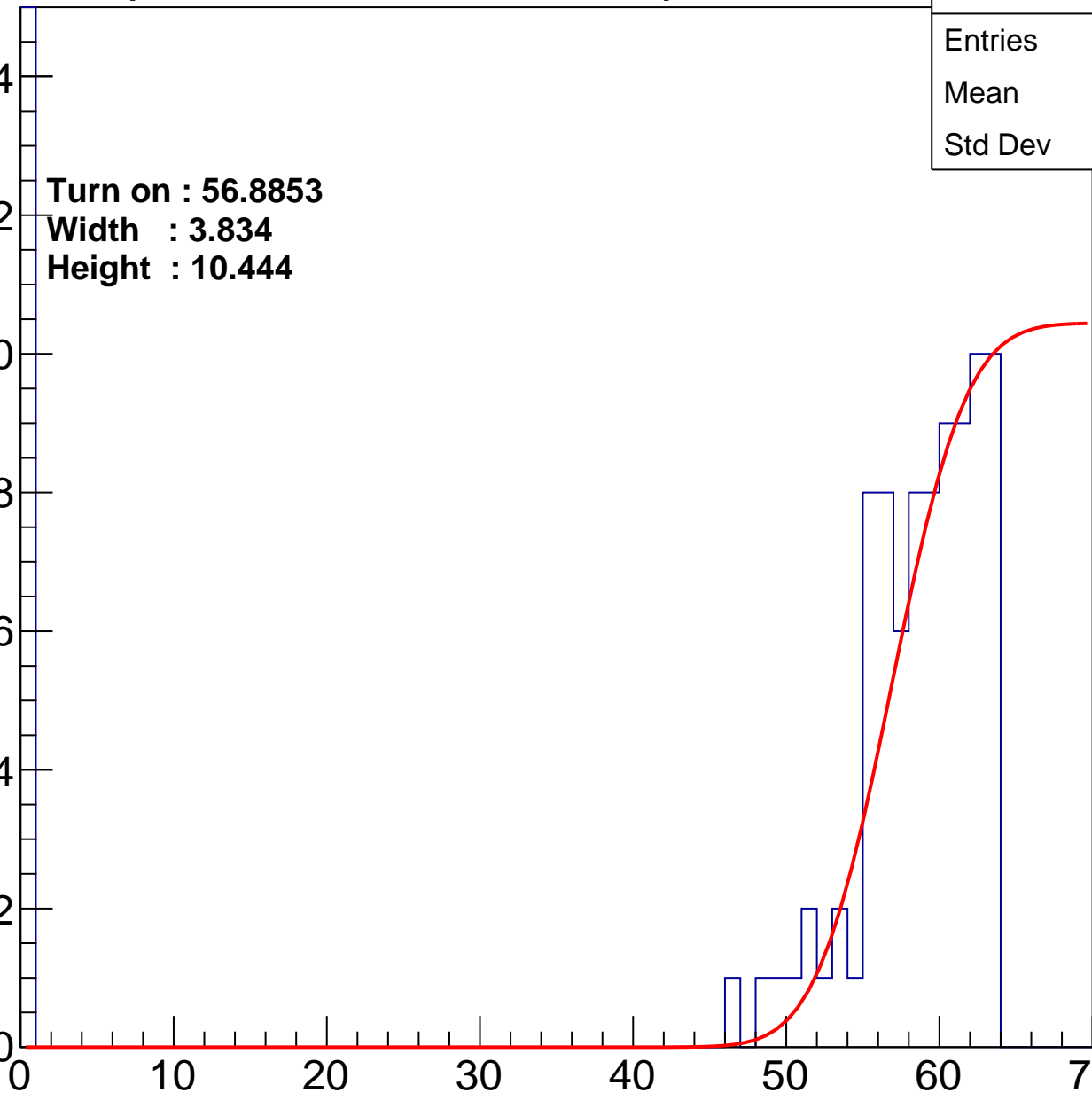
Width : 3.834

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U25-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	26.52
Std Dev	29.13

Turn on : 56.8853

Width : 3.834

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl

