

B0L002S, U21-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.3318

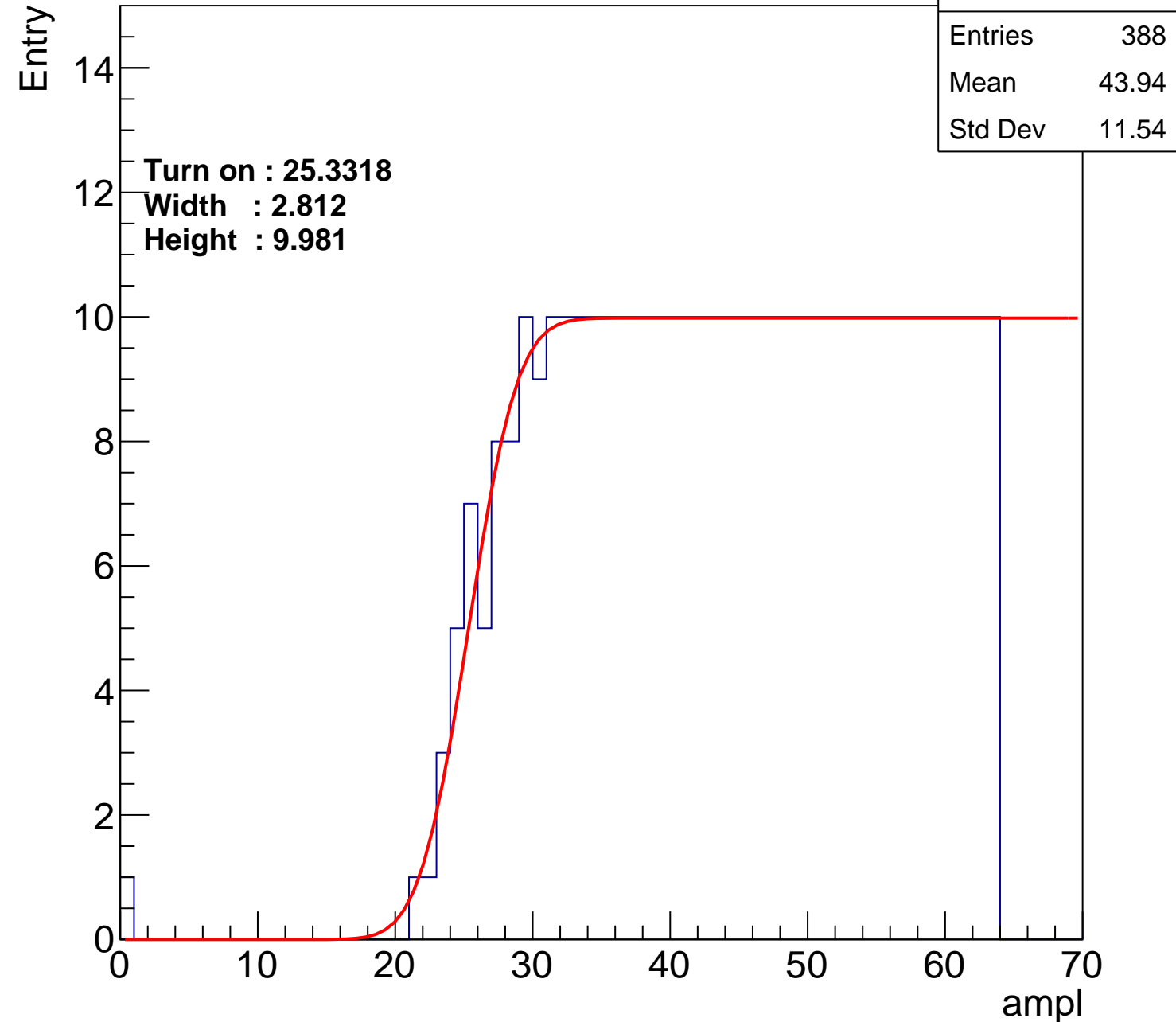
Width : 2.812

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch1

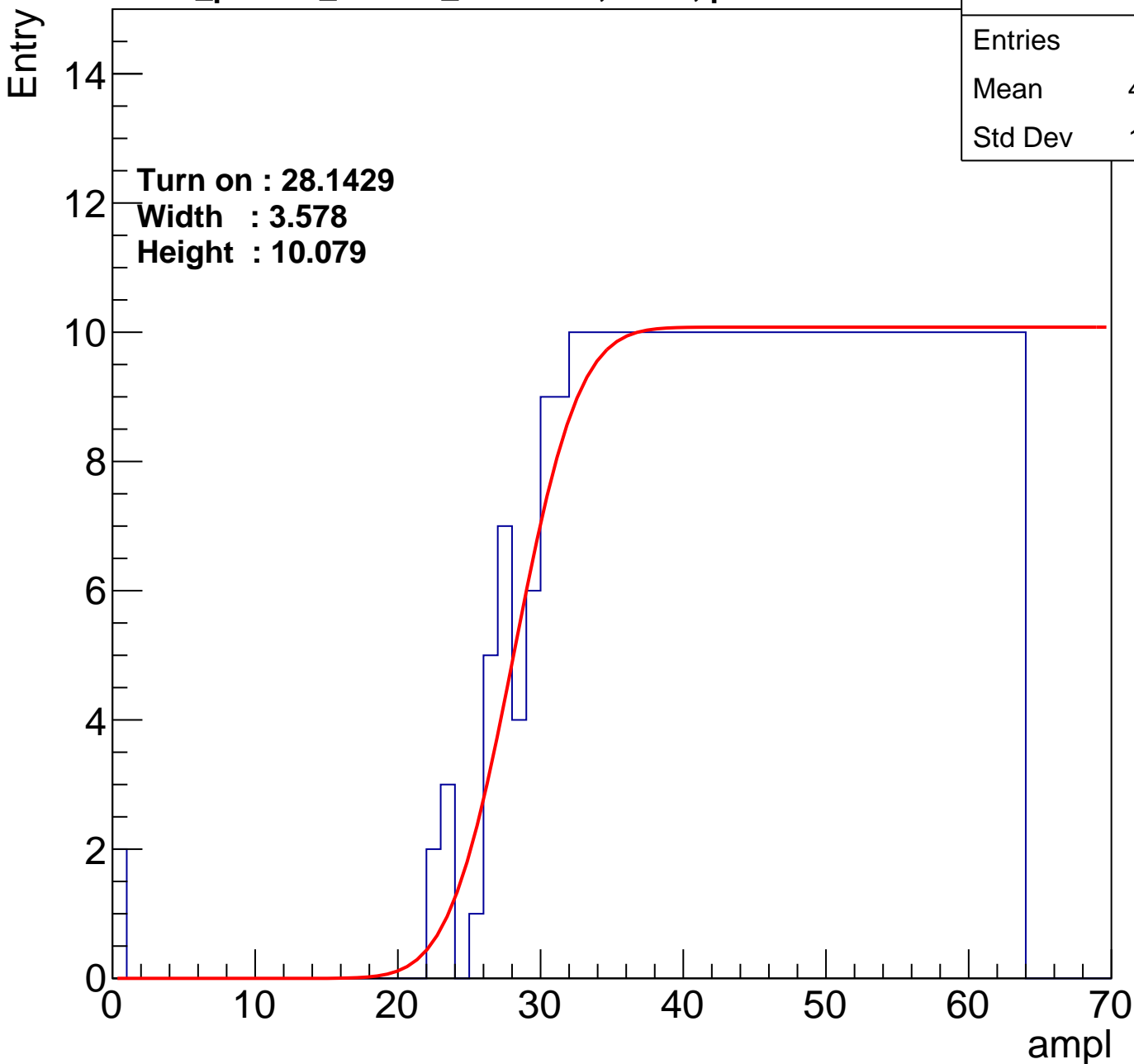
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 28.1429

Width : 3.578

Height : 10.079



B0L002S, U21-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	43.98
Std Dev	11.94

Turn on : 25.2504

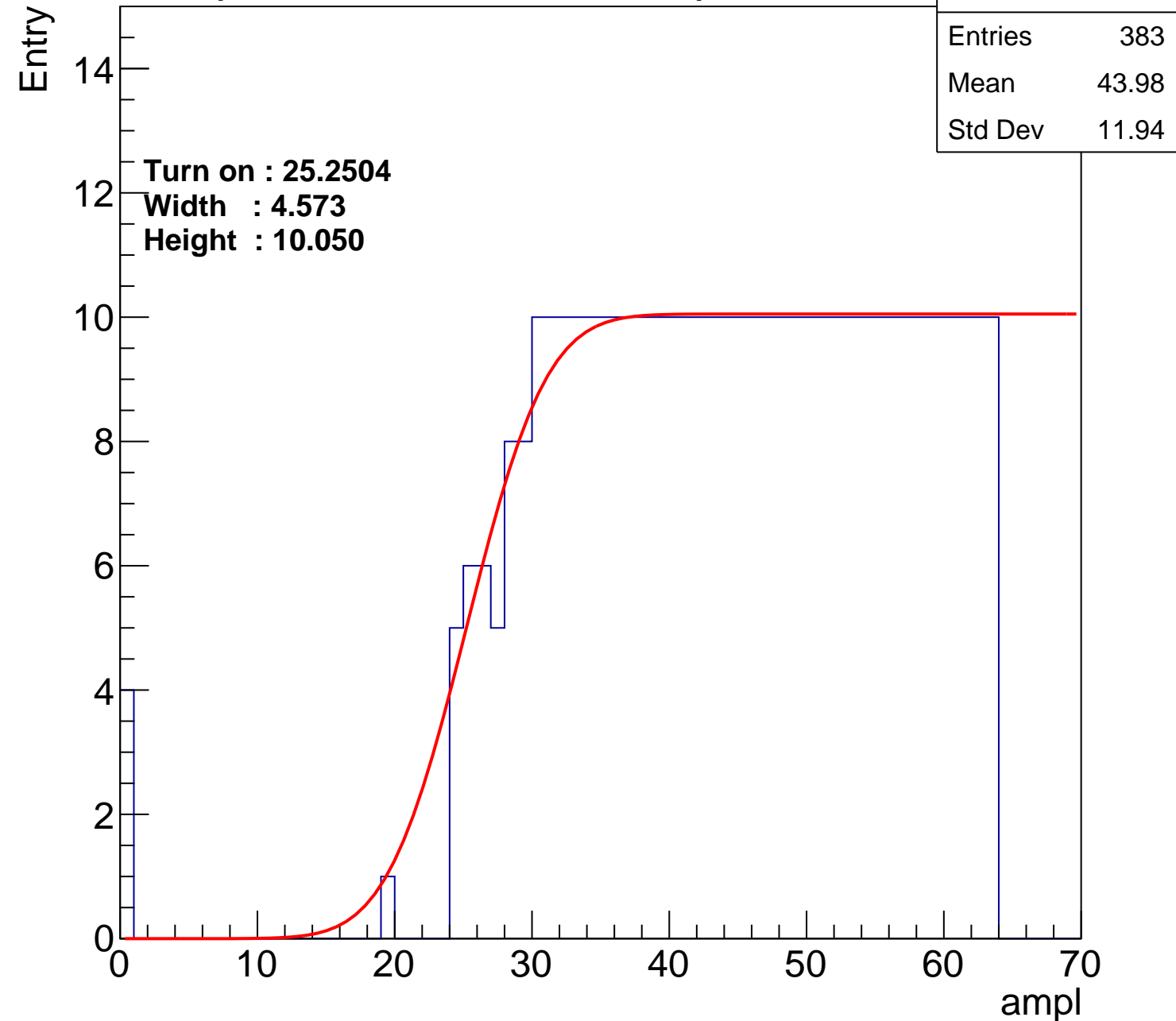
Width : 4.573

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch3

calib_packv5_042523_0143.root, FC#8, port C1

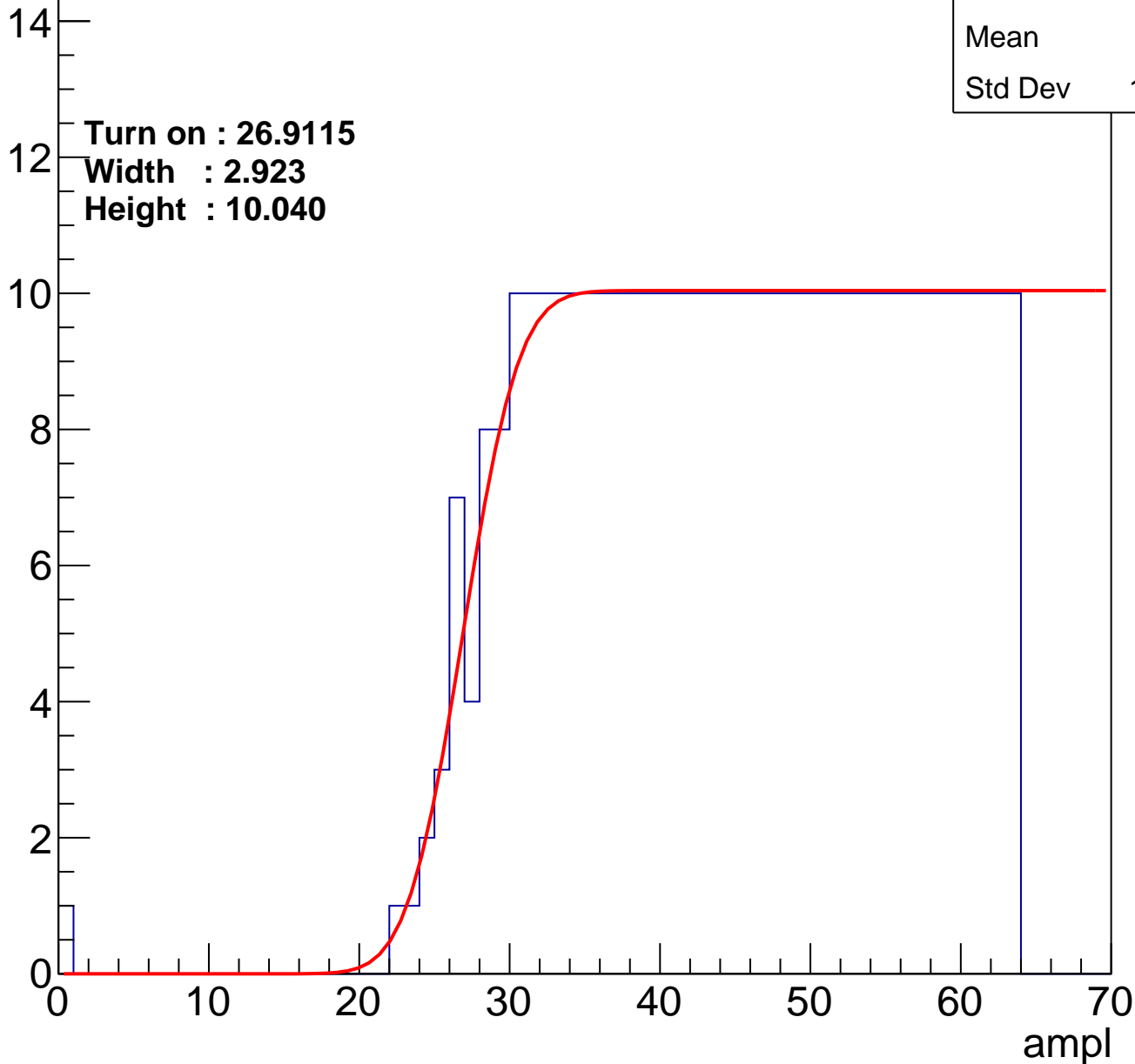
Entry

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.9115

Width : 2.923

Height : 10.040



B0L002S, U21-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.6857

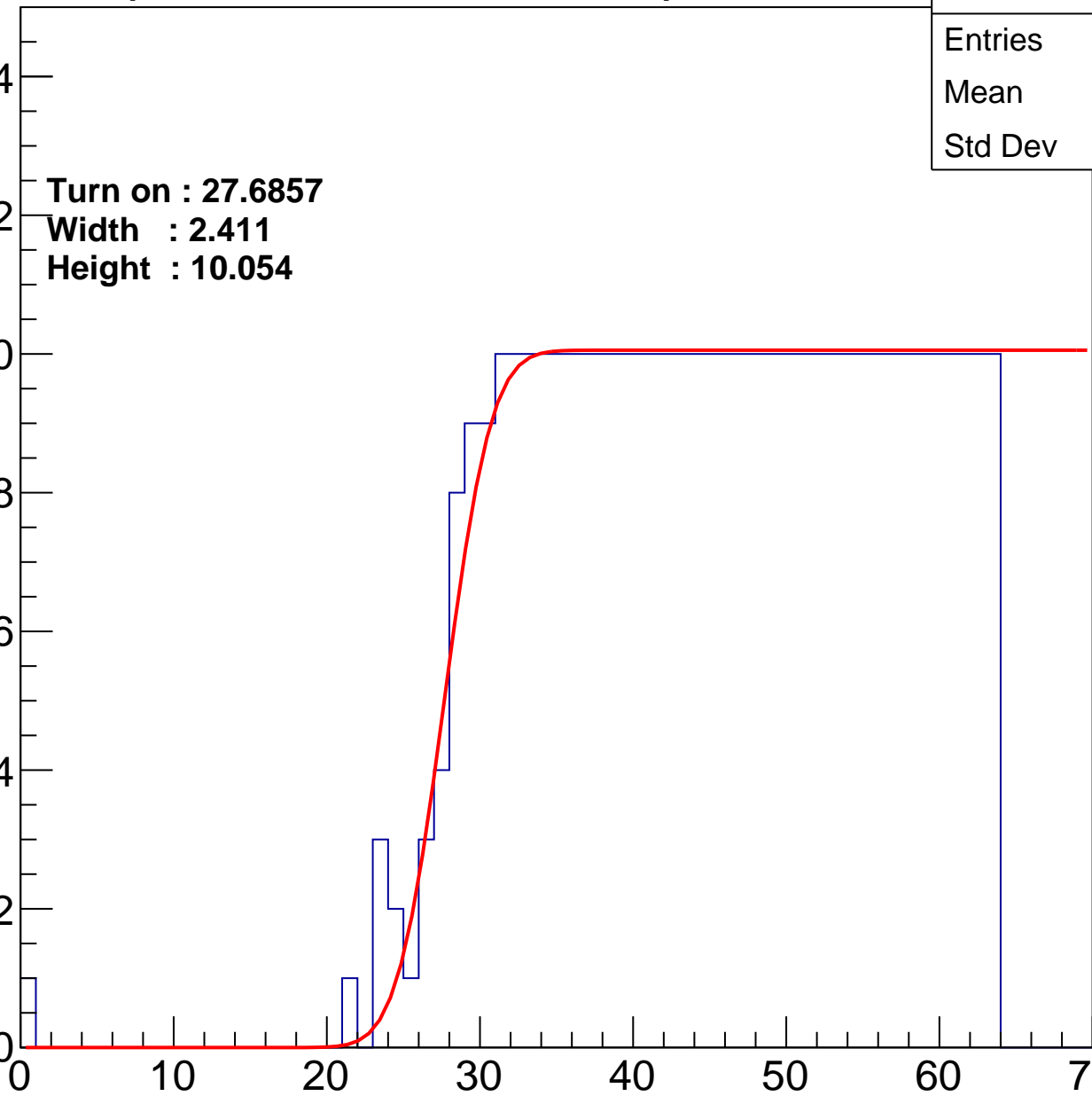
Width : 2.411

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.42
Std Dev	10.72

Turn on : 28.0763

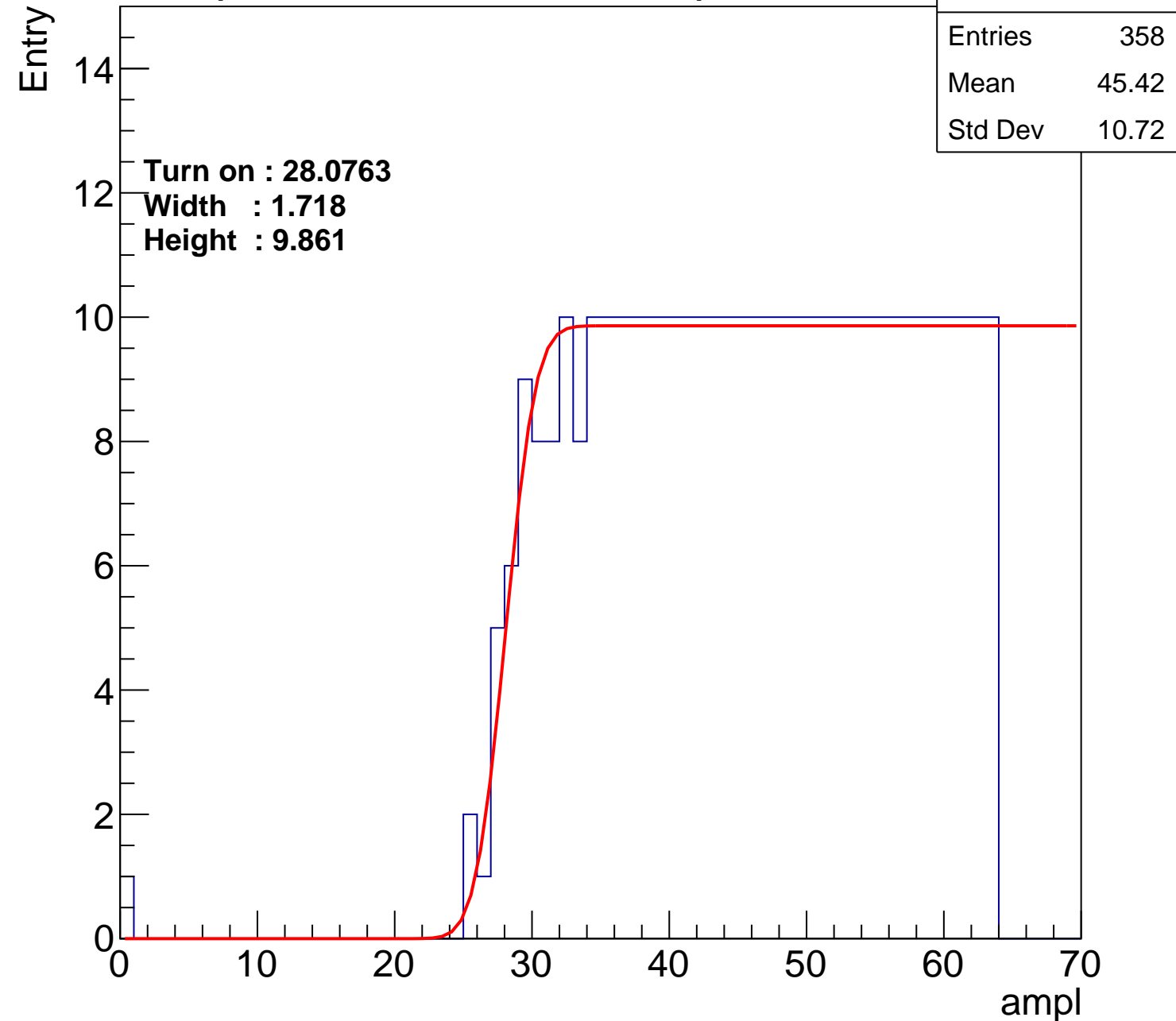
Width : 1.718

Height : 9.861

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.59
Std Dev	11.42

Turn on : 26.5739

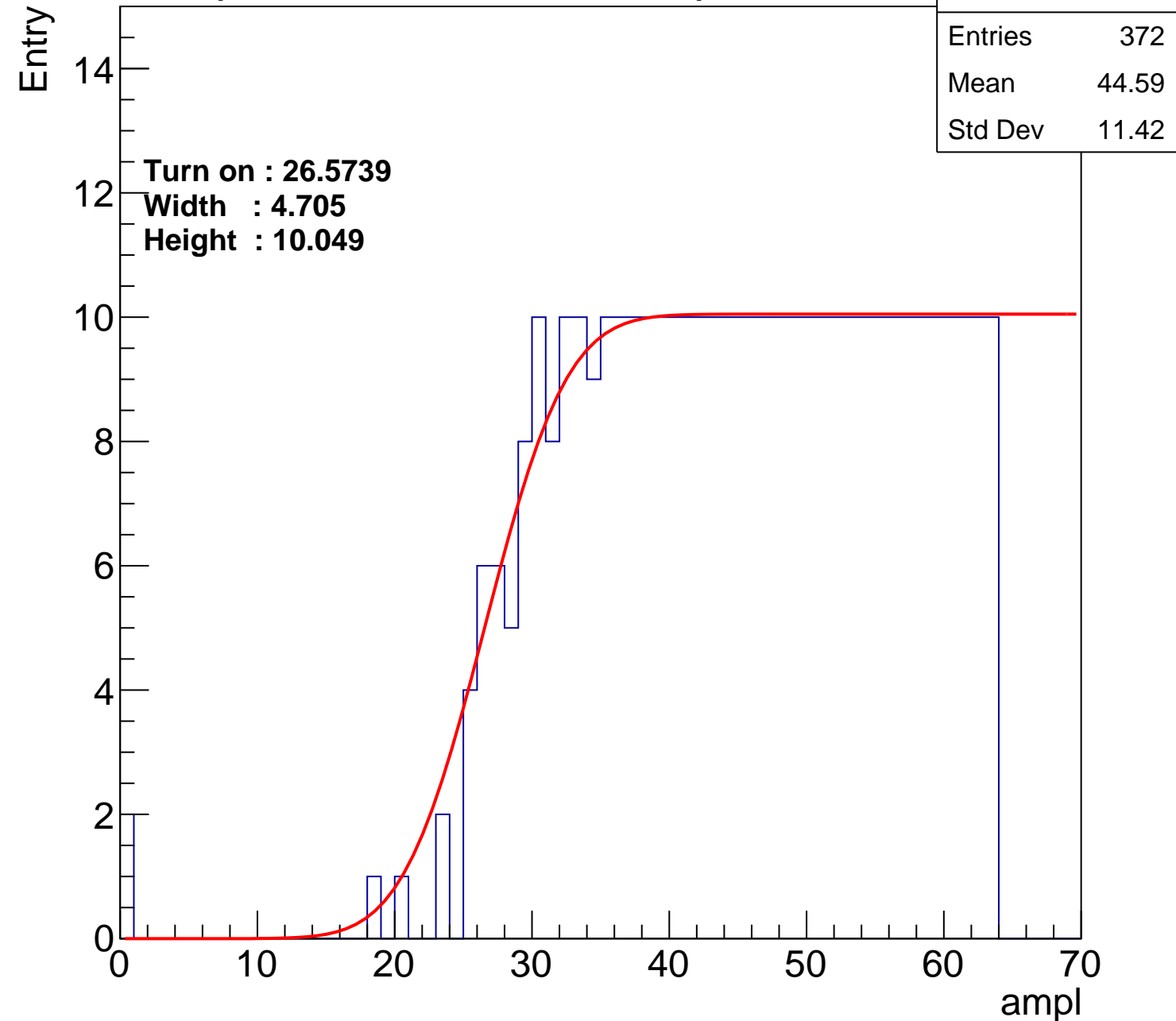
Width : 4.705

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.92
Std Dev	10.46

Turn on : 29.3011

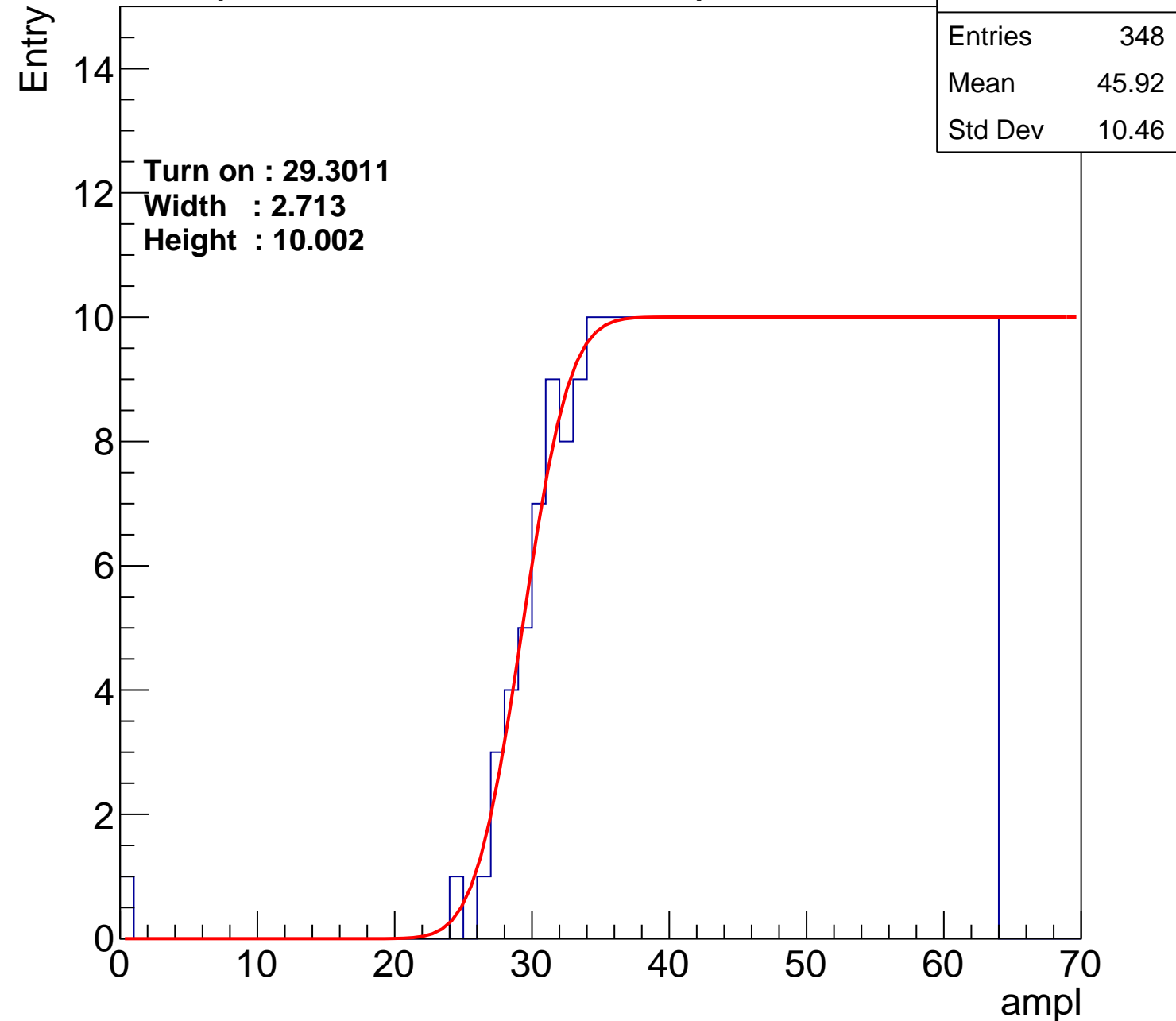
Width : 2.713

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.66
Std Dev	10.84

Turn on : 29.4682

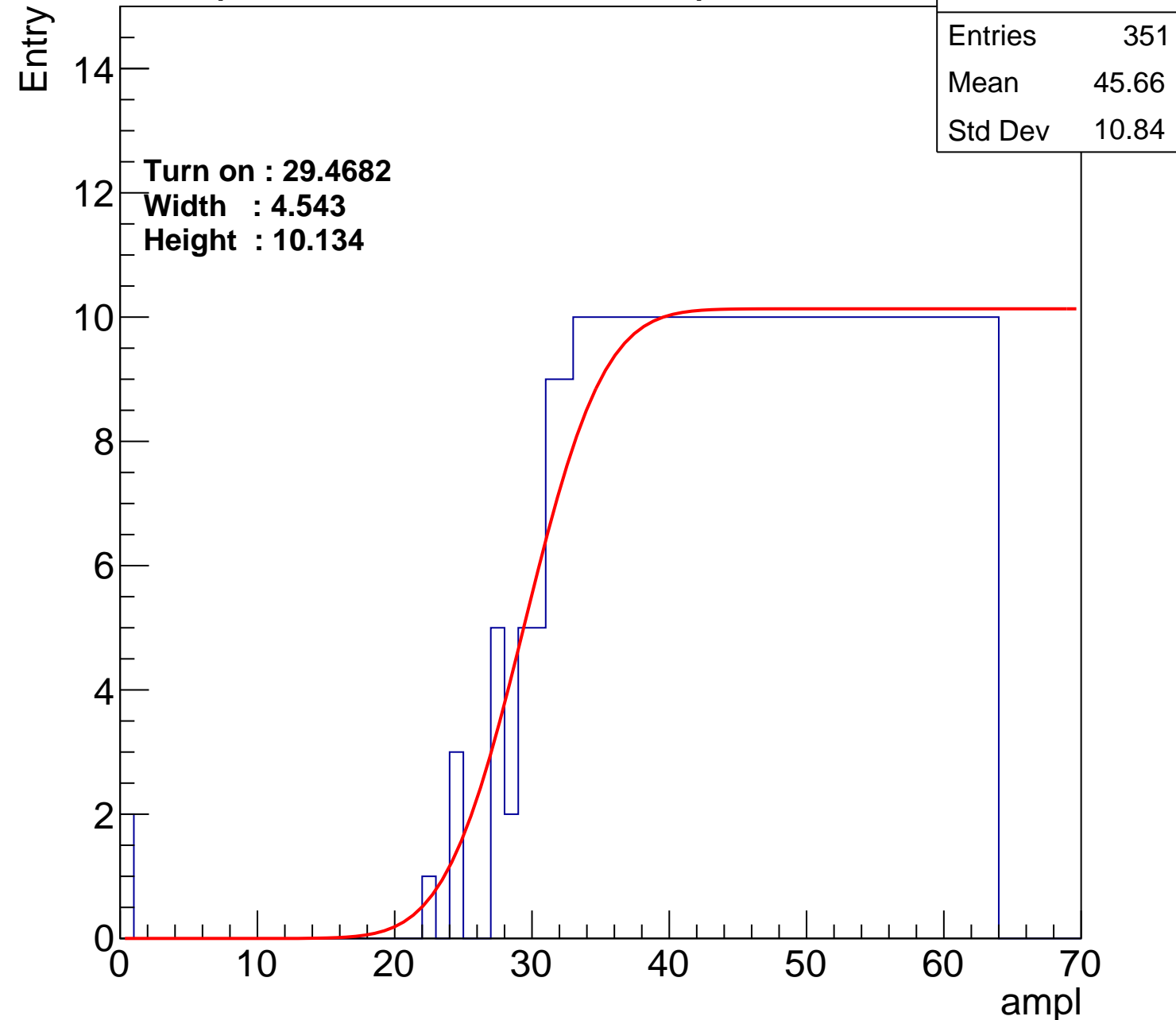
Width : 4.543

Height : 10.134

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 29.0175

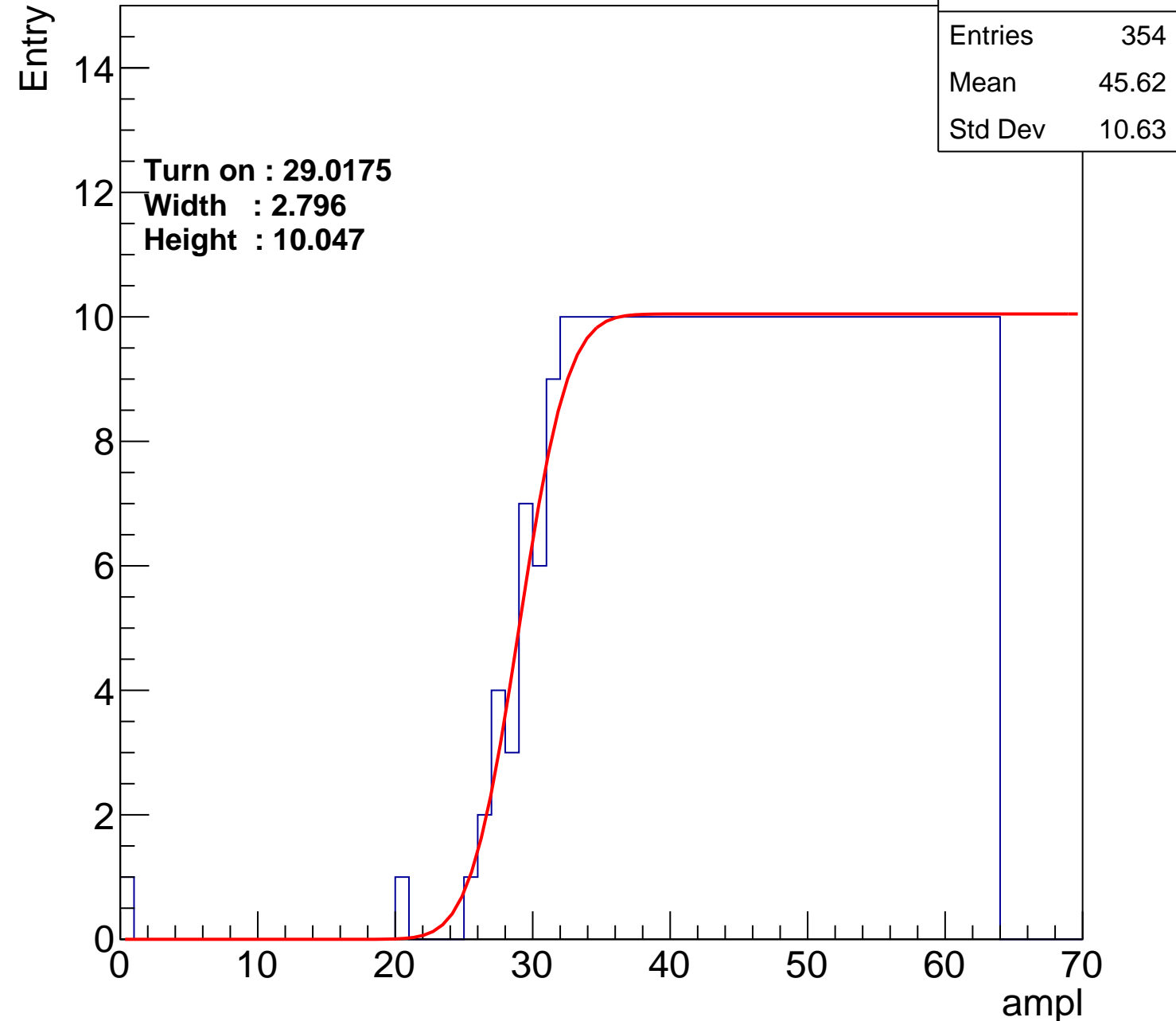
Width : 2.796

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.32
Std Dev	10.78

Turn on : 27.9515

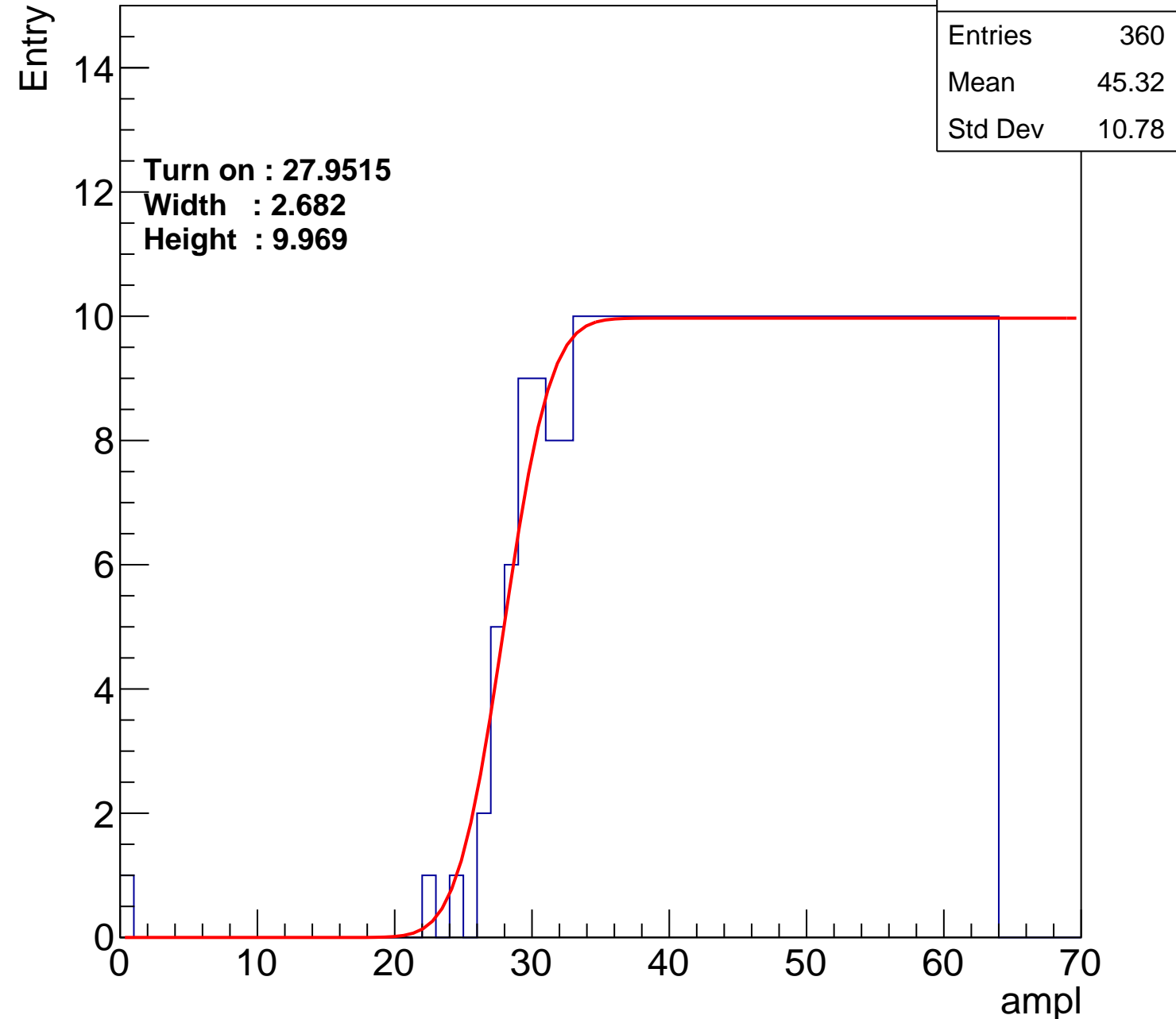
Width : 2.682

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.93
Std Dev	11.19

Turn on : 27.8276

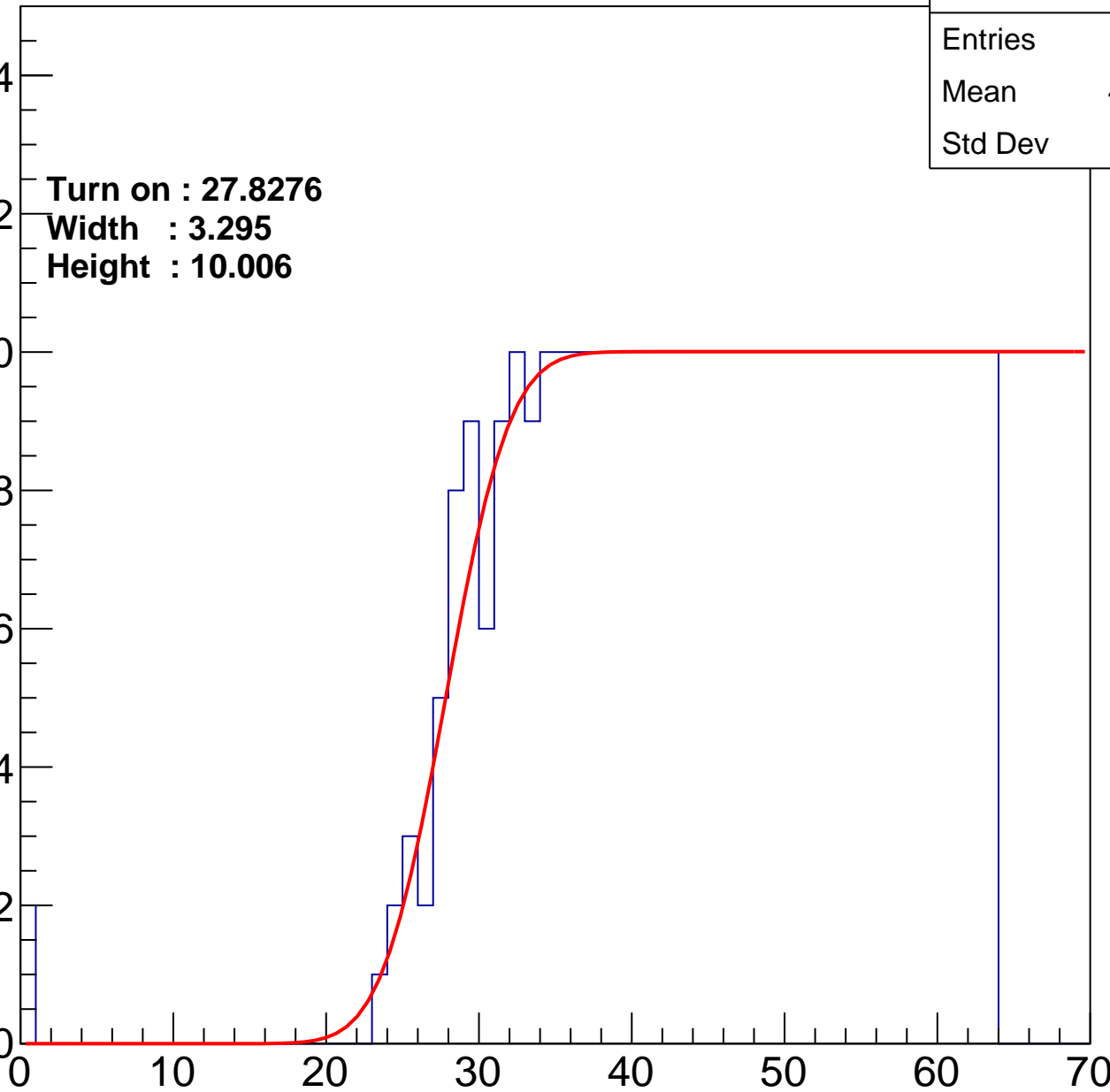
Width : 3.295

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.16
Std Dev	11.34

Turn on : 28.7594

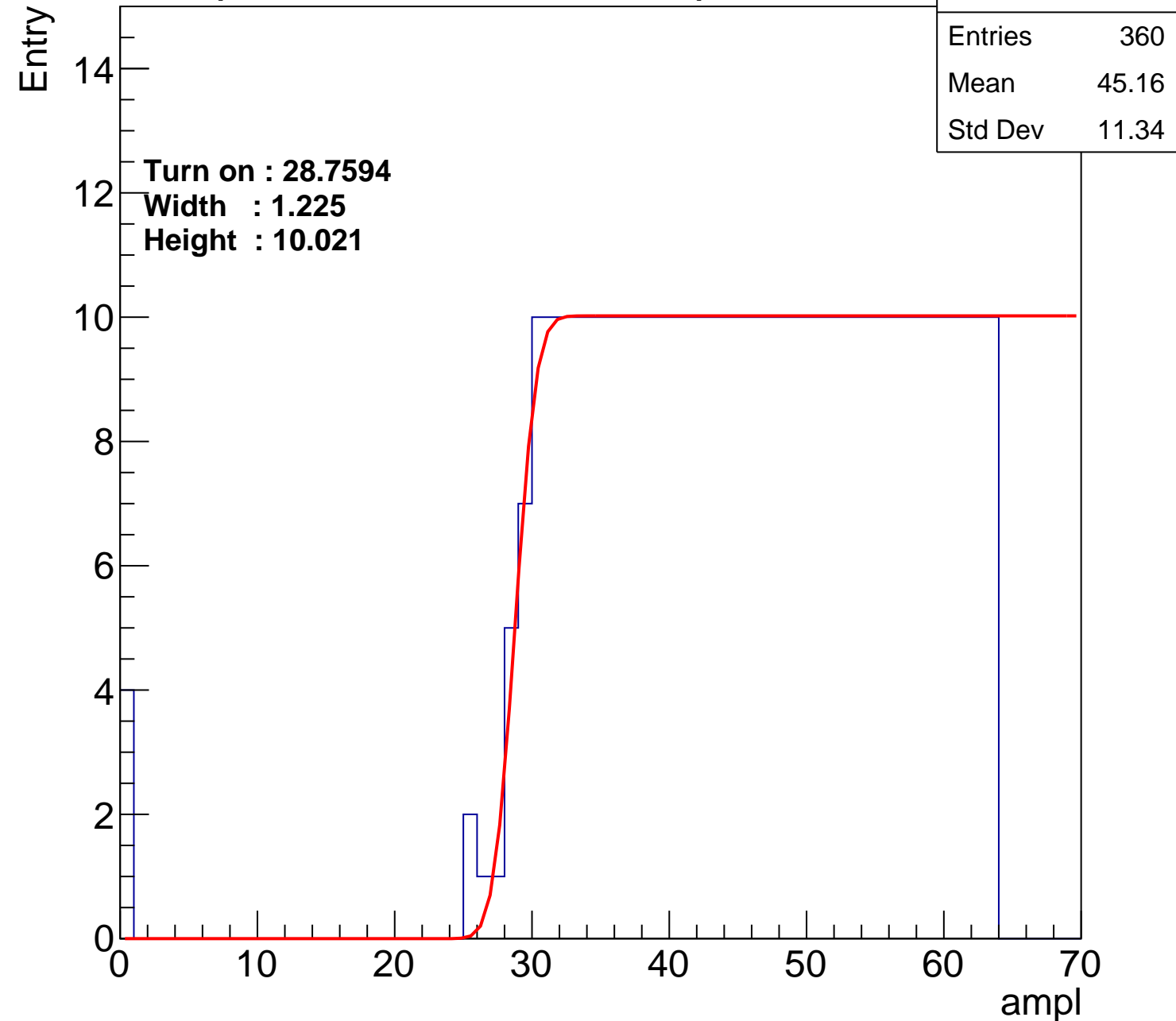
Width : 1.225

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.03
Std Dev	11.47

Turn on : 28.6672

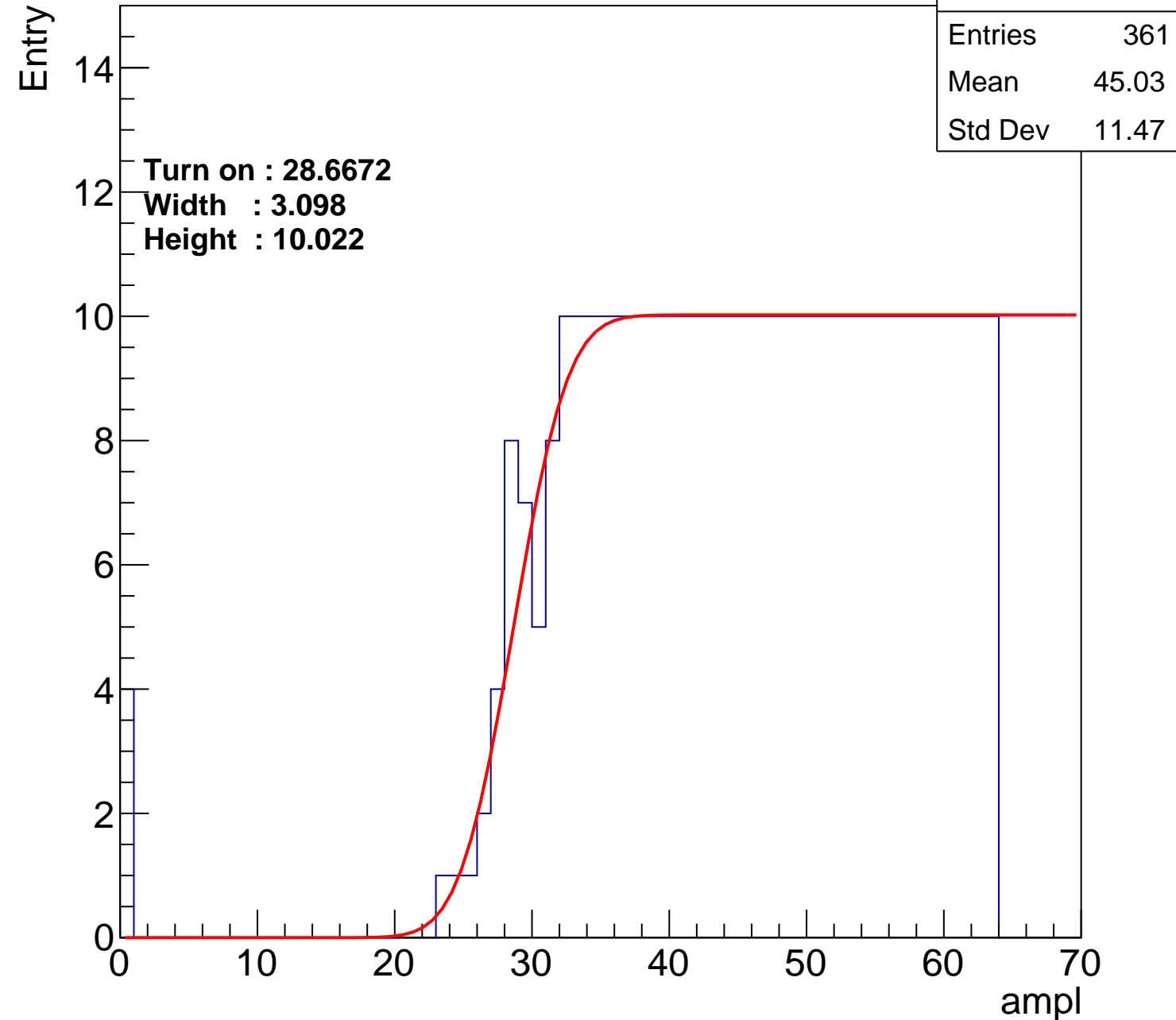
Width : 3.098

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.01
Std Dev	11.27

Turn on : 28.0131

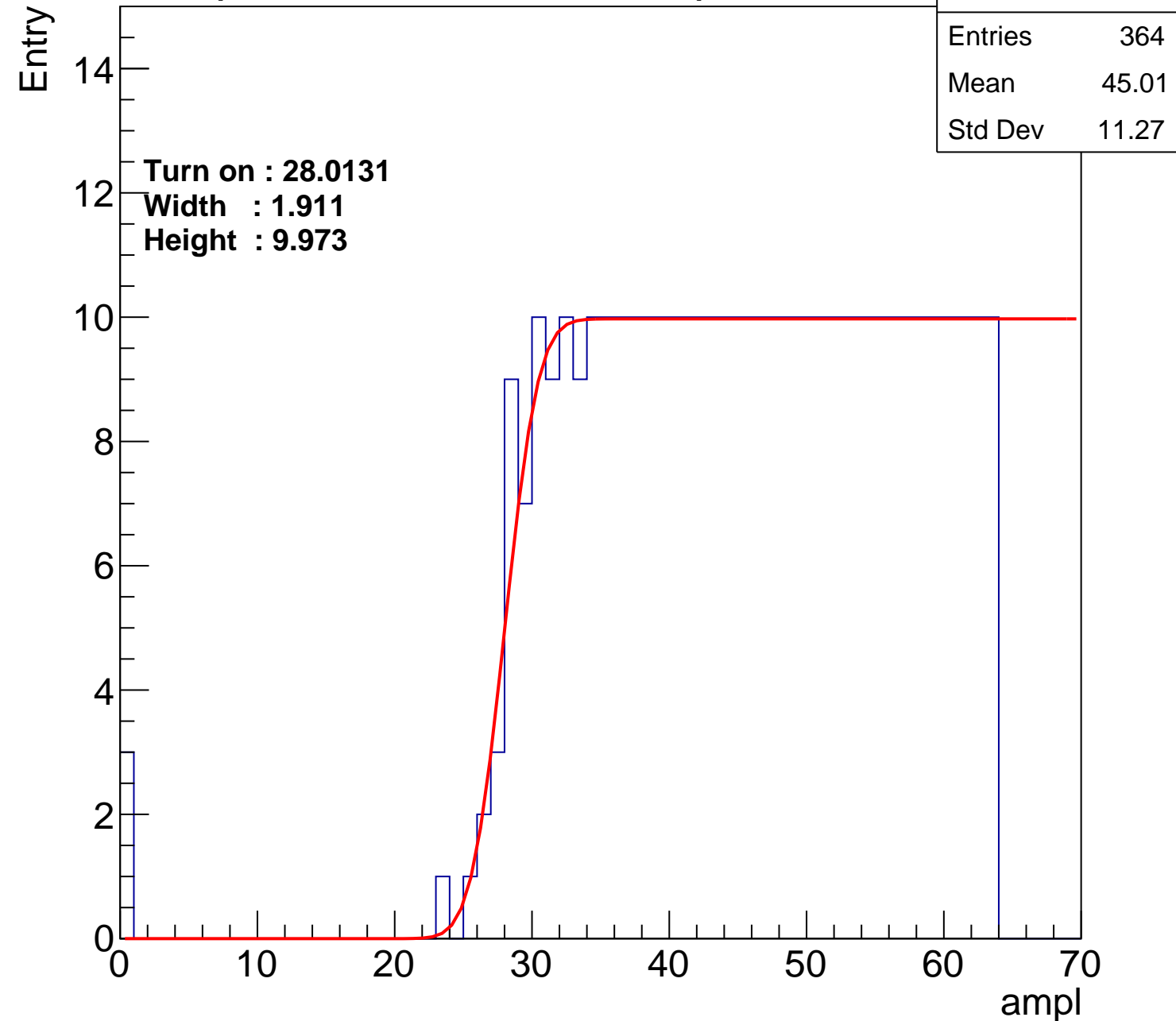
Width : 1.911

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch15

calib_packv5_042523_0143.root, FC#8, port C1

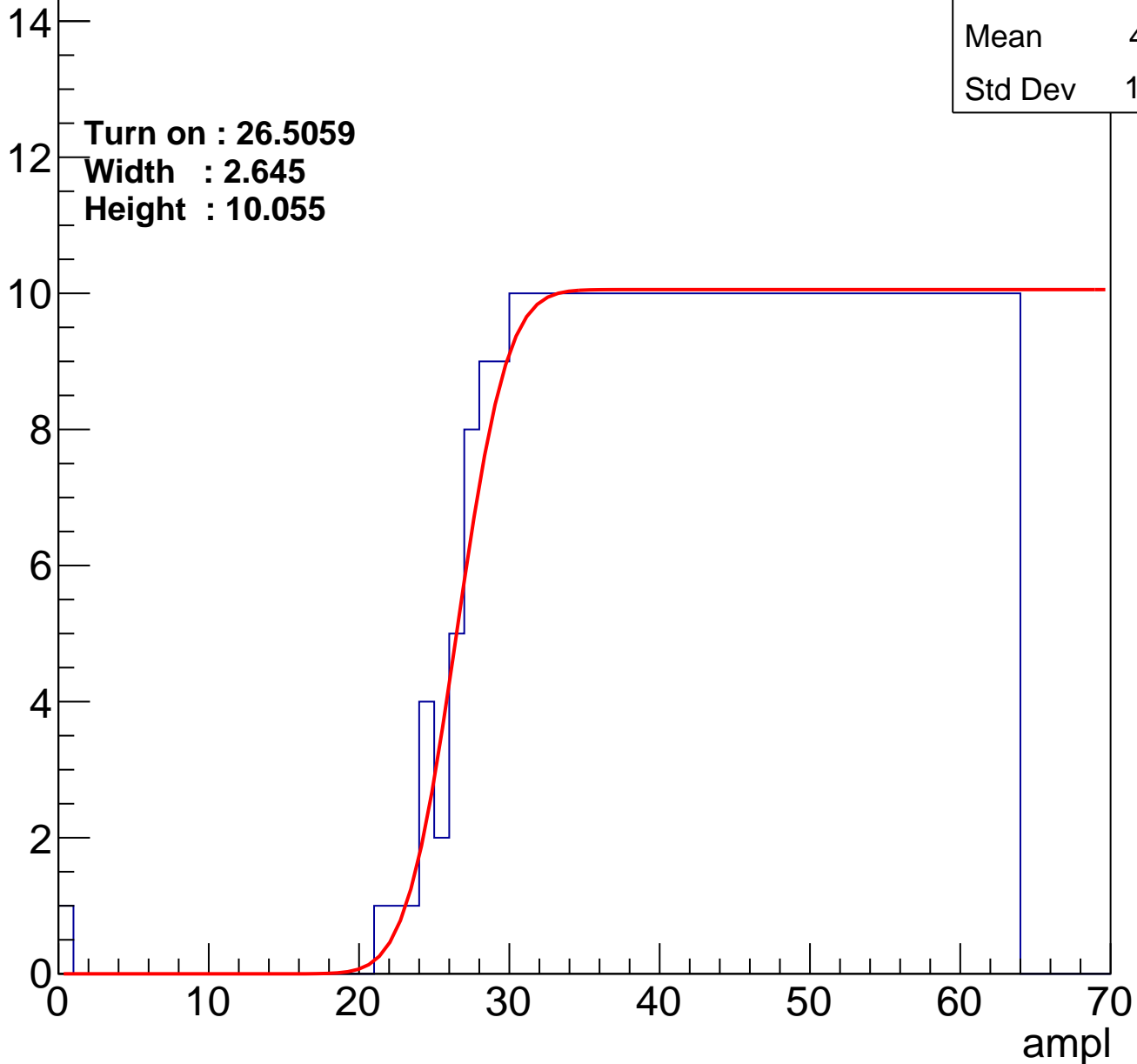
Entries	381
Mean	44.31
Std Dev	11.32

Turn on : 26.5059

Width : 2.645

Height : 10.055

Entry

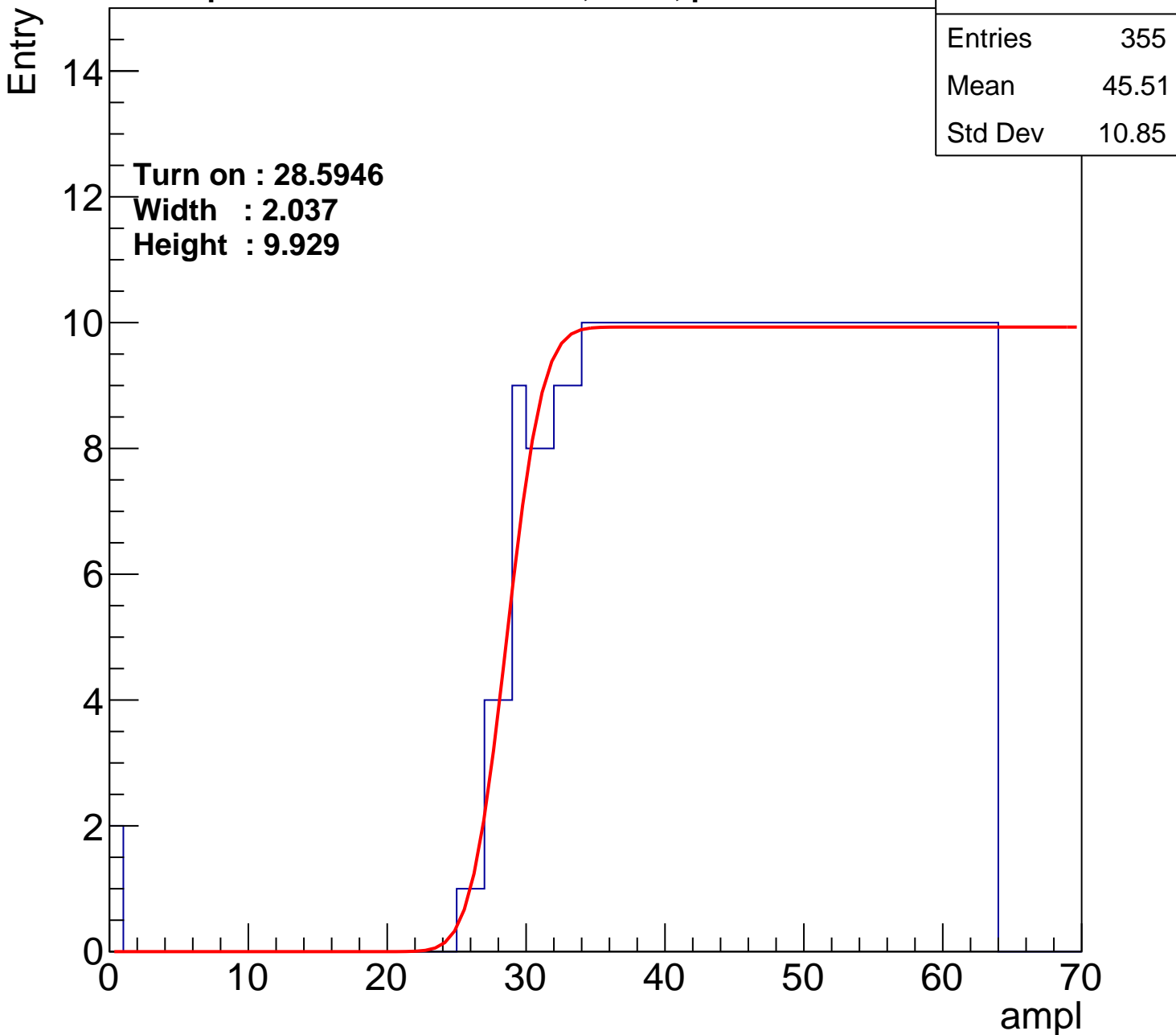


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.51
Std Dev	10.85

Height : 9.929



B0L002S, U21-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.7358

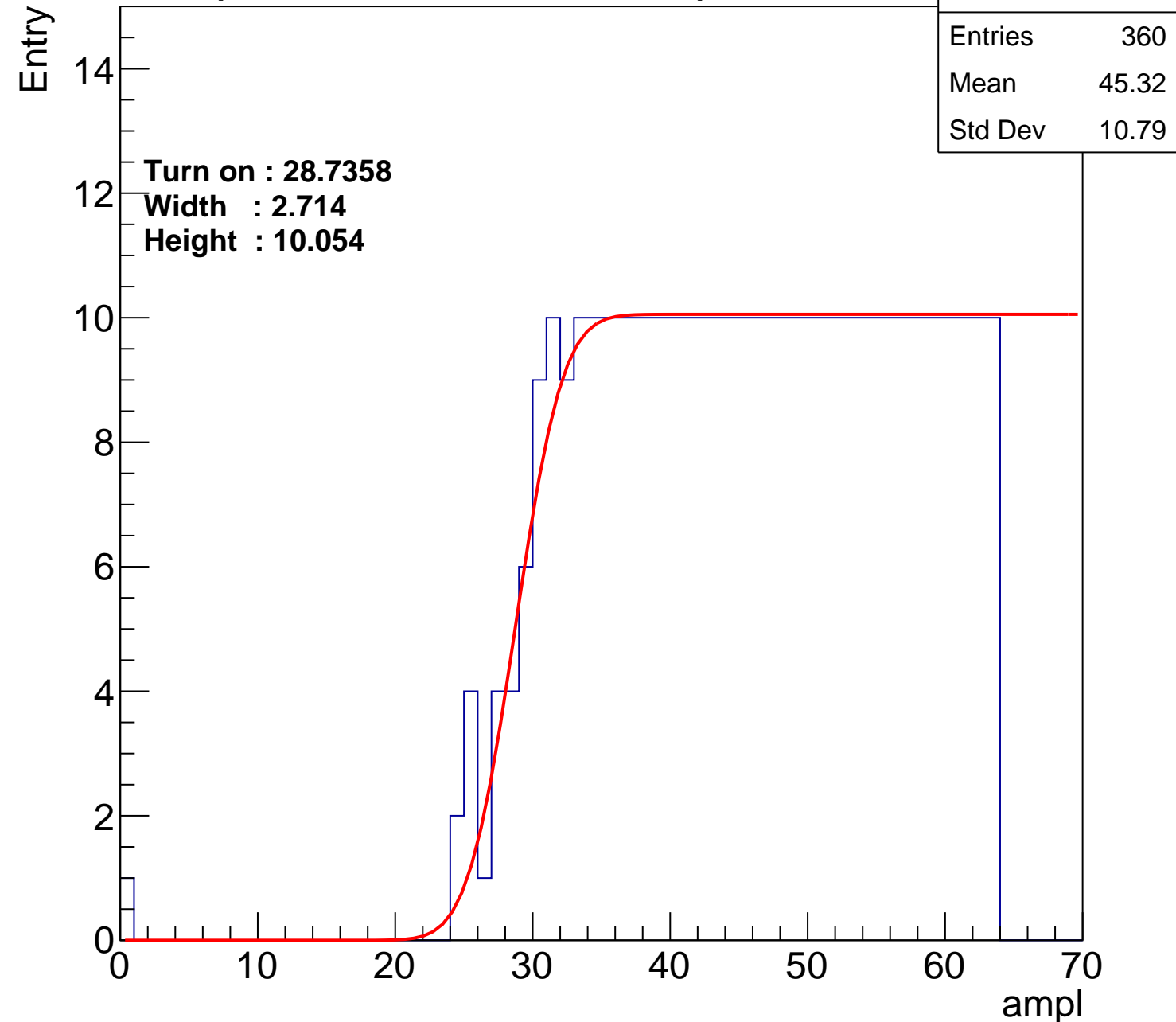
Width : 2.714

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.9938

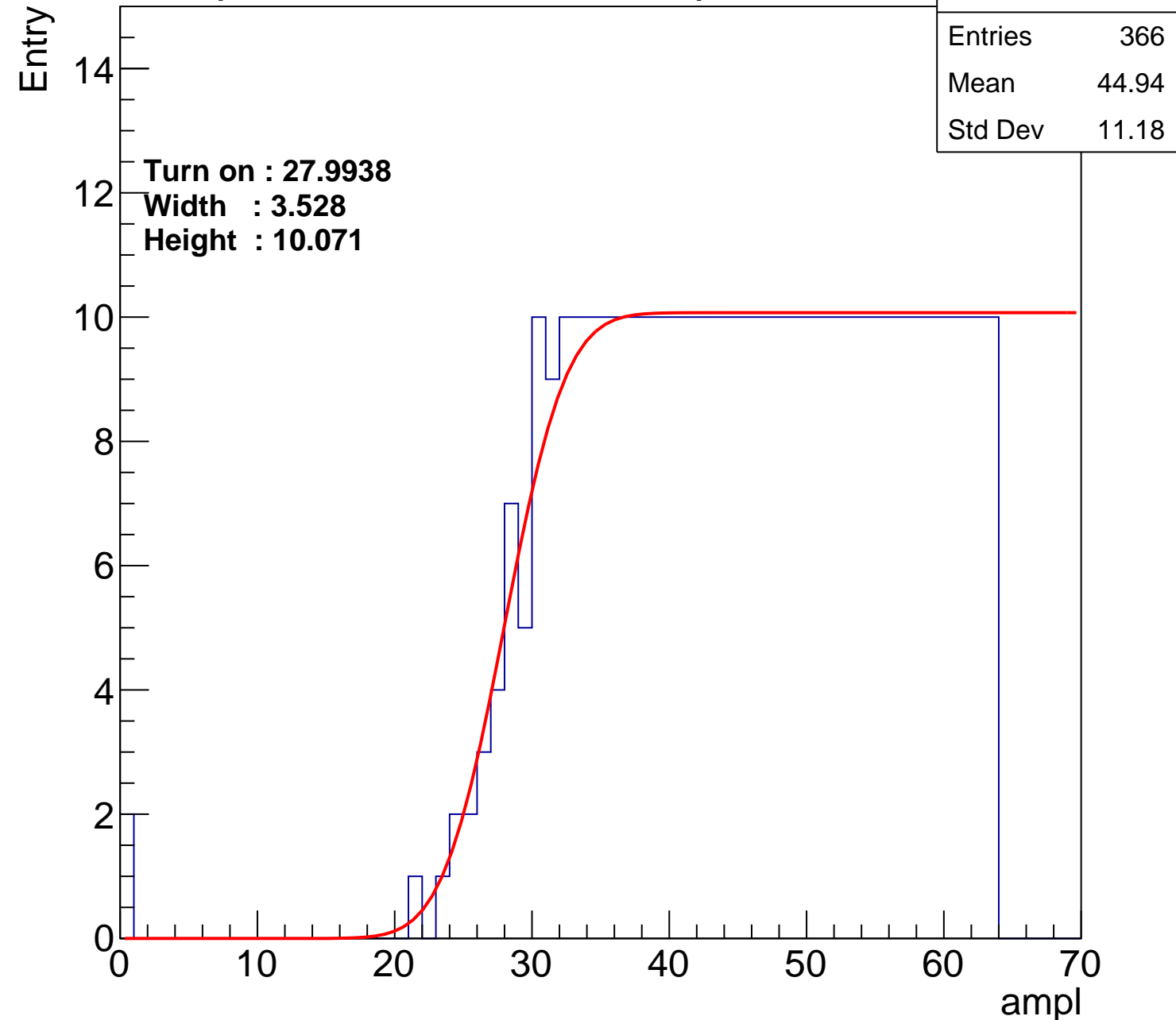
Width : 3.528

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.54
Std Dev	10.65

Turn on : 29.1973

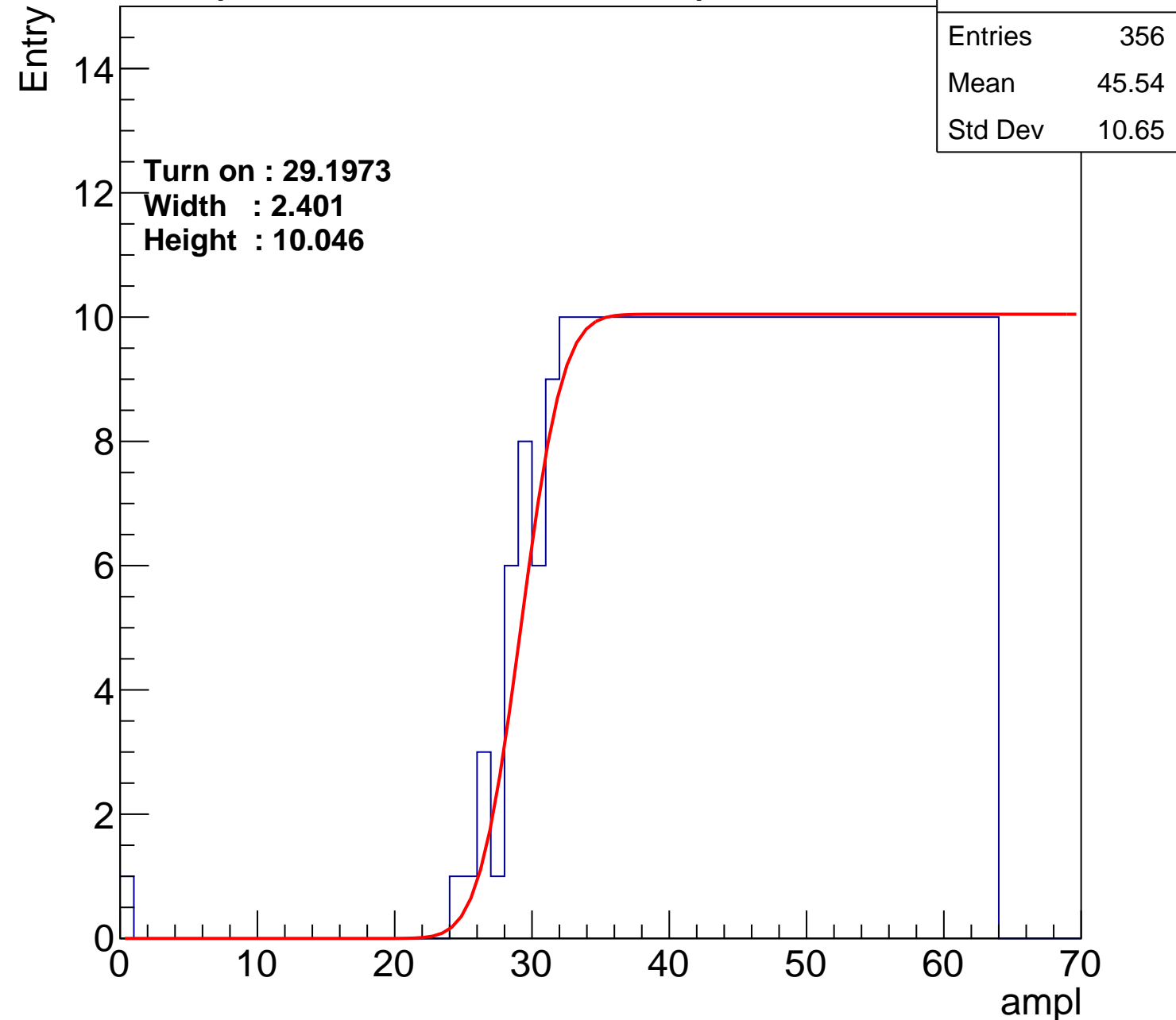
Width : 2.401

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.7541

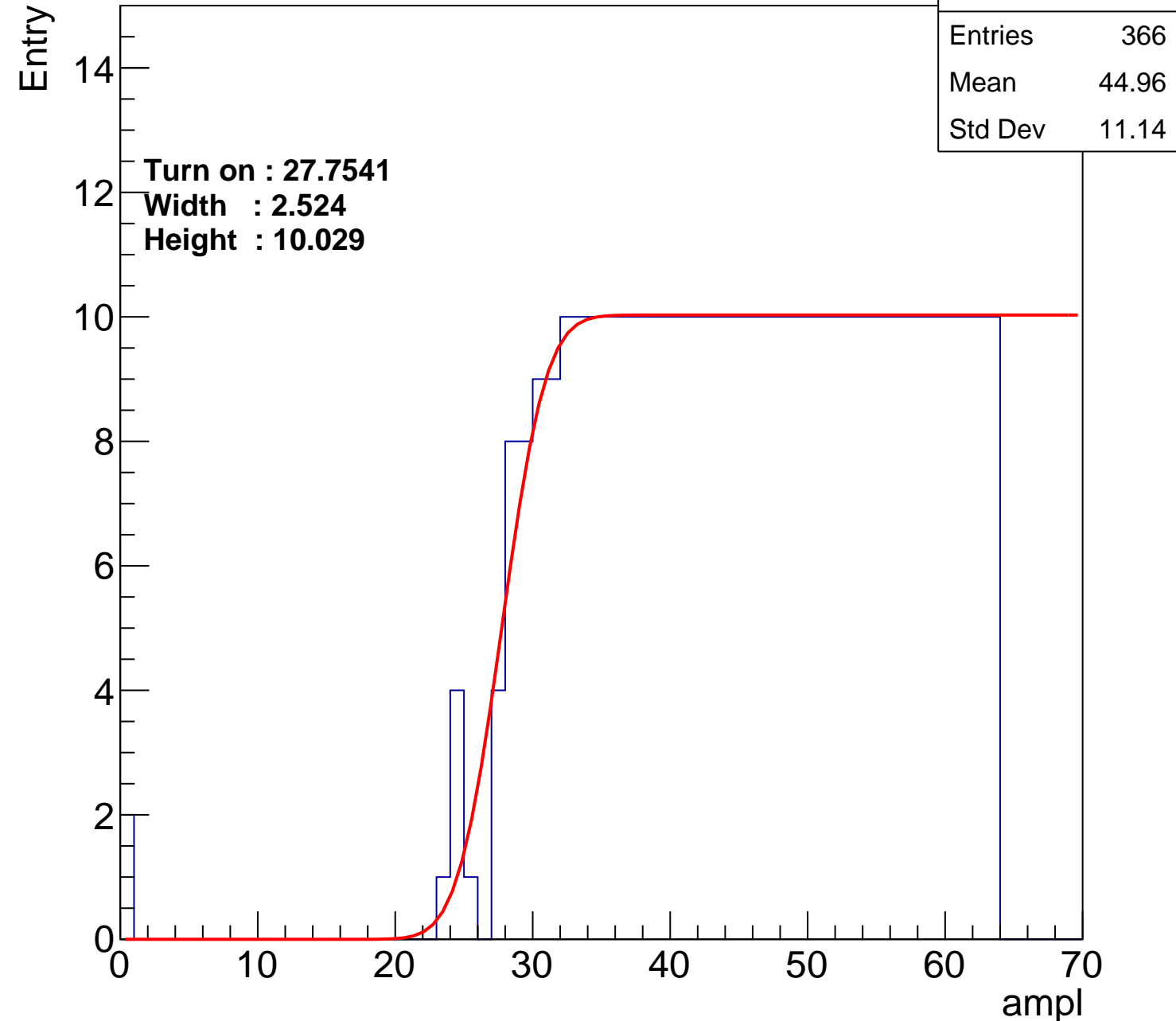
Width : 2.524

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch21

calib_packv5_042523_0143.root, FC#8, port C1

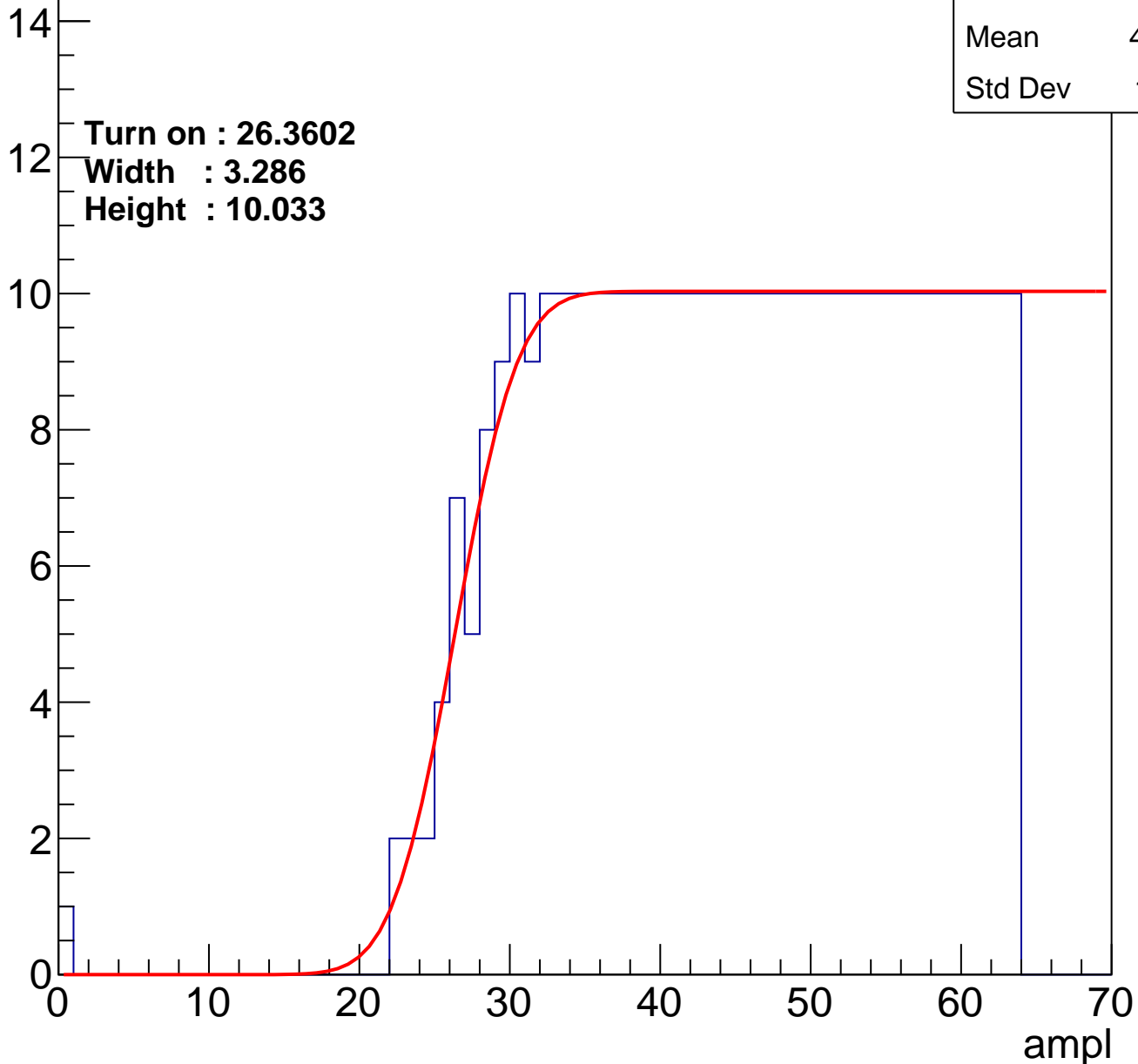
Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.3602

Width : 3.286

Height : 10.033

Entry



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.9158
Width : 2.237
Height : 10.051



B0L002S, U21-ch23

calib_packv5_042523_0143.root, FC#8, port C1

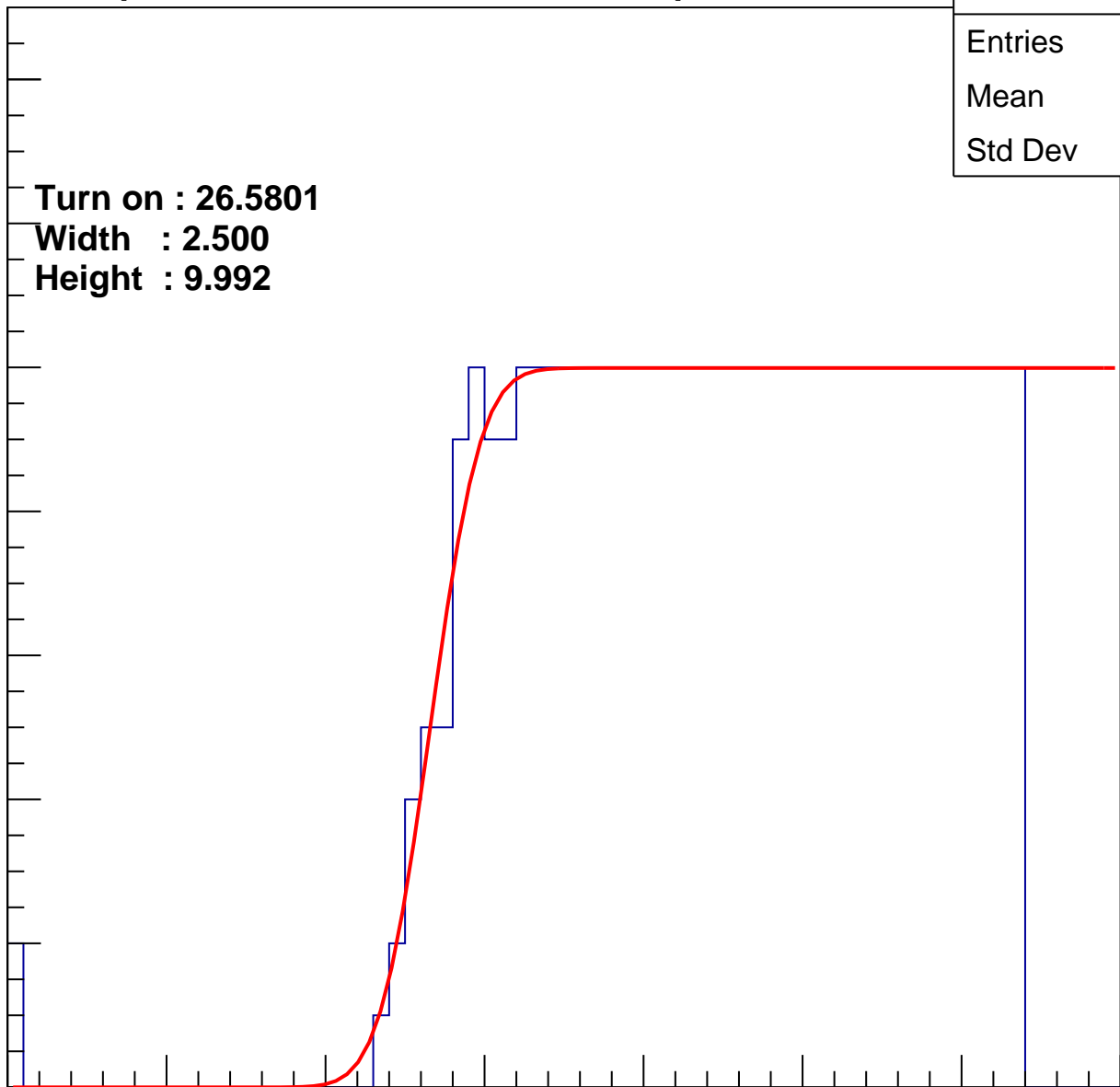
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5801
Width : 2.500
Height : 9.992

Entries	376
Mean	44.49
Std Dev	11.37

ampl



B0L002S, U21-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.98
Std Dev	10.4

Turn on : 29.4416

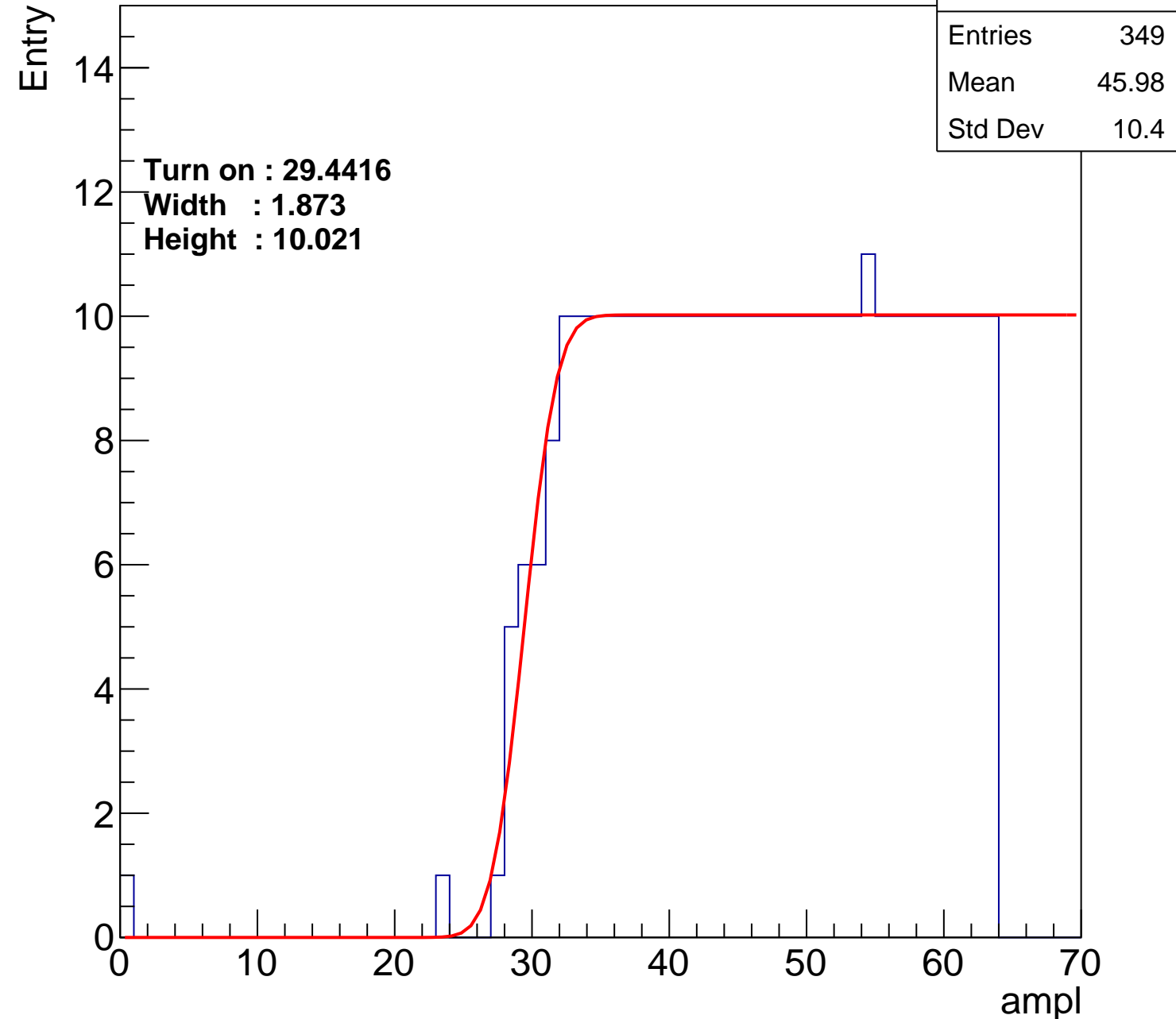
Width : 1.873

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.14
Std Dev	11.56

Turn on : 29.1224

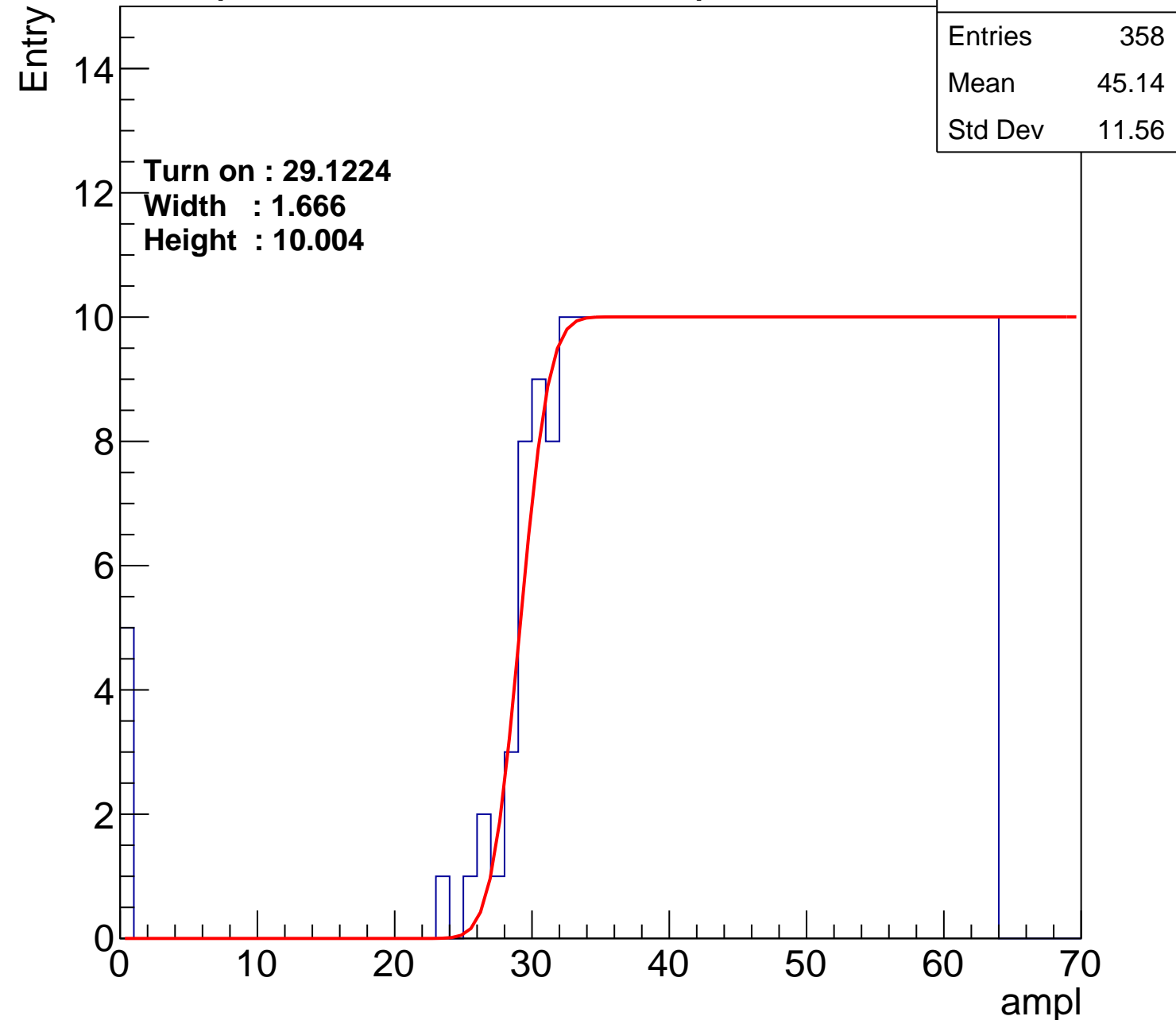
Width : 1.666

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.43
Std Dev	11.46

Turn on : 26.8469

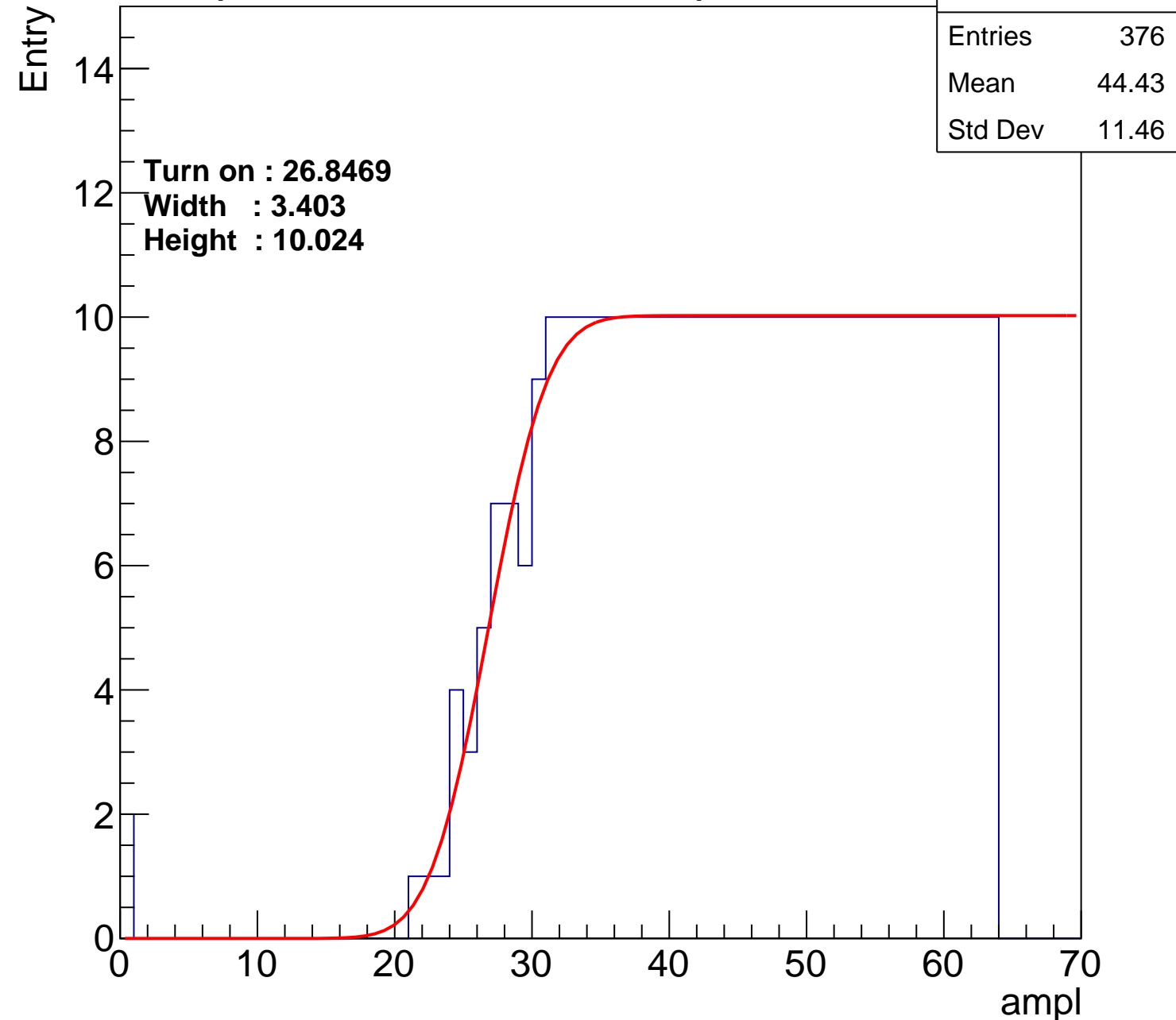
Width : 3.403

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.3366

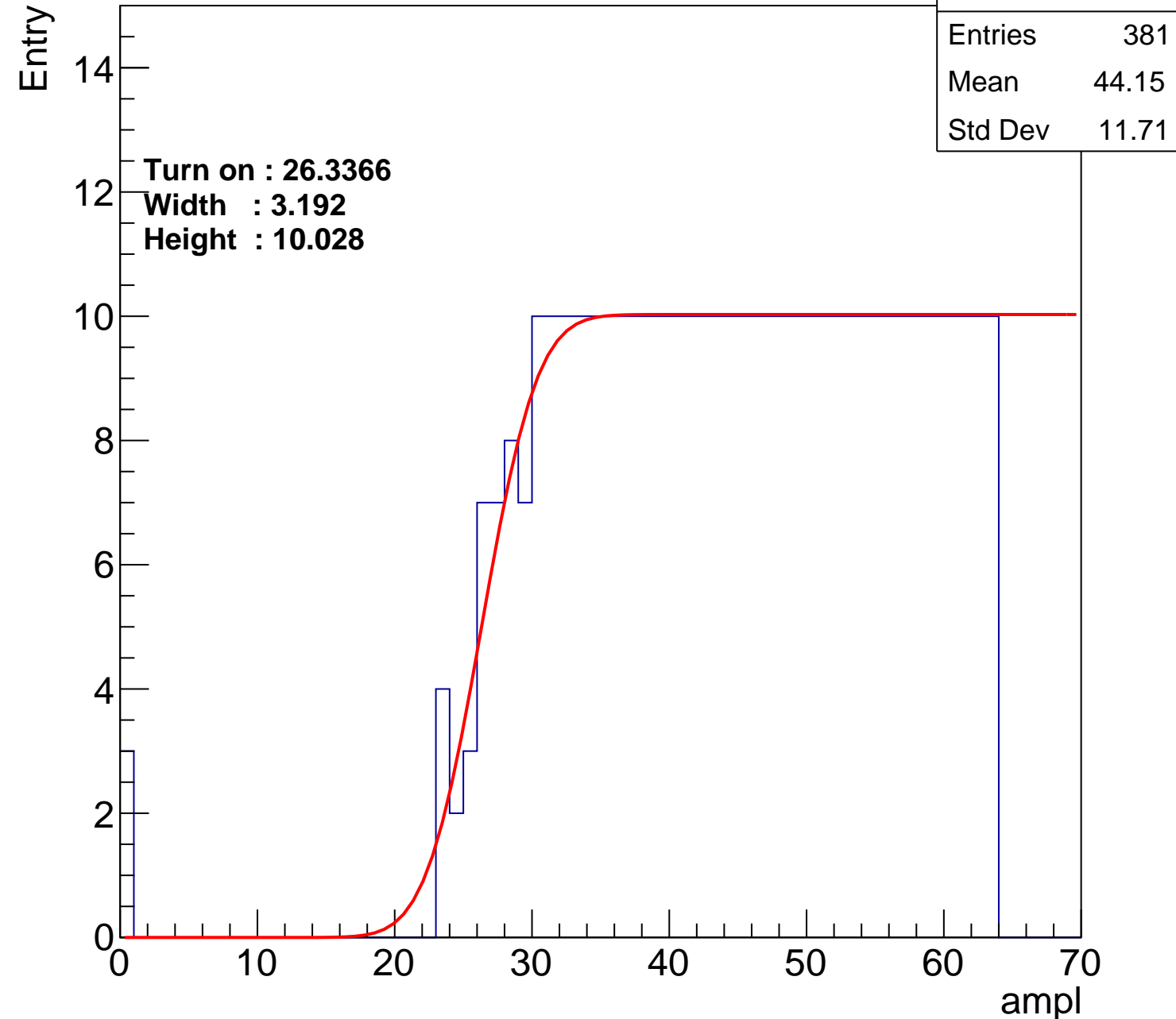
Width : 3.192

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.91
Std Dev	11.32

Turn on : 27.8081

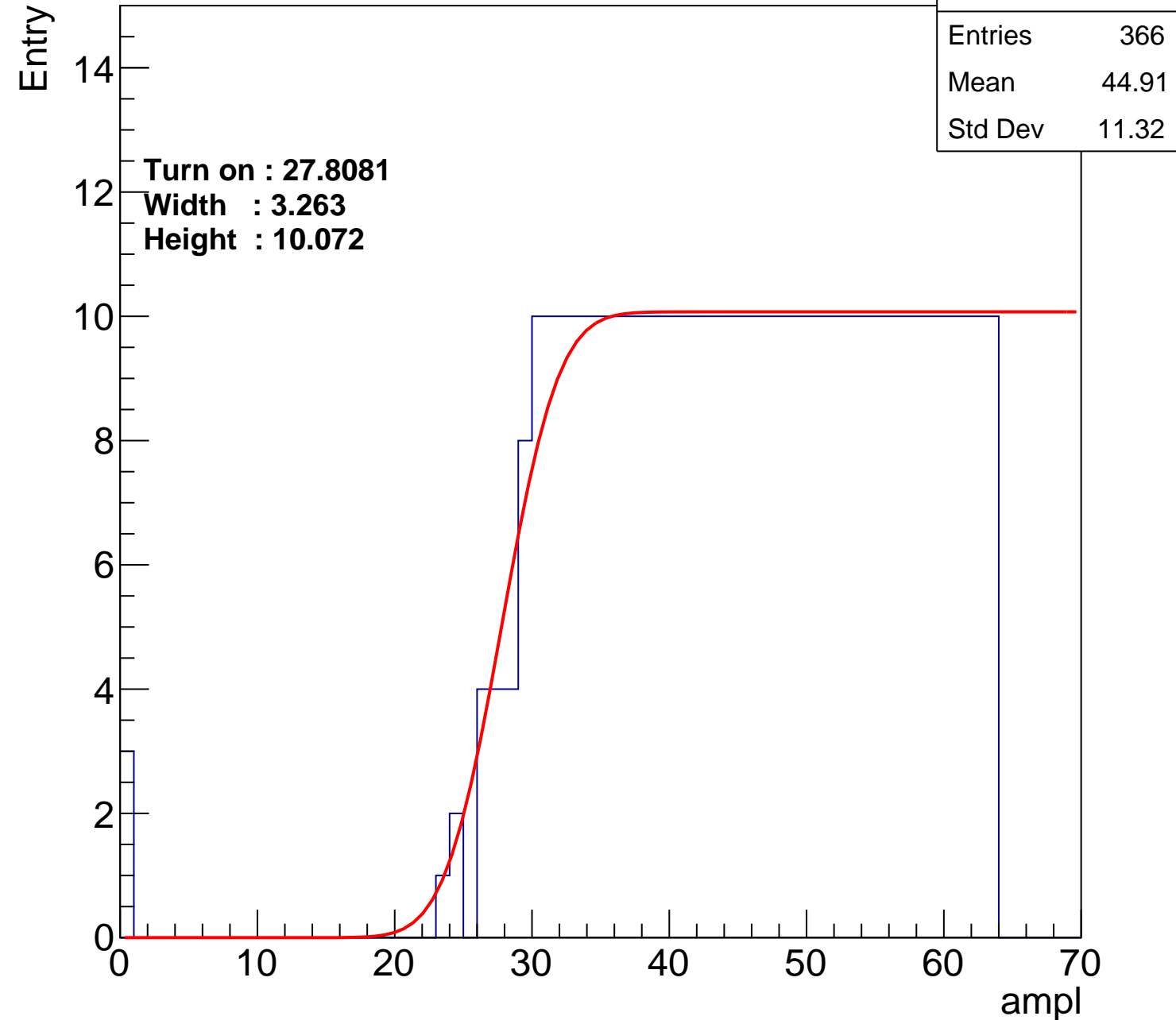
Width : 3.263

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch29

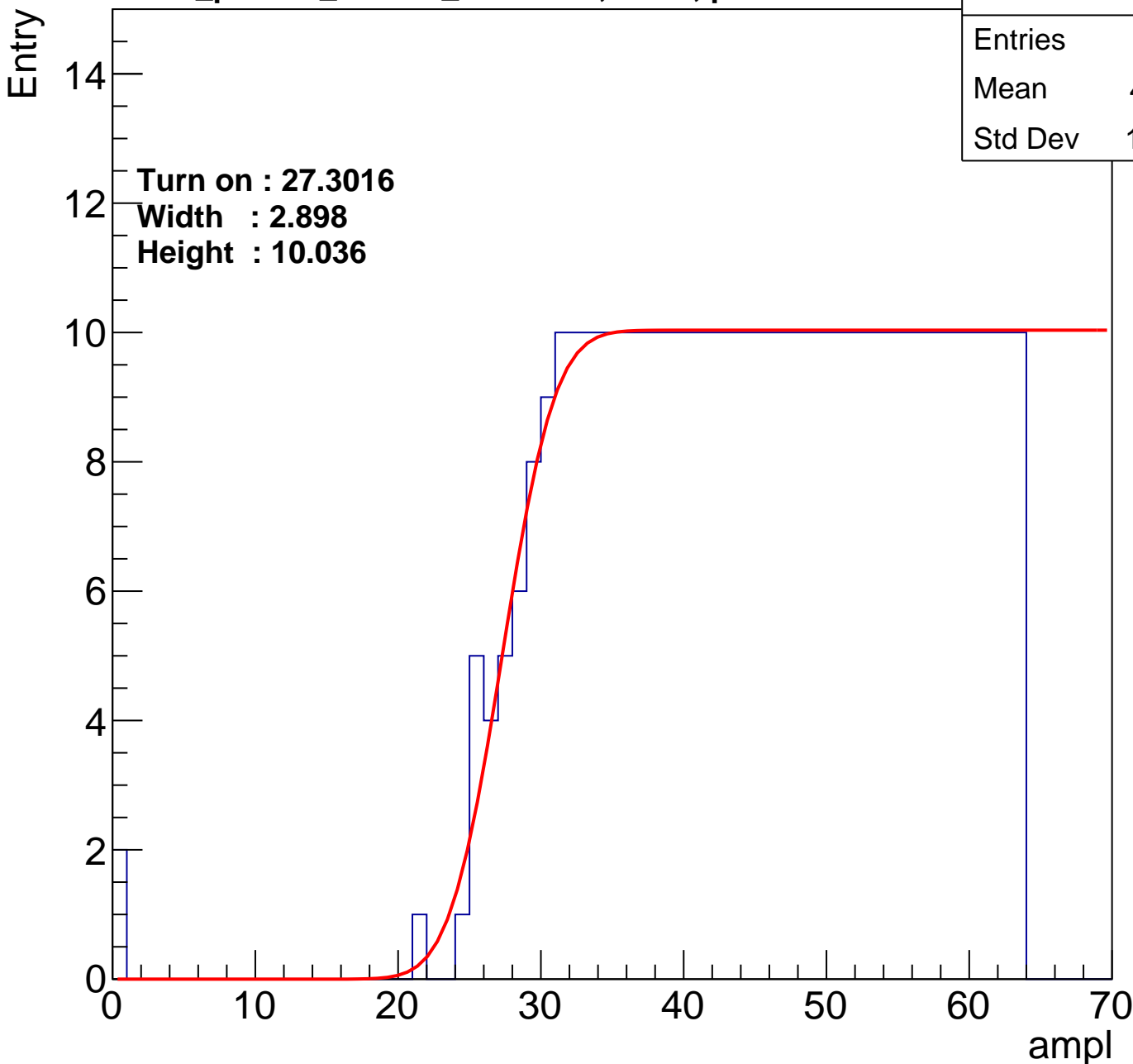
calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 27.3016

Width : 2.898

Height : 10.036



B0L002S, U21-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.8428

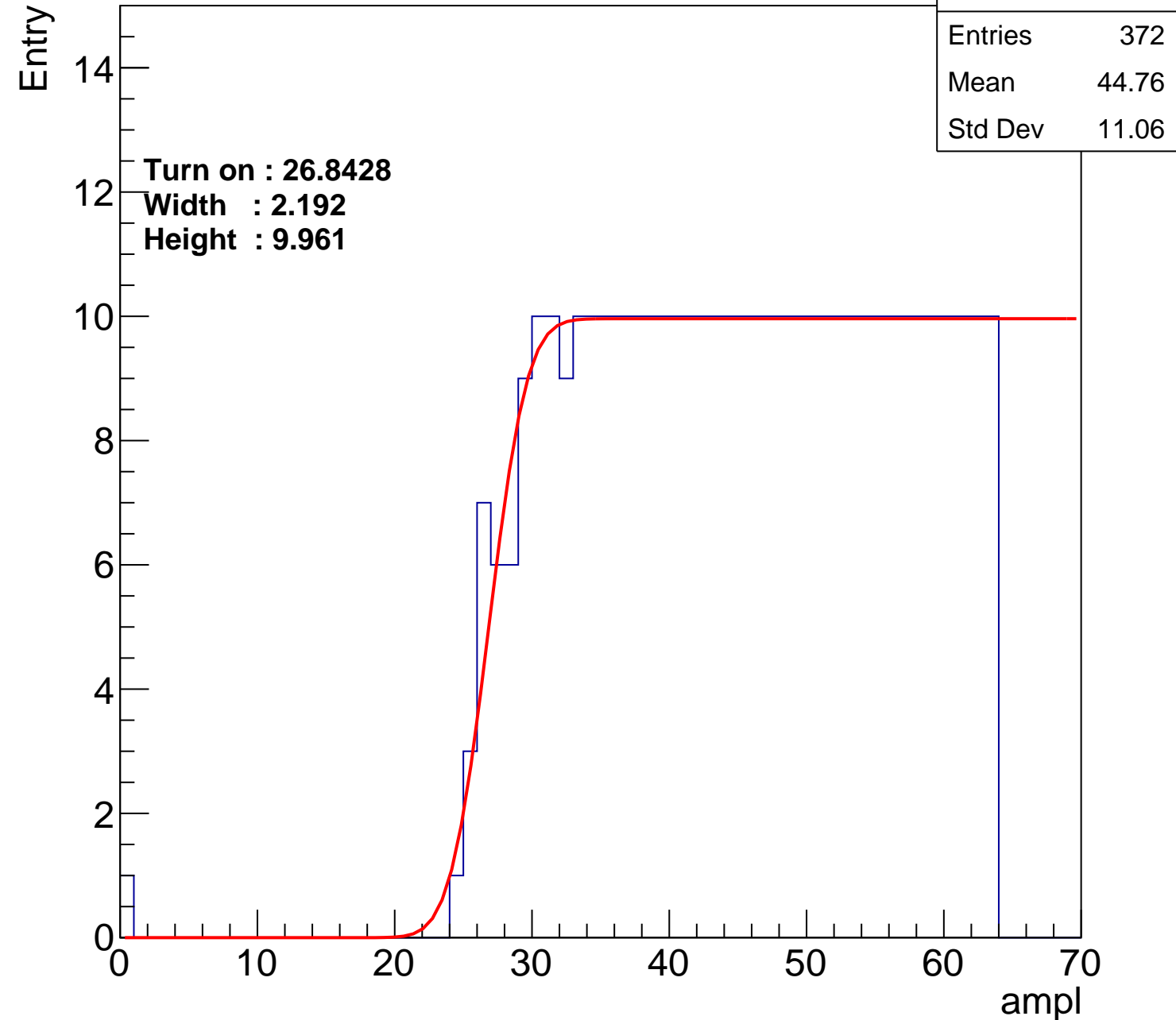
Width : 2.192

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.05
Std Dev	11.44

Turn on : 28.2236

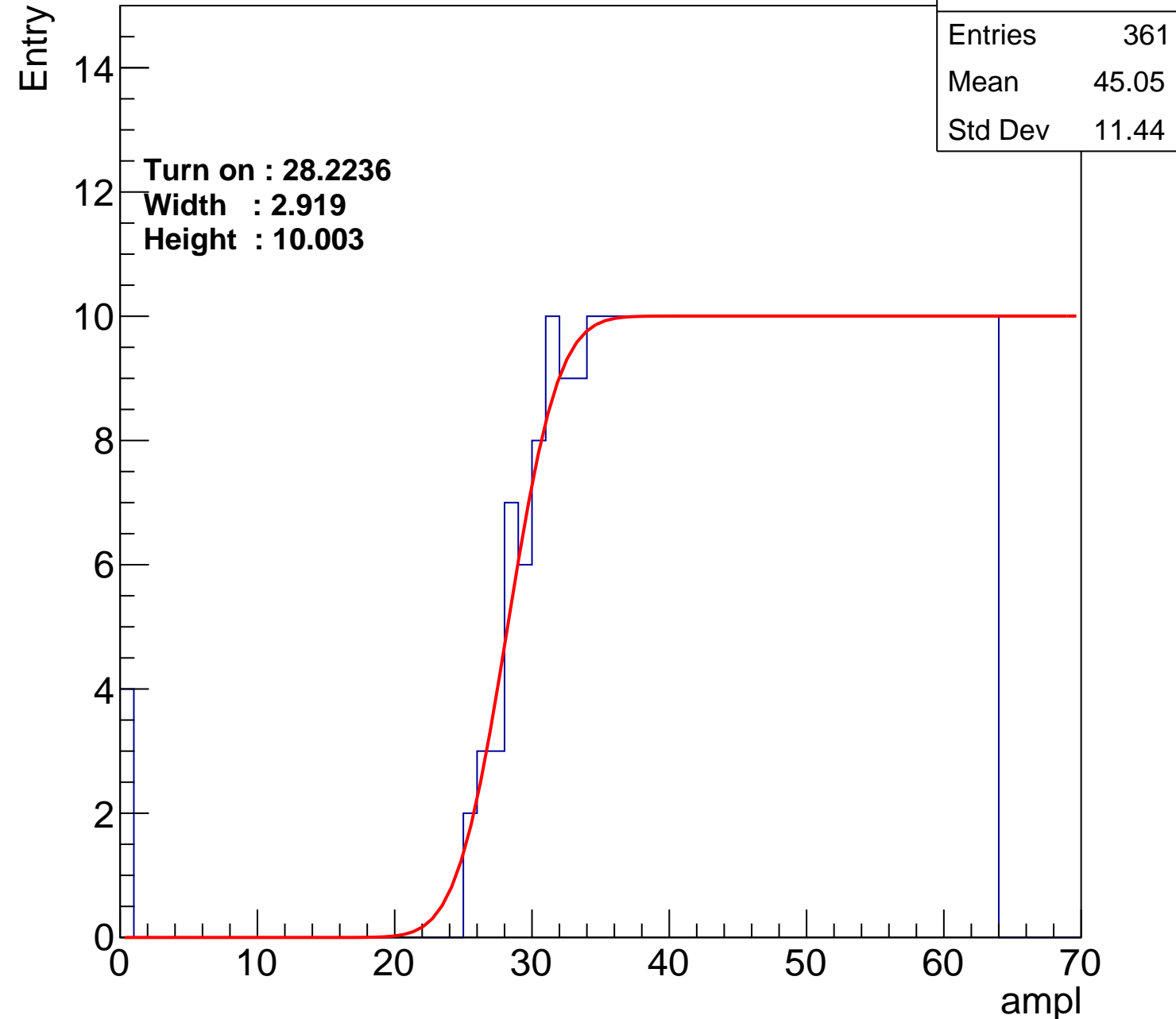
Width : 2.919

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch32

calib_packv5_042523_0143.root, FC#8, port C1

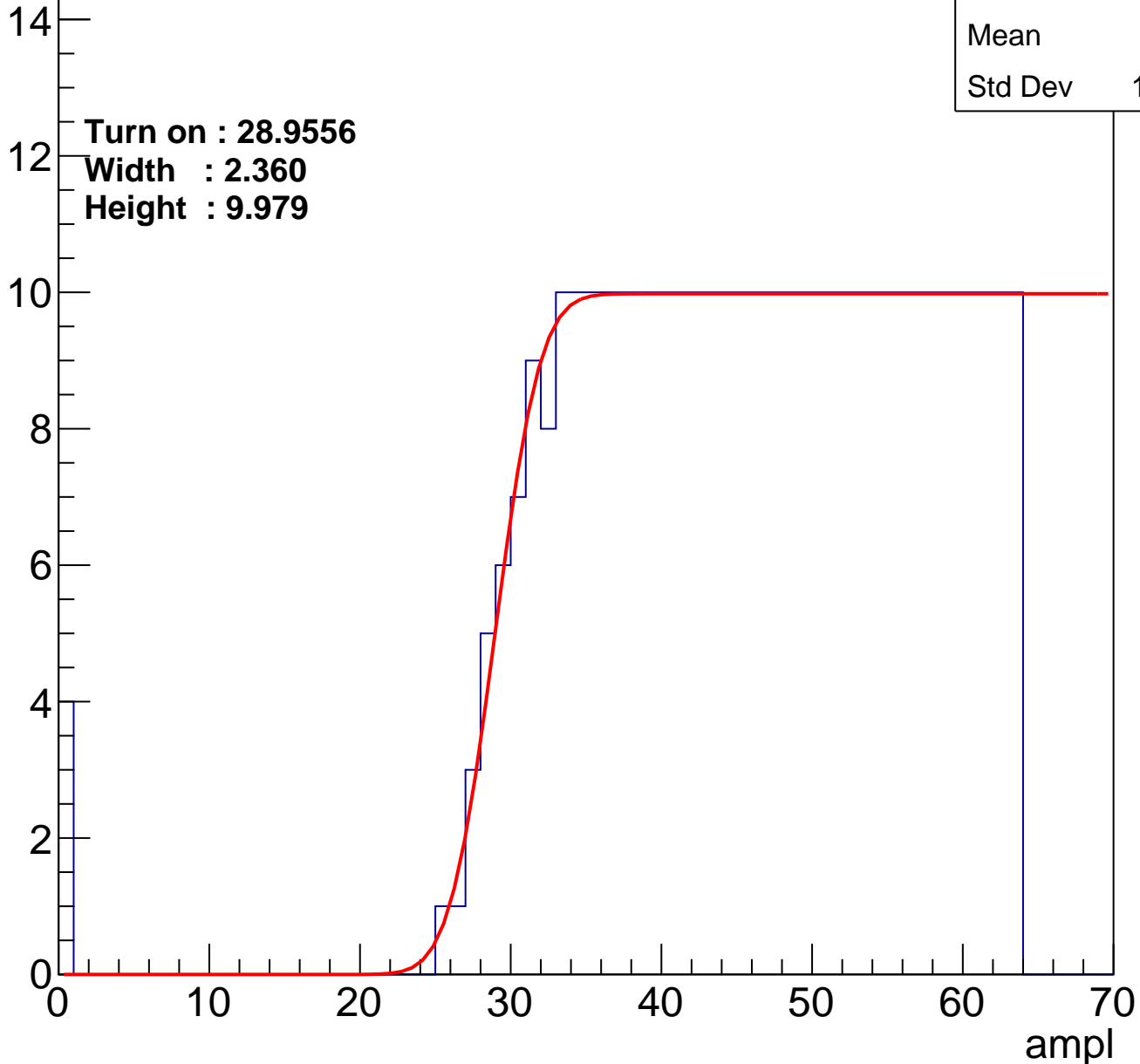
Entries	354
Mean	45.4
Std Dev	11.28

Turn on : 28.9556

Width : 2.360

Height : 9.979

Entry



B0L002S, U21-ch33

calib_packv5_042523_0143.root, FC#8, port C1

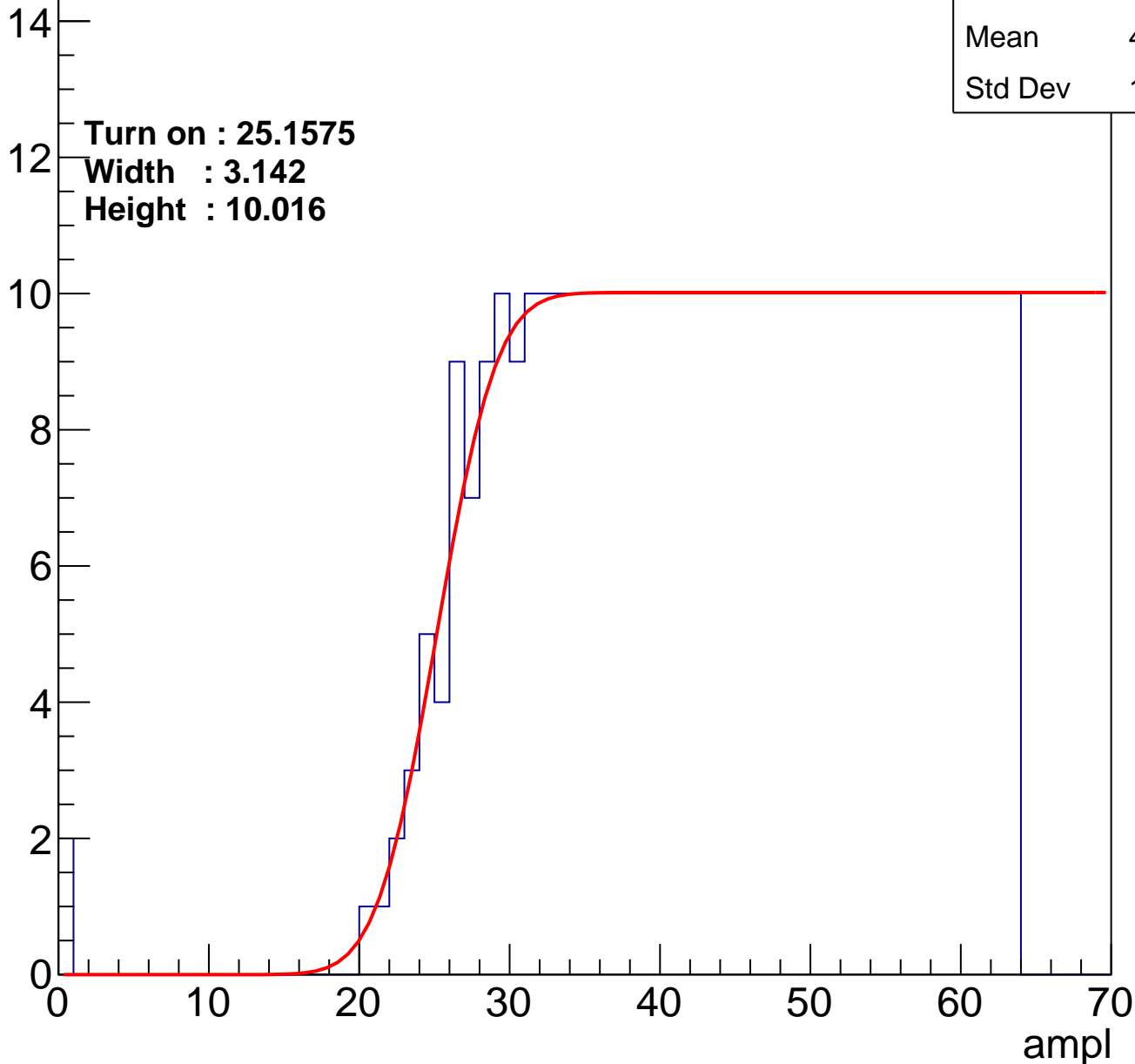
Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 25.1575

Width : 3.142

Height : 10.016

Entry



B0L002S, U21-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 26.9682

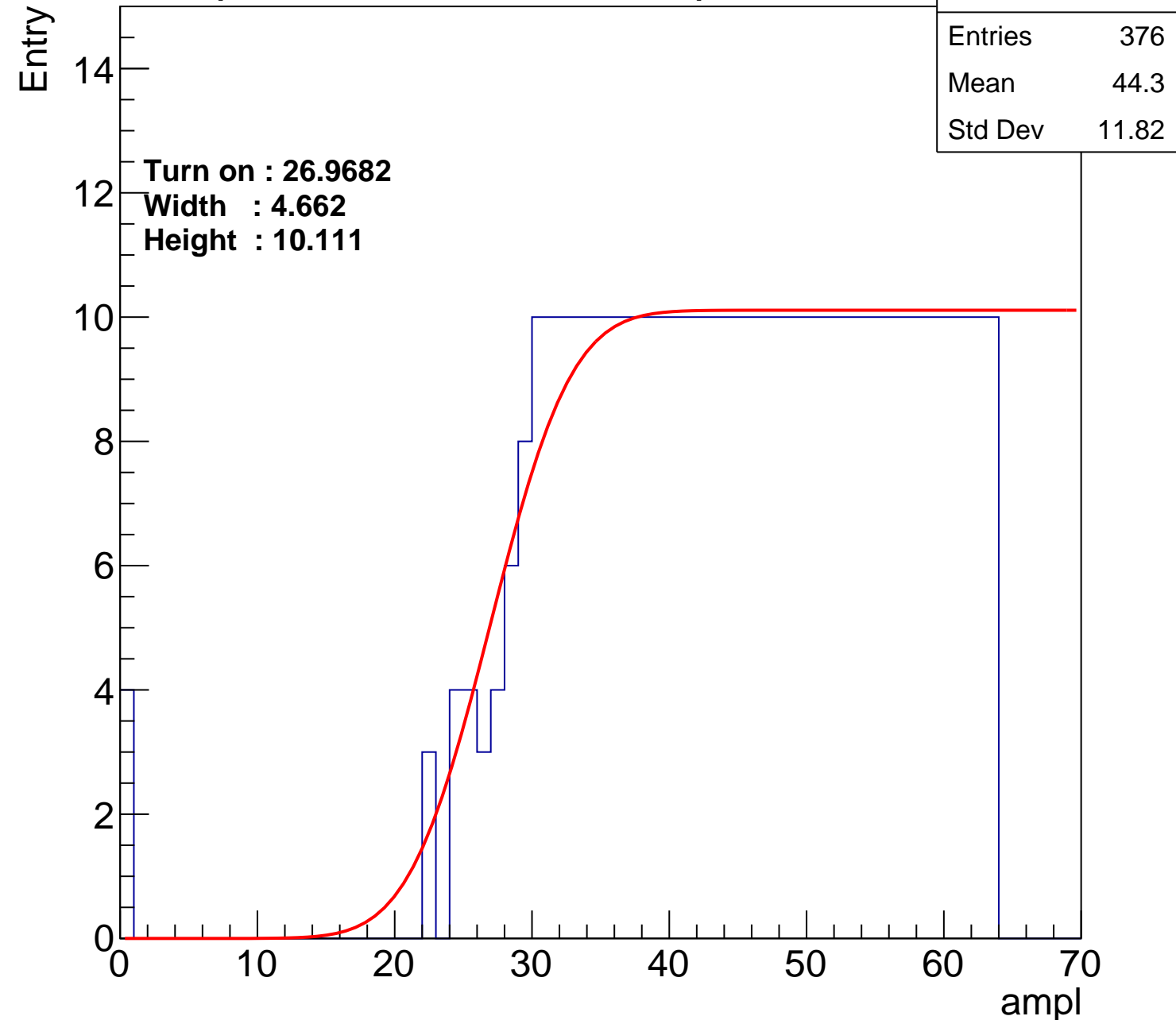
Width : 4.662

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch35

calib_packv5_042523_0143.root, FC#8, port C1

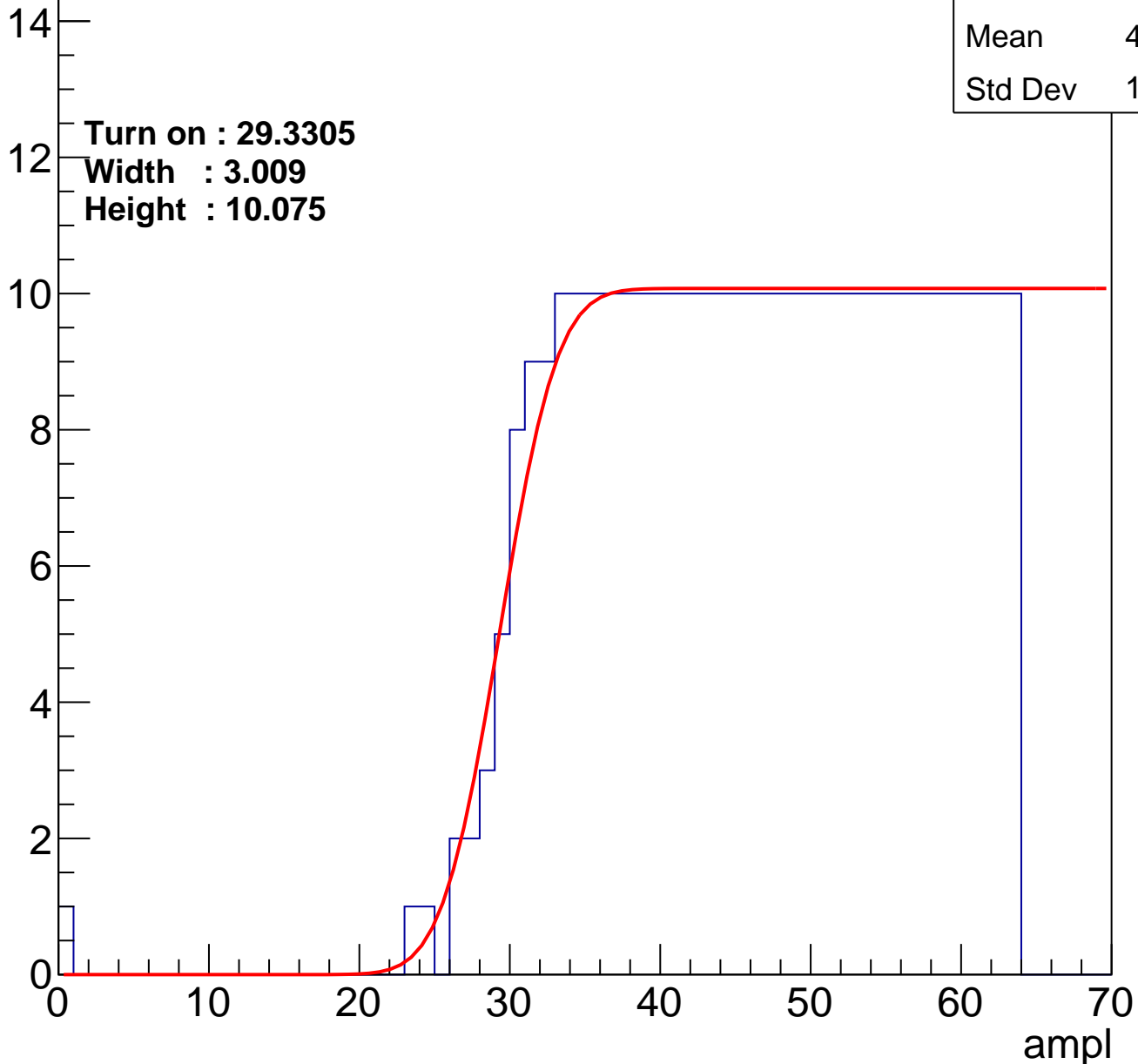
Entries	351
Mean	45.78
Std Dev	10.53

Turn on : 29.3305

Width : 3.009

Height : 10.075

Entry



B0L002S, U21-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.3845

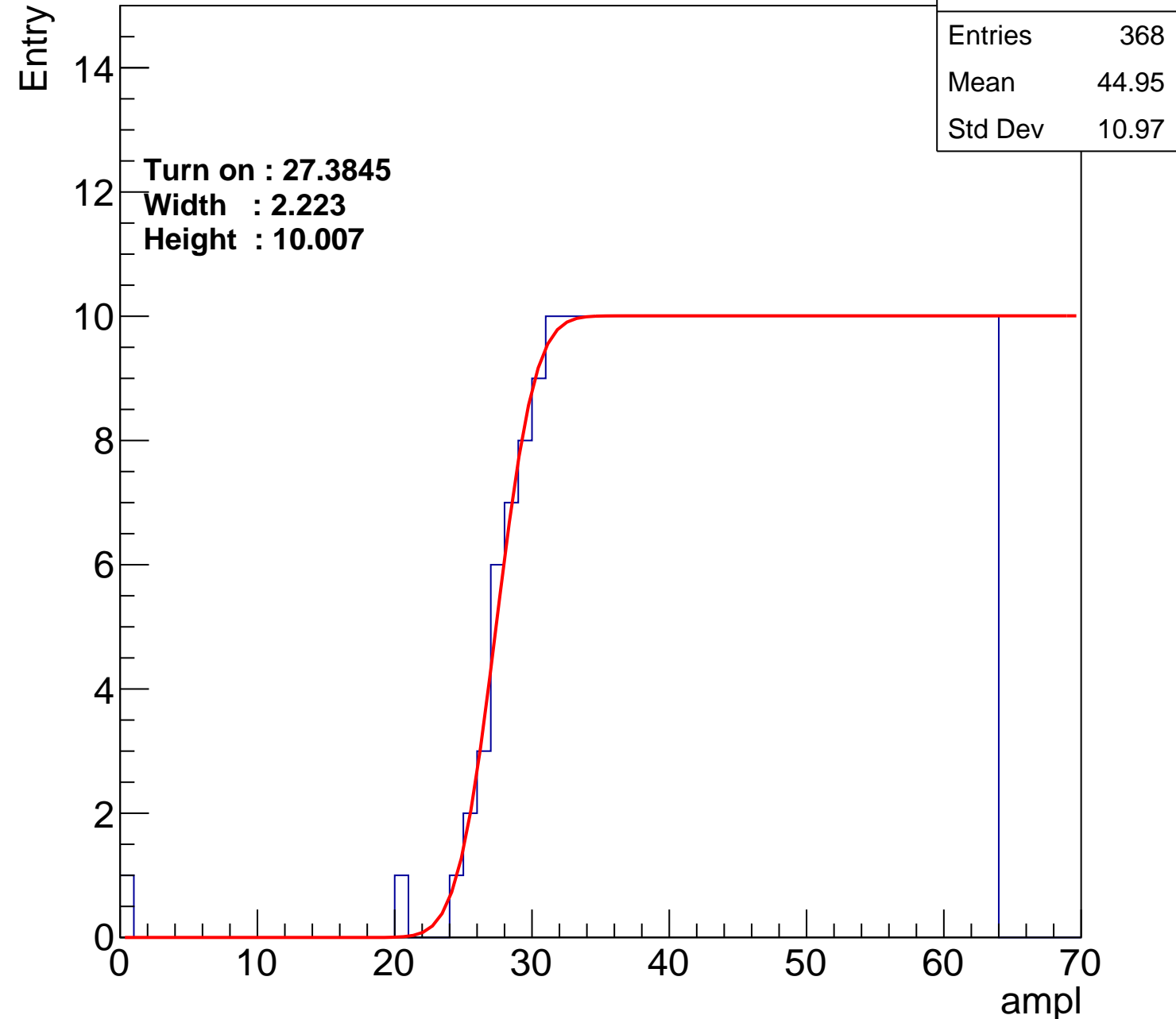
Width : 2.223

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.61
Std Dev	11.14

Turn on : 26.6963

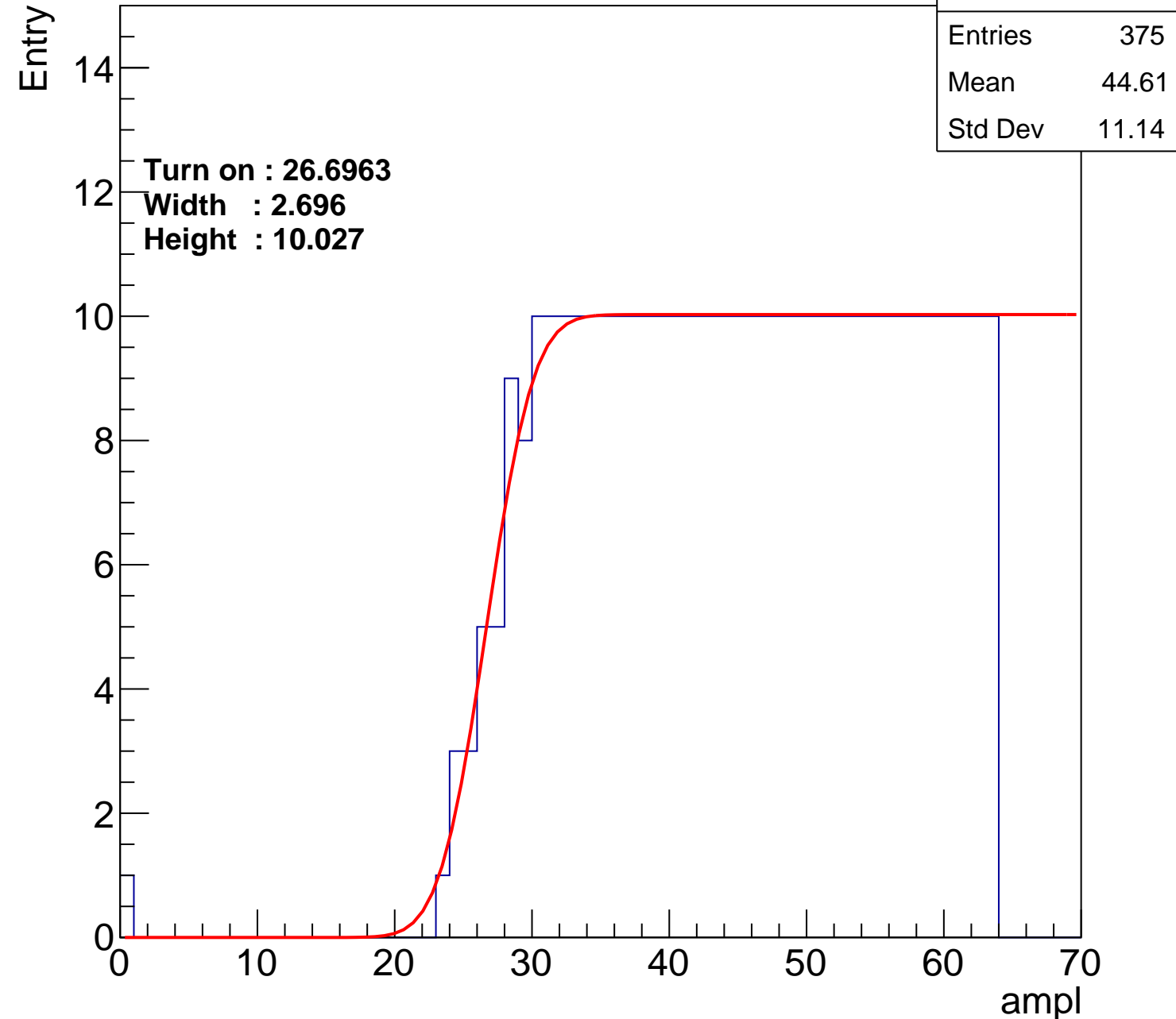
Width : 2.696

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.99
Std Dev	11

Turn on : 27.6711

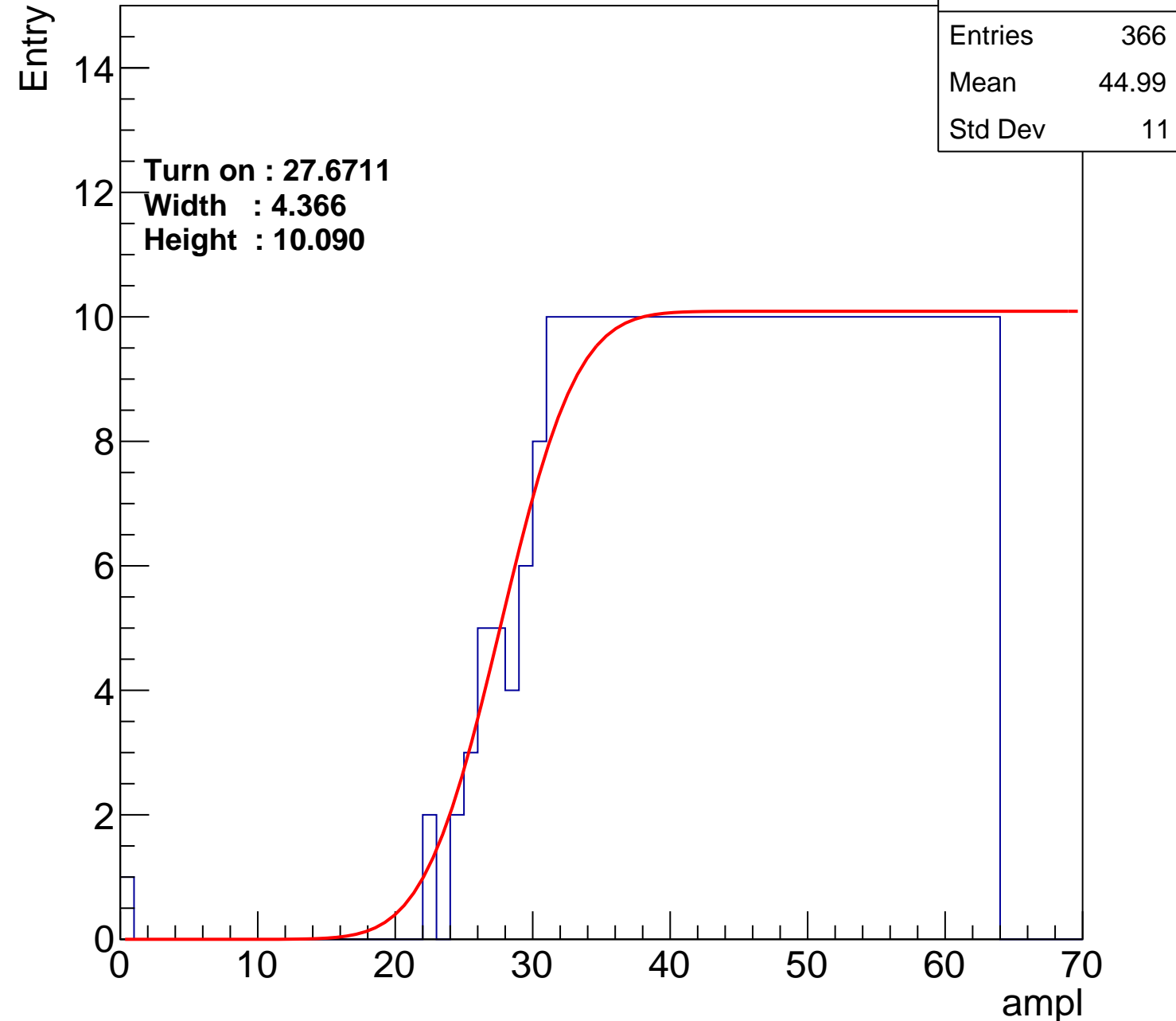
Width : 4.366

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.2146

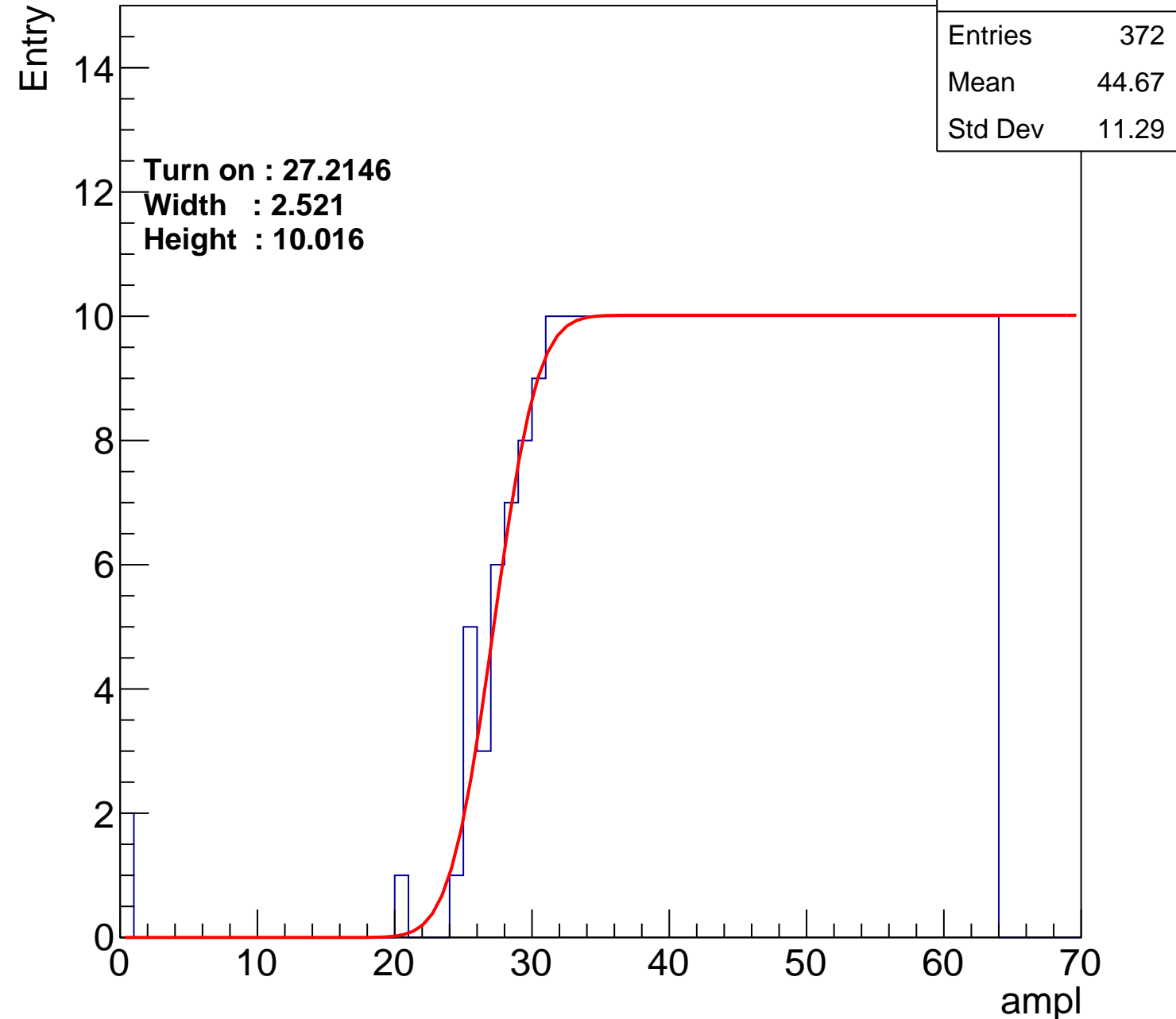
Width : 2.521

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch40

calib_packv5_042523_0143.root, FC#8, port C1

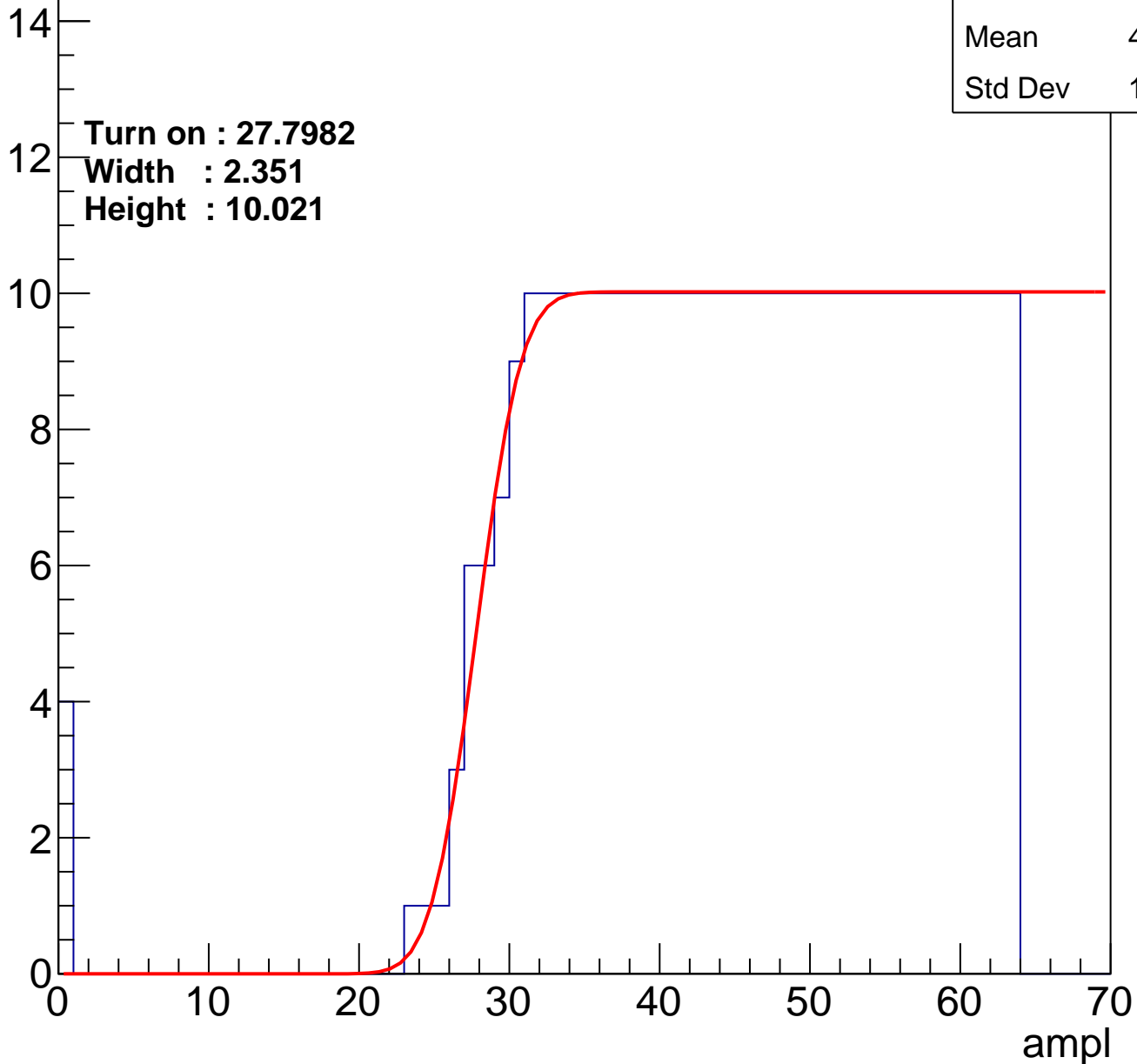
Entries	368
Mean	44.74
Std Dev	11.56

Turn on : 27.7982

Width : 2.351

Height : 10.021

Entry



B0L002S, U21-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.91
Std Dev	10.48

Turn on : 29.4772

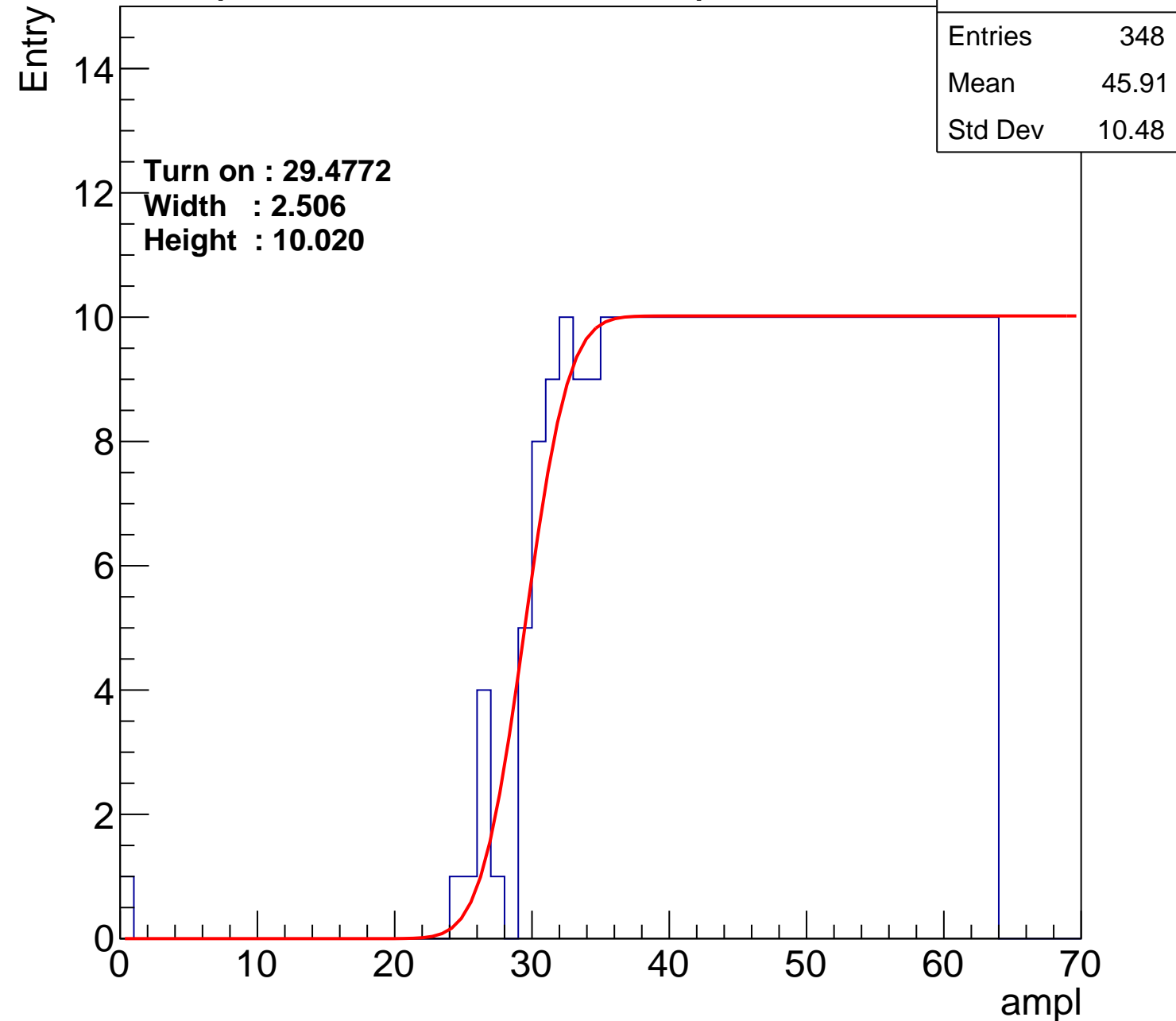
Width : 2.506

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.61
Std Dev	10.58

Turn on : 28.6475

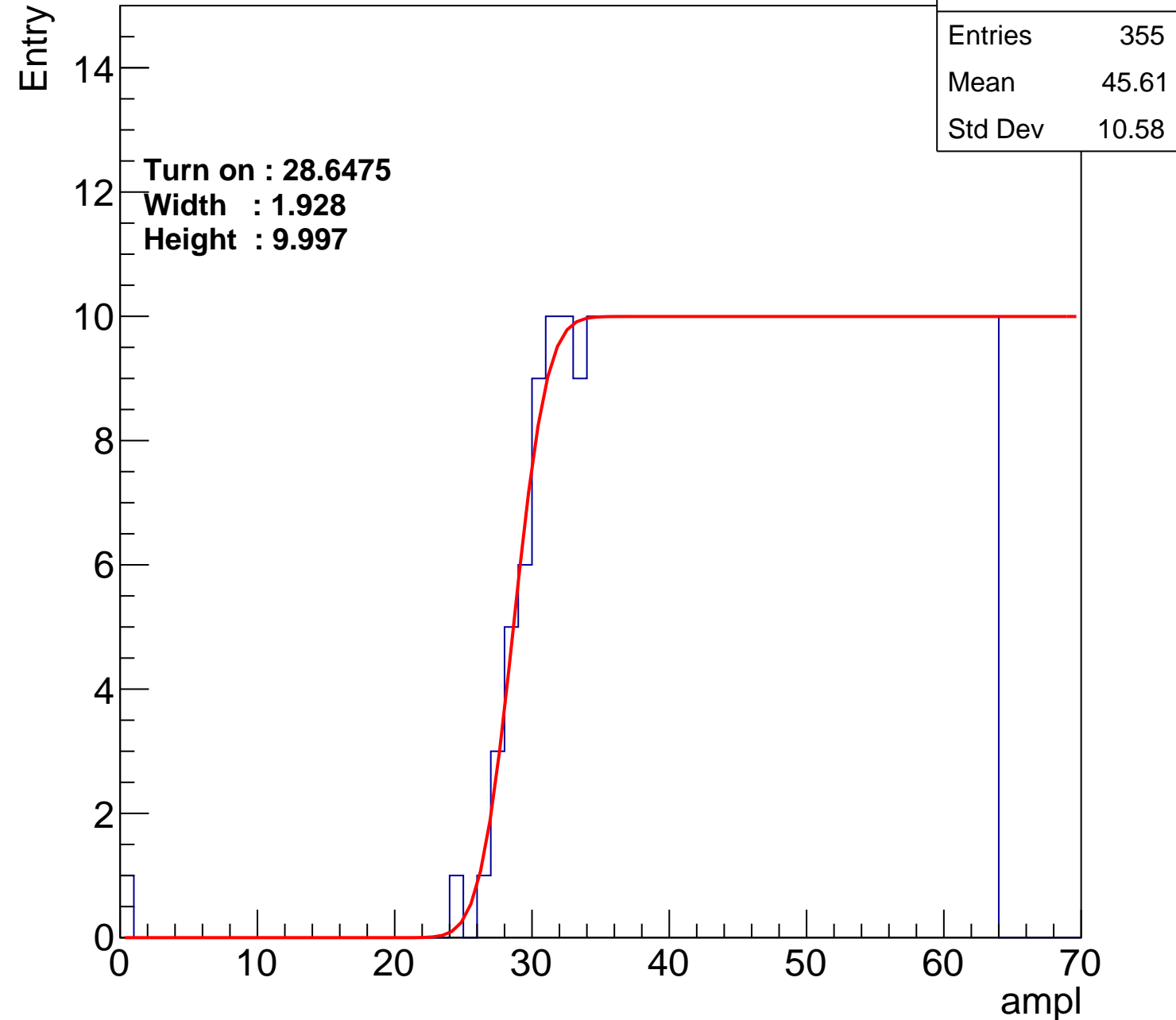
Width : 1.928

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.66
Std Dev	10.62

Turn on : 29.2090

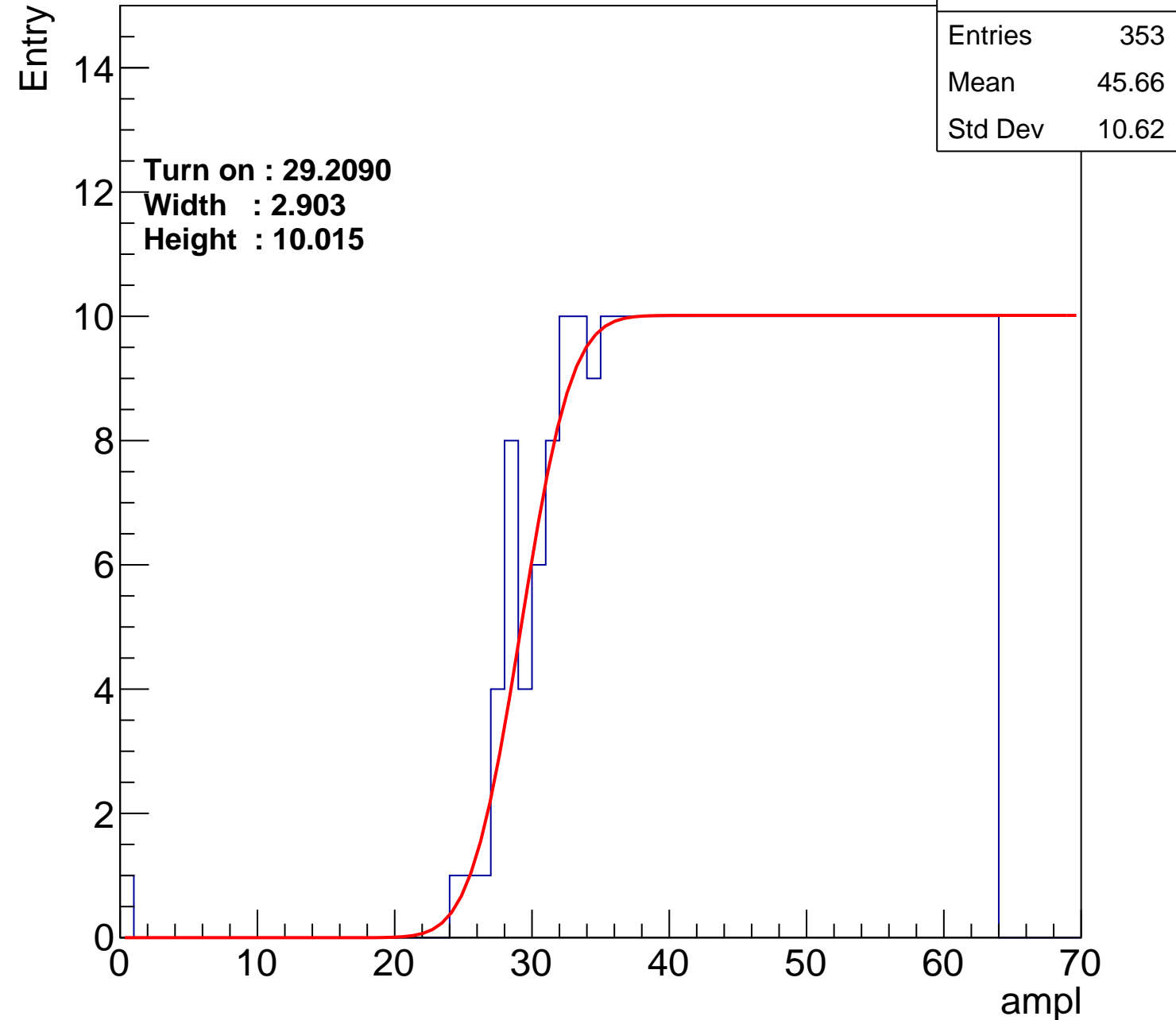
Width : 2.903

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.99
Std Dev	11.16

Turn on : 28.0560

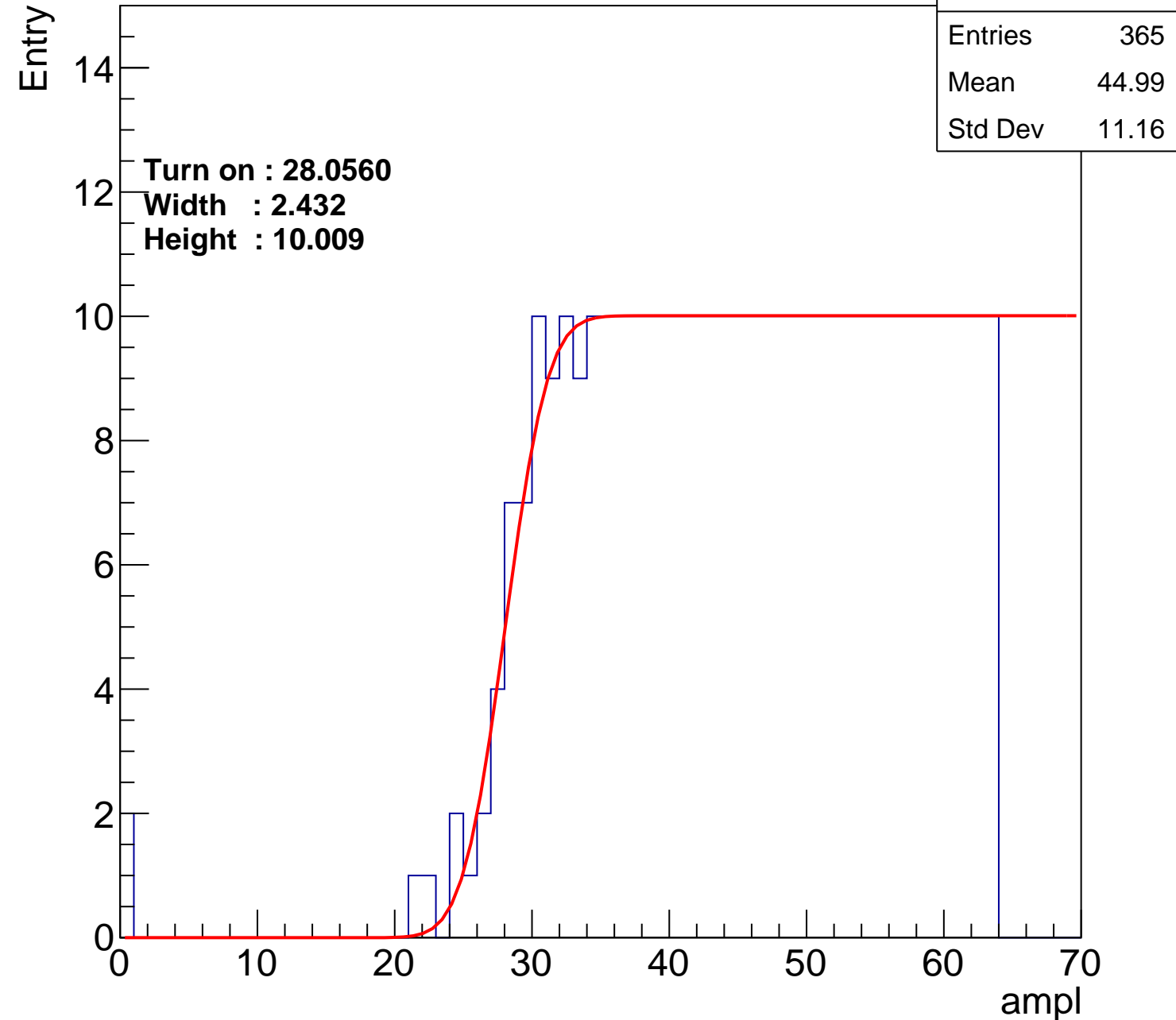
Width : 2.432

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.22
Std Dev	10.53

Turn on : 30.3875

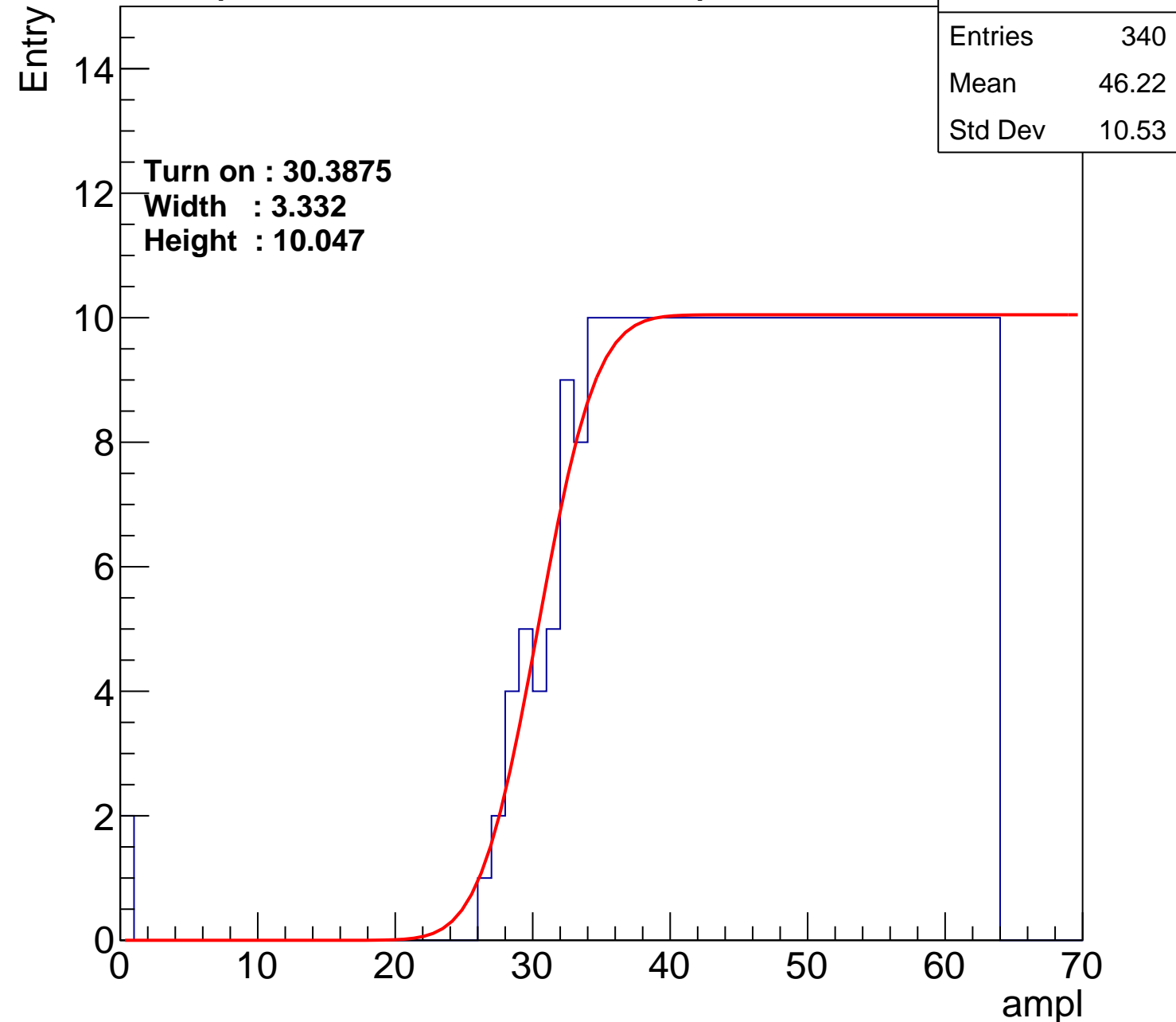
Width : 3.332

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.87
Std Dev	11.38

Turn on : 27.8835

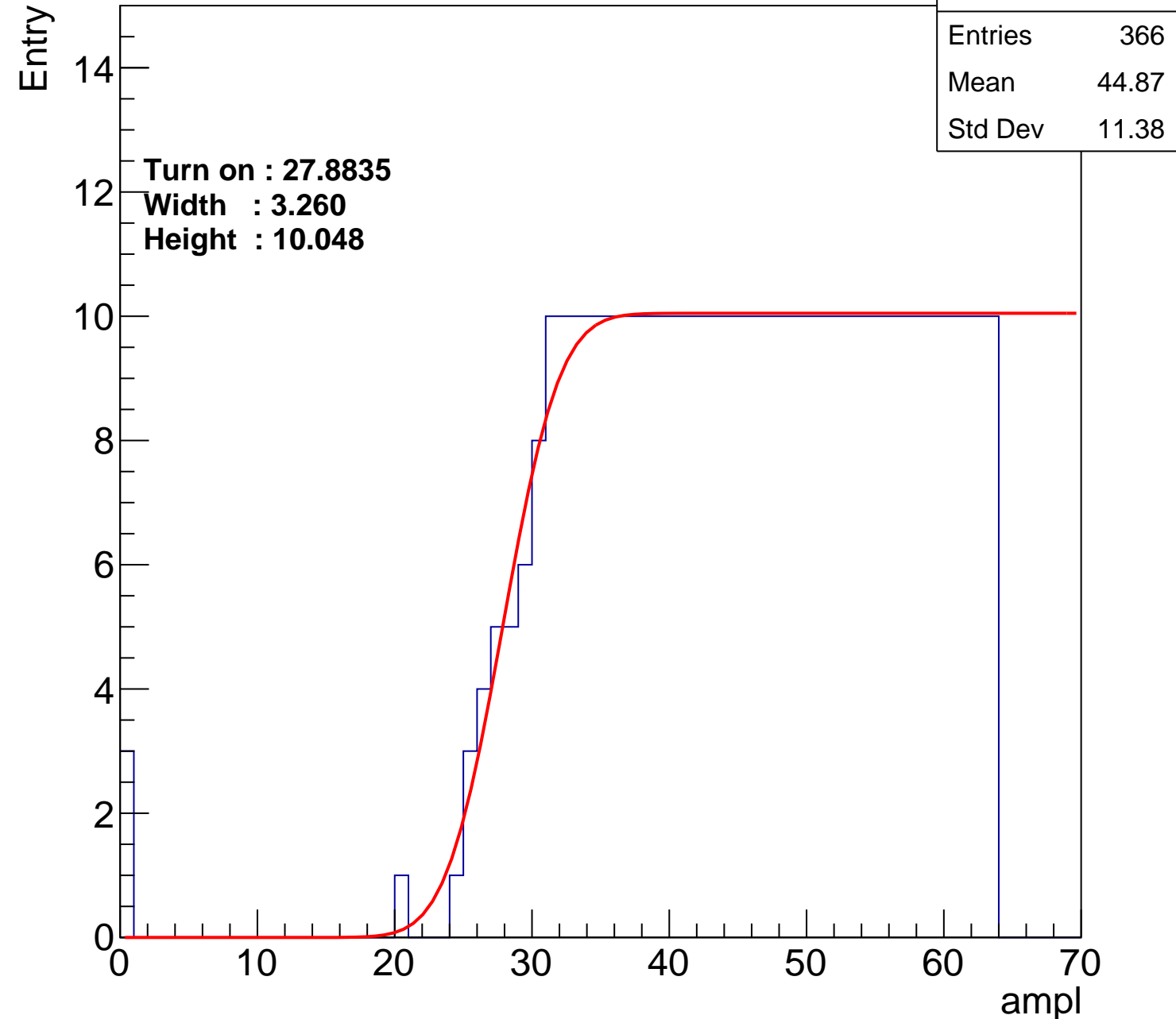
Width : 3.260

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.01
Std Dev	11.36

Turn on : 28.0742

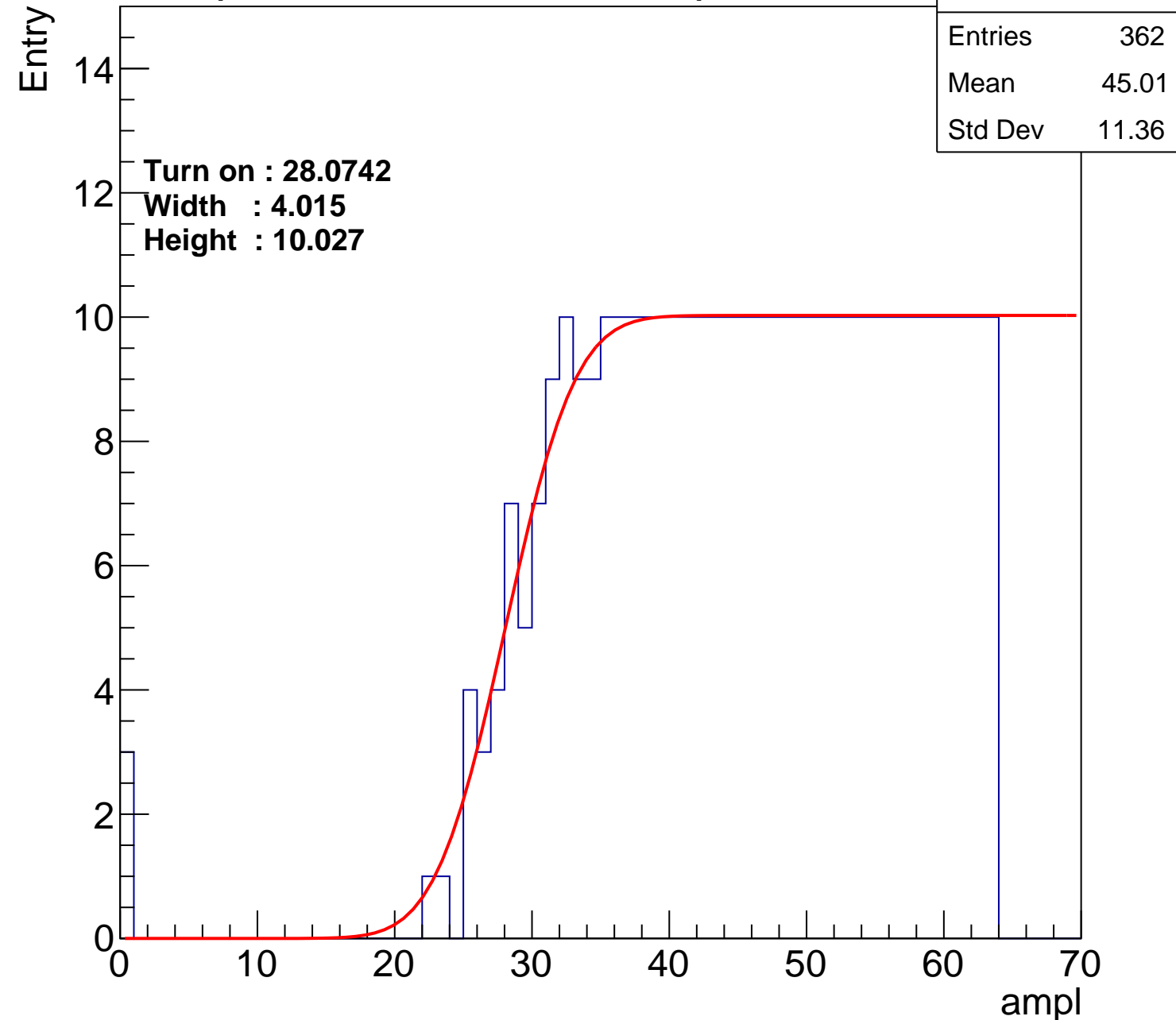
Width : 4.015

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch48

calib_packv5_042523_0143.root, FC#8, port C1

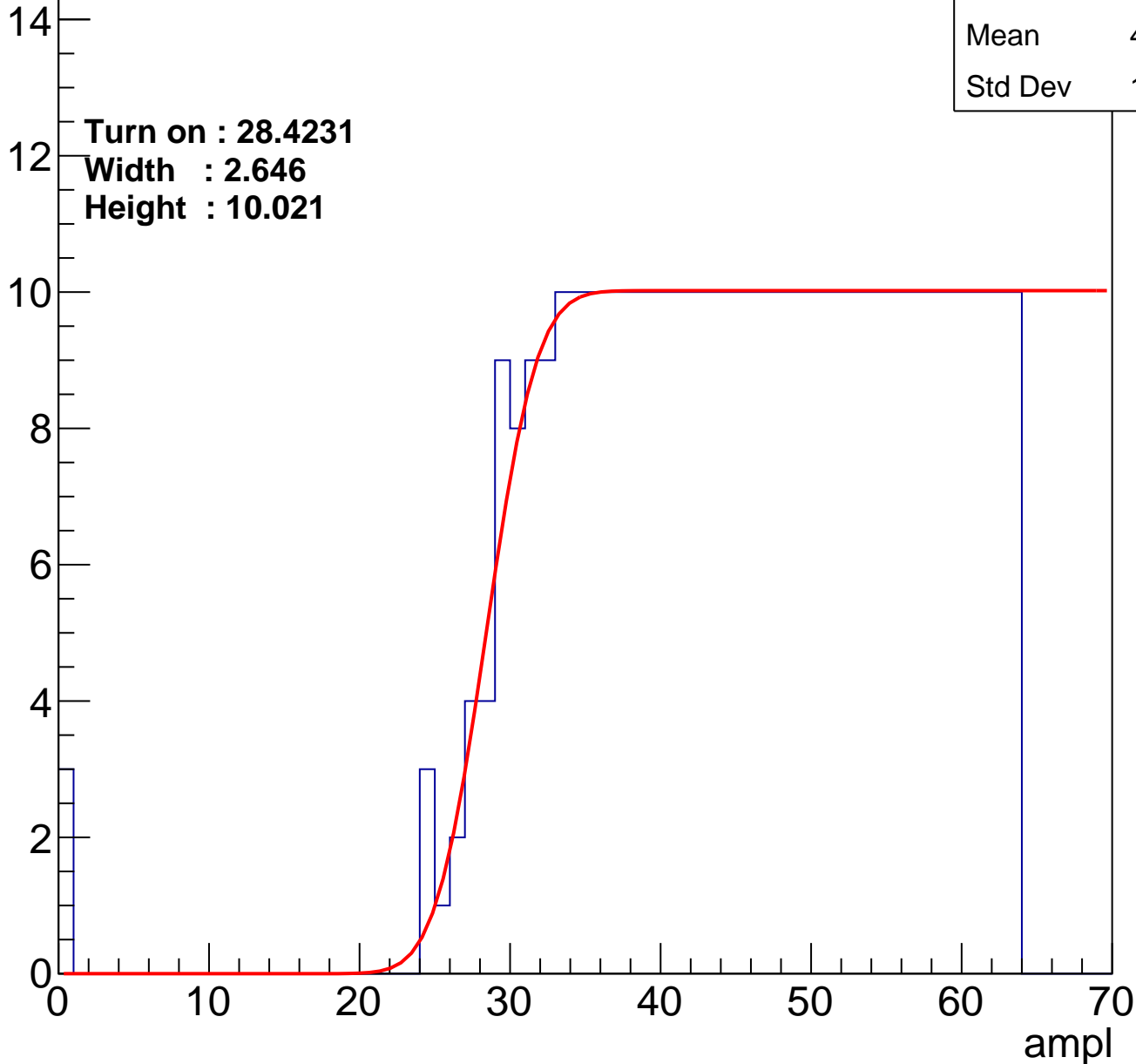
Entries	362
Mean	45.07
Std Dev	11.27

Turn on : 28.4231

Width : 2.646

Height : 10.021

Entry



B0L002S, U21-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 27.7968

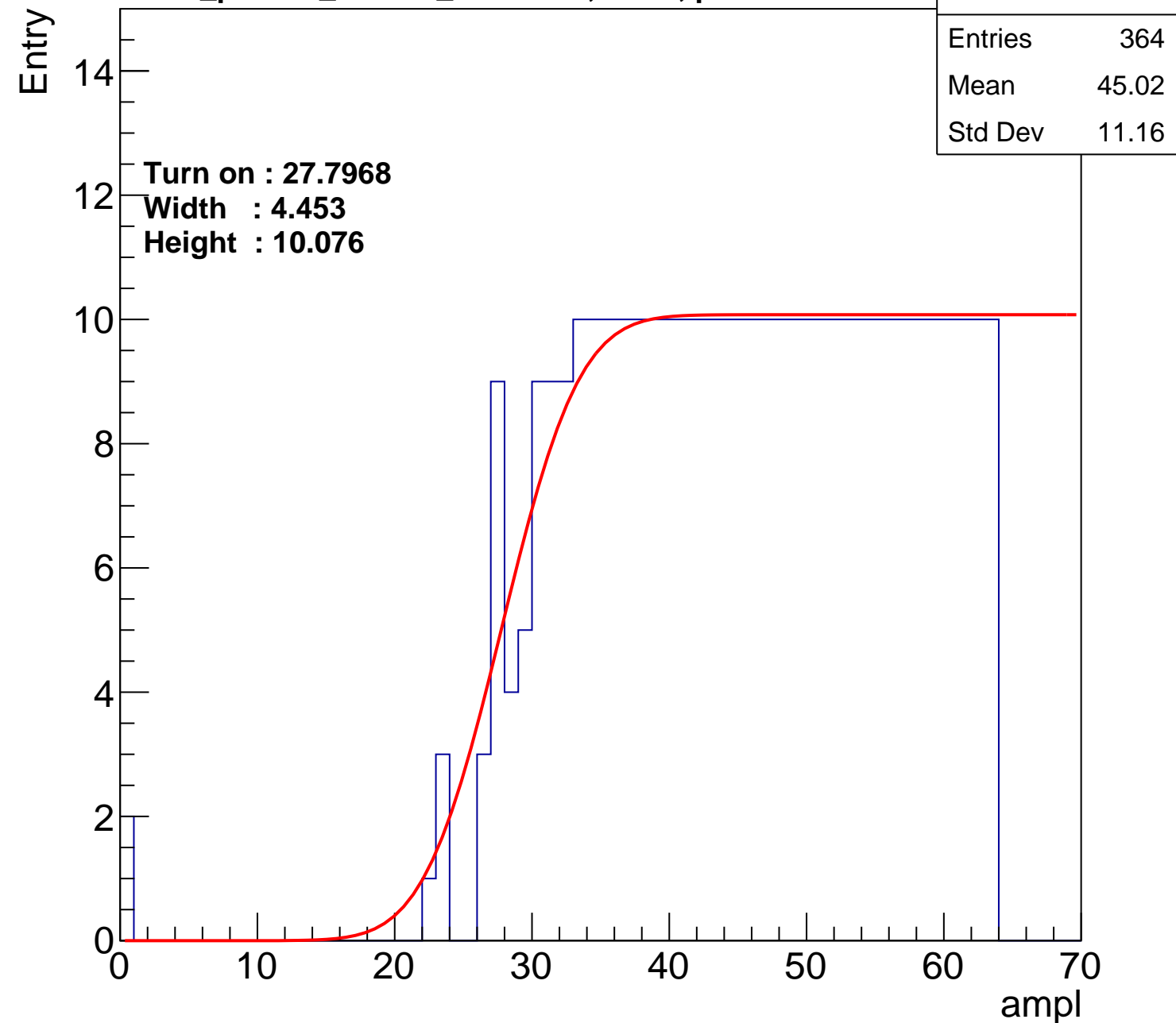
Width : 4.453

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.18
Std Dev	10.85

Turn on : 28.0589

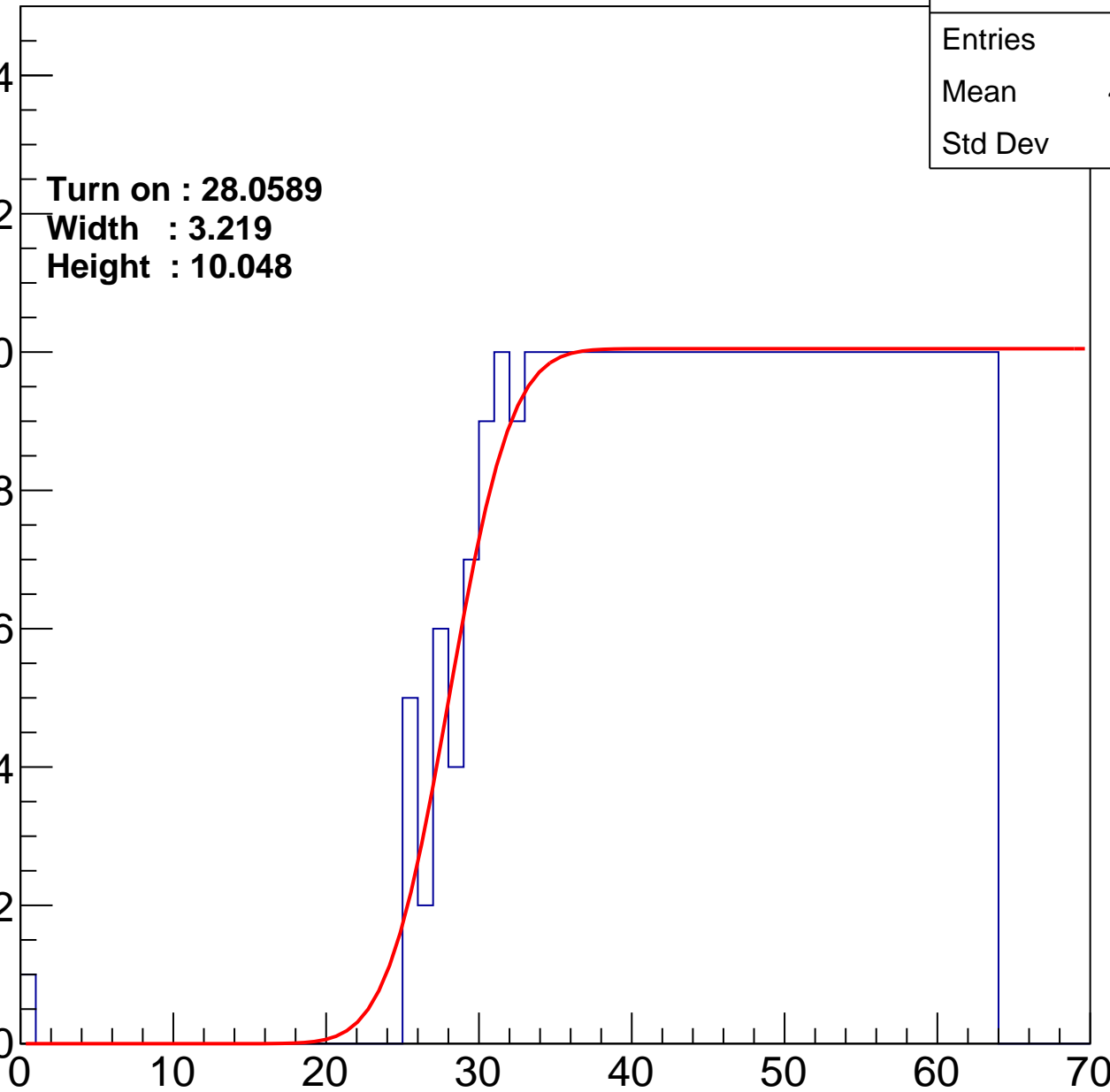
Width : 3.219

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.4751

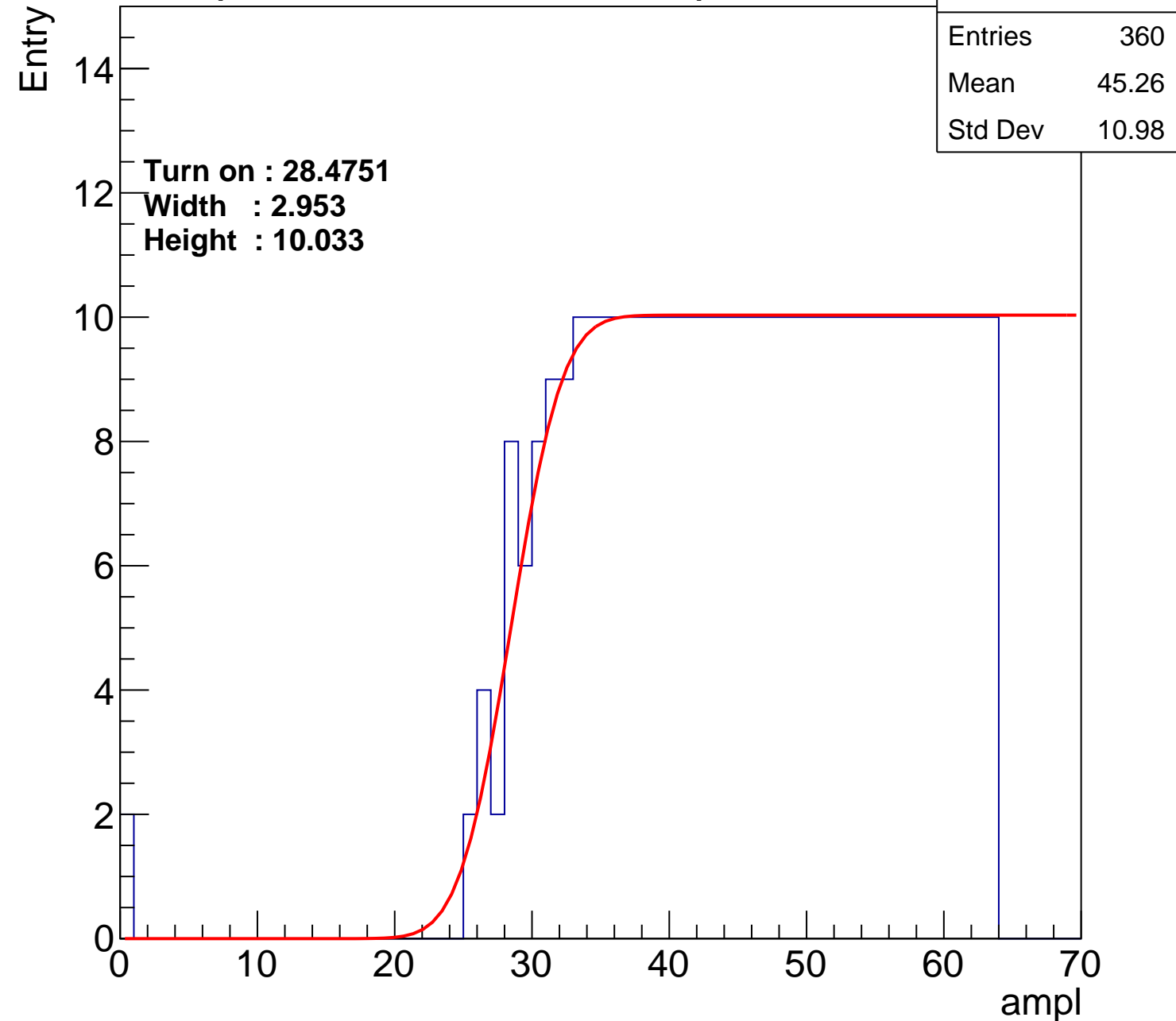
Width : 2.953

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.7899

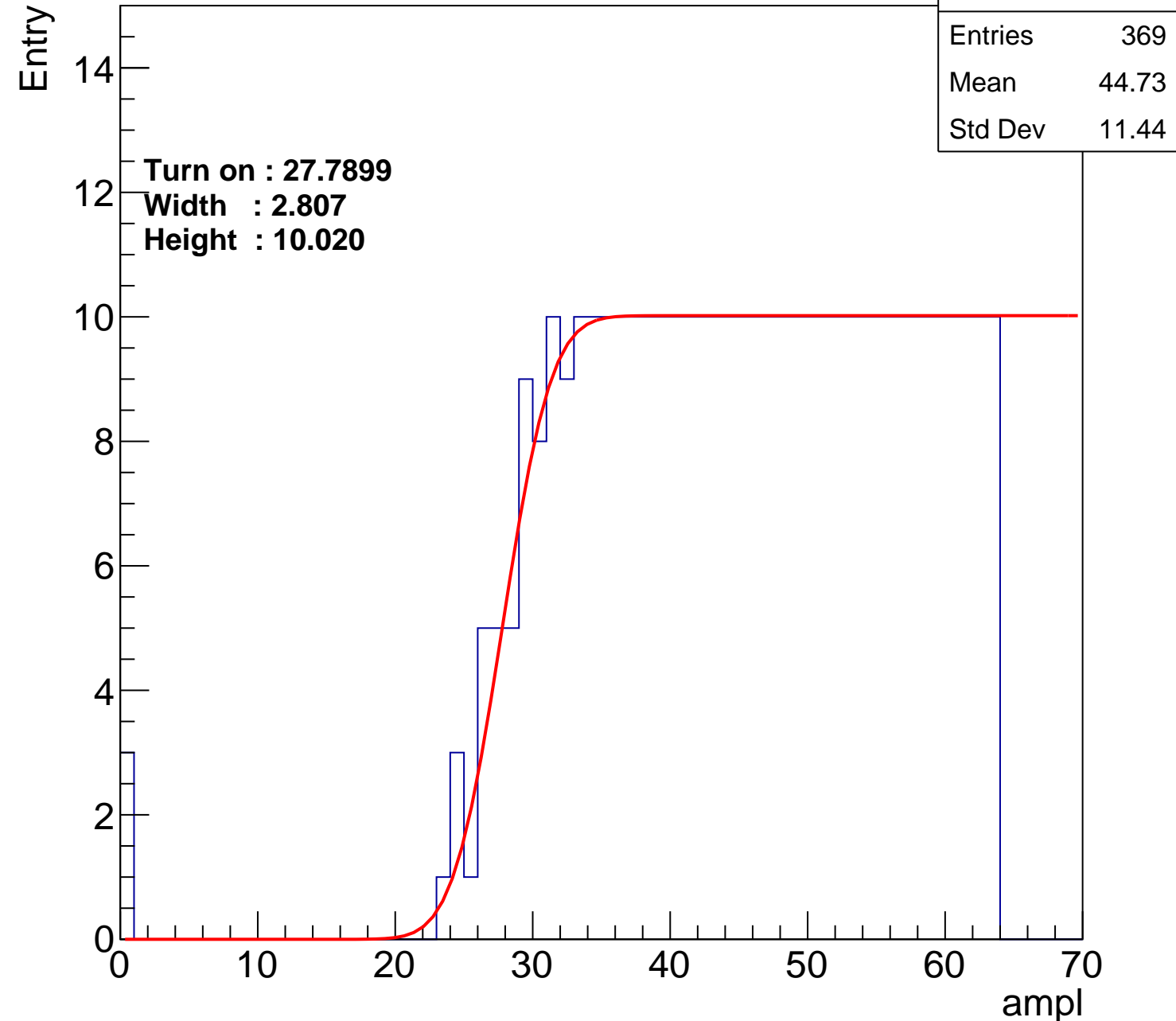
Width : 2.807

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 27.7196

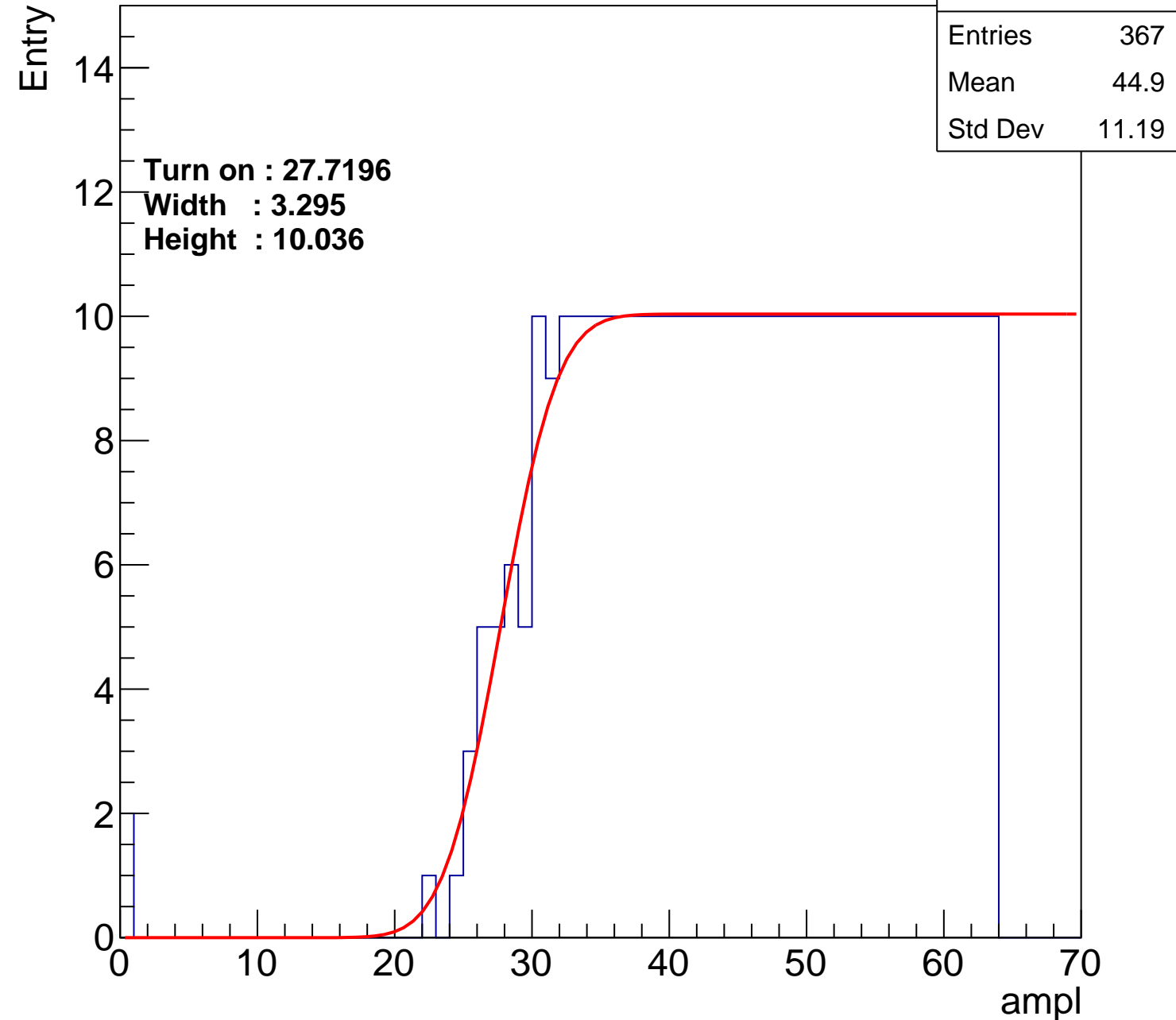
Width : 3.295

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.29
Std Dev	11.2

Turn on : 28.5003

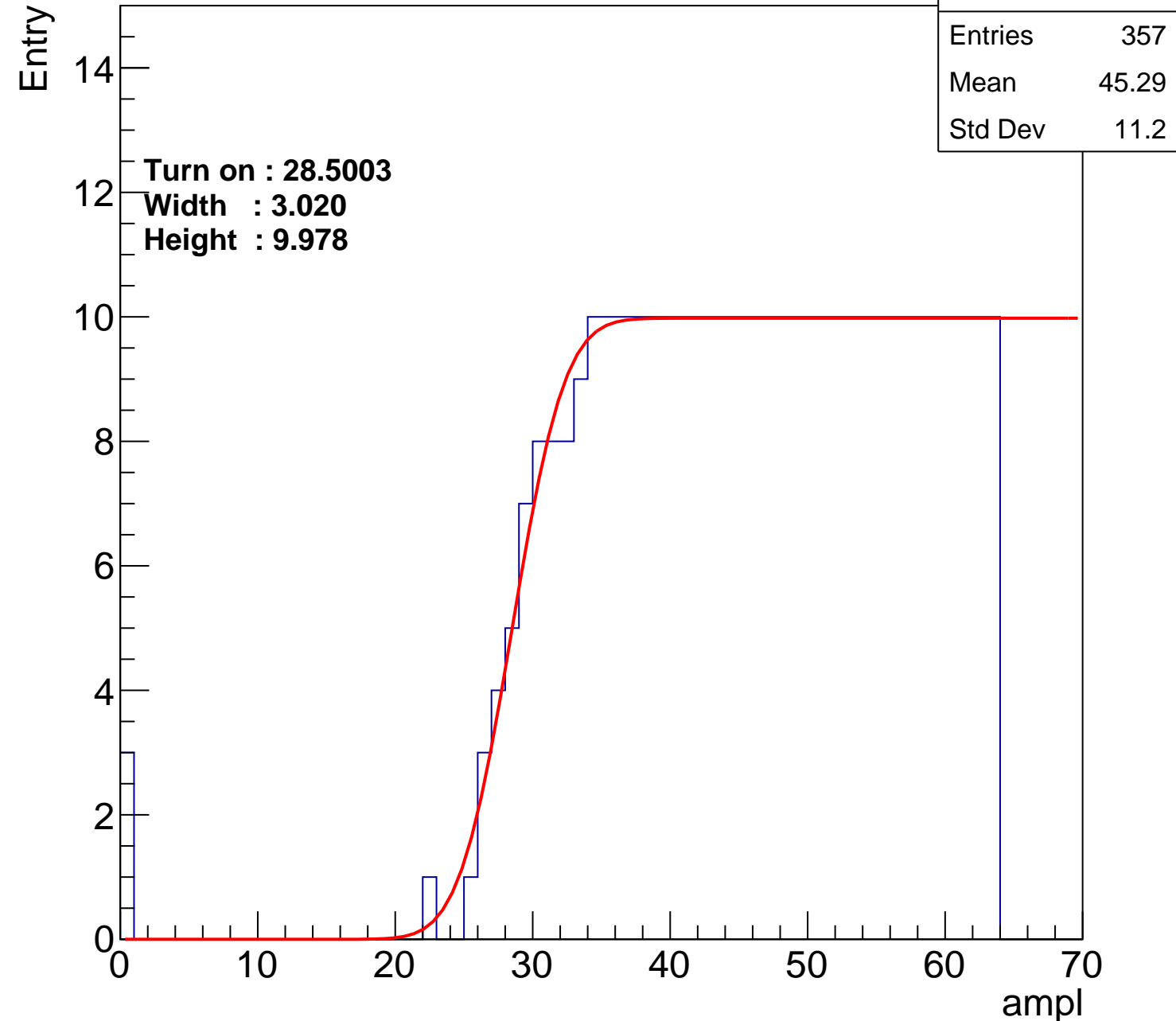
Width : 3.020

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.4957

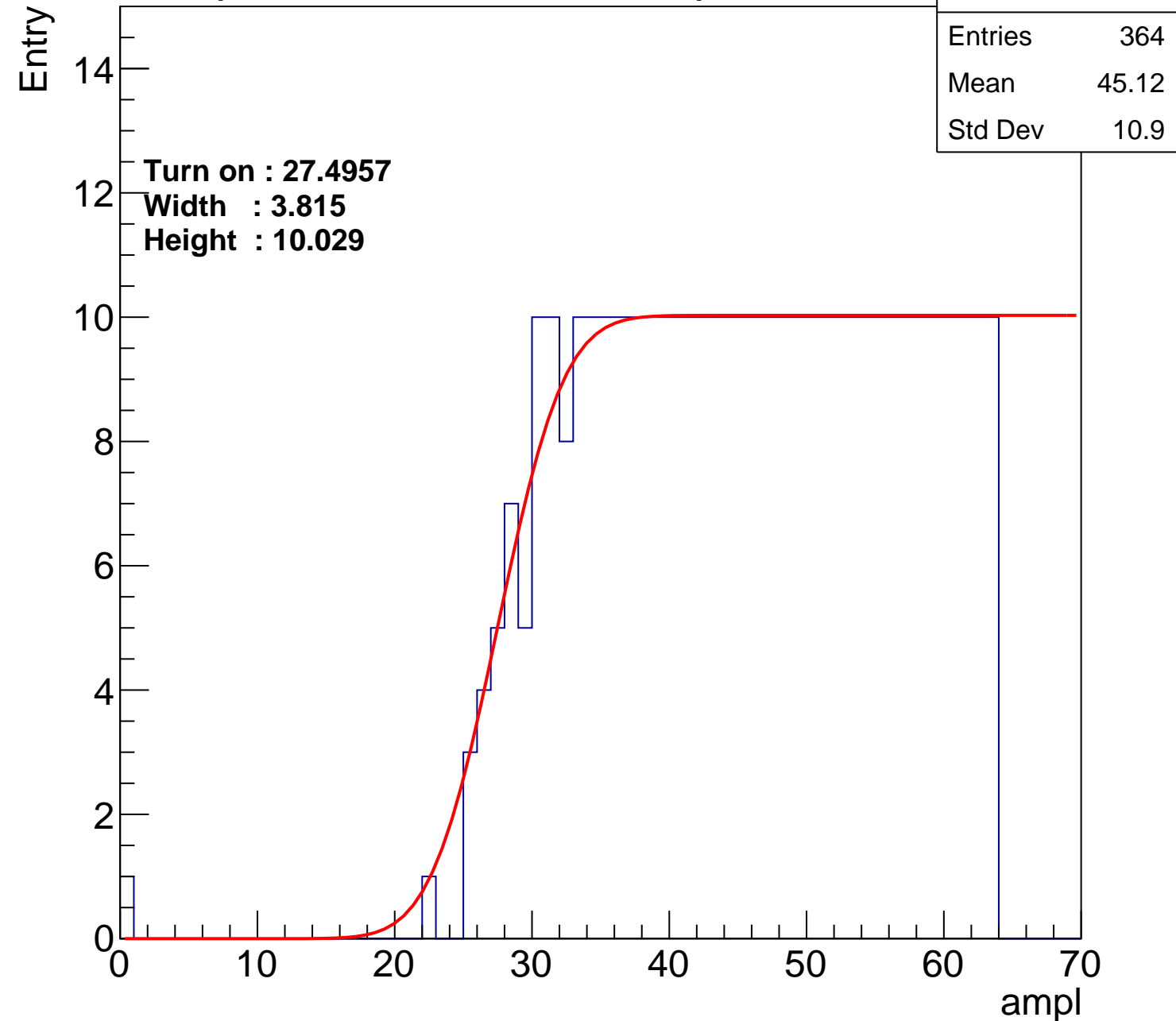
Width : 3.815

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.52
Std Dev	10.68

Turn on : 28.7889

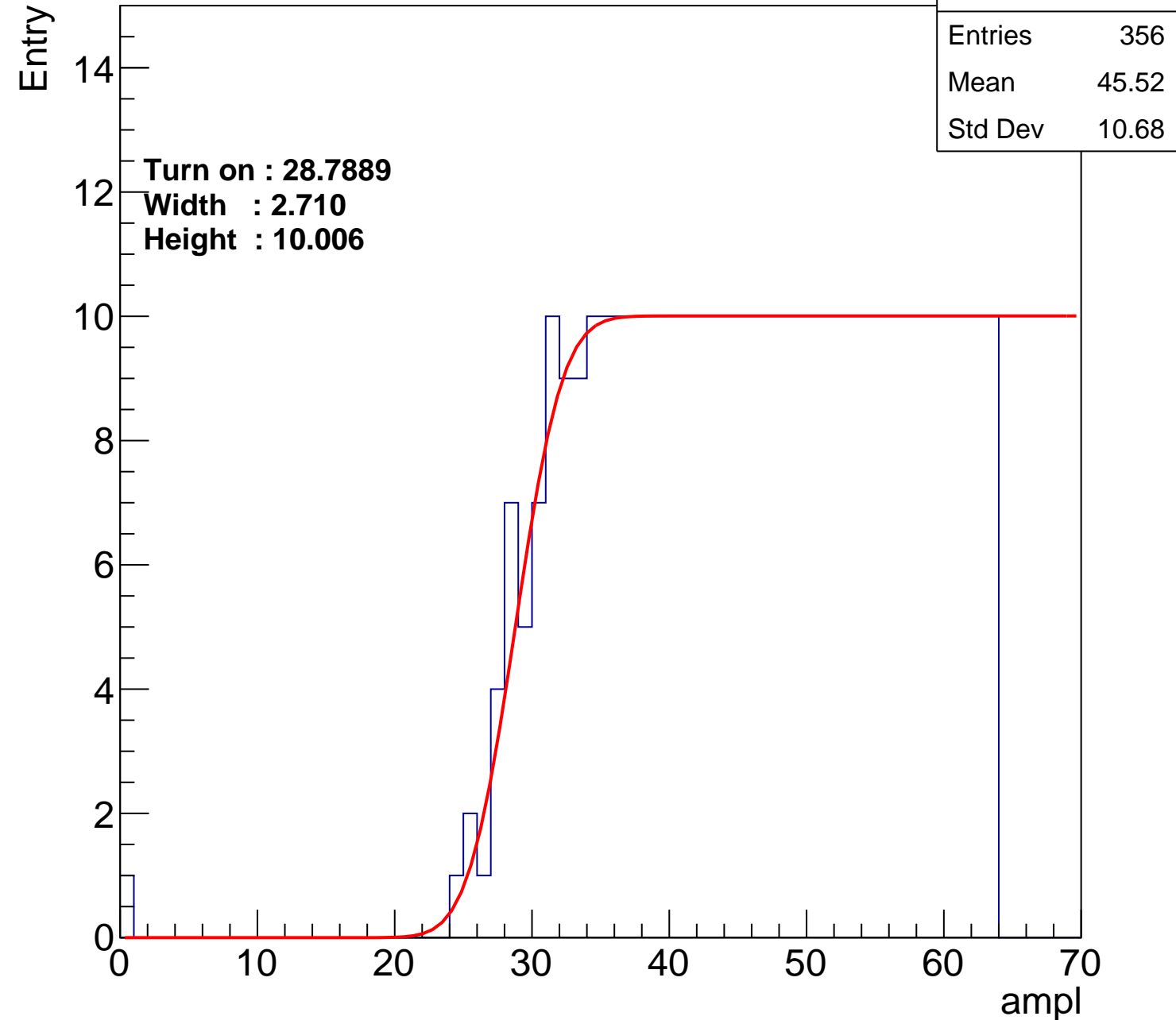
Width : 2.710

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 27.3938

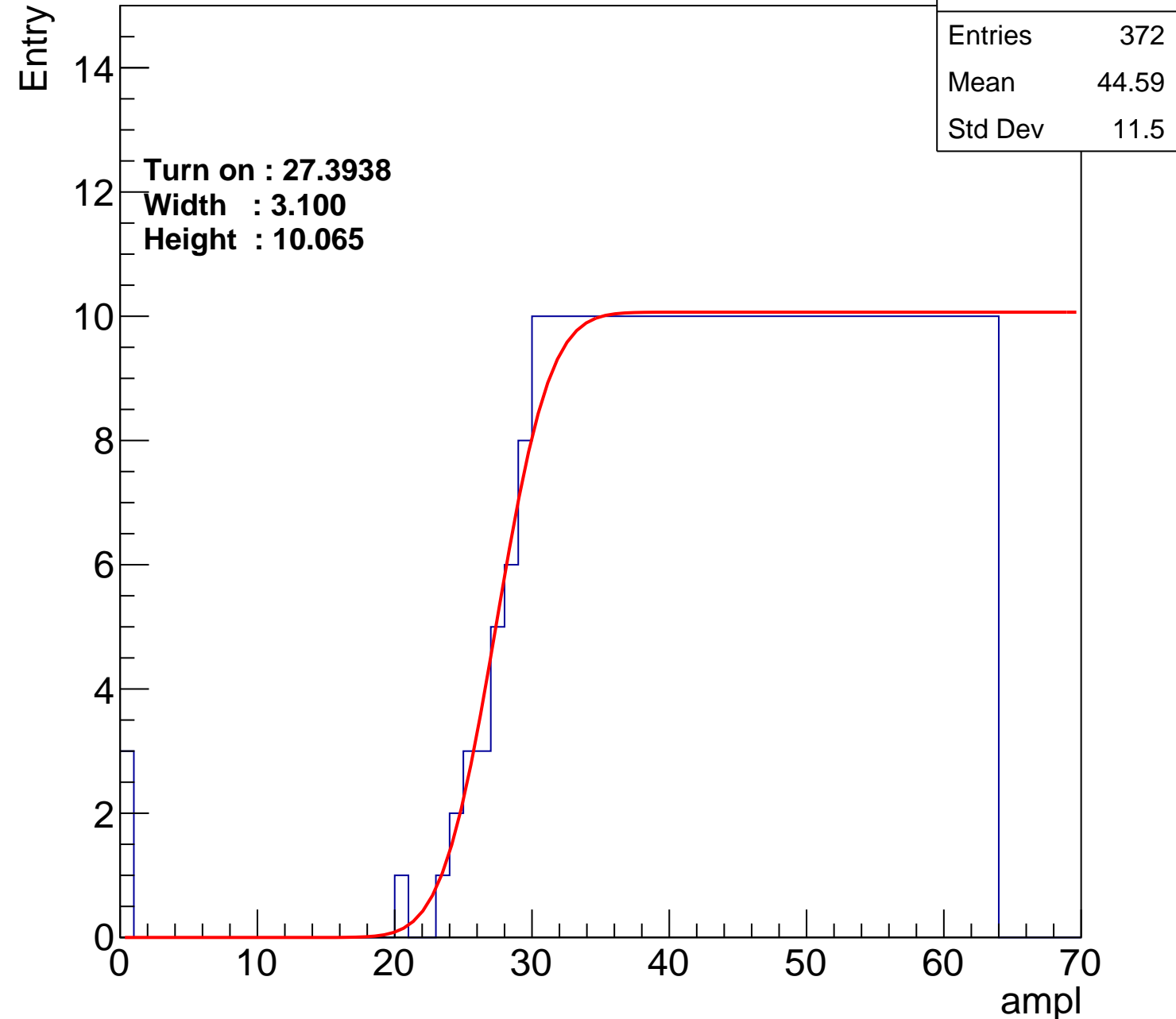
Width : 3.100

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.21
Std Dev	11

Std Dev	11
---------	----

Height : 10.014



B0L002S, U21-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 26.9886

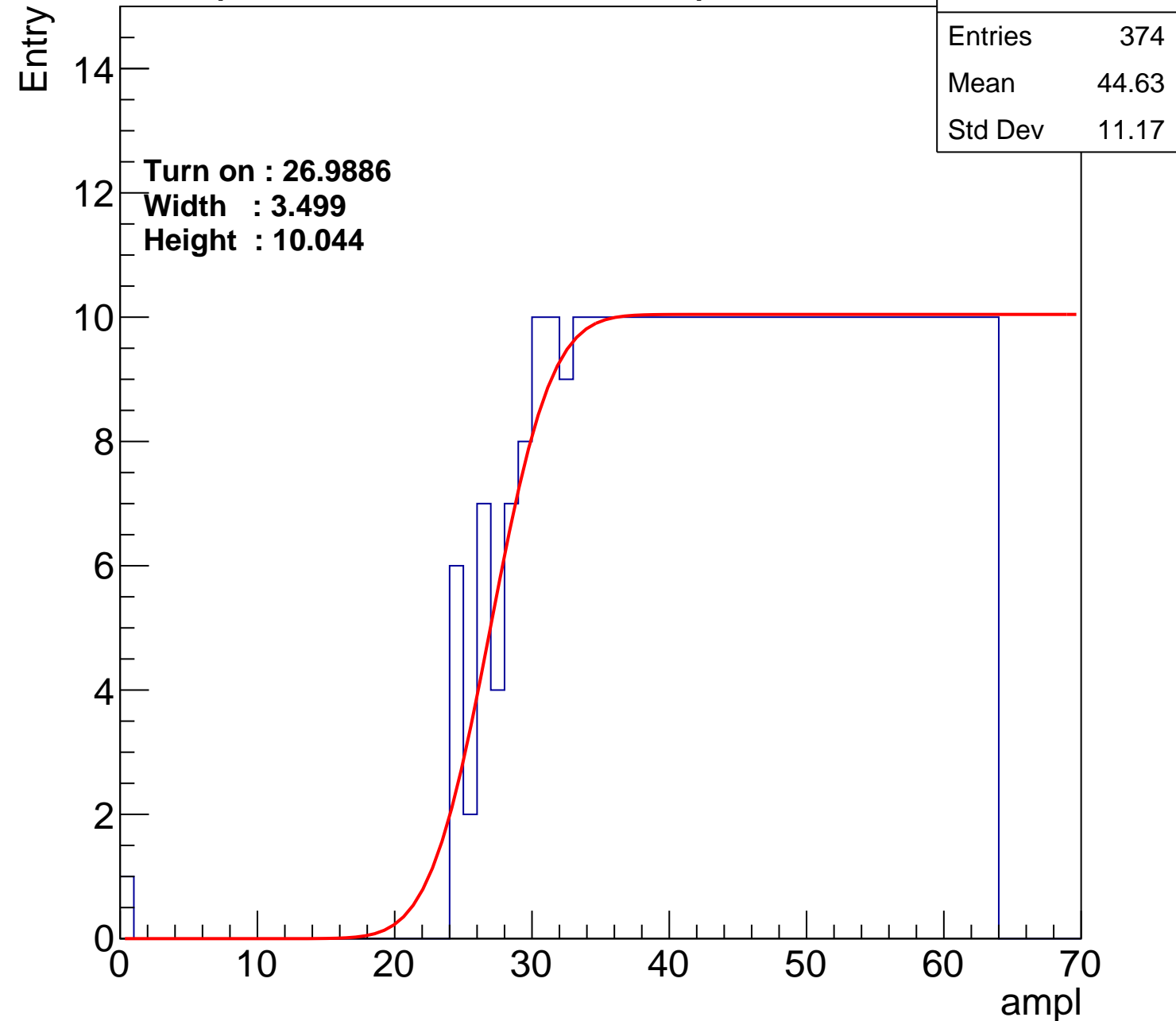
Width : 3.499

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch60

calib_packv5_042523_0143.root, FC#8, port C1

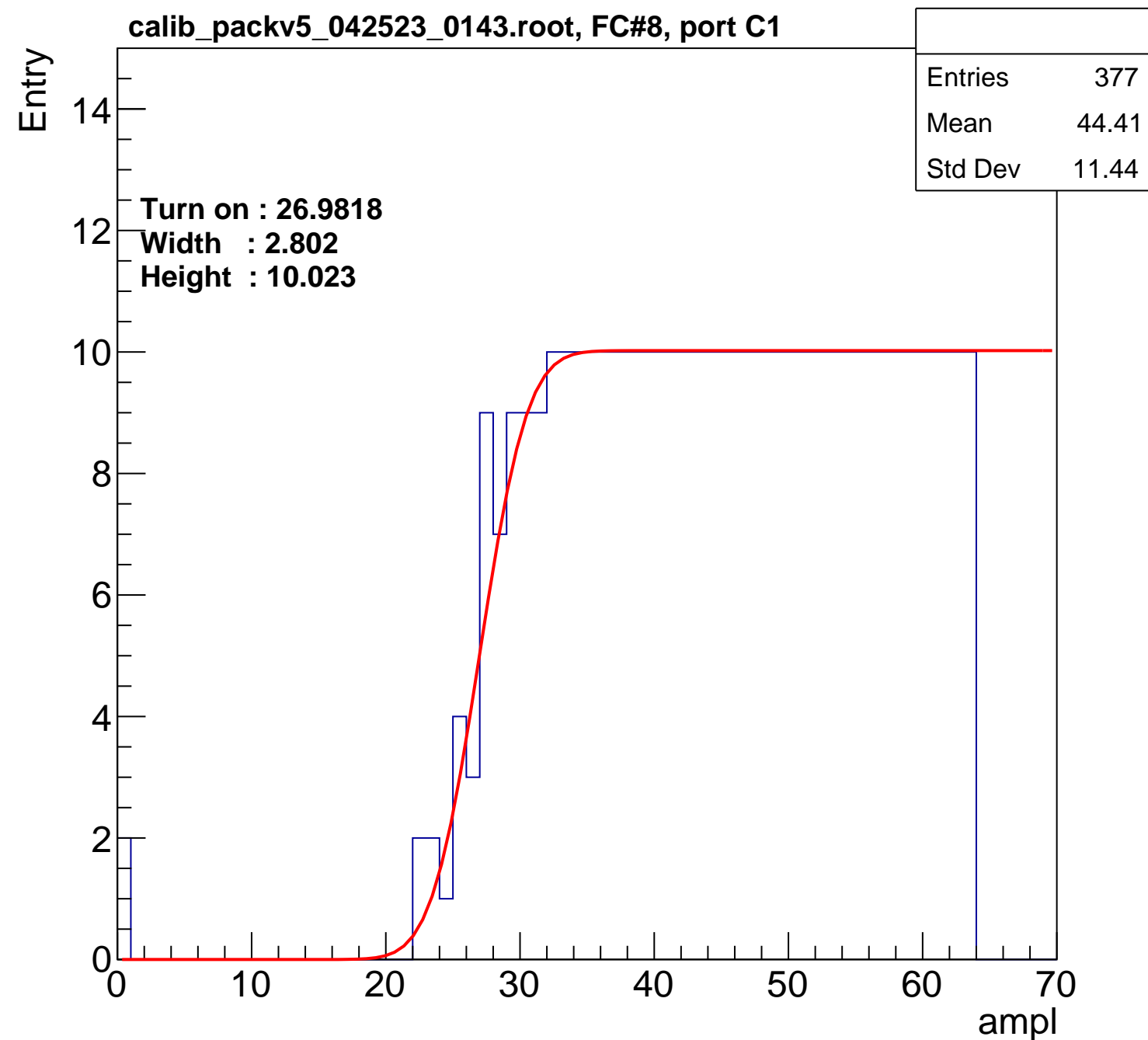
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9818
Width : 2.802
Height : 10.023

Entries	377
Mean	44.41
Std Dev	11.44

ampl



B0L002S, U21-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.36
Std Dev	10.93

Turn on : 28.8531

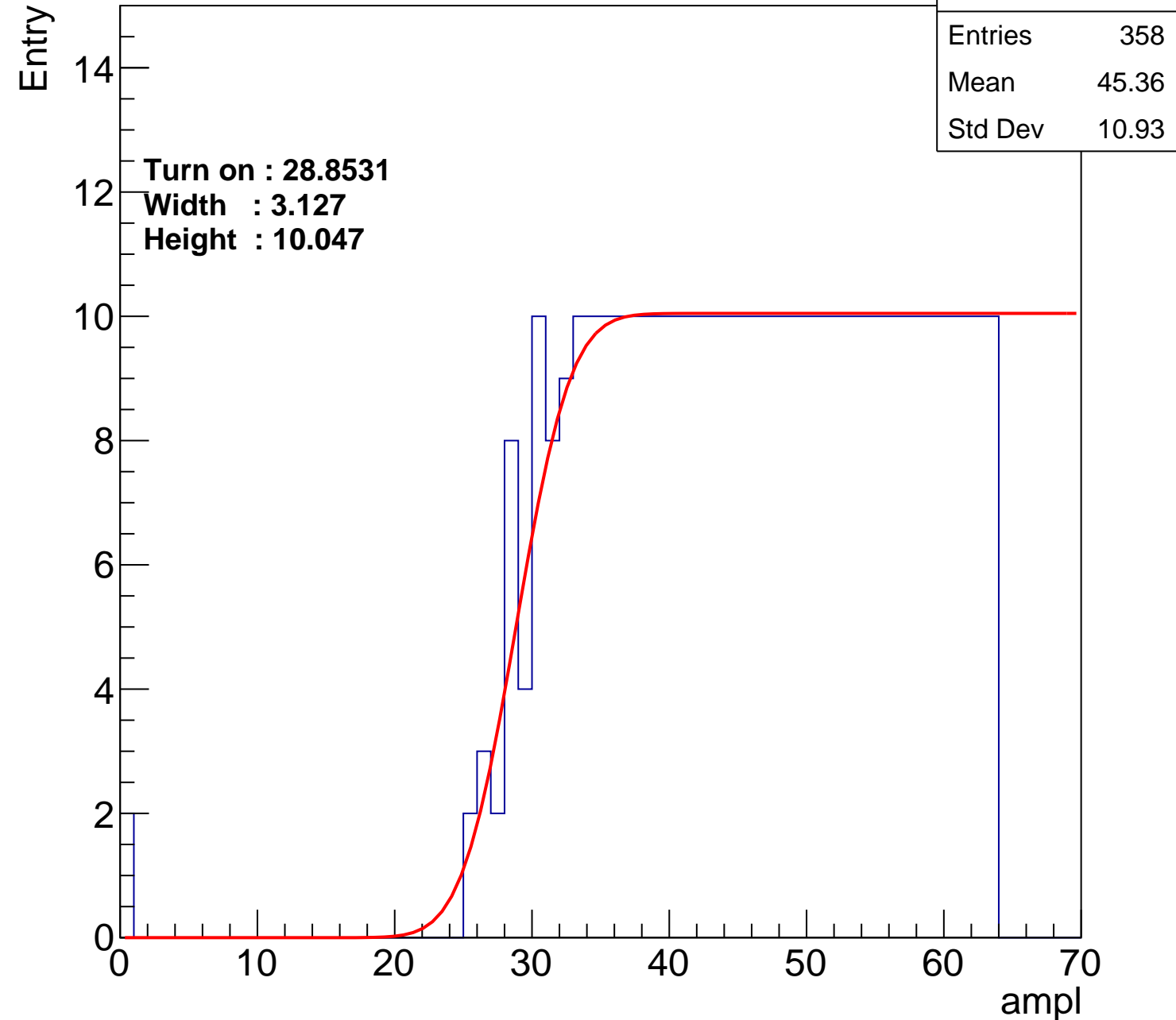
Width : 3.127

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.06
Std Dev	10.89

Turn on : 27.2400

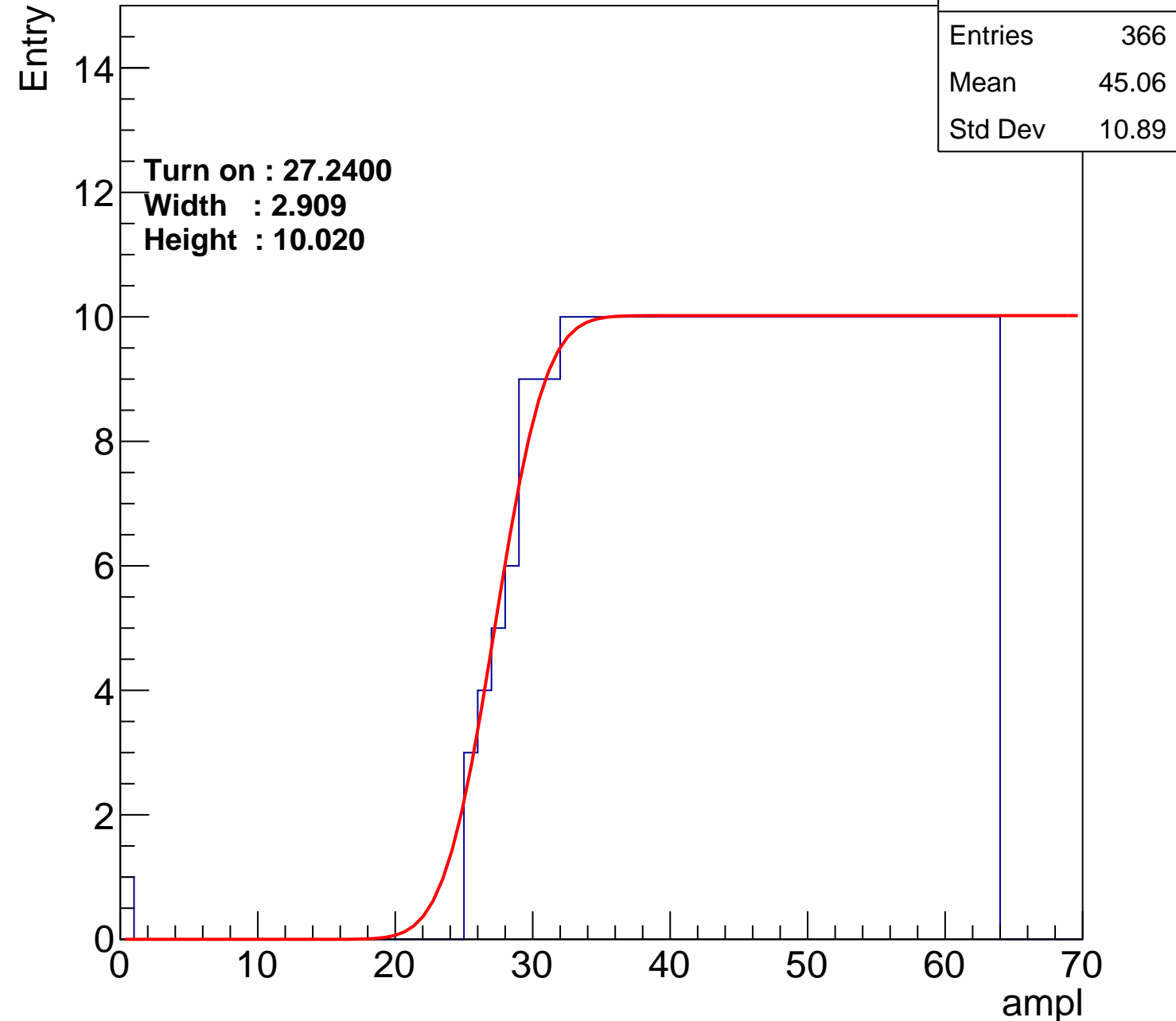
Width : 2.909

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.92
Std Dev	11.06

Turn on : 27.4093

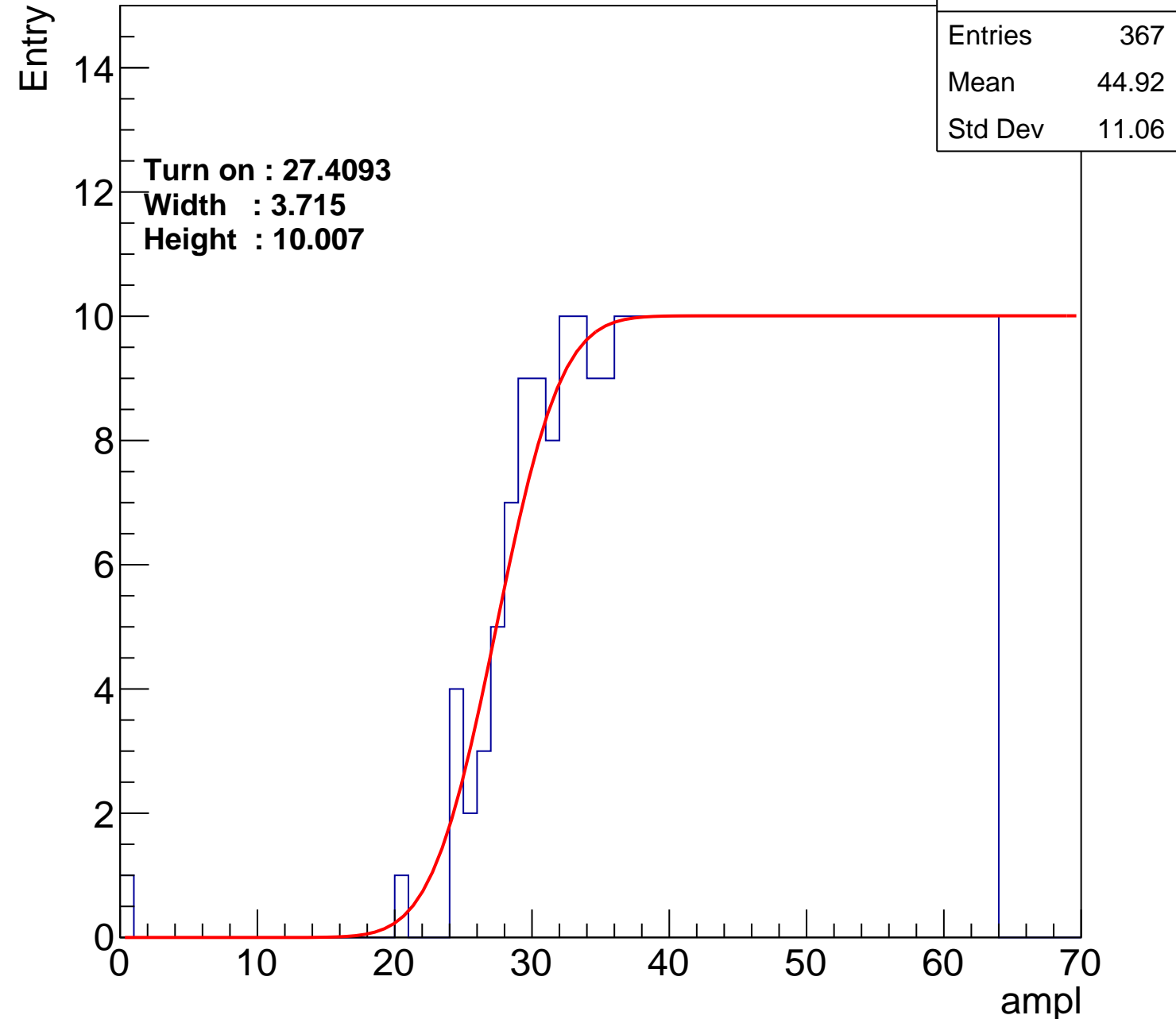
Width : 3.715

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.78
Std Dev	11.82

Turn on : 25.3772

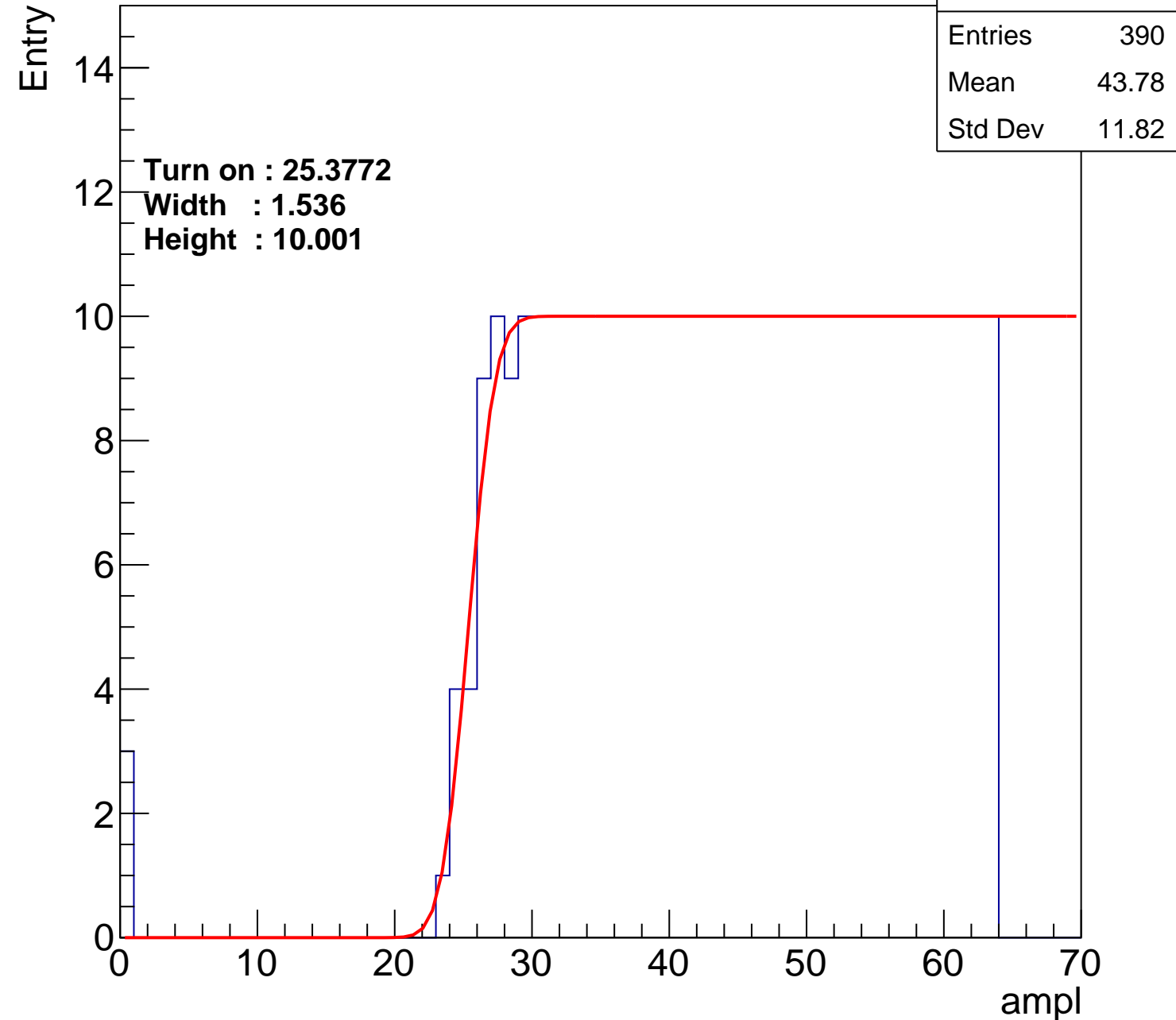
Width : 1.536

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch65

calib_packv5_042523_0143.root, FC#8, port C1

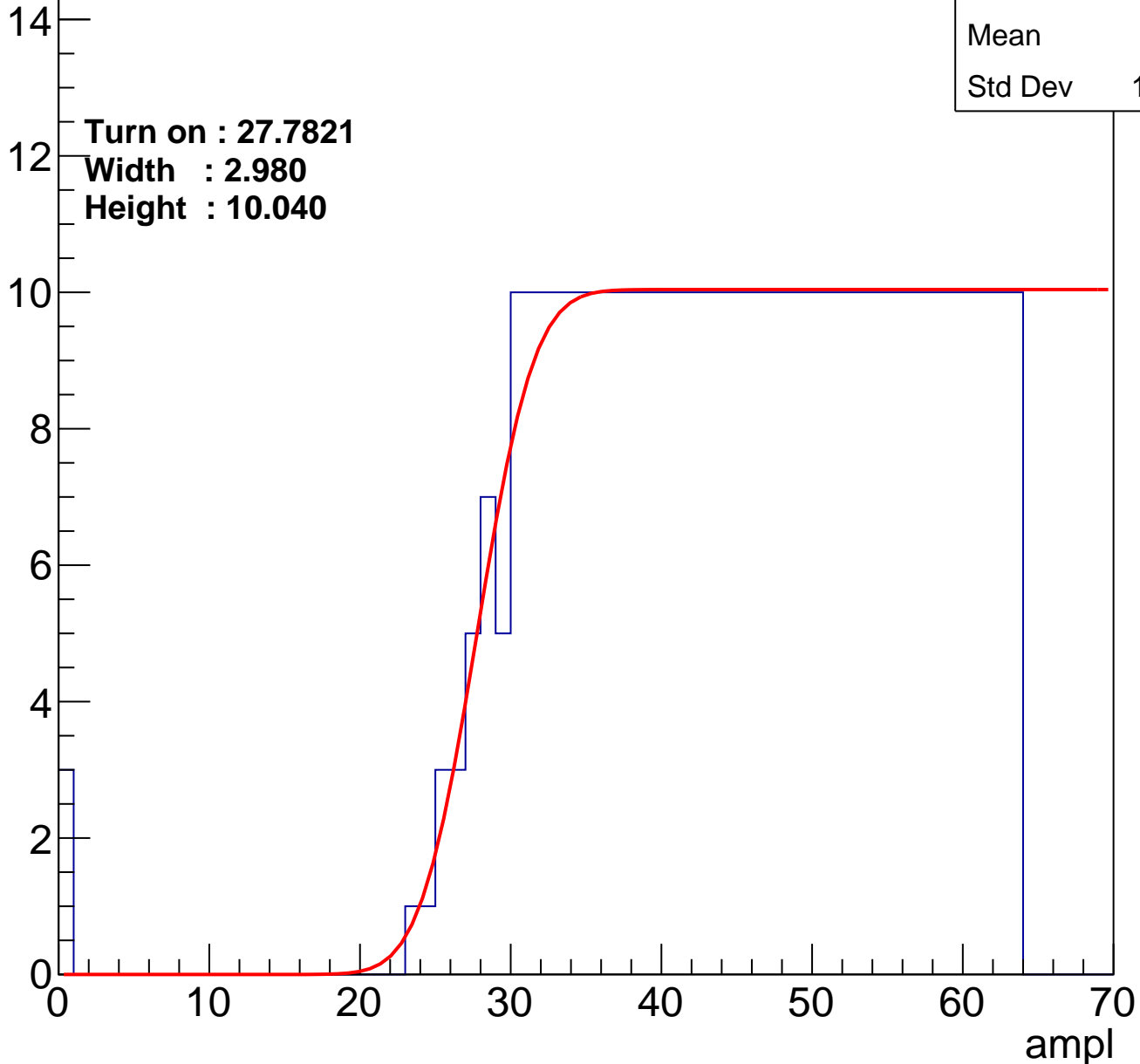
Entries	368
Mean	44.8
Std Dev	11.38

Turn on : 27.7821

Width : 2.980

Height : 10.040

Entry



B0L002S, U21-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.91
Std Dev	10.48

Turn on : 29.8835

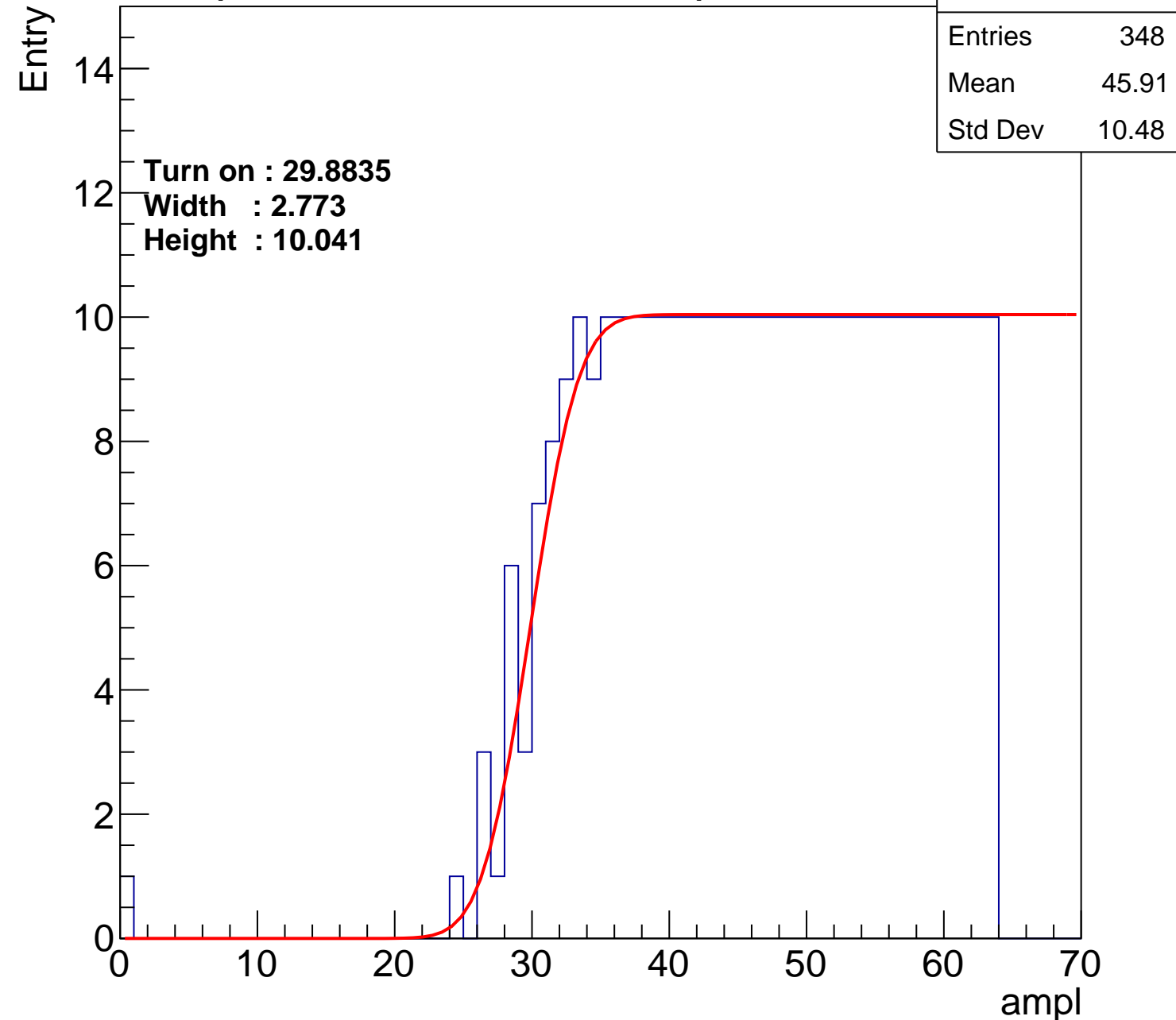
Width : 2.773

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.78
Std Dev	10.91

Turn on : 29.4367

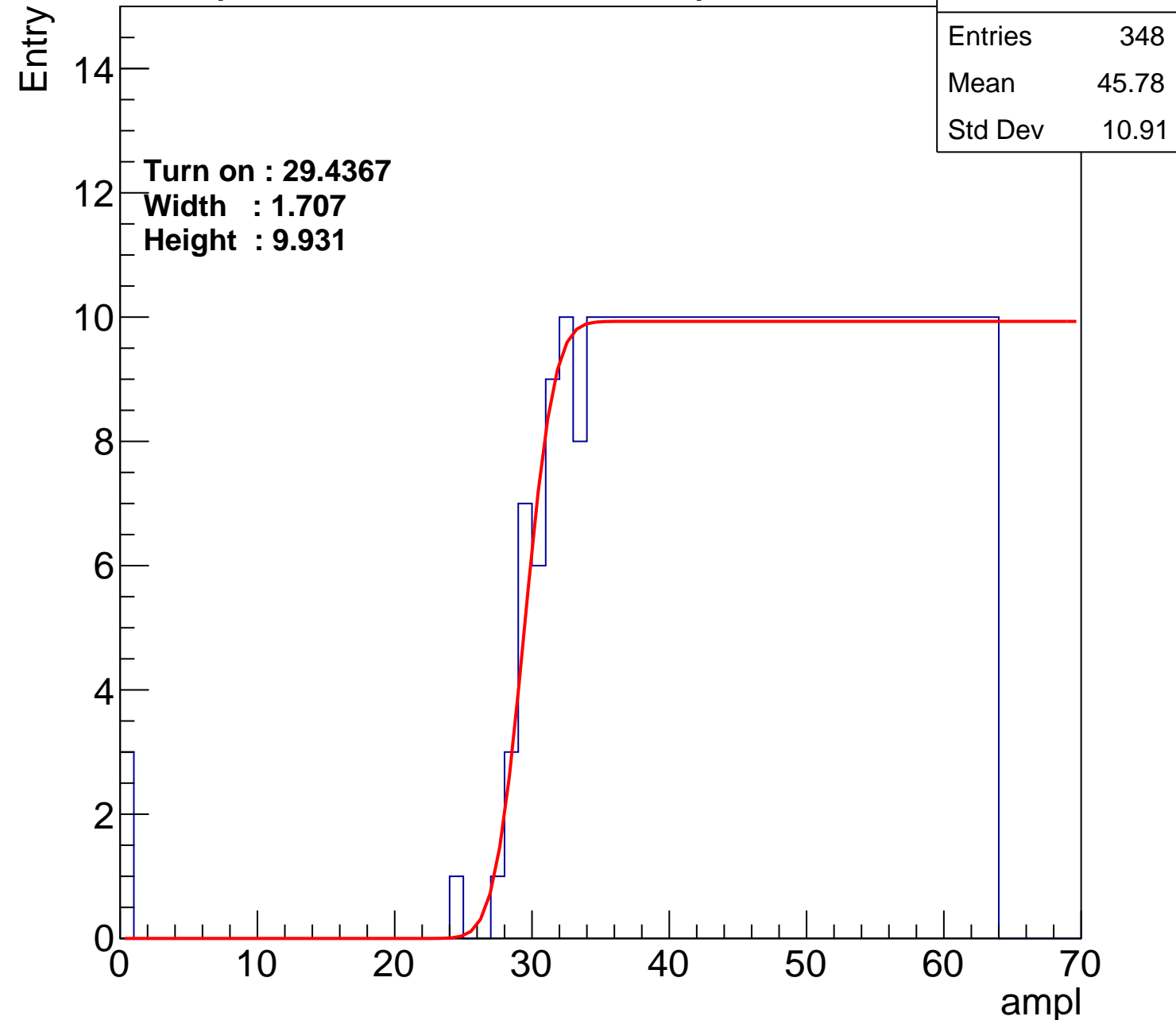
Width : 1.707

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.4380

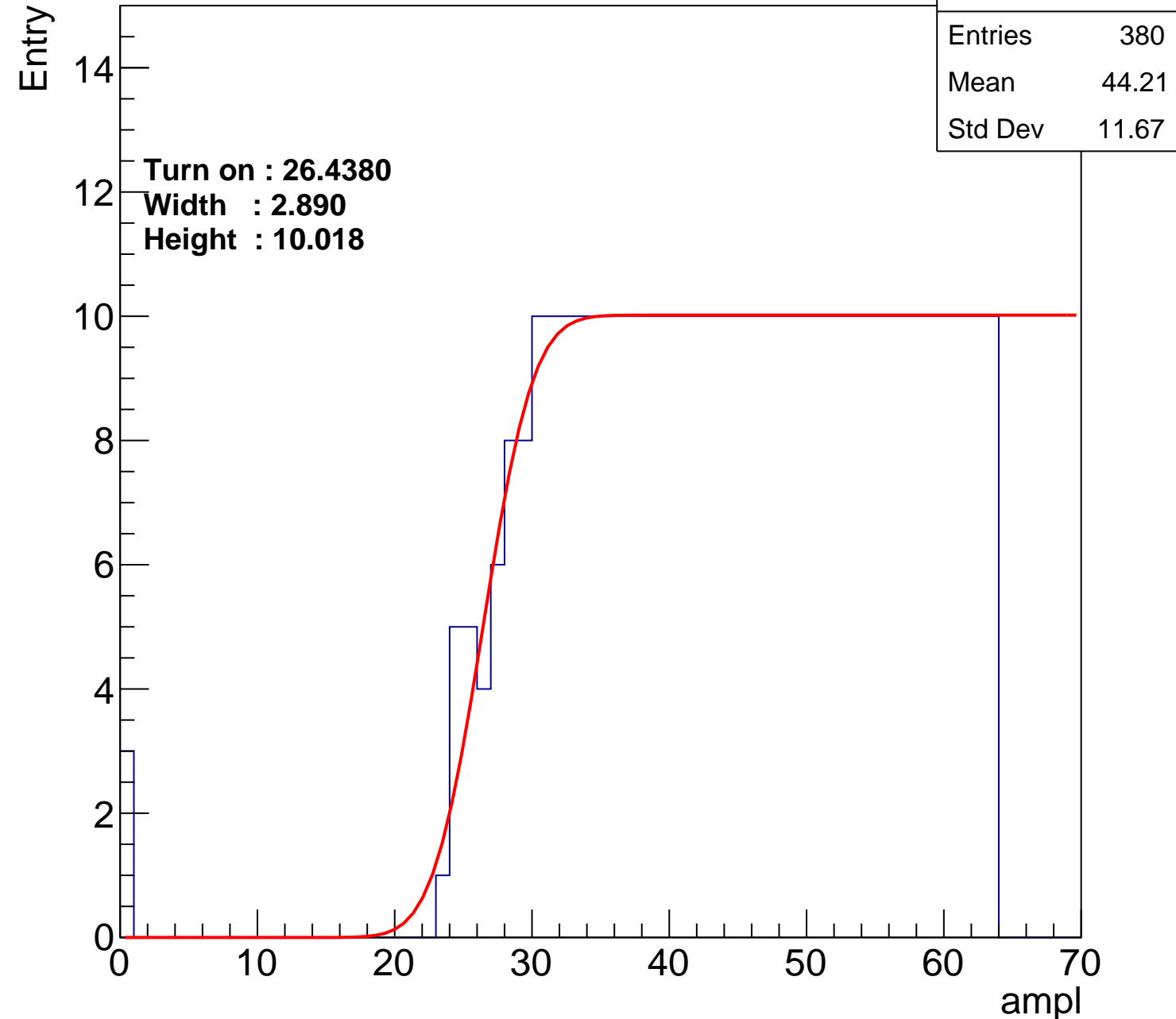
Width : 2.890

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.73
Std Dev	11.16

Turn on : 27.9023

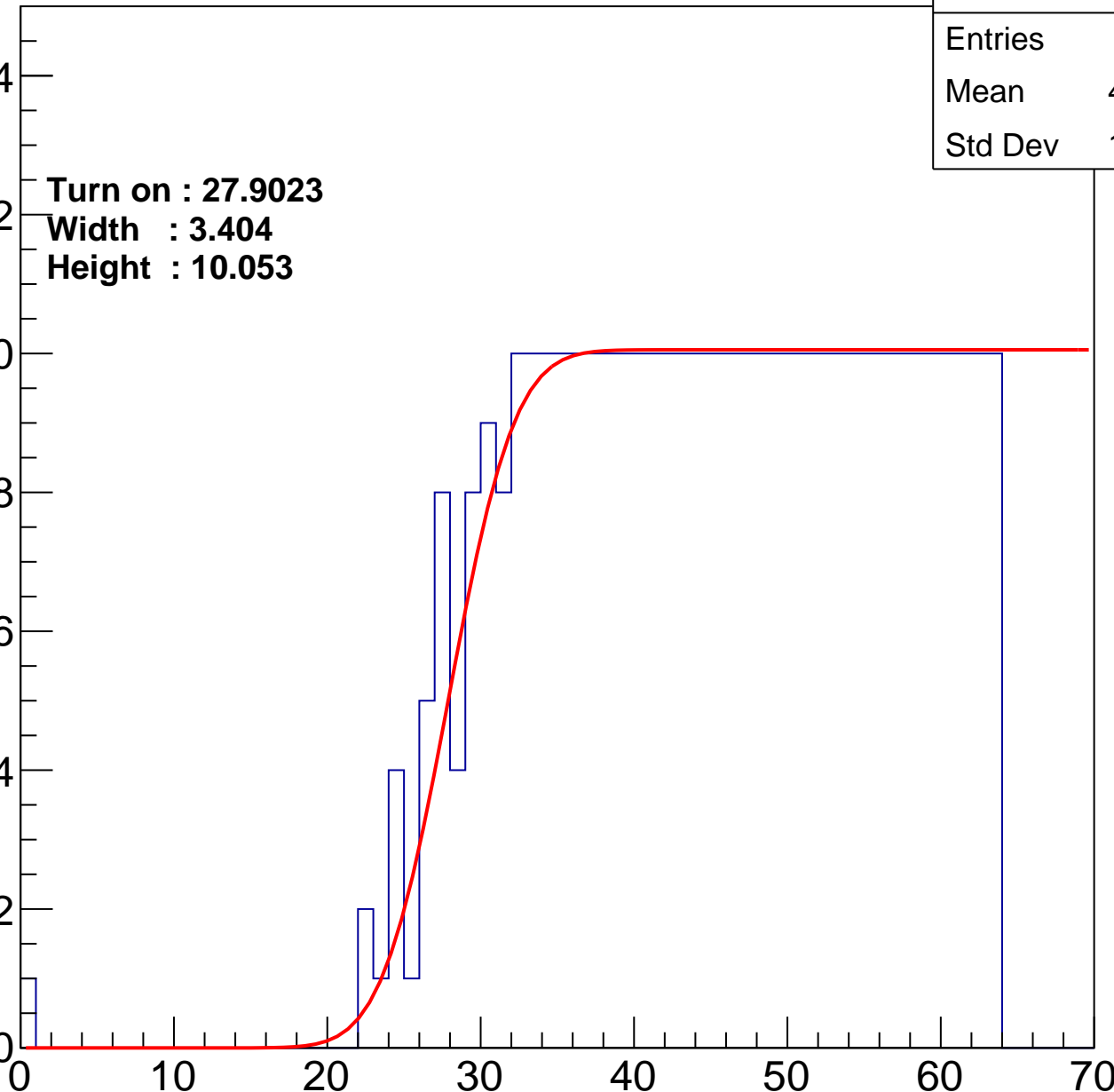
Width : 3.404

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.2
Std Dev	11.64

Turn on : 26.1778

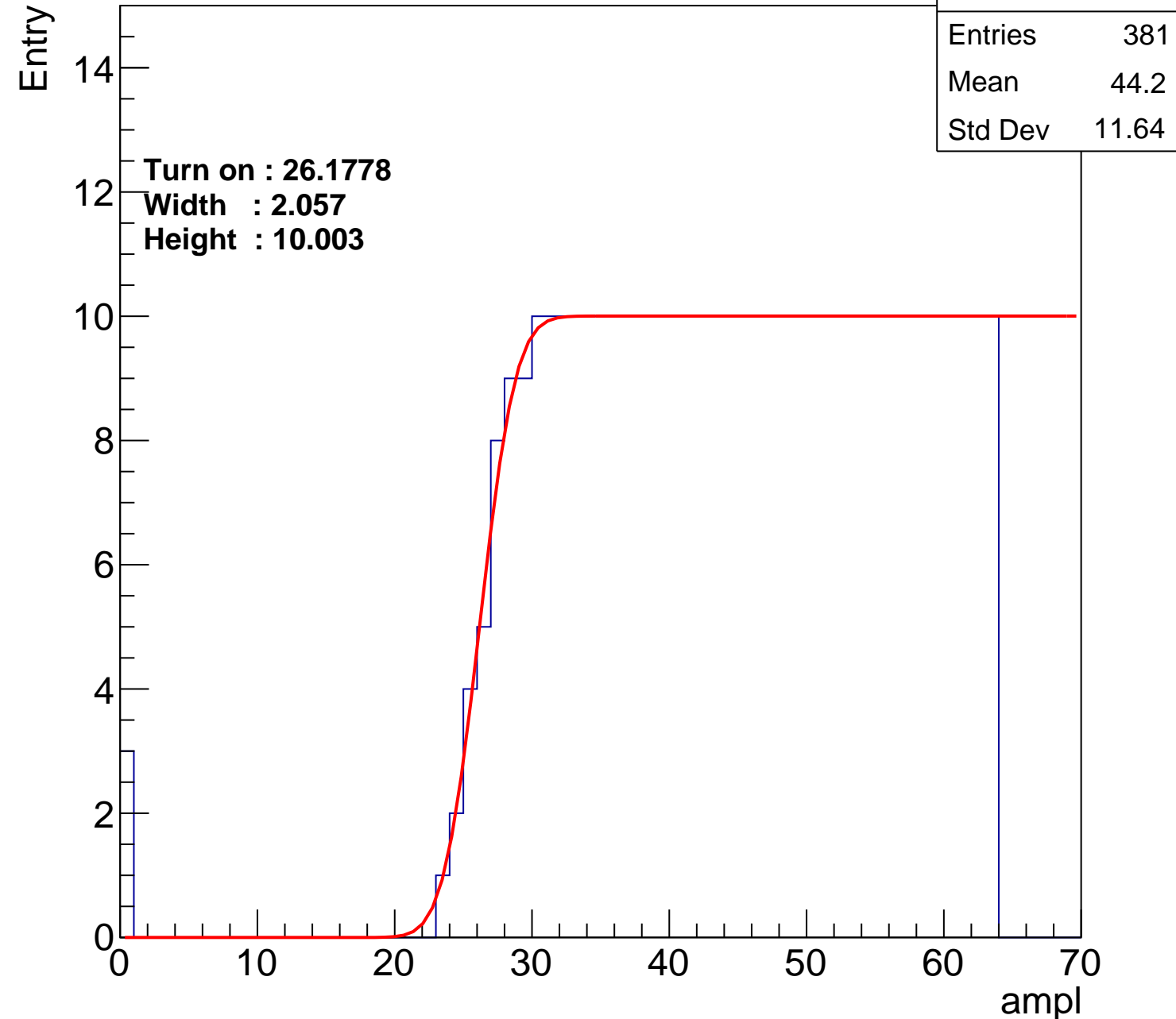
Width : 2.057

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.3047

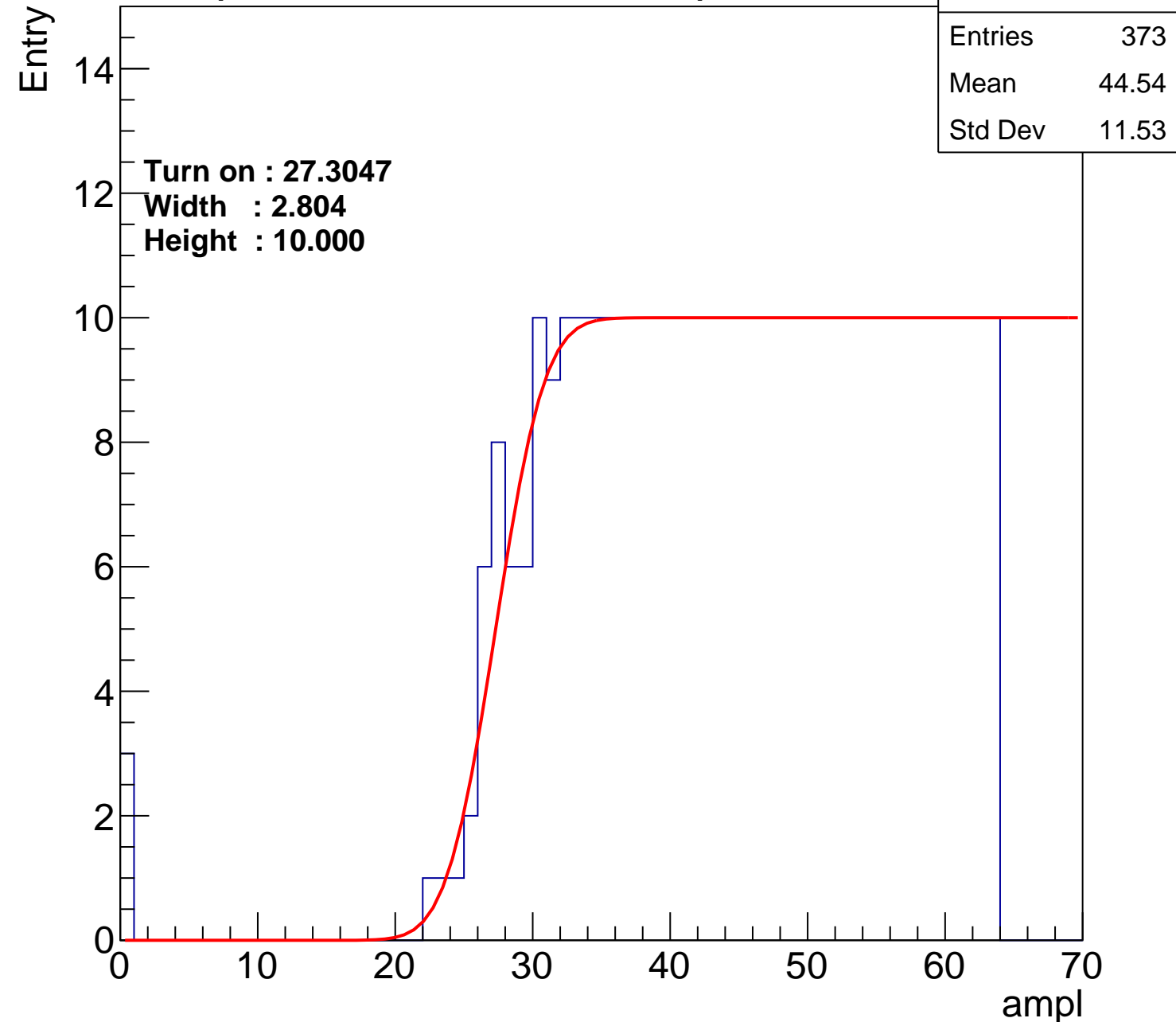
Width : 2.804

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch72

calib_packv5_042523_0143.root, FC#8, port C1

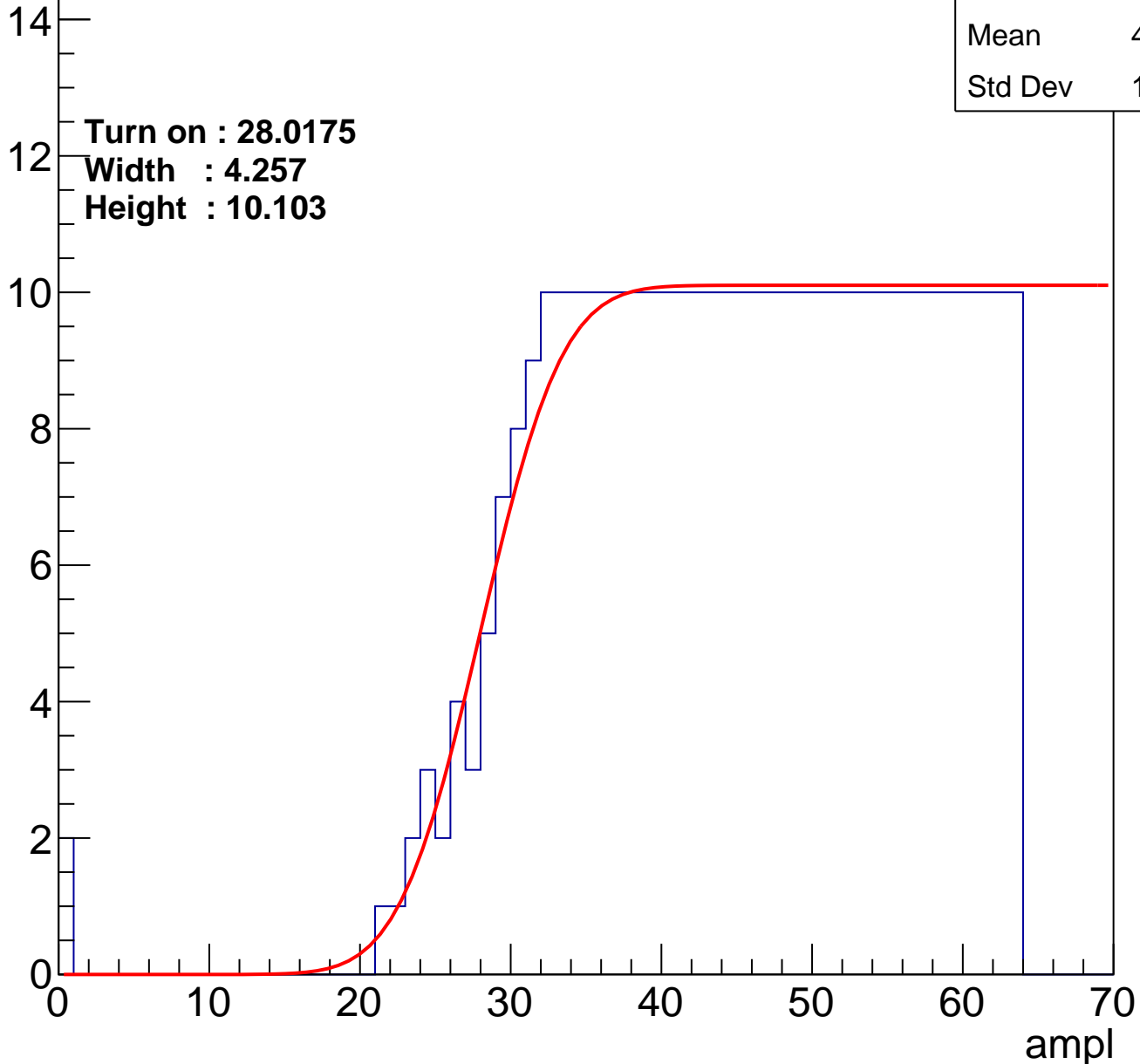
Entries	367
Mean	44.84
Std Dev	11.28

Turn on : 28.0175

Width : 4.257

Height : 10.103

Entry



B0L002S, U21-ch73

calib_packv5_042523_0143.root, FC#8, port C1

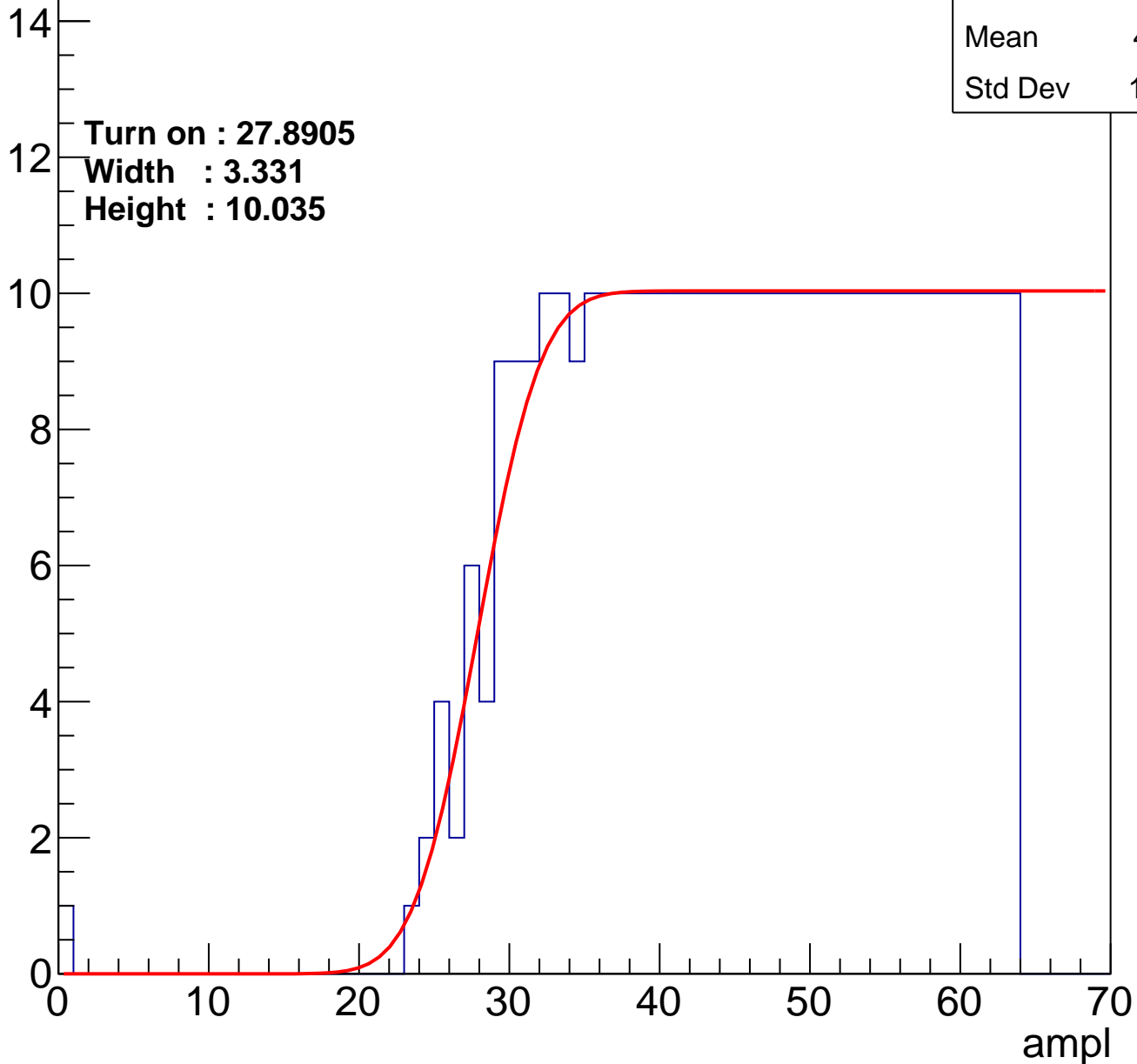
Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 27.8905

Width : 3.331

Height : 10.035

Entry



B0L002S, U21-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.83
Std Dev	11.43

Turn on : 28.2964

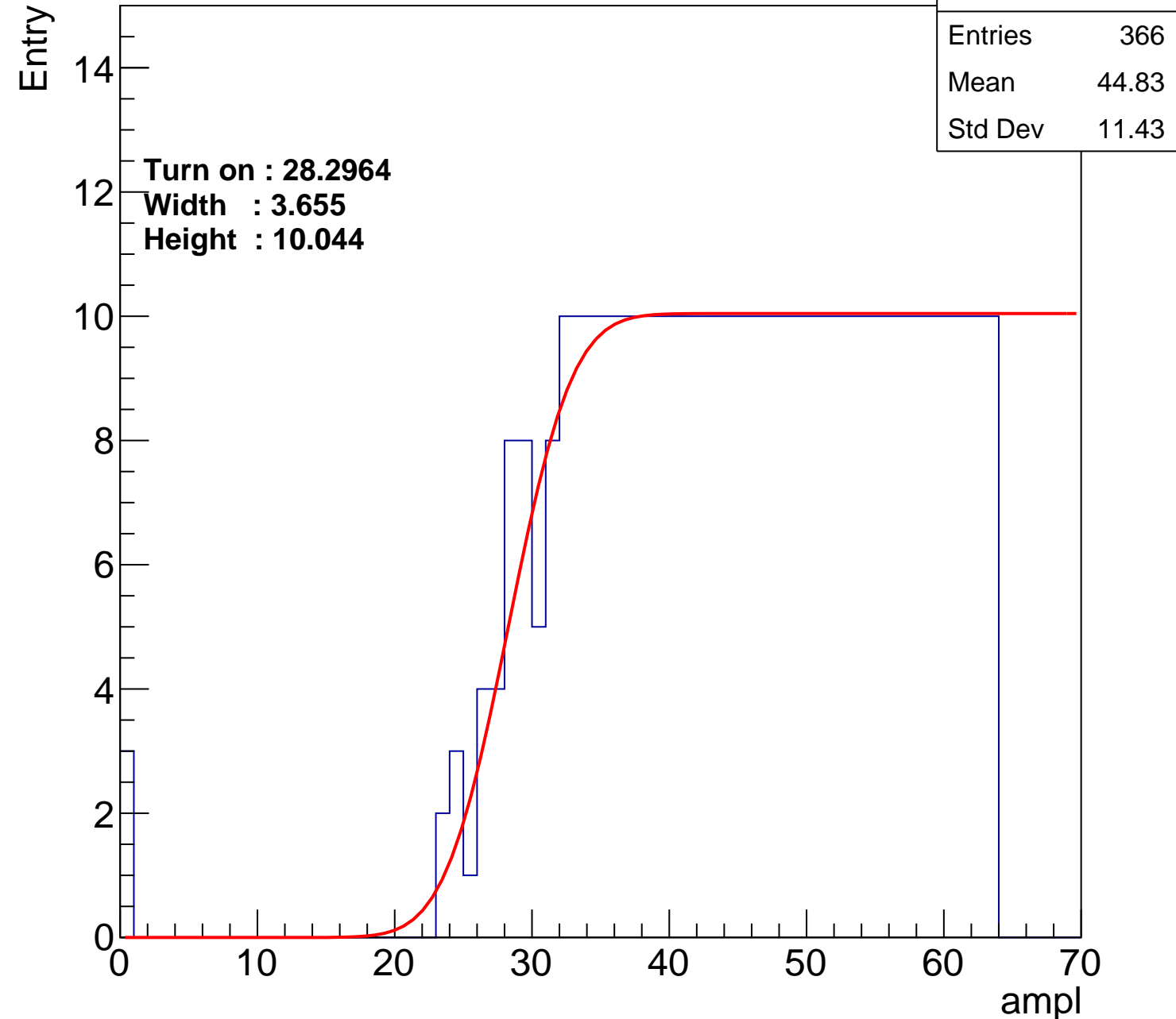
Width : 3.655

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch75

calib_packv5_042523_0143.root, FC#8, port C1

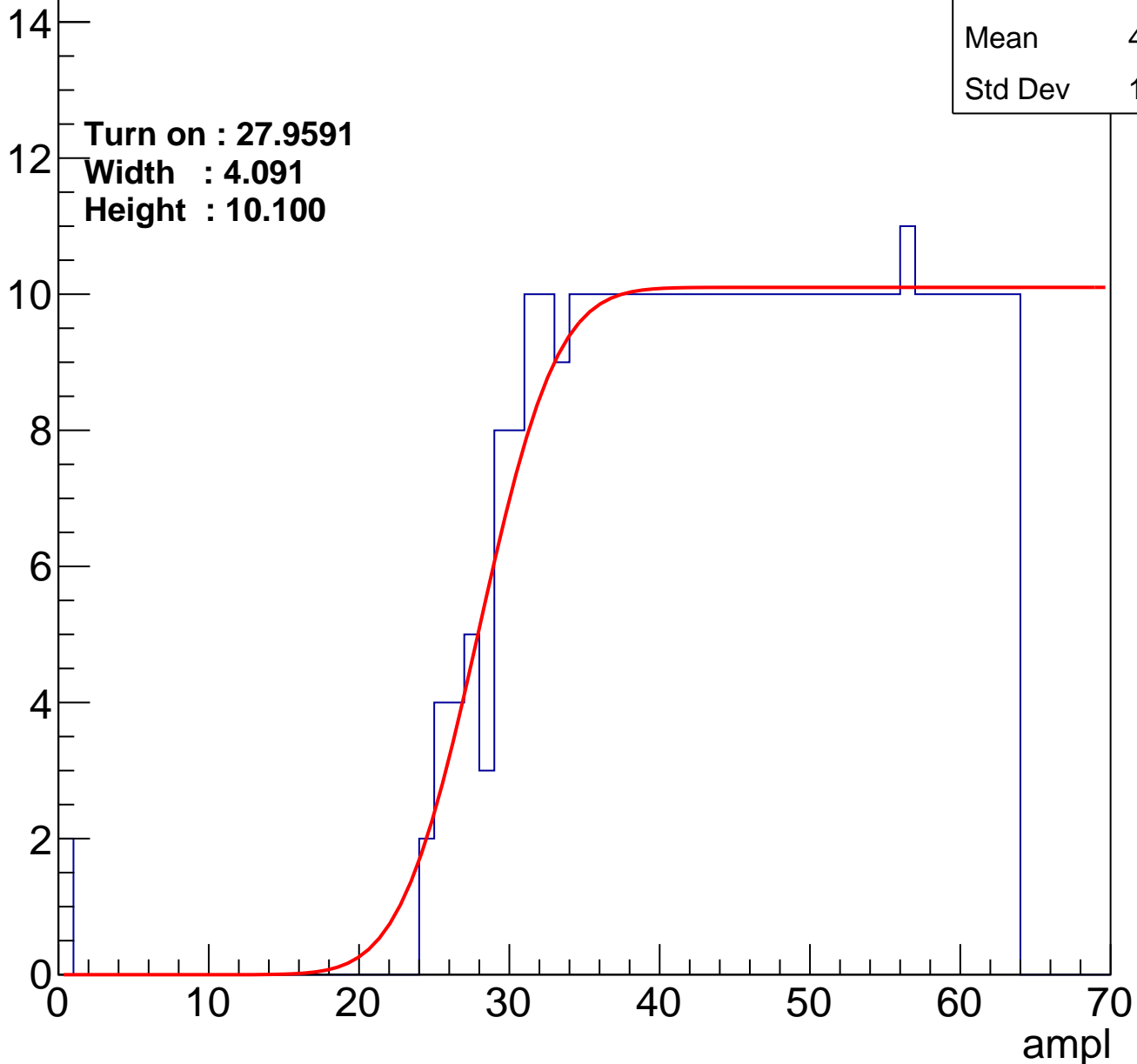
Entries	366
Mean	45.02
Std Dev	11.15

Turn on : 27.9591

Width : 4.091

Height : 10.100

Entry



B0L002S, U21-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.6
Std Dev	11.44

Turn on : 26.7801

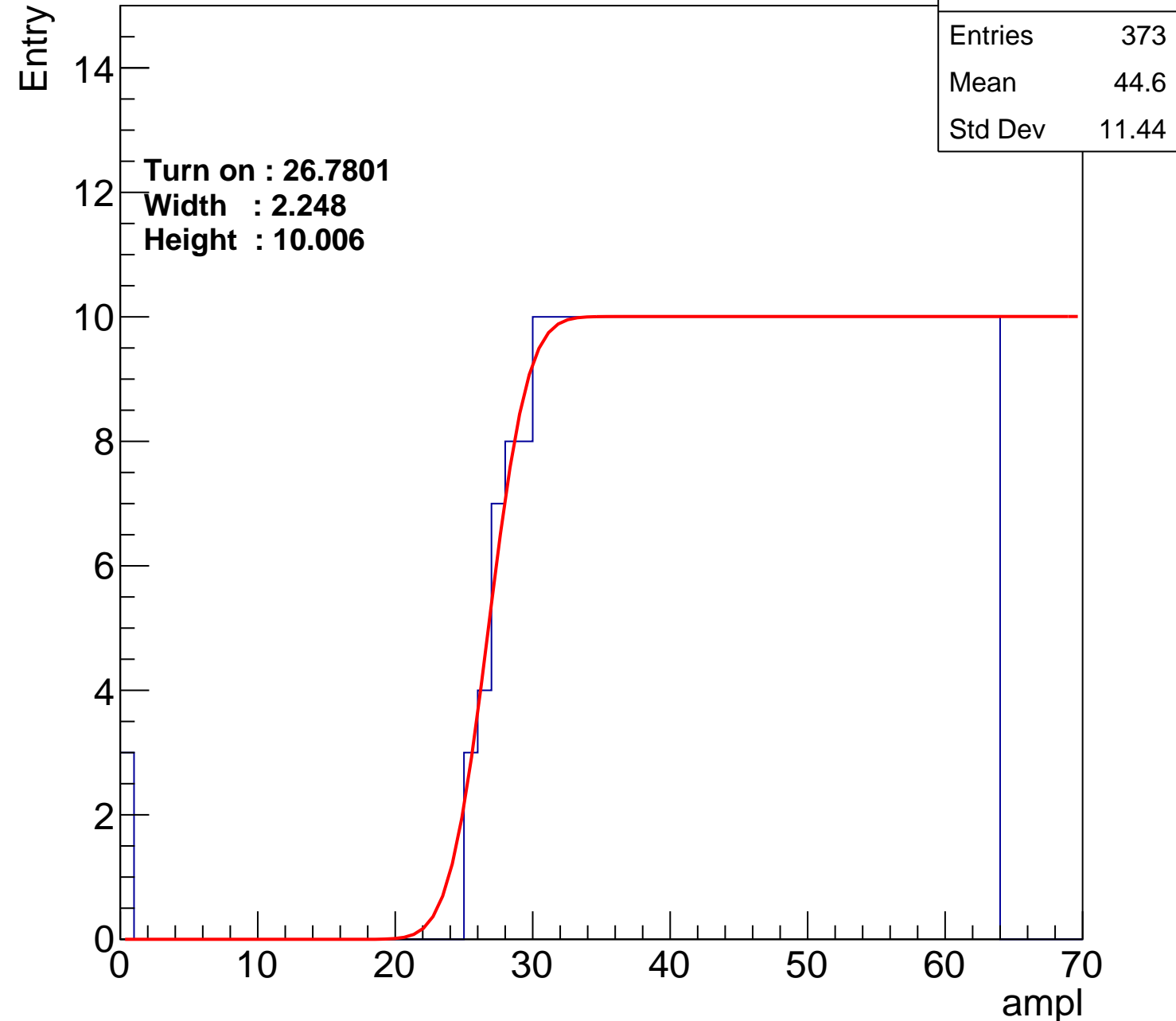
Width : 2.248

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.23
Std Dev	11.7

Turn on : 27.5361

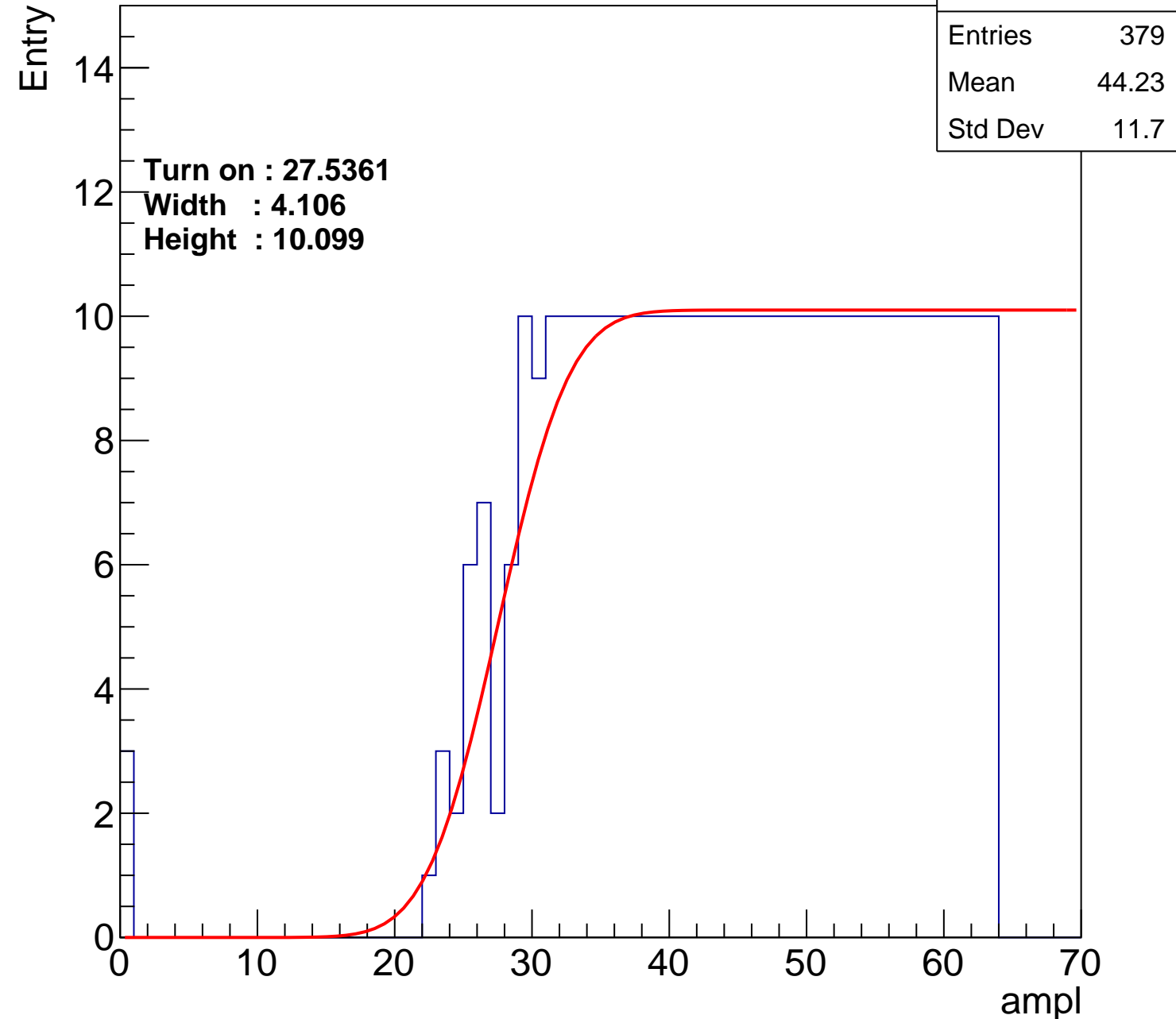
Width : 4.106

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.0283
Width : 2.535
Height : 10.017



B0L002S, U21-ch79

calib_packv5_042523_0143.root, FC#8, port C1

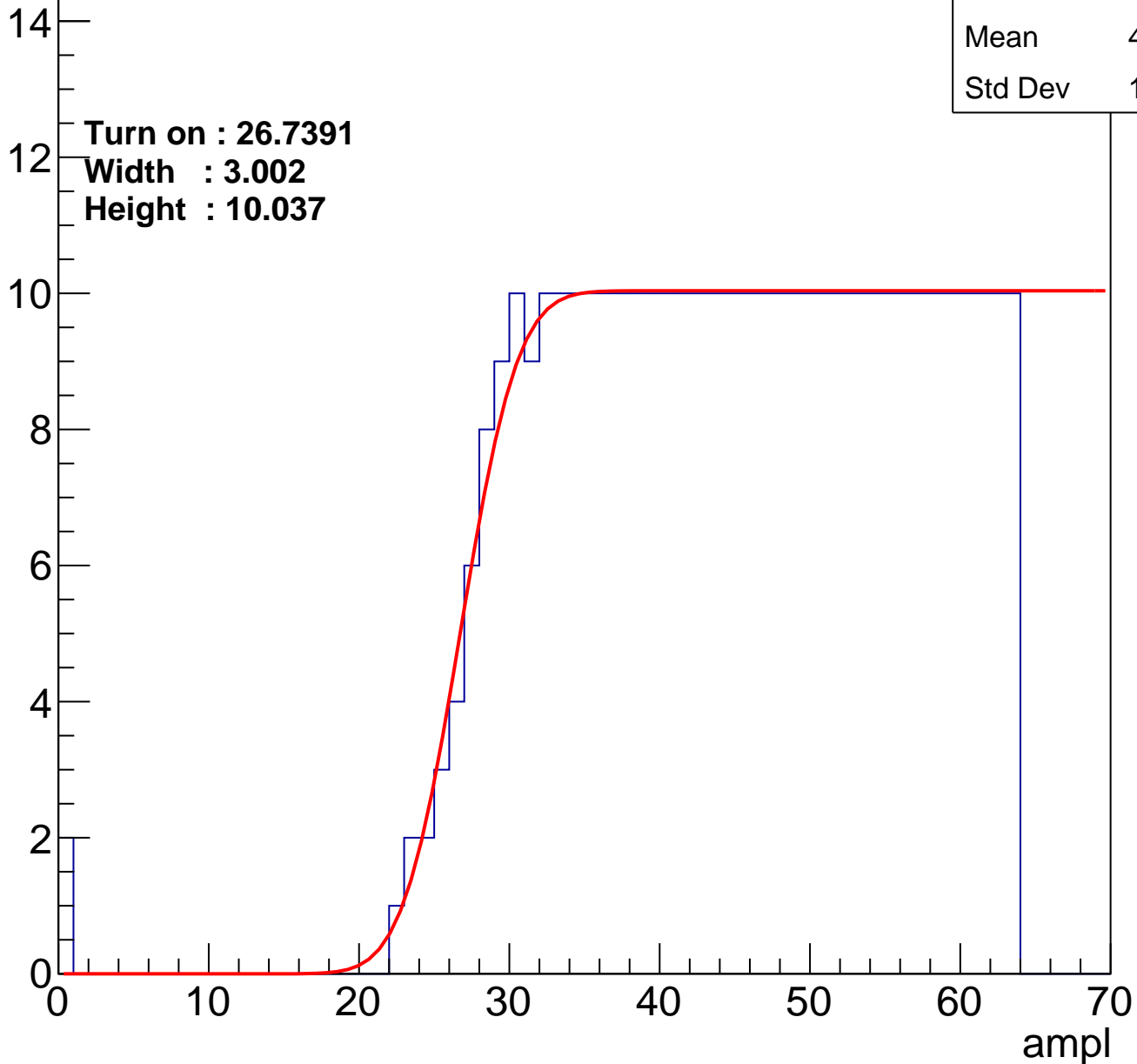
Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.7391

Width : 3.002

Height : 10.037

Entry



B0L002S, U21-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.25
Std Dev	10.84

Turn on : 27.8812

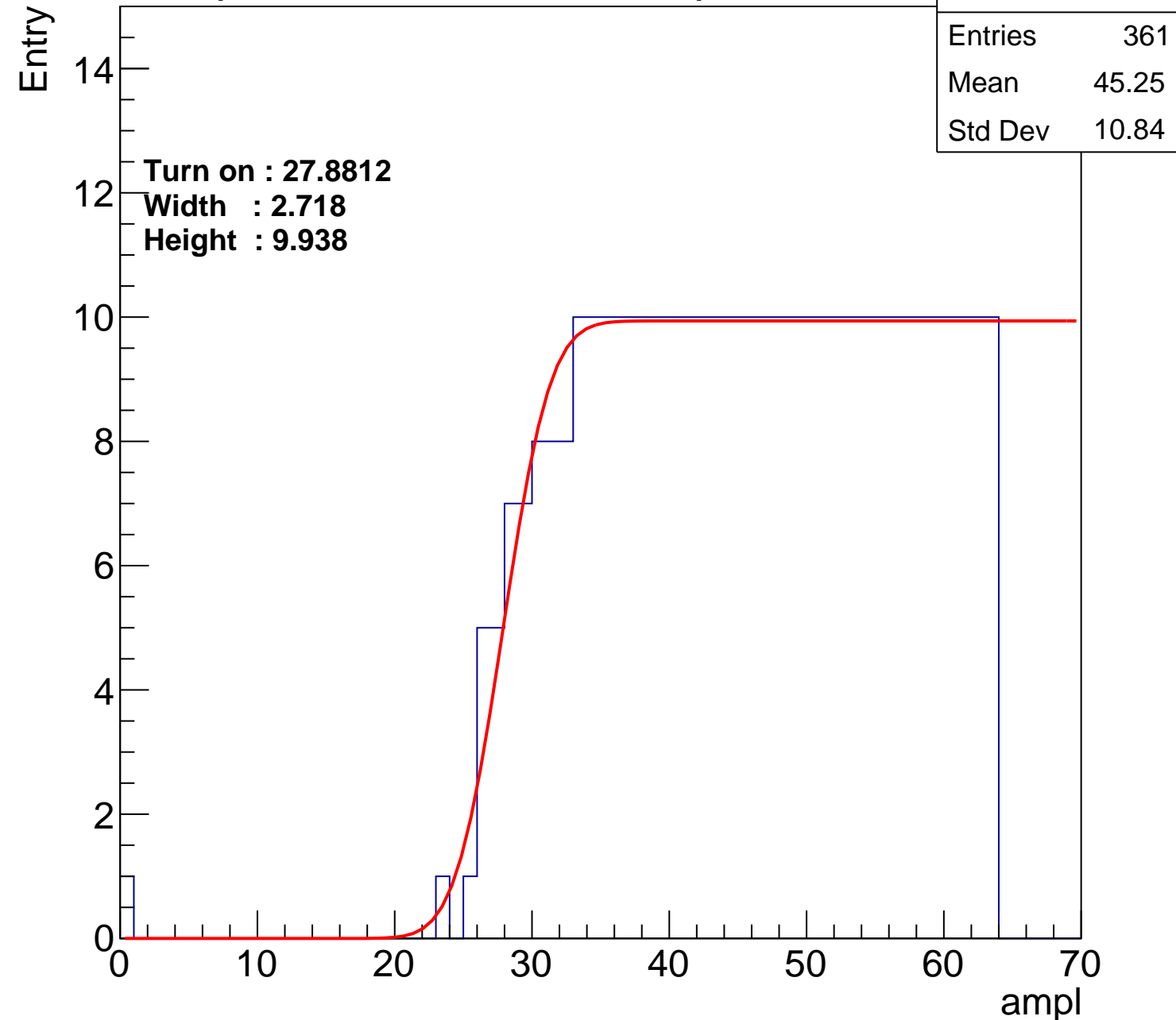
Width : 2.718

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch81

calib_packv5_042523_0143.root, FC#8, port C1

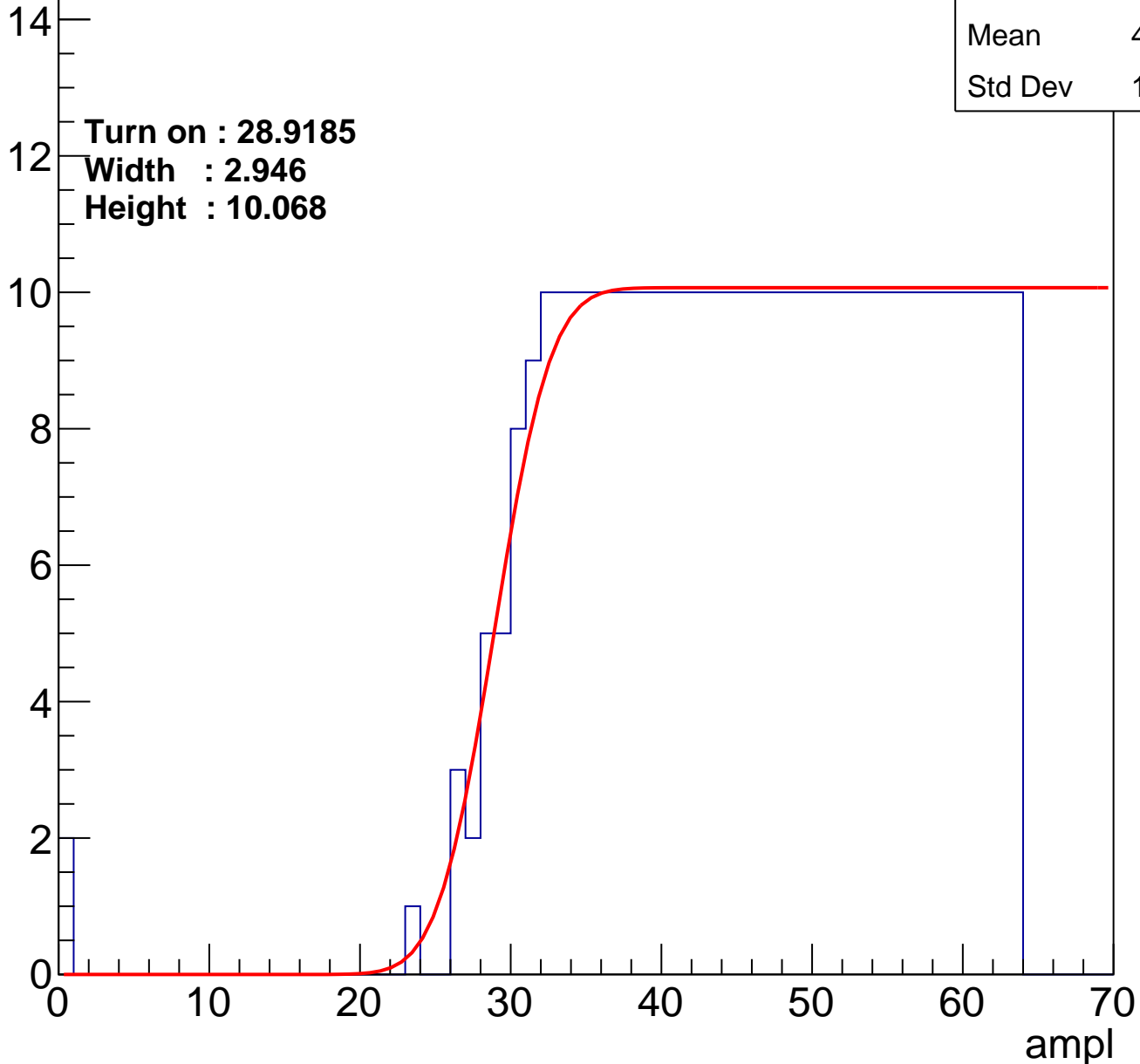
Entries	355
Mean	45.52
Std Dev	10.84

Turn on : 28.9185

Width : 2.946

Height : 10.068

Entry



B0L002S, U21-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.1
Std Dev	10.94

Turn on : 27.9251

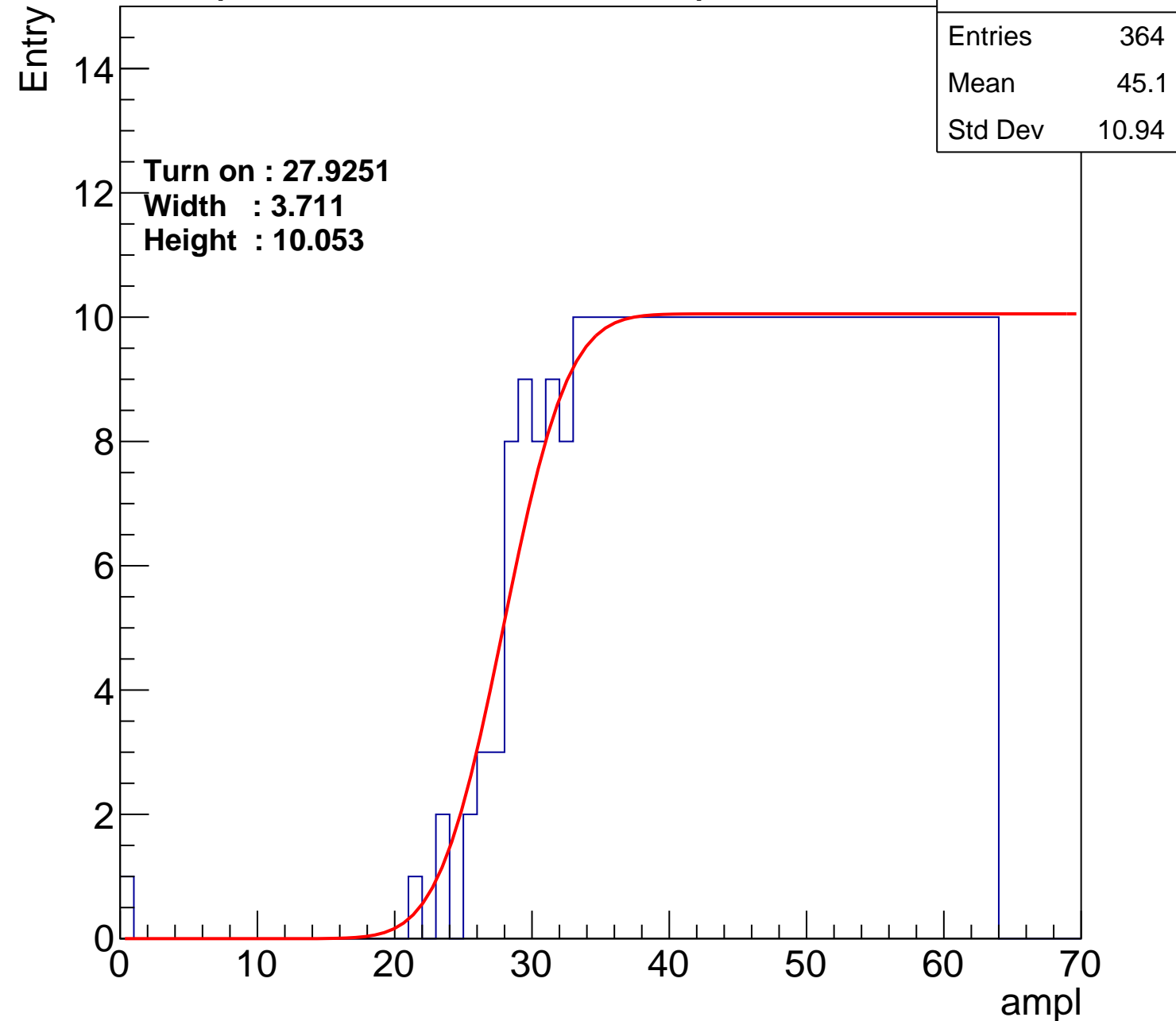
Width : 3.711

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	43.84
Std Dev	12.29

Turn on : 26.1659

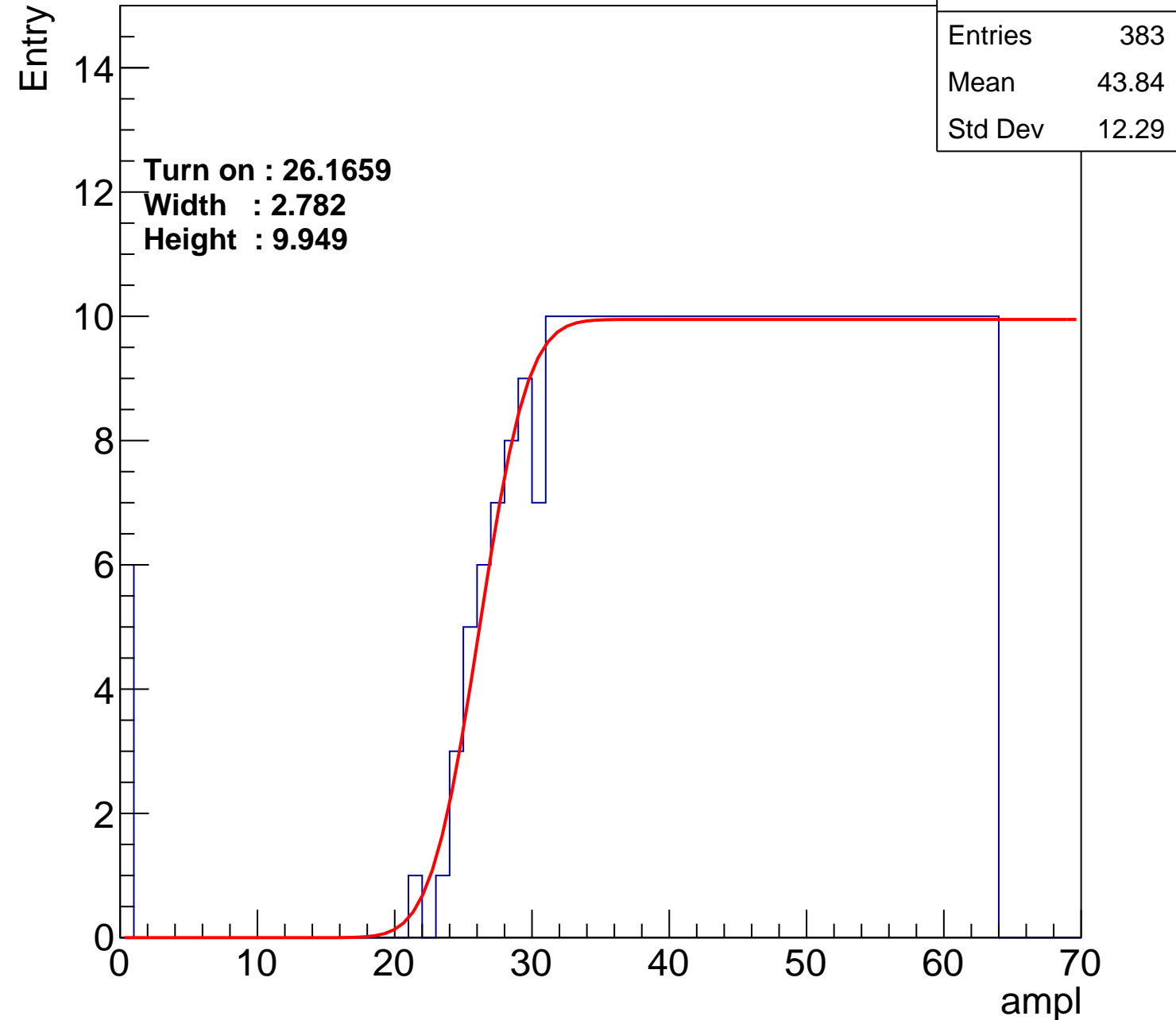
Width : 2.782

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch84

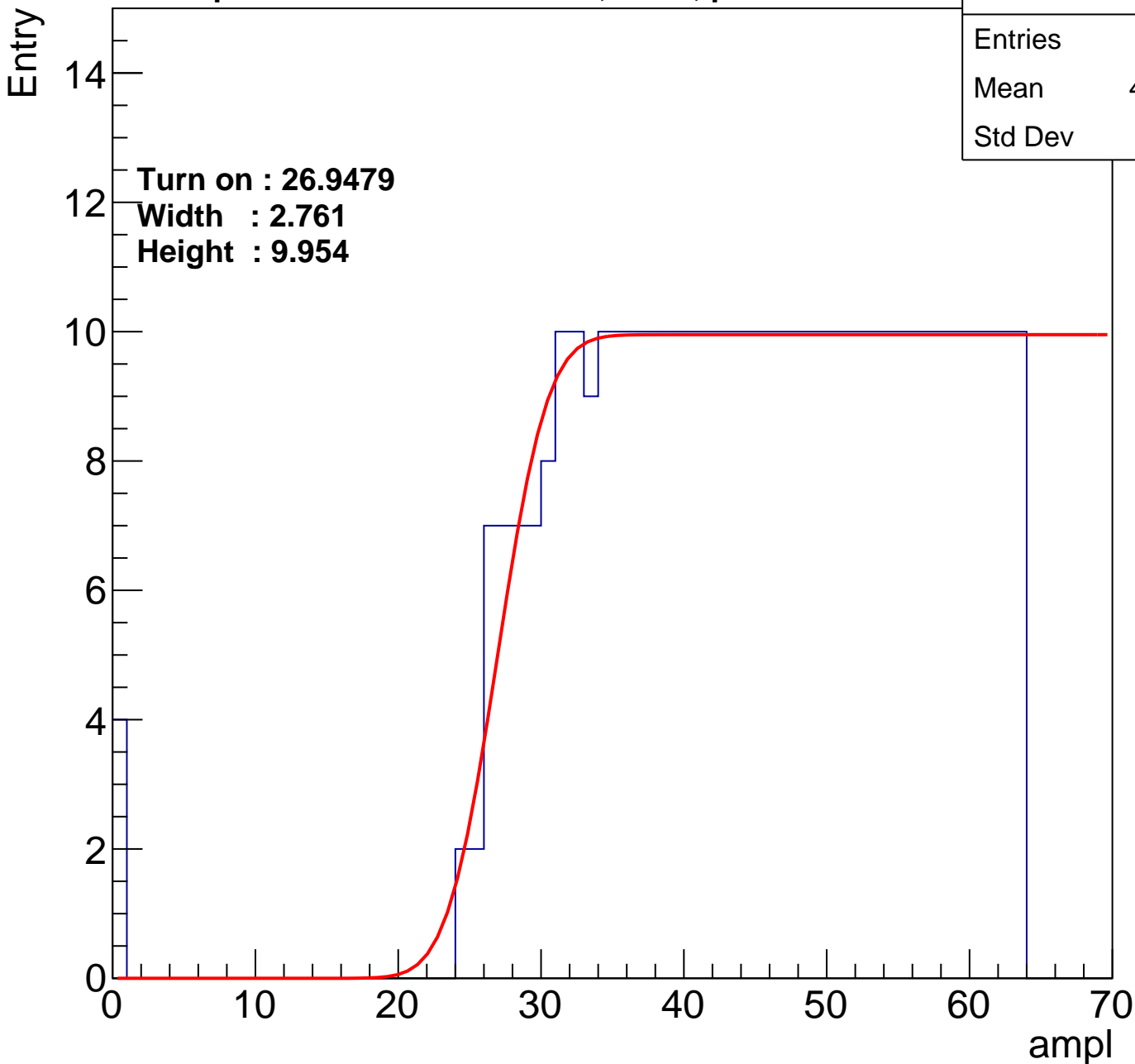
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.46
Std Dev	11.71

Turn on : 26.9479

Width : 2.761

Height : 9.954



B0L002S, U21-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.27
Std Dev	11.17

Turn on : 28.4838

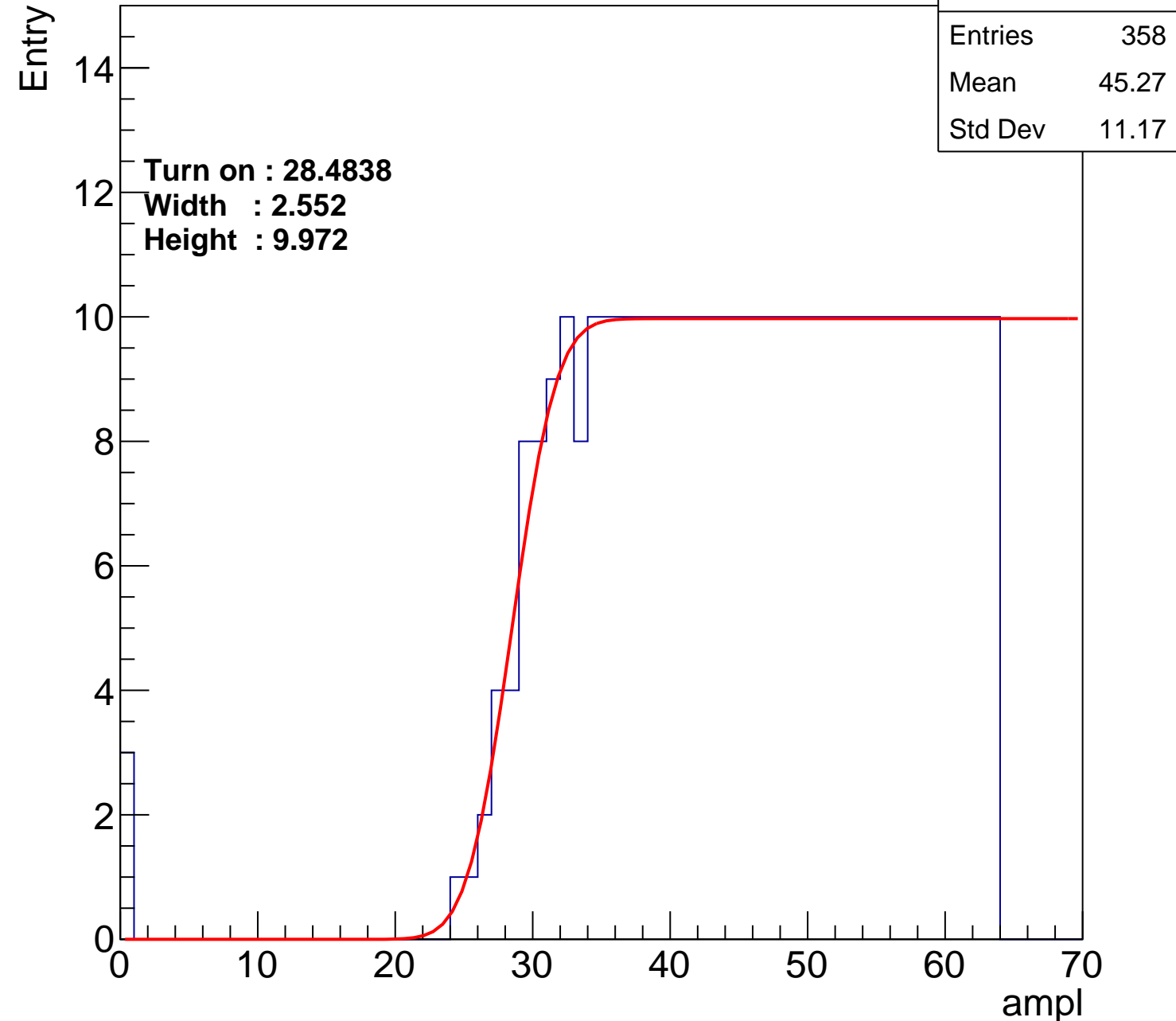
Width : 2.552

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.2
Std Dev	11.02

Turn on : 27.8907

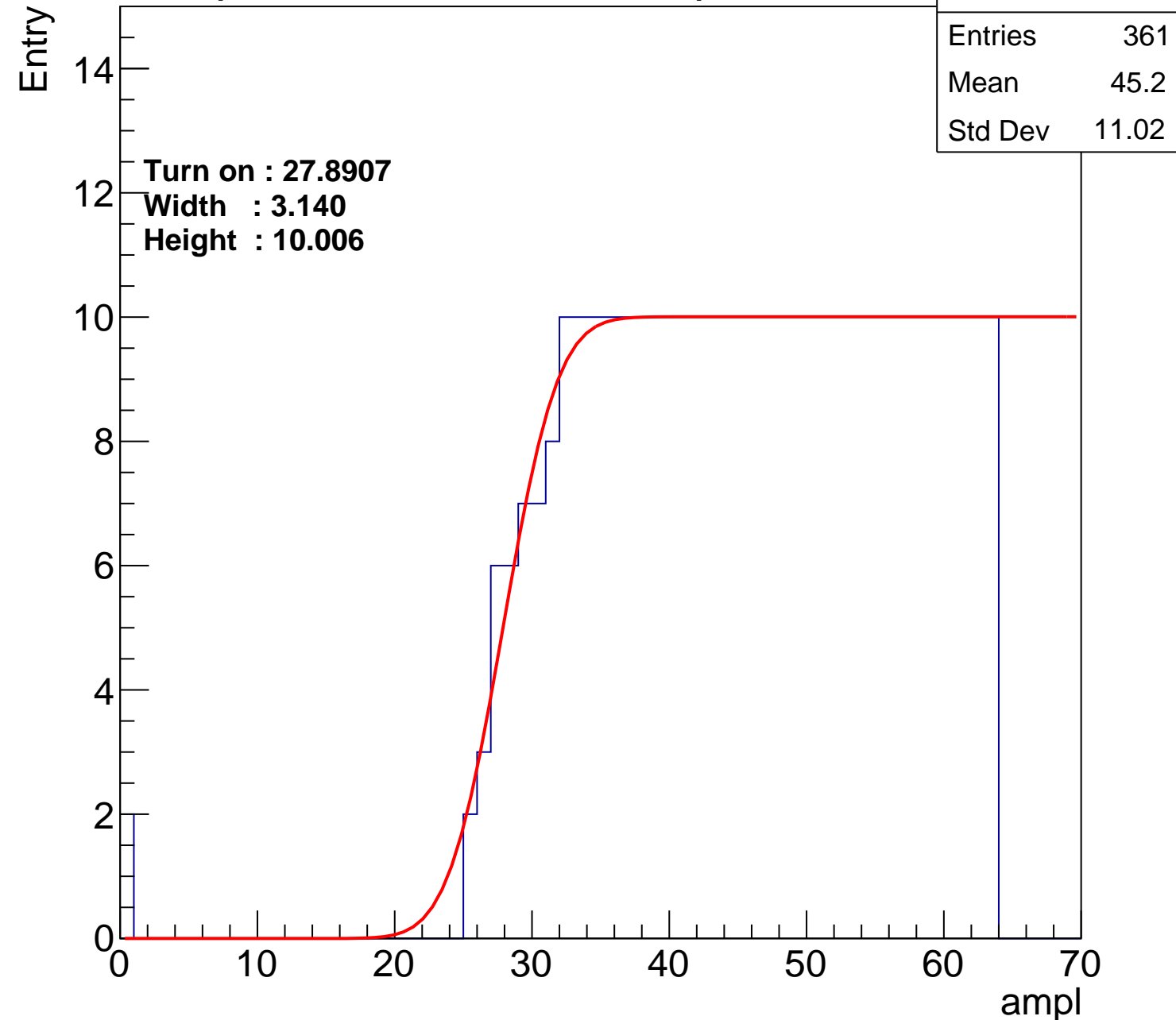
Width : 3.140

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.13
Std Dev	11.87

Turn on : 26.5002

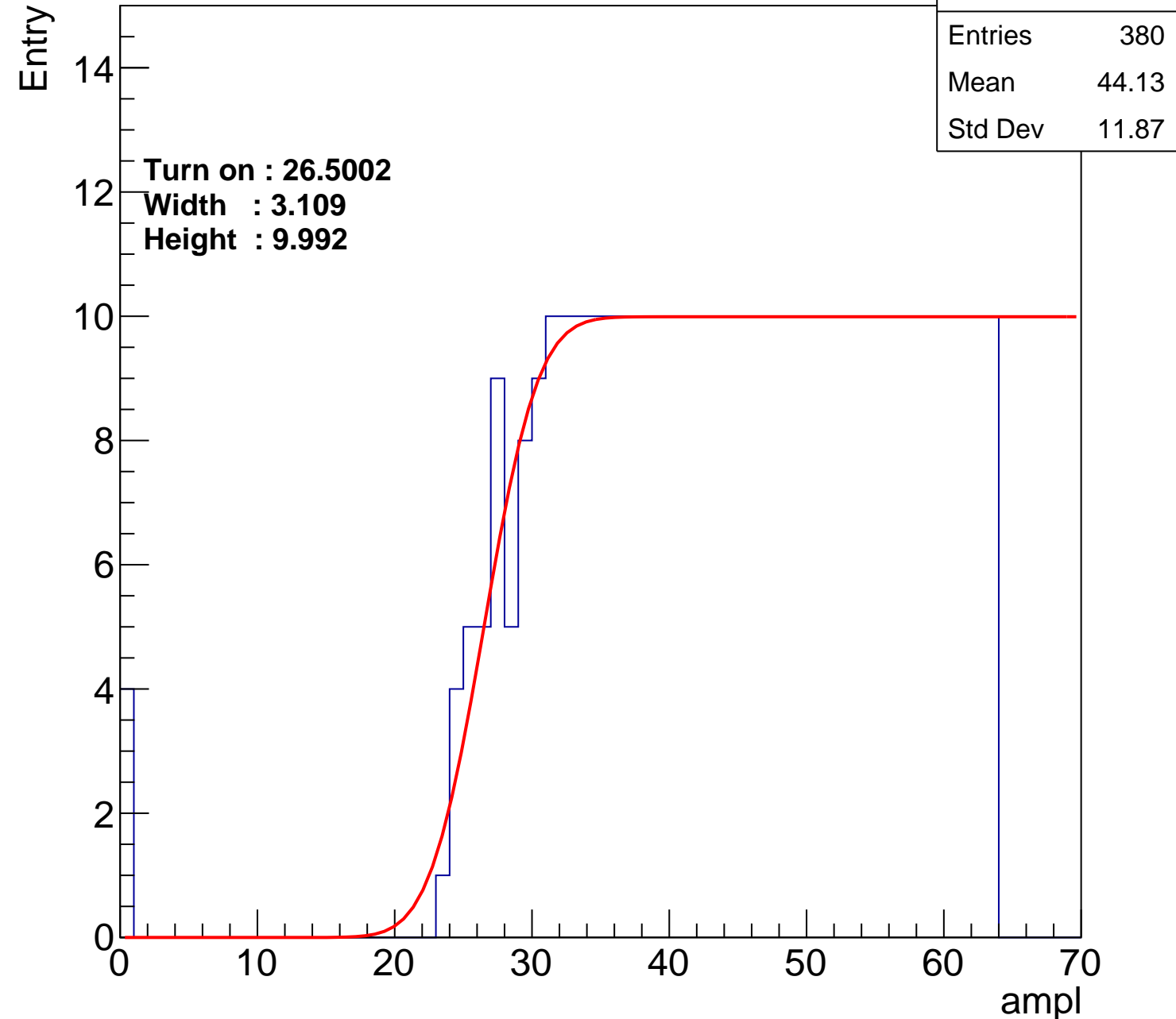
Width : 3.109

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch88

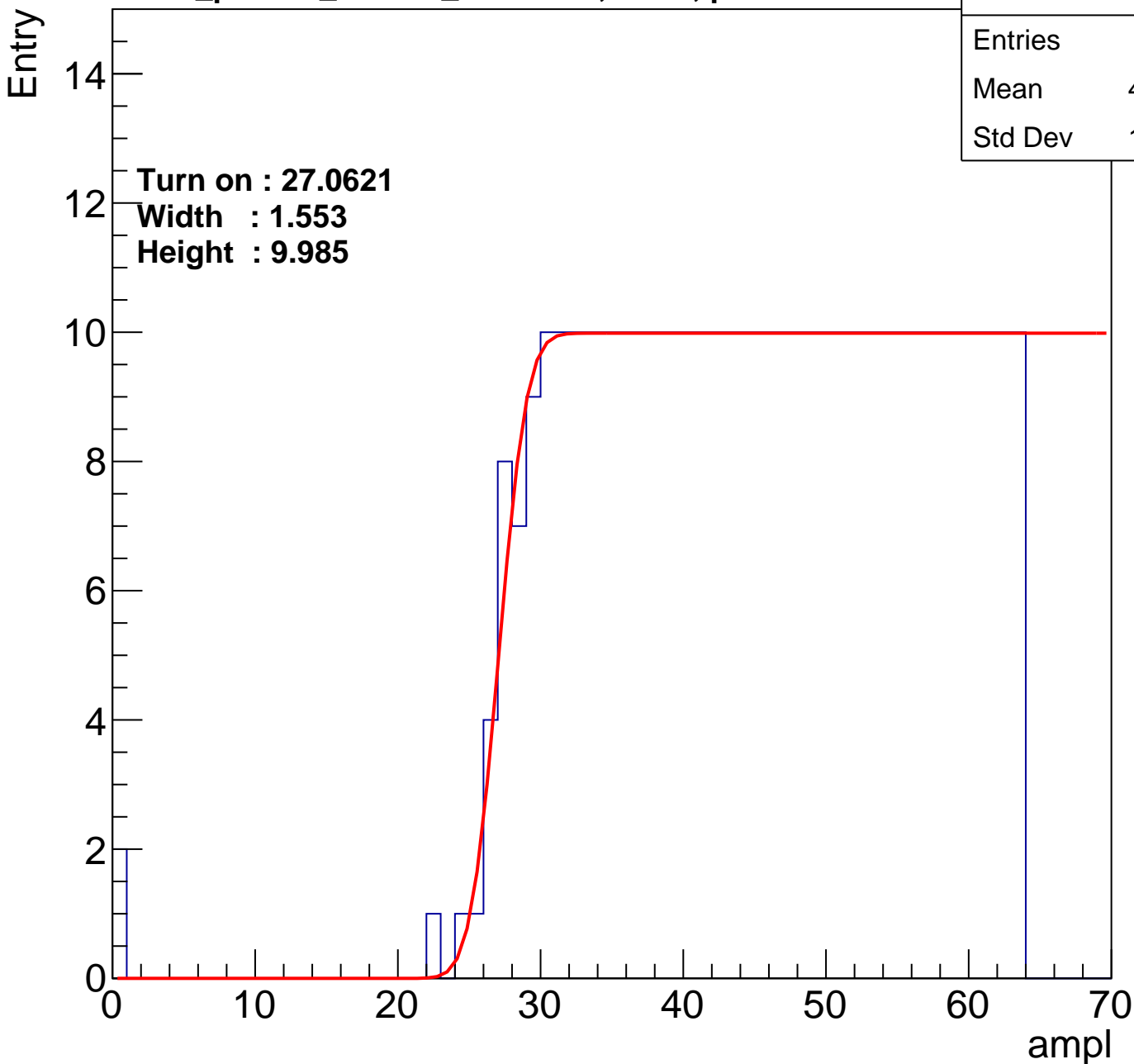
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.66
Std Dev	11.25

Turn on : 27.0621

Width : 1.553

Height : 9.985



B0L002S, U21-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.8780

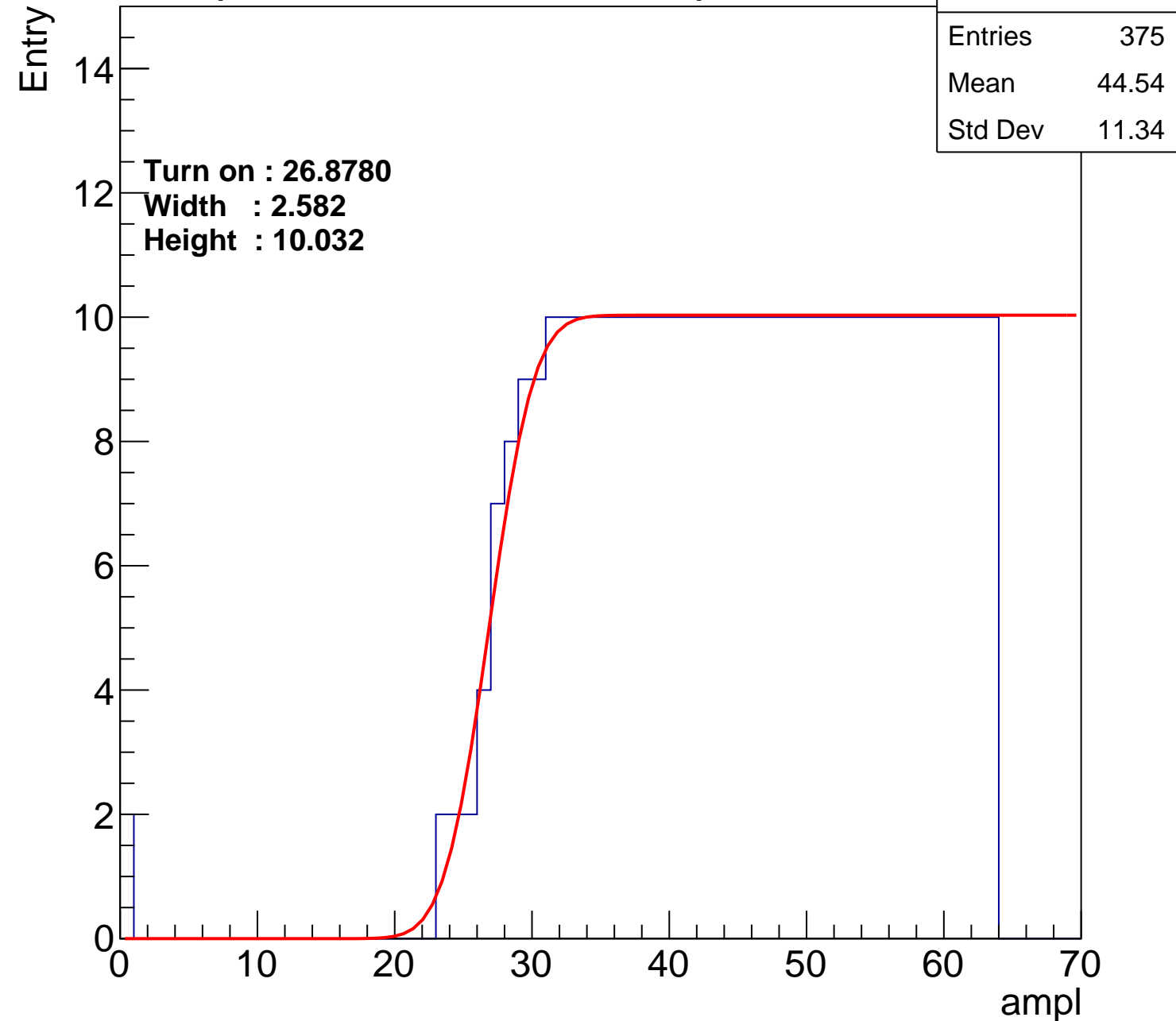
Width : 2.582

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.8565

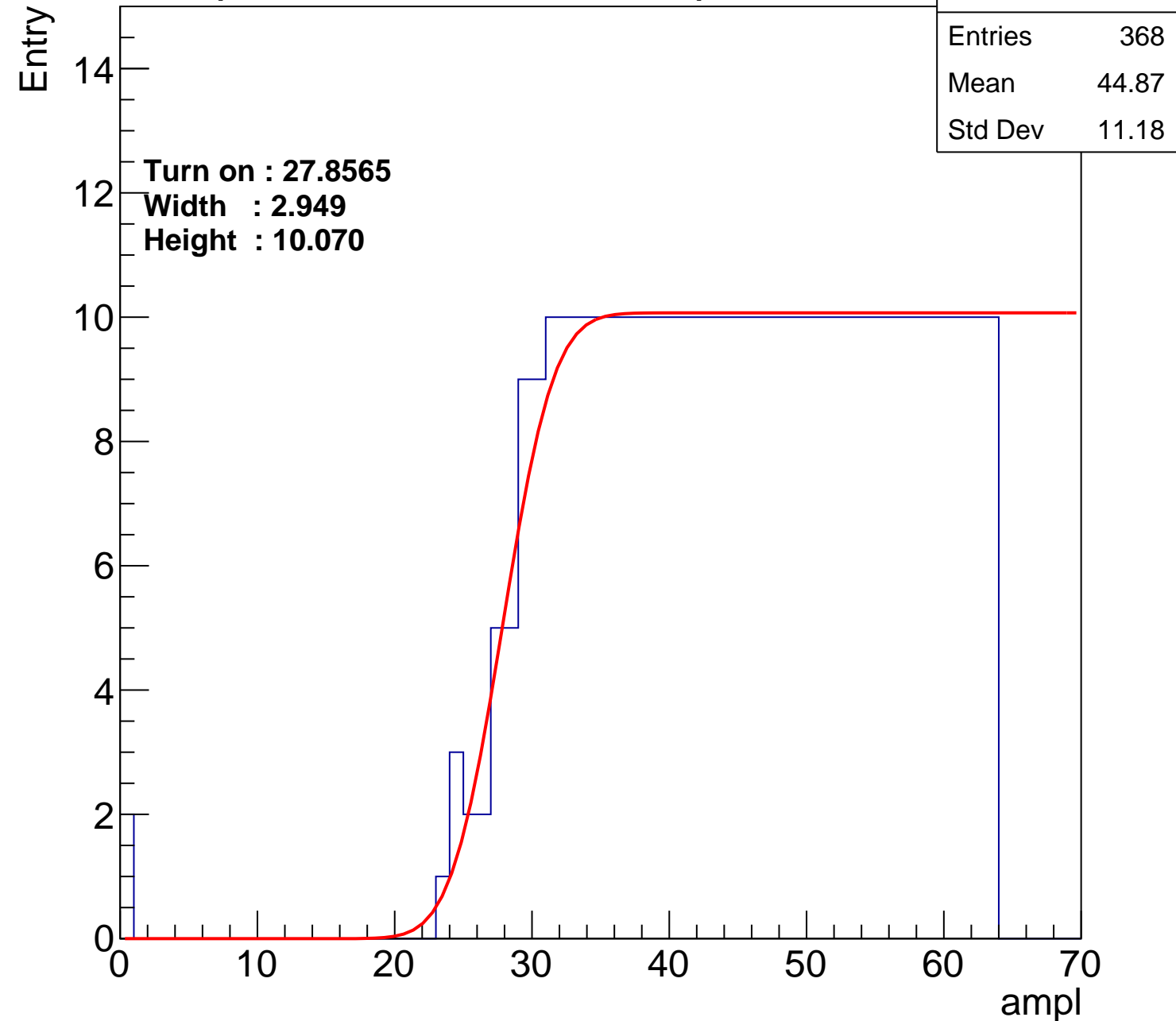
Width : 2.949

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.51
Std Dev	11.57

Turn on : 27.4164

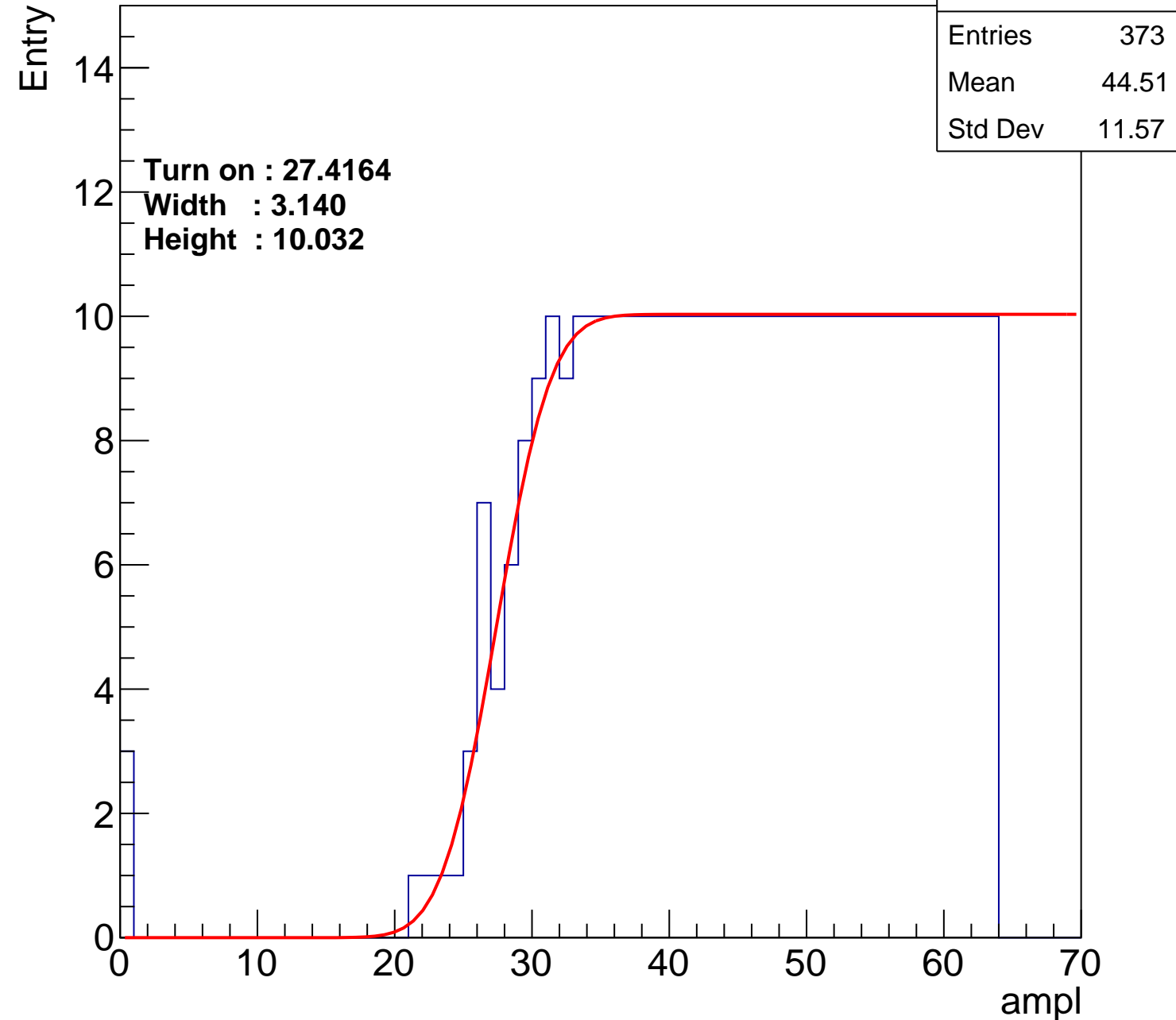
Width : 3.140

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.8782
Width : 2.925
Height : 10.038



B0L002S, U21-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 27.4919

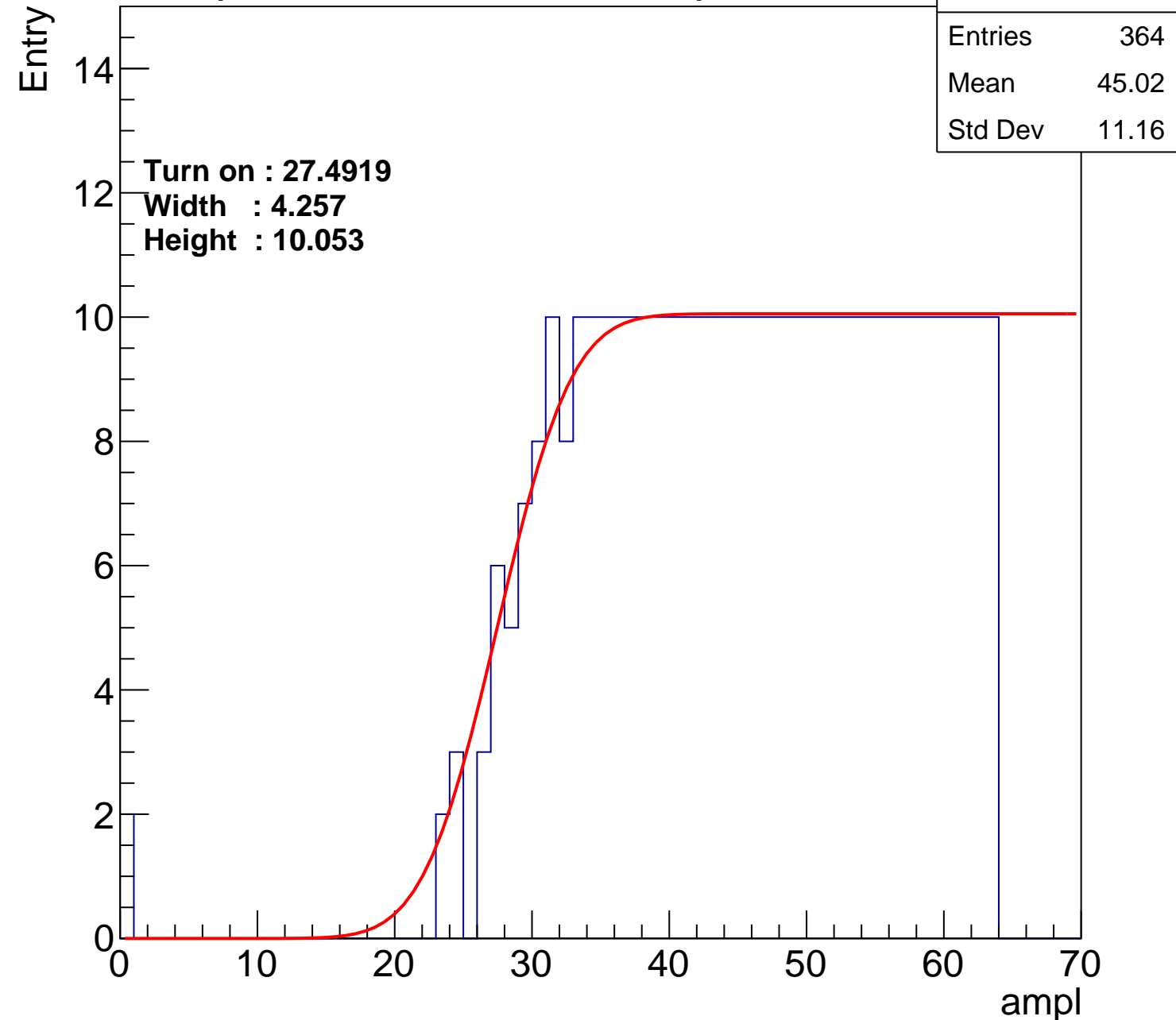
Width : 4.257

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.4672

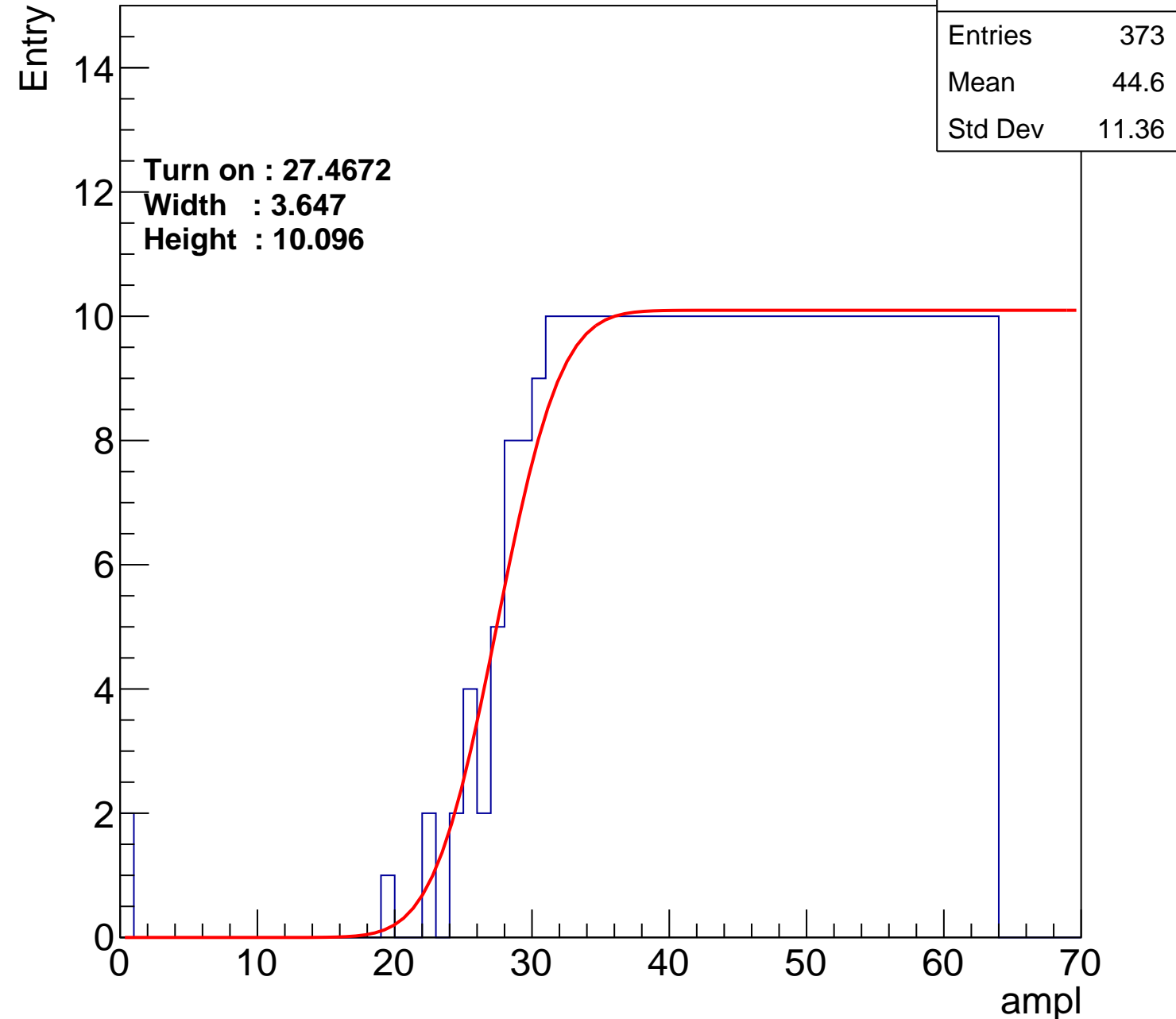
Width : 3.647

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.7
Std Dev	10.55

Turn on : 28.8736

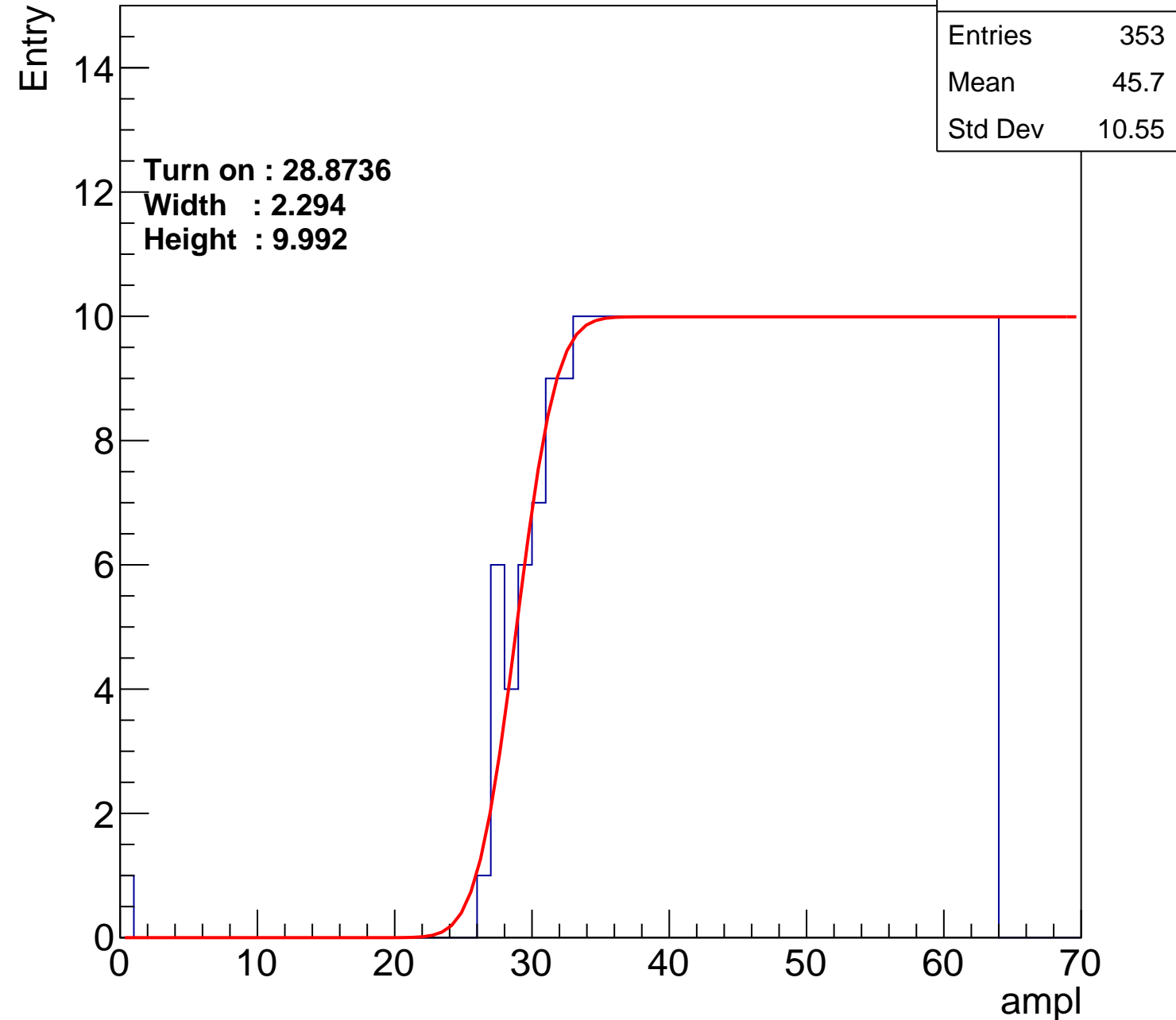
Width : 2.294

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.47
Std Dev	11.44

Turn on : 26.7191

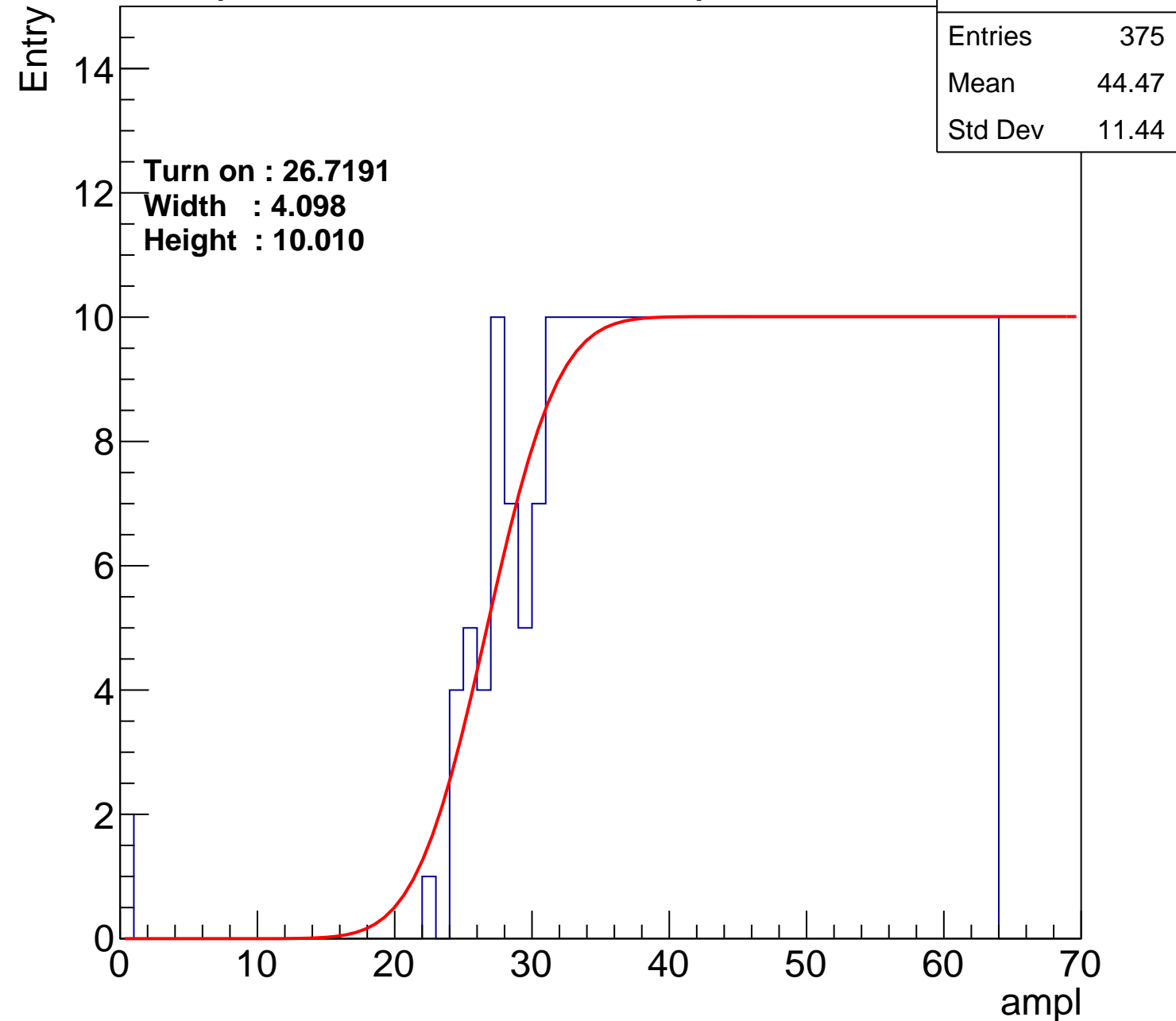
Width : 4.098

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch97

calib_packv5_042523_0143.root, FC#8, port C1

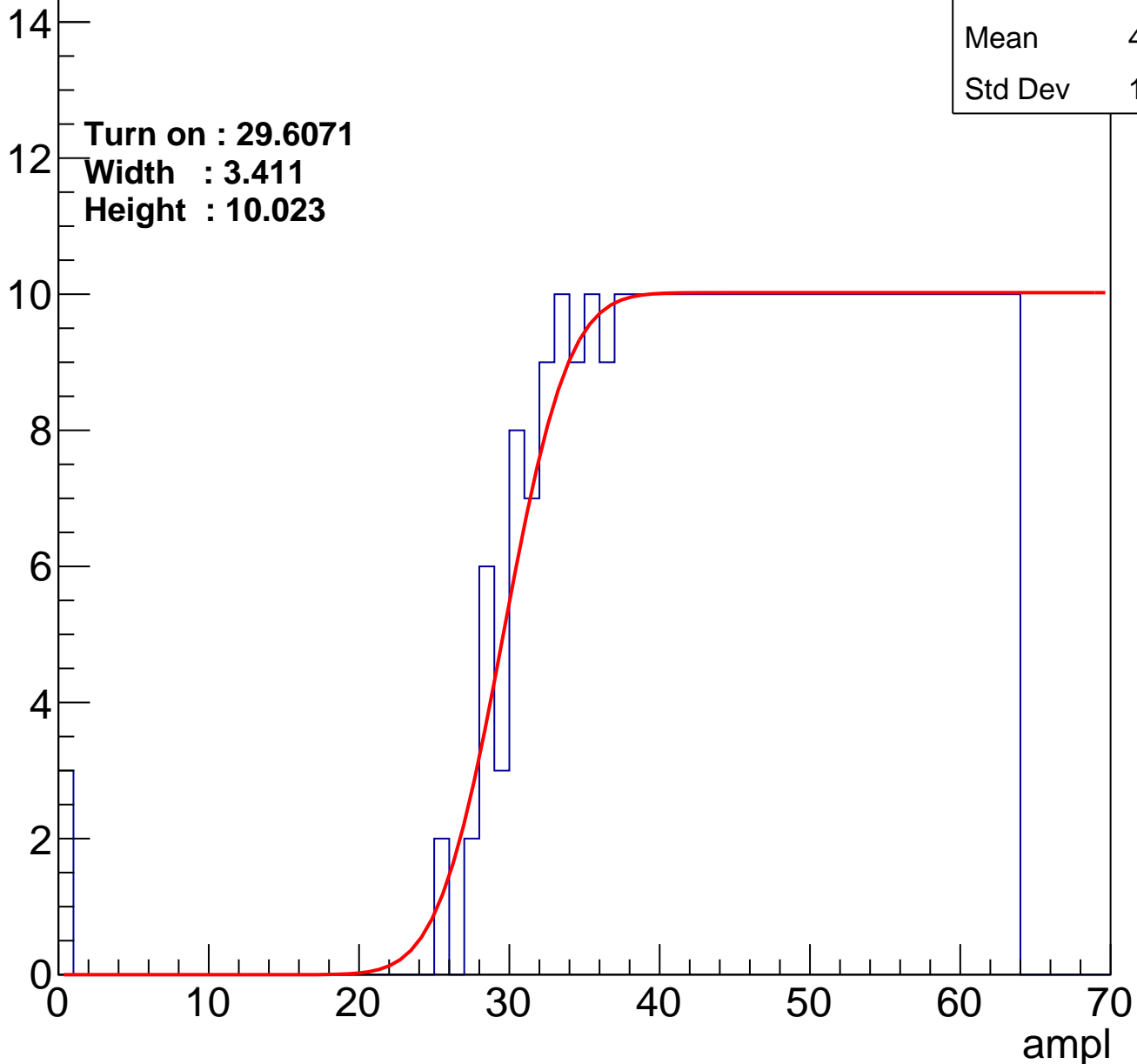
Entries	348
Mean	45.73
Std Dev	10.98

Turn on : 29.6071

Width : 3.411

Height : 10.023

Entry



B0L002S, U21-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.59
Std Dev	10.83

Turn on : 29.6765

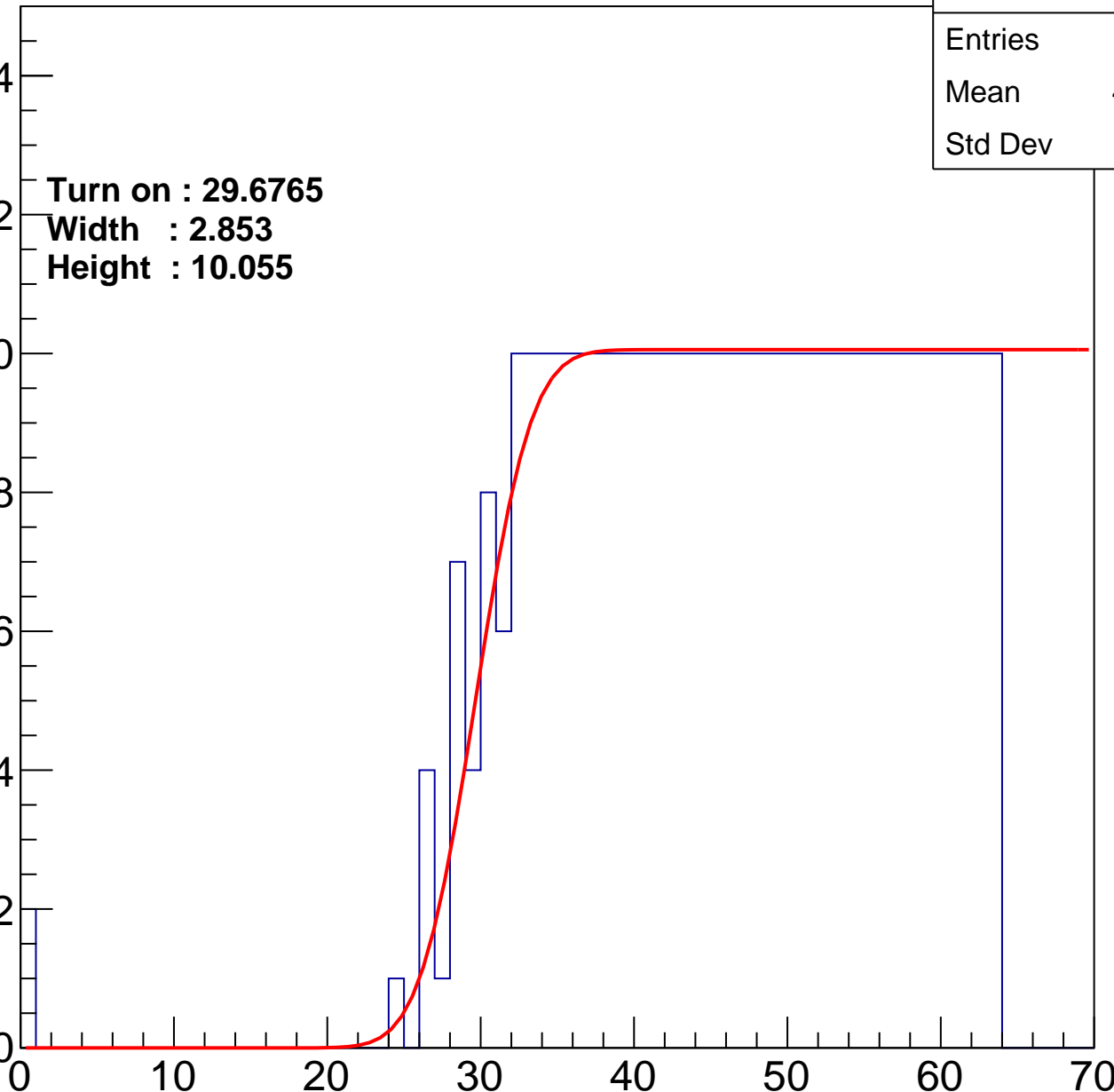
Width : 2.853

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.41
Std Dev	10.9

Turn on : 28.6294

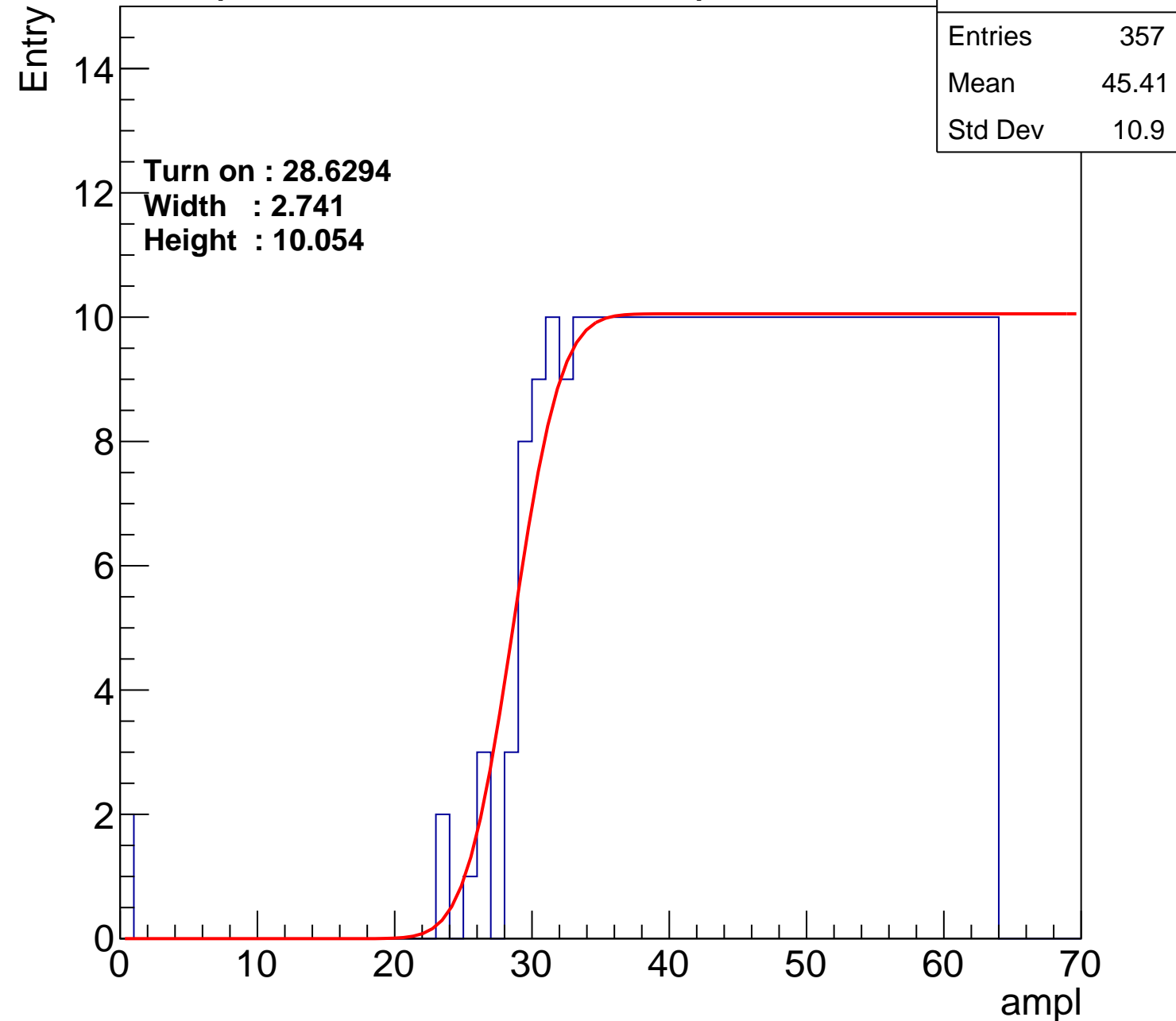
Width : 2.741

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 26.9969

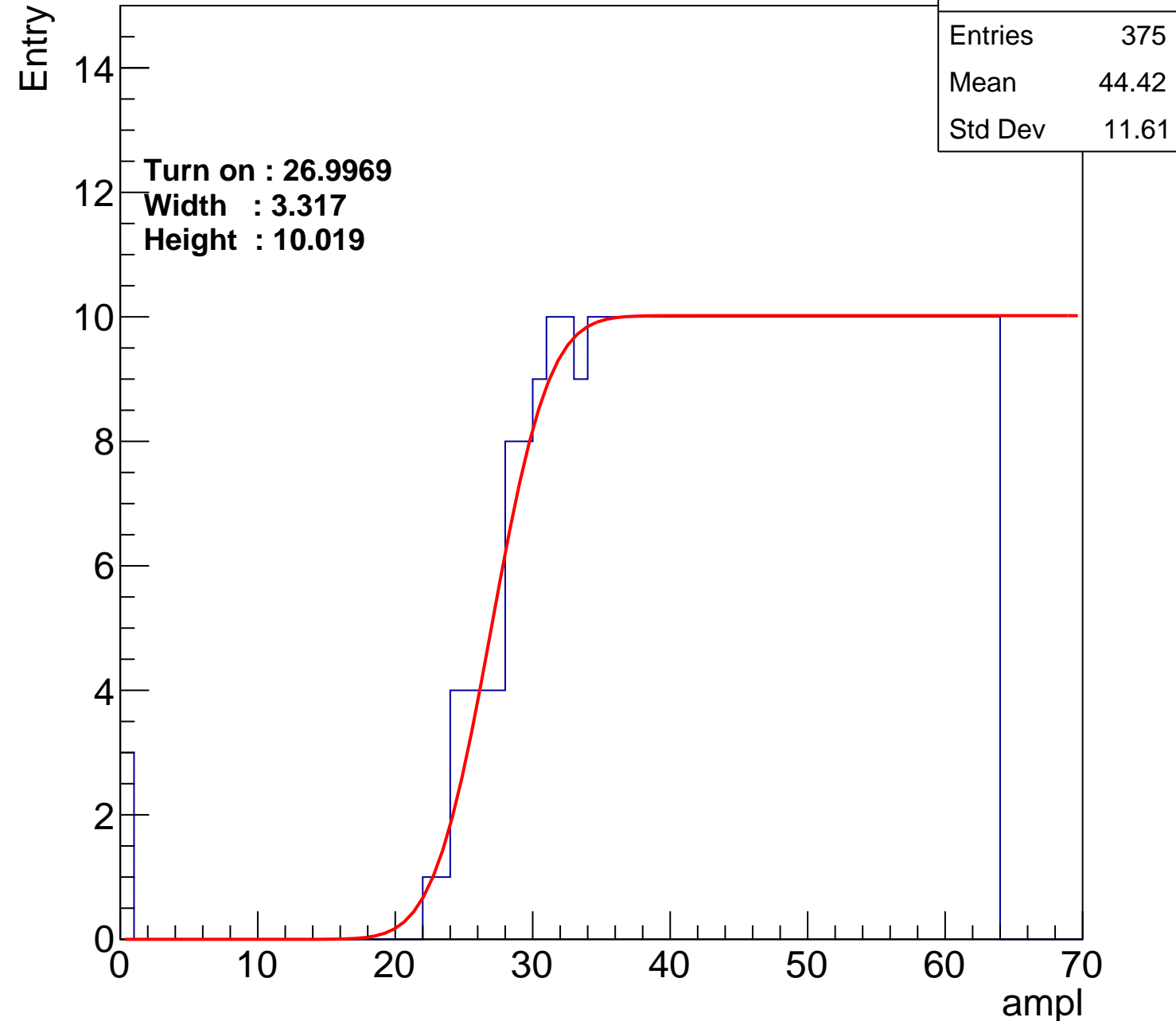
Width : 3.317

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.6
Std Dev	11.8

Turn on : 27.9644

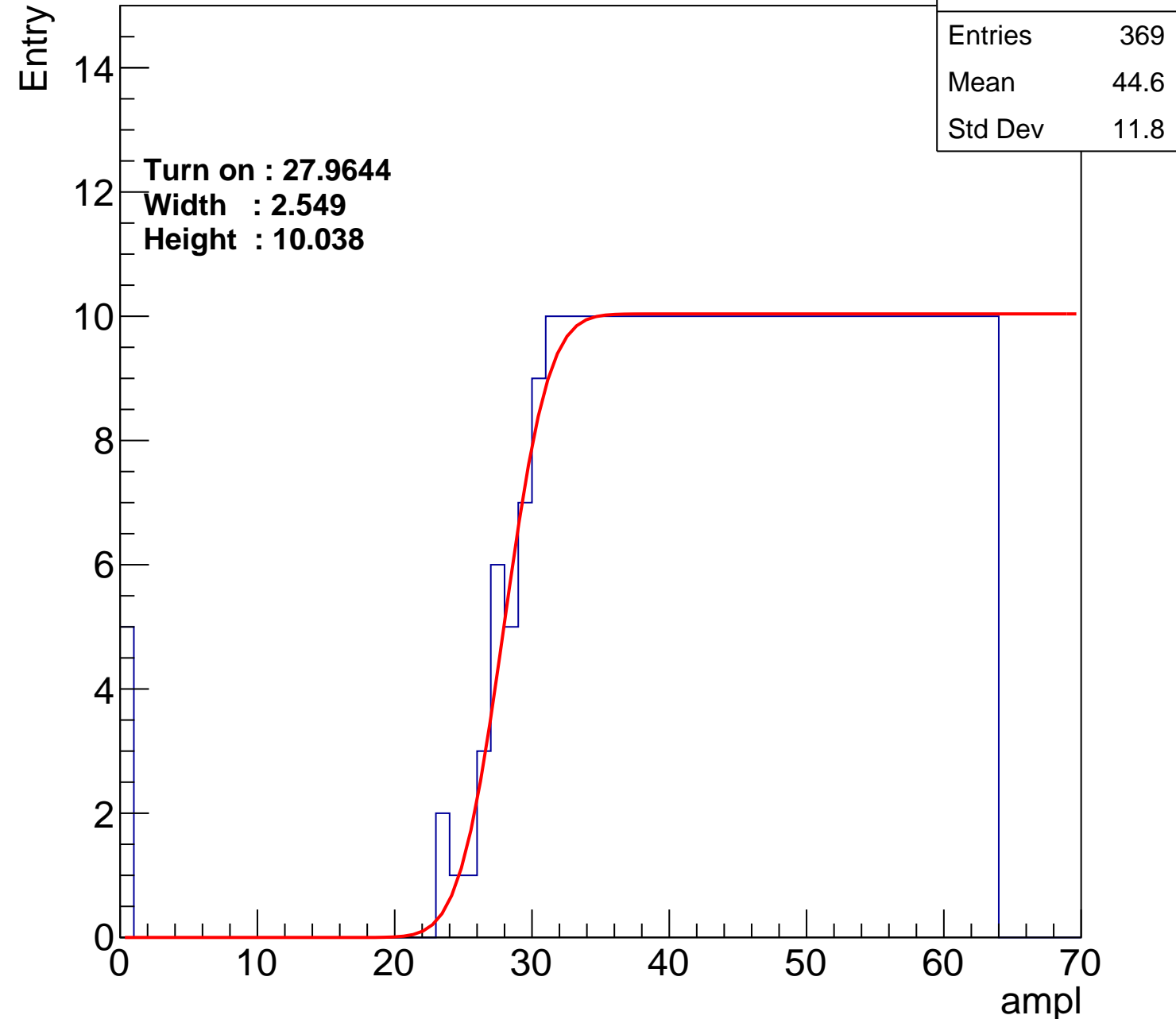
Width : 2.549

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.28
Std Dev	10.8

Turn on : 27.9048

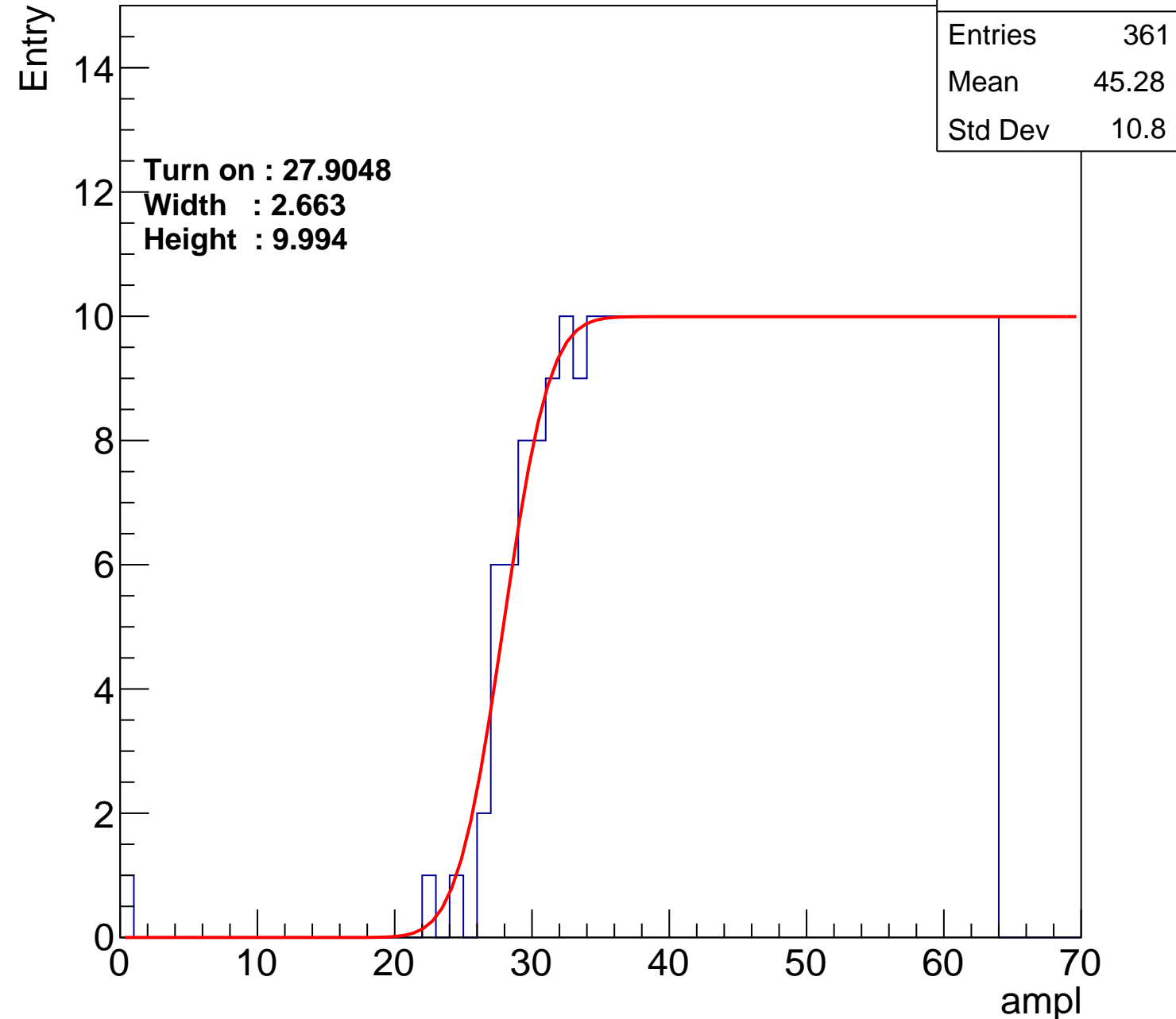
Width : 2.663

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.44
Std Dev	11.26

Turn on : 29.5516

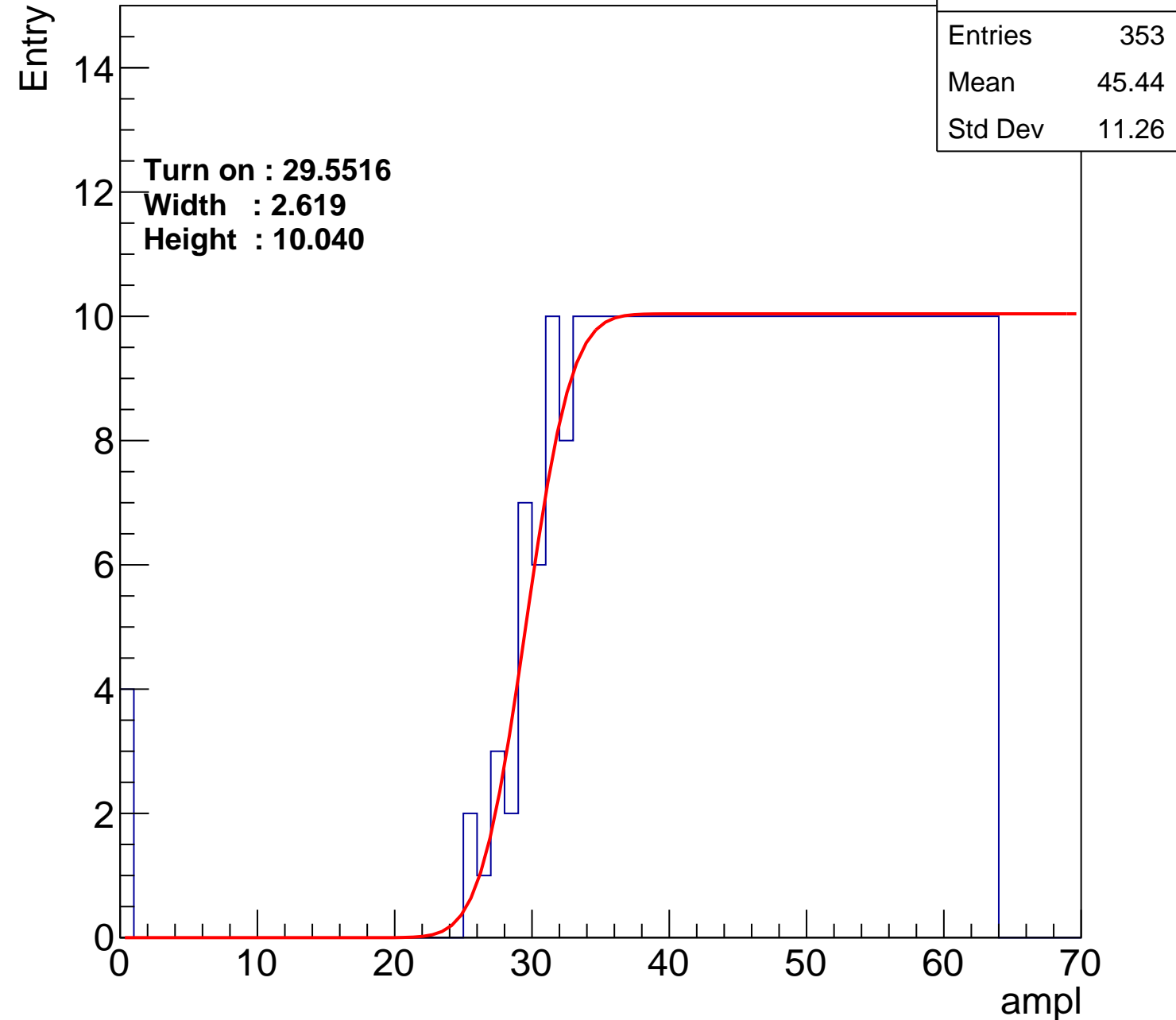
Width : 2.619

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.04
Std Dev	12.08

Turn on : 26.9561

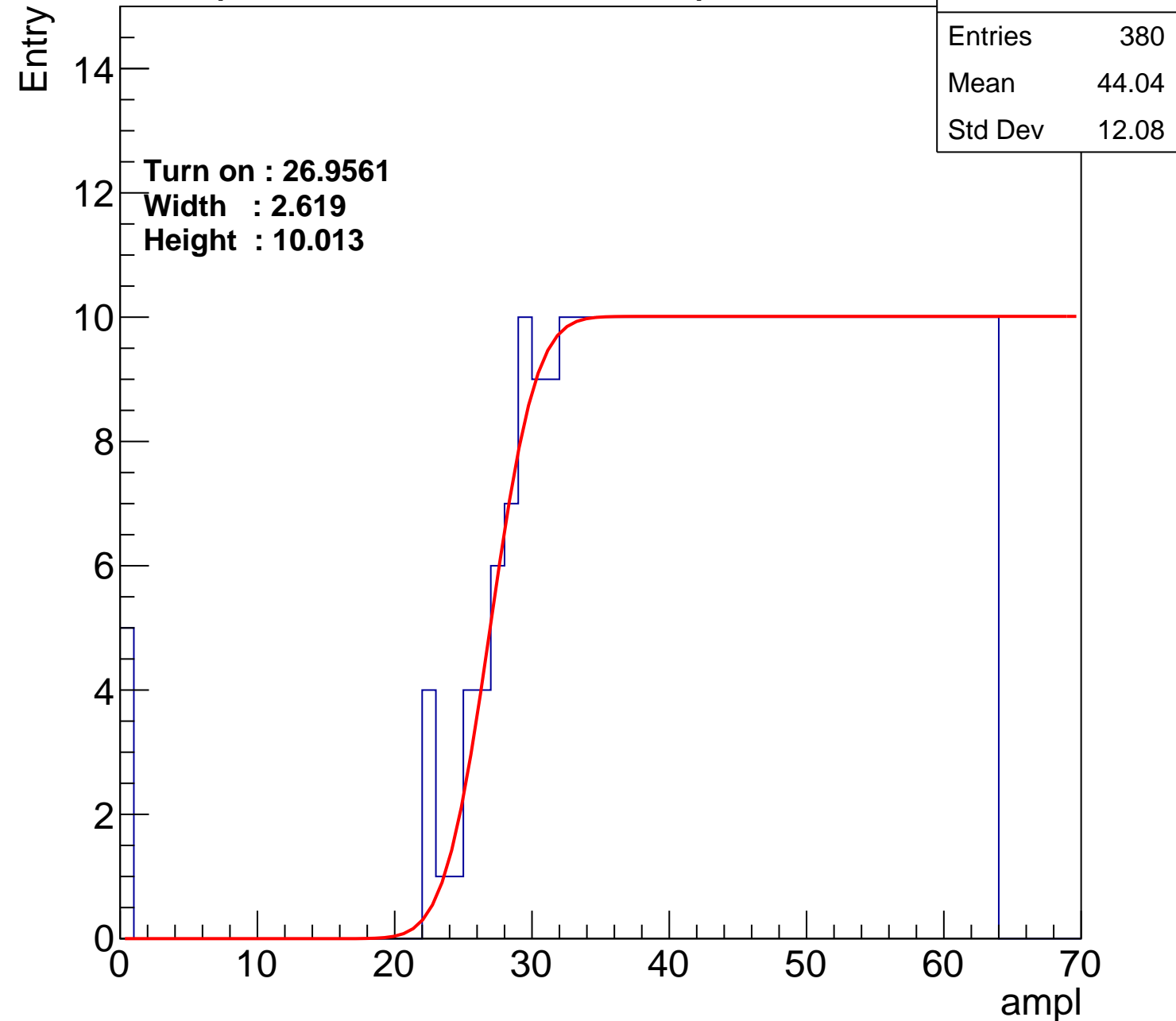
Width : 2.619

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.47
Std Dev	10.76

Turn on : 29.1428

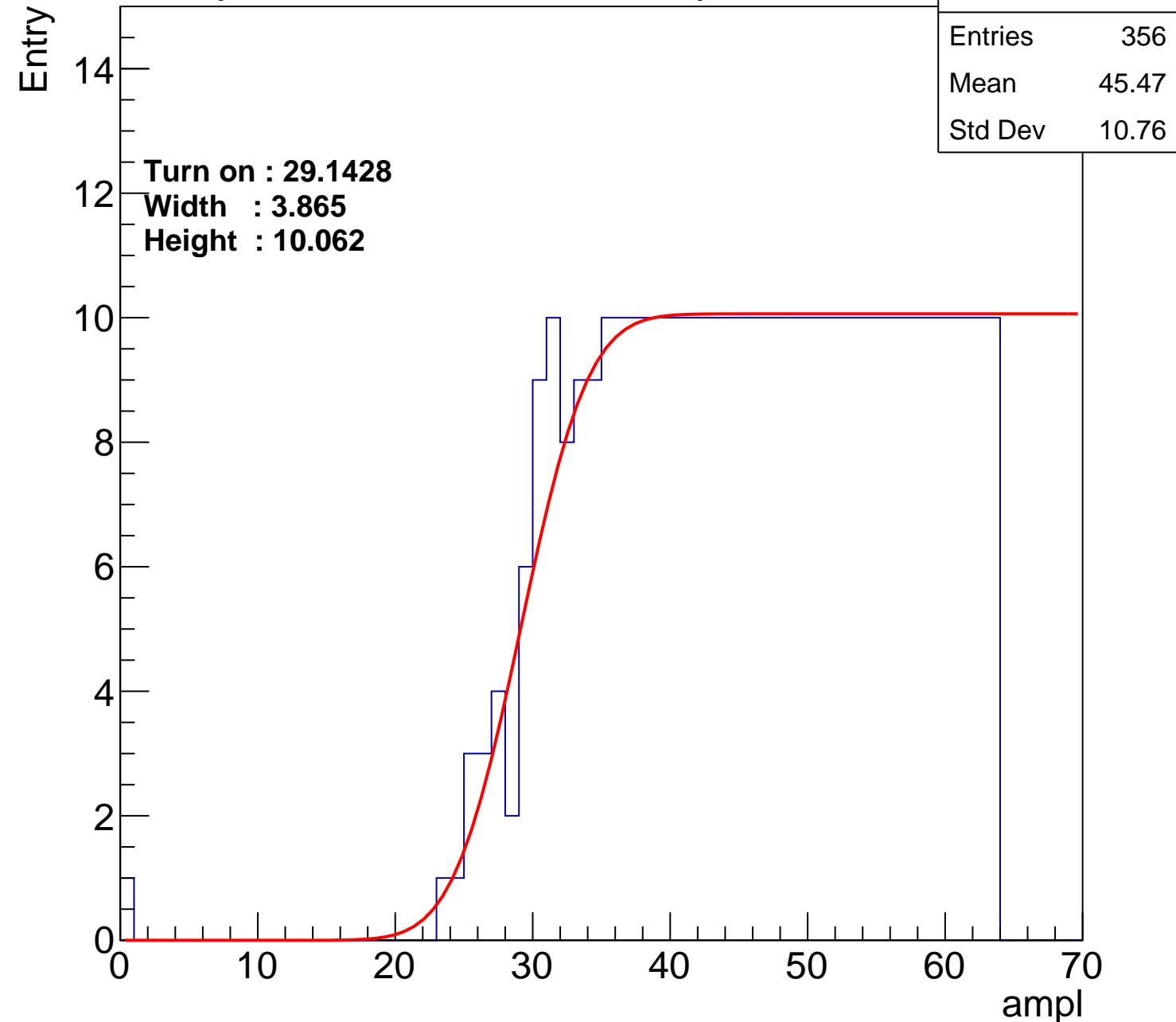
Width : 3.865

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.55
Std Dev	10.84

Turn on : 28.8010

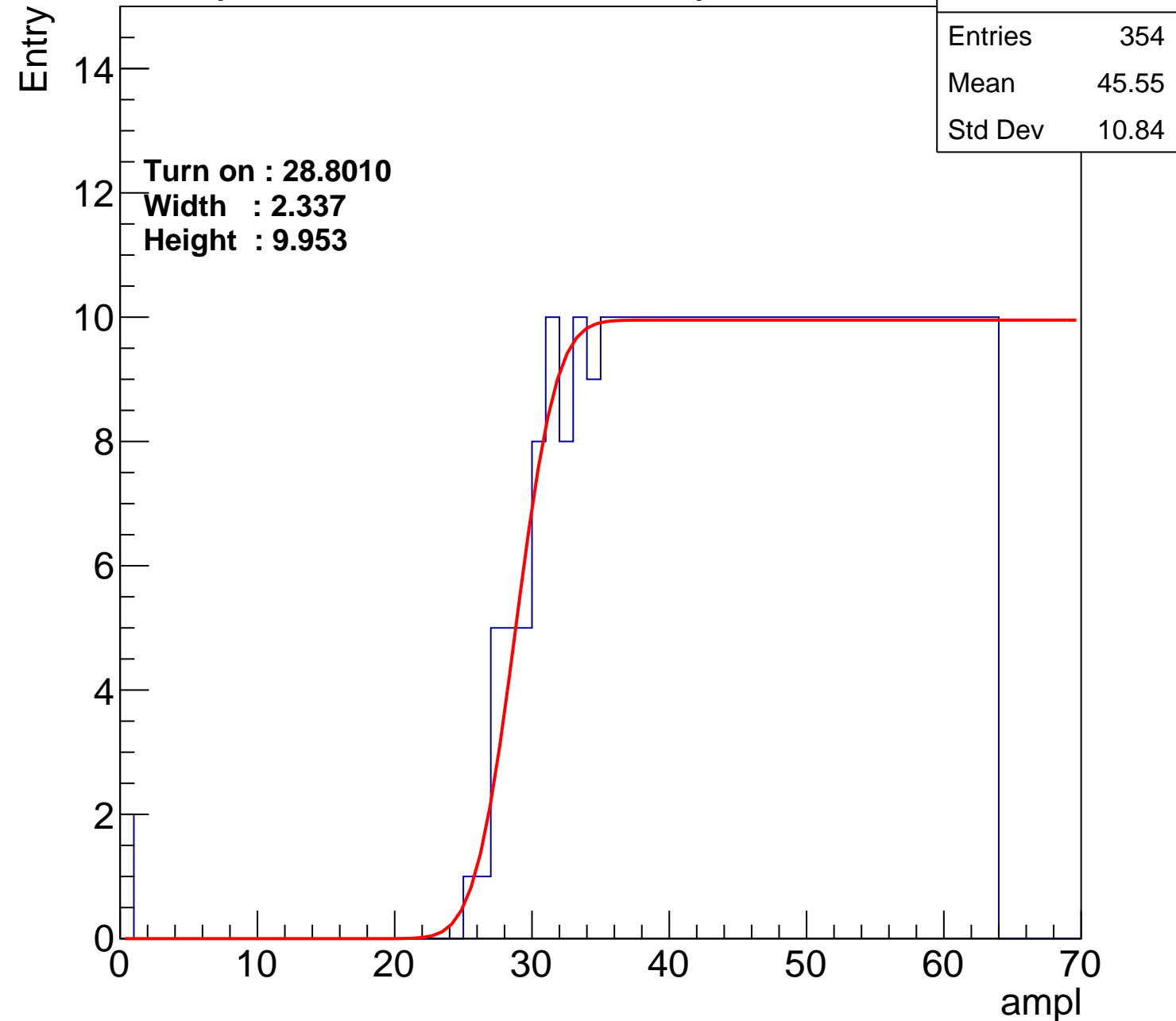
Width : 2.337

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.8443

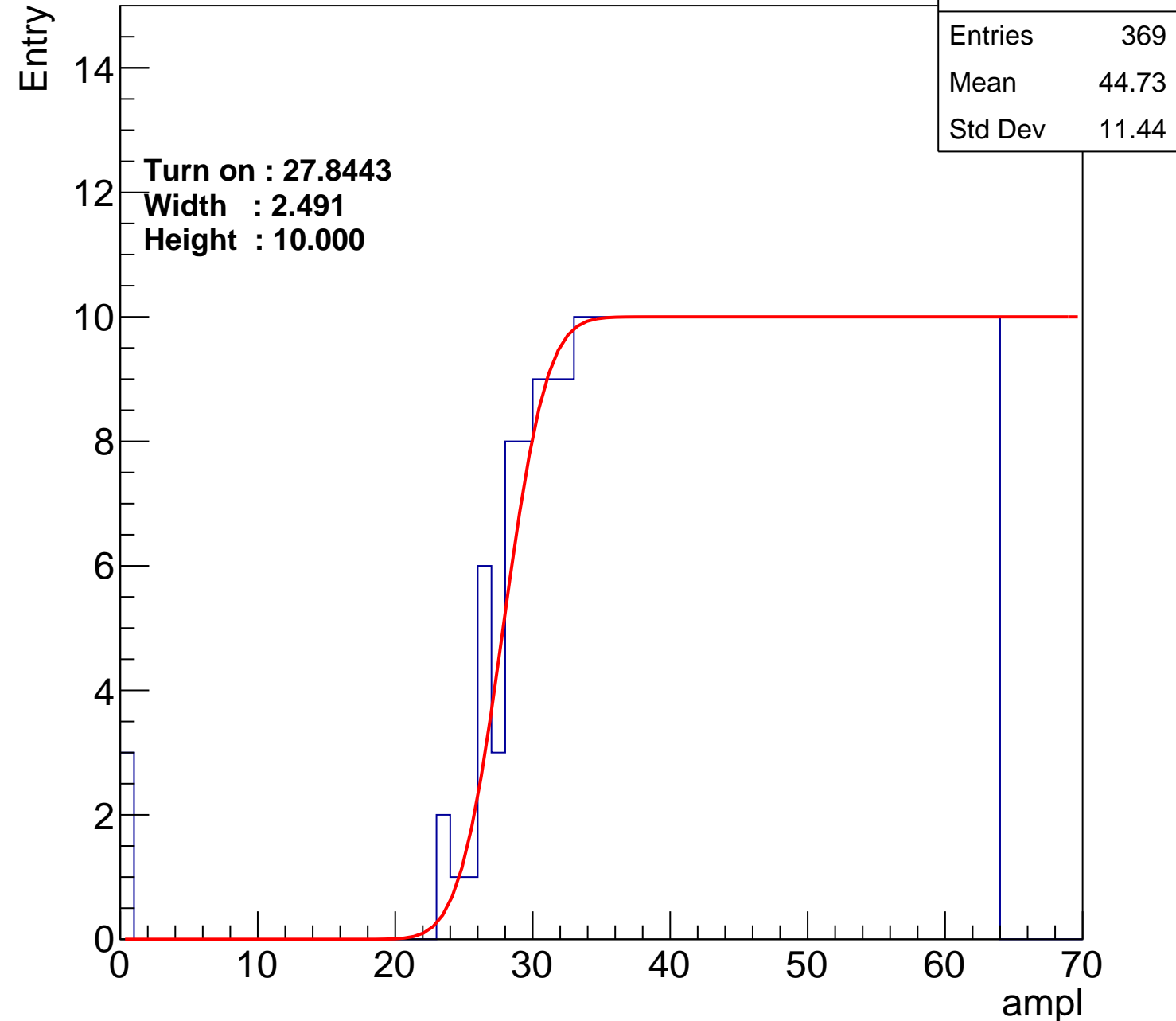
Width : 2.491

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.79
Std Dev	10.73

Turn on : 29.2218

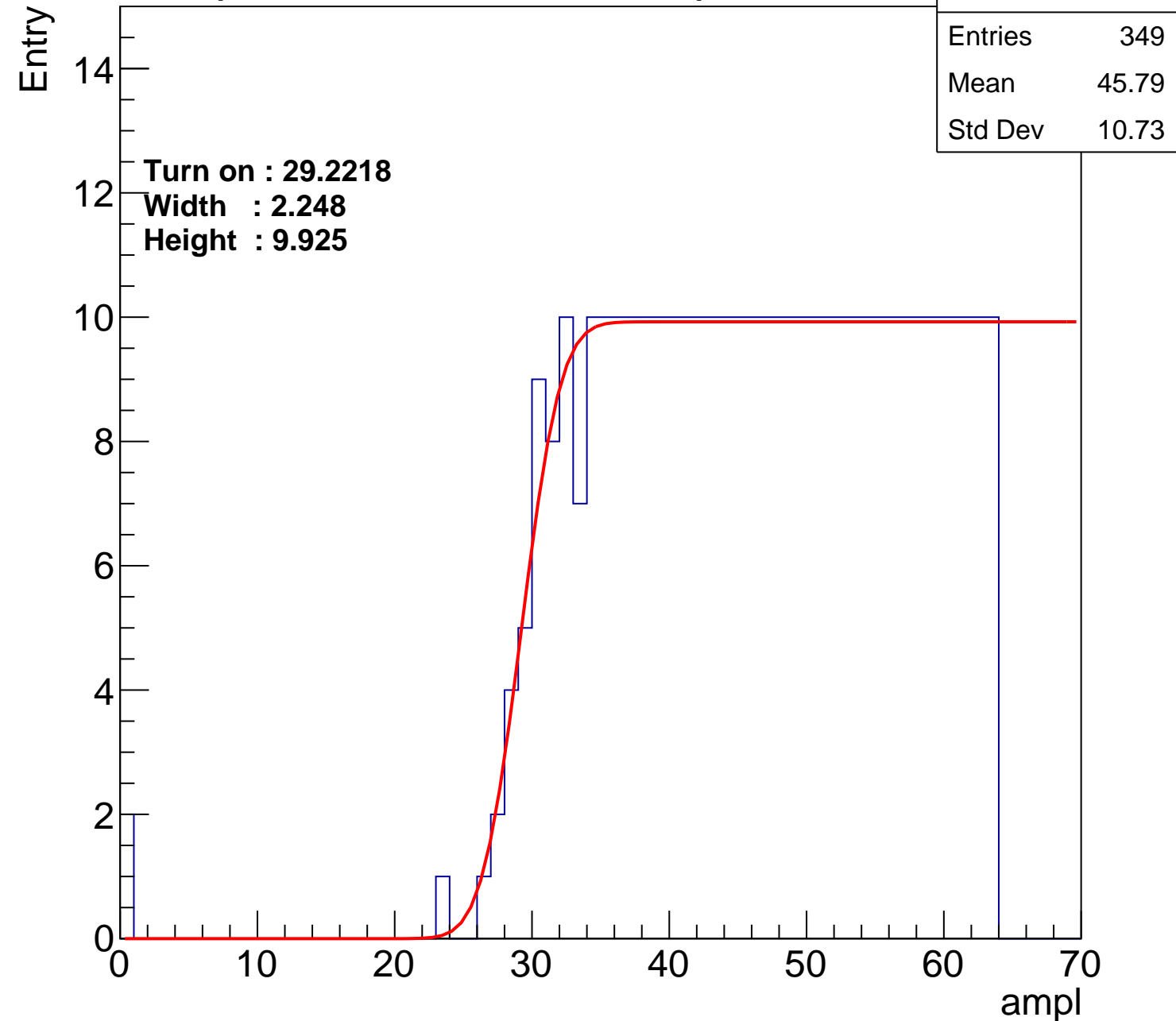
Width : 2.248

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.06
Std Dev	11.23

Turn on : 28.4695

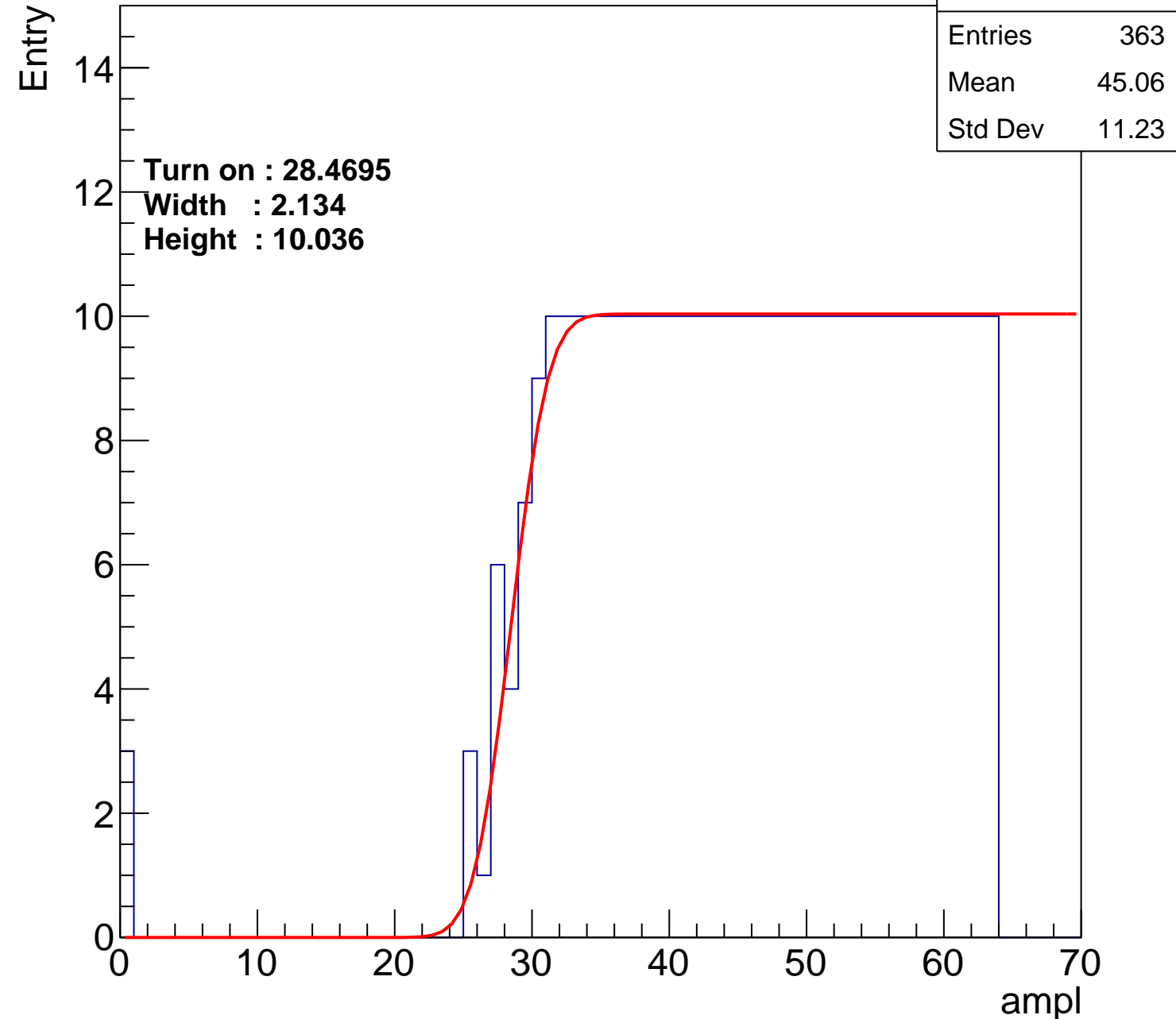
Width : 2.134

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.53
Std Dev	11.24

Turn on : 29.6708

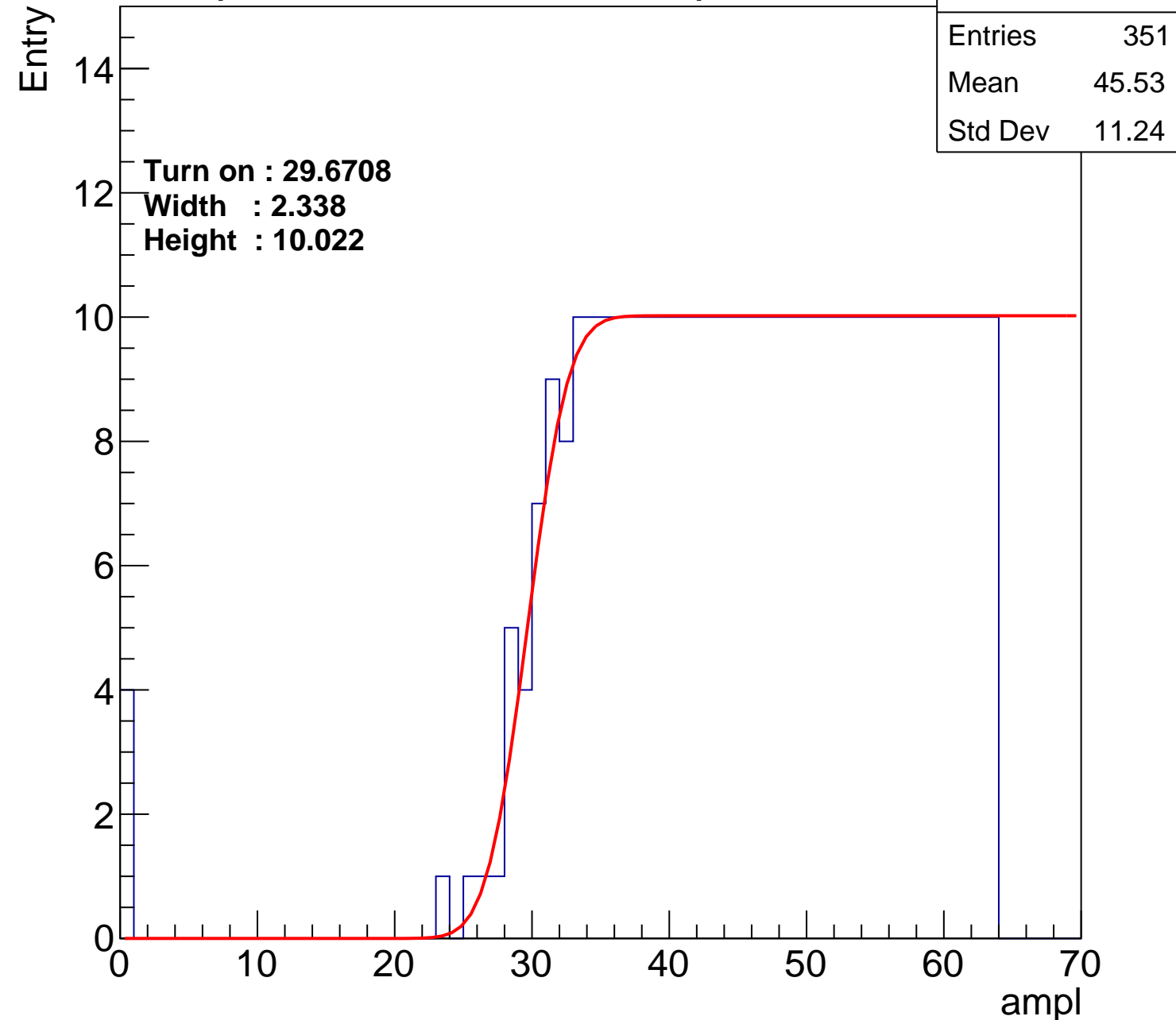
Width : 2.338

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch111

calib_packv5_042523_0143.root, FC#8, port C1

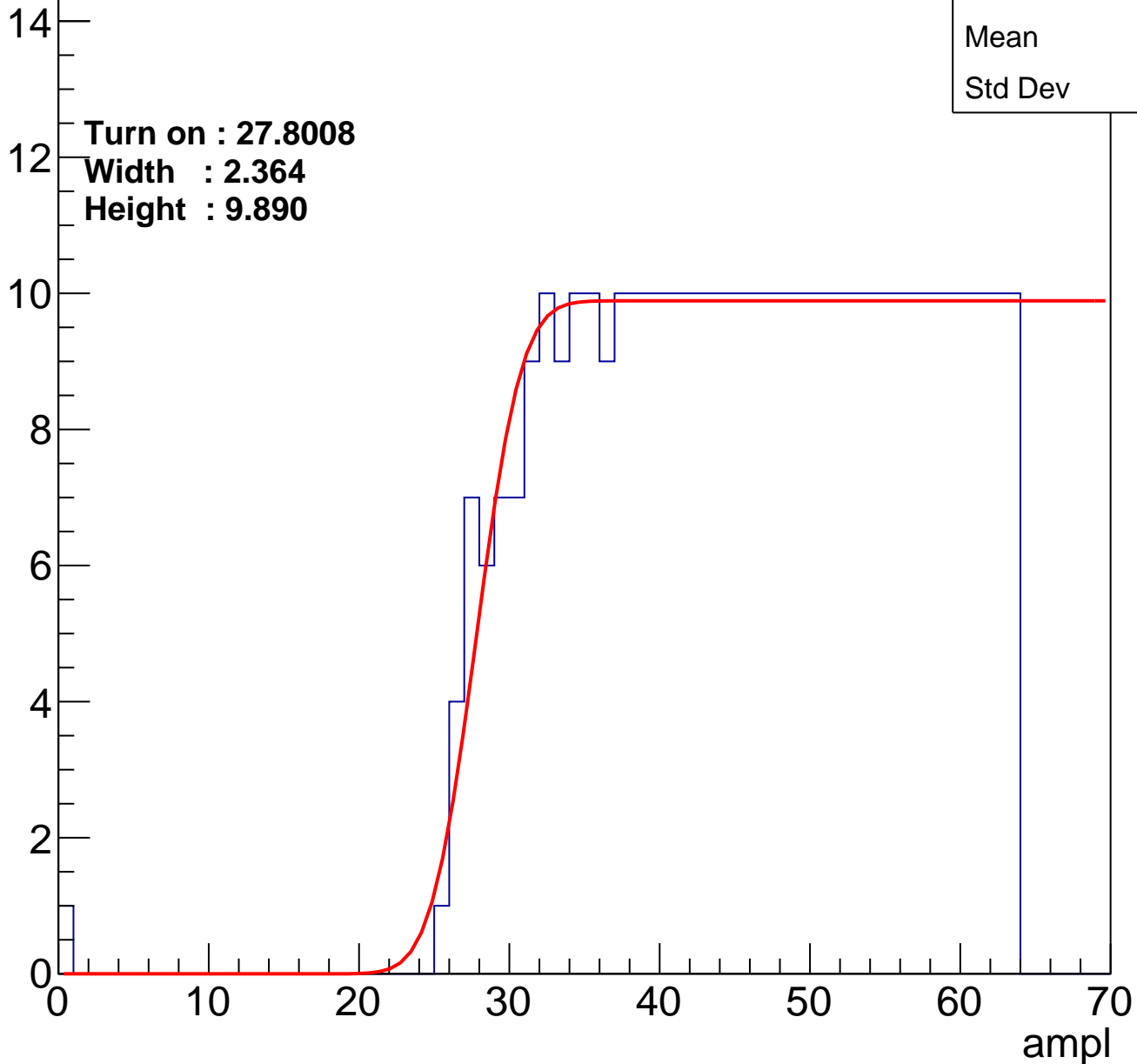
Entries	360
Mean	45.3
Std Dev	10.8

Turn on : 27.8008

Width : 2.364

Height : 9.890

Entry



B0L002S, U21-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.43
Std Dev	11.46

Turn on : 26.5393

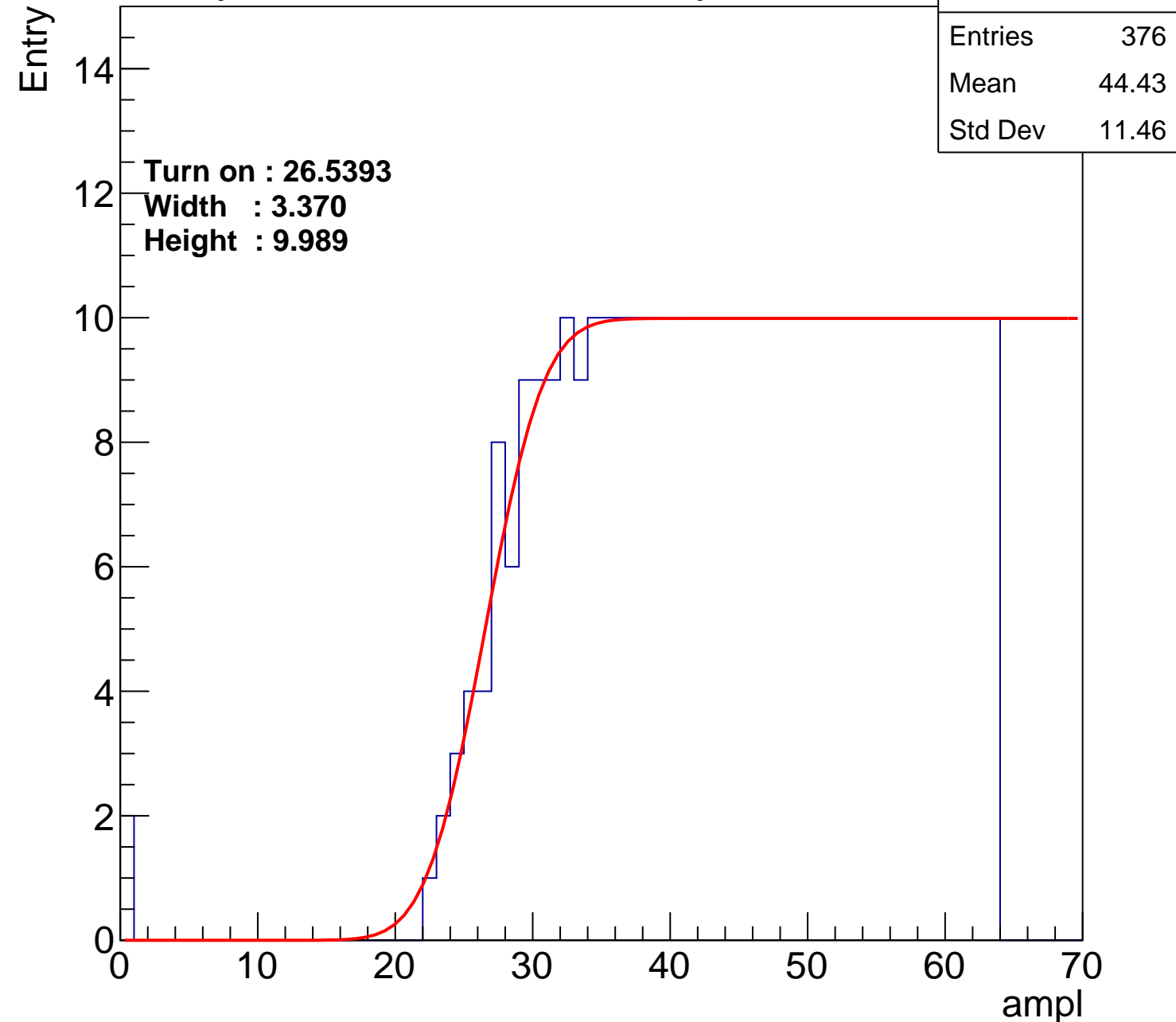
Width : 3.370

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch113

calib_packv5_042523_0143.root, FC#8, port C1

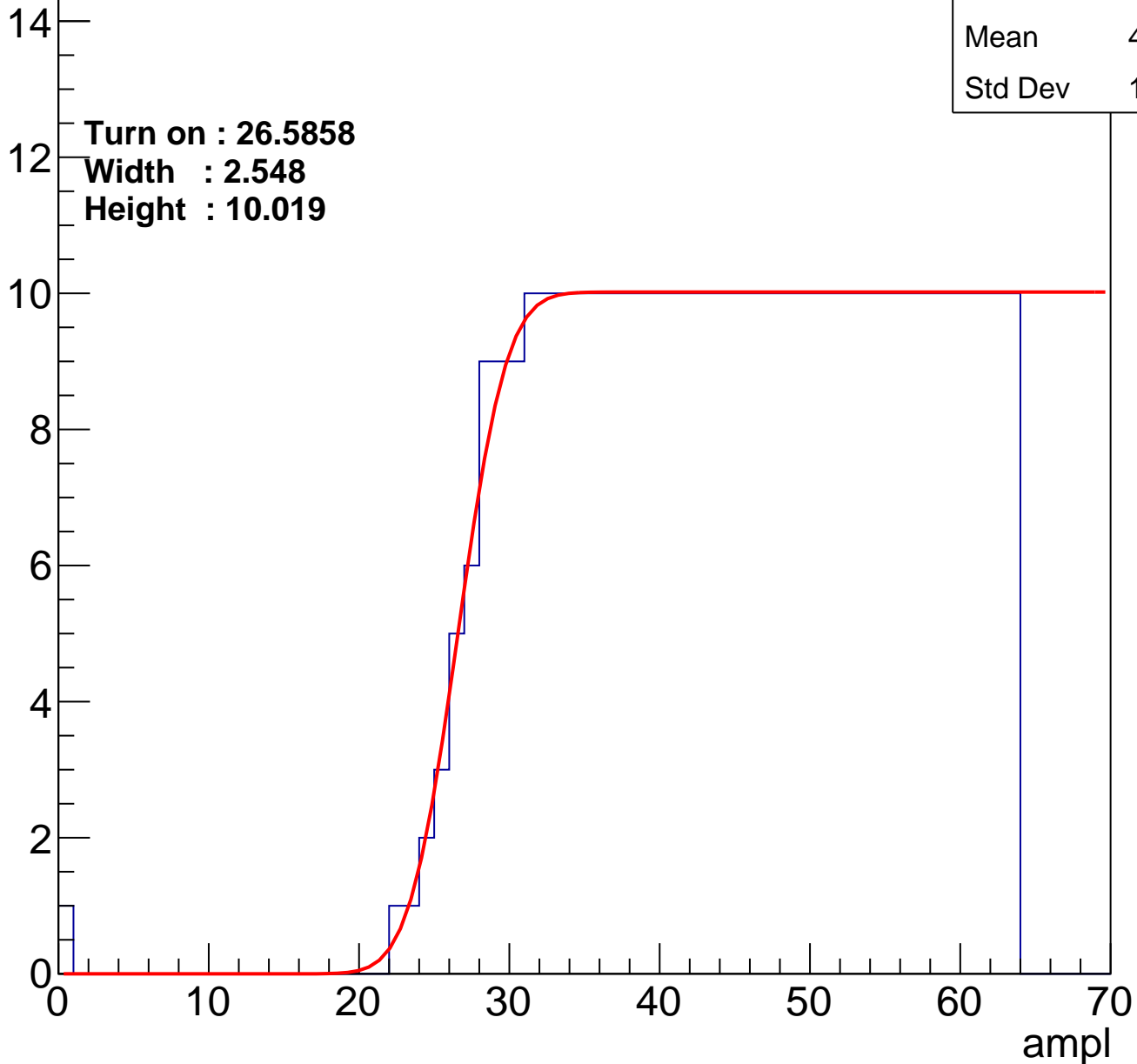
Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 26.5858

Width : 2.548

Height : 10.019

Entry



B0L002S, U21-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.55
Std Dev	11.97

Turn on : 27.7491

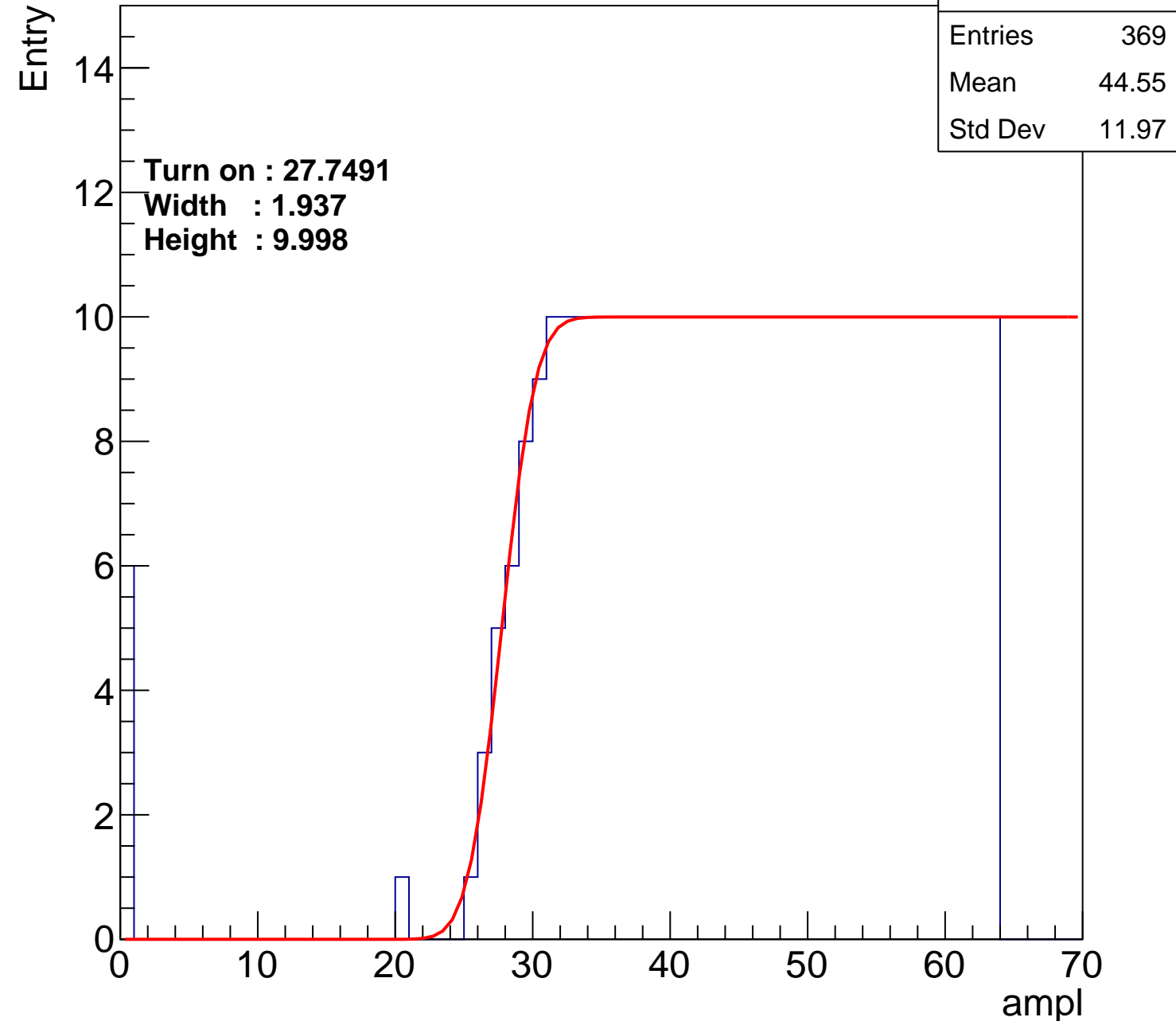
Width : 1.937

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch115

calib_packv5_042523_0143.root, FC#8, port C1

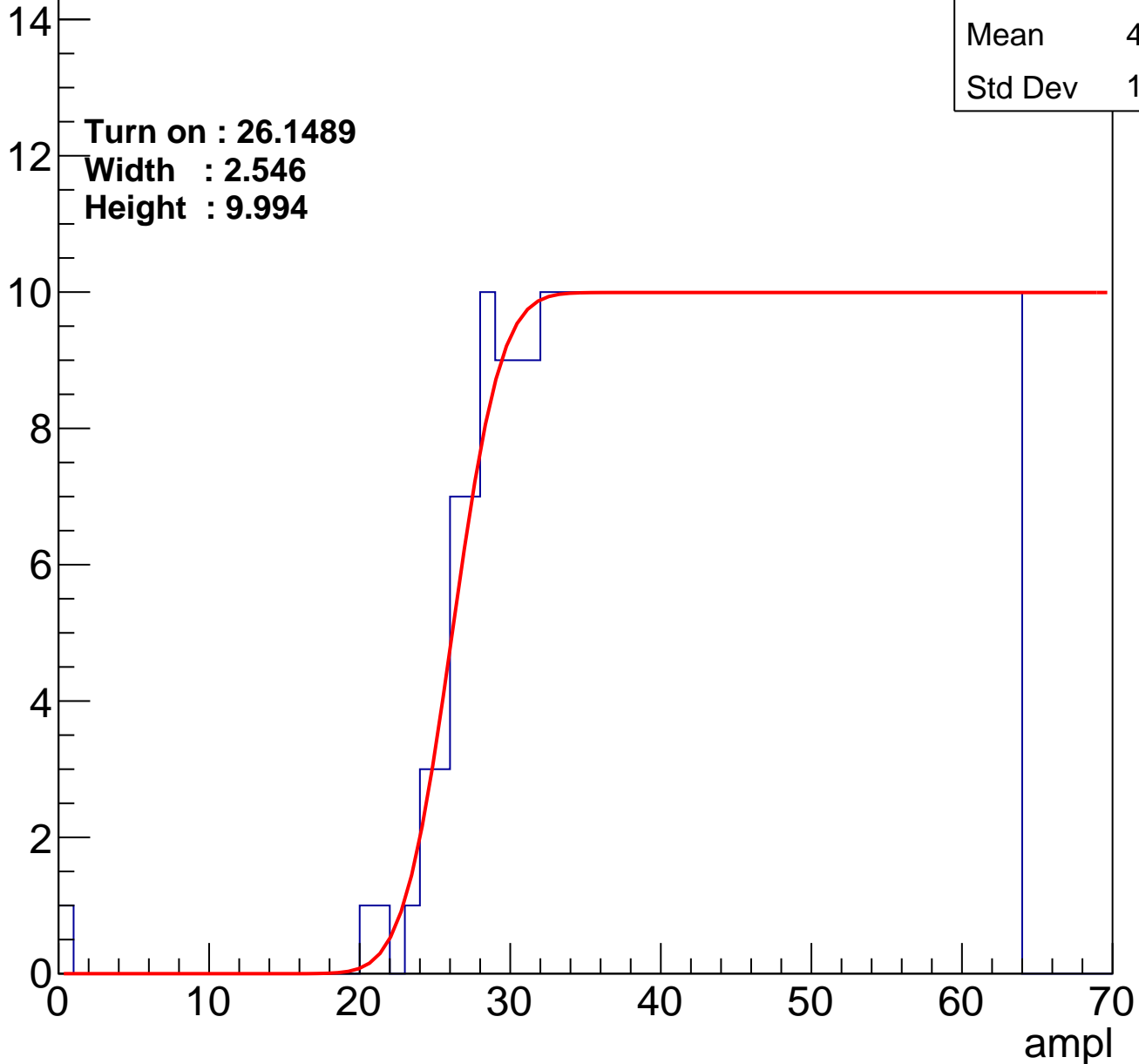
Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.1489

Width : 2.546

Height : 9.994

Entry



B0L002S, U21-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.33
Std Dev	10.8

Turn on : 27.9695

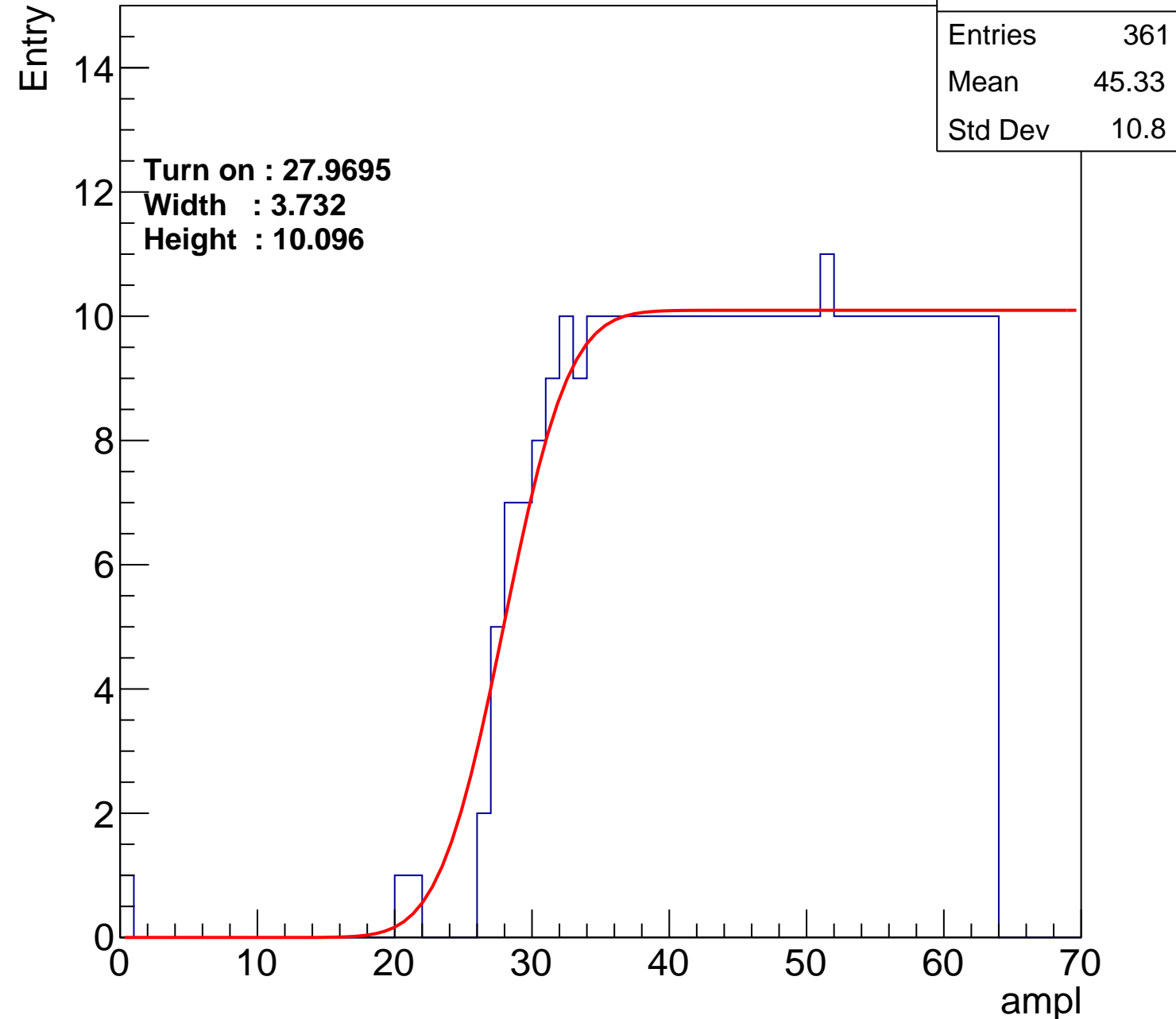
Width : 3.732

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.37
Std Dev	10.97

Turn on : 29.0736

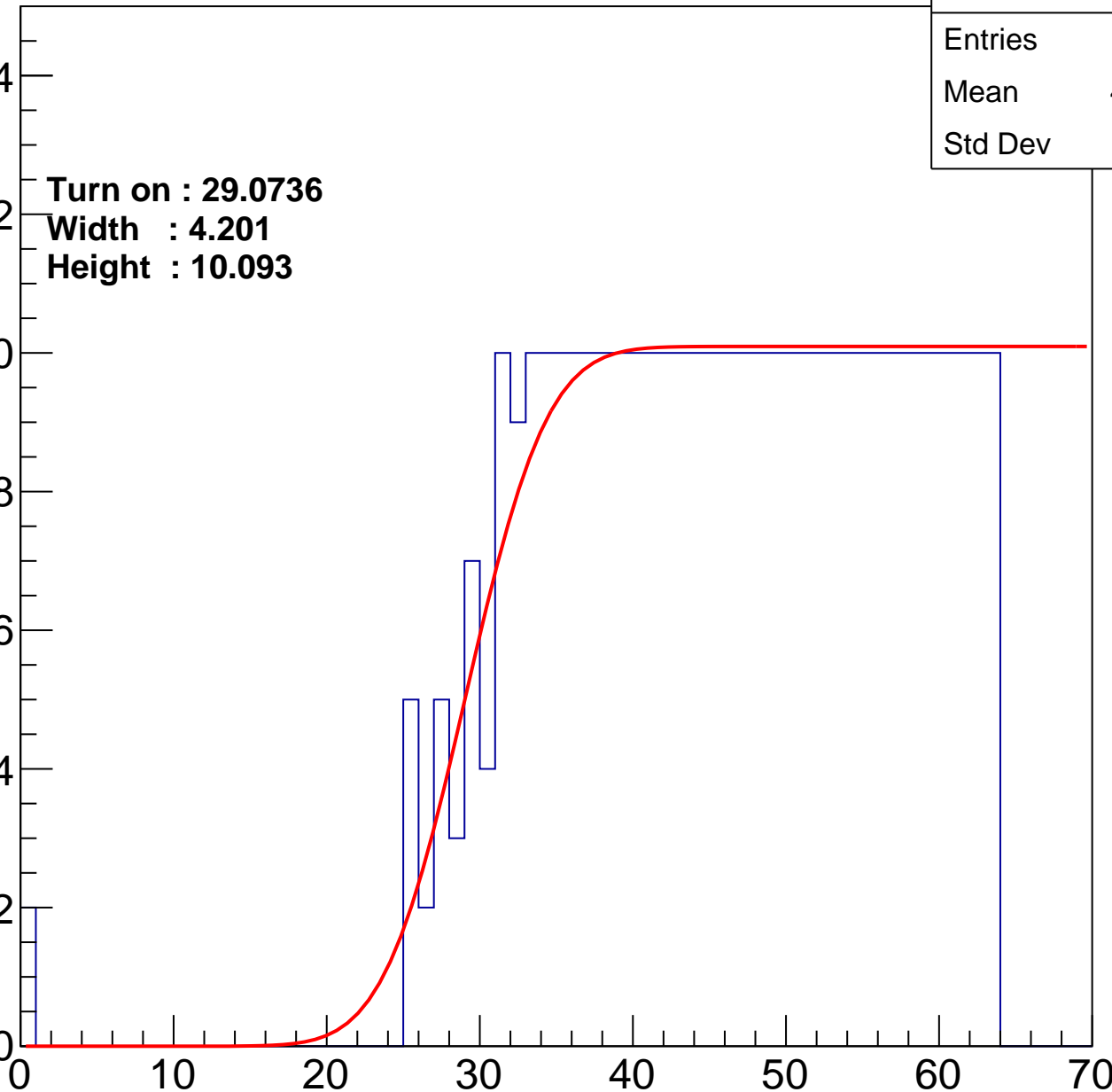
Width : 4.201

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 27.2134

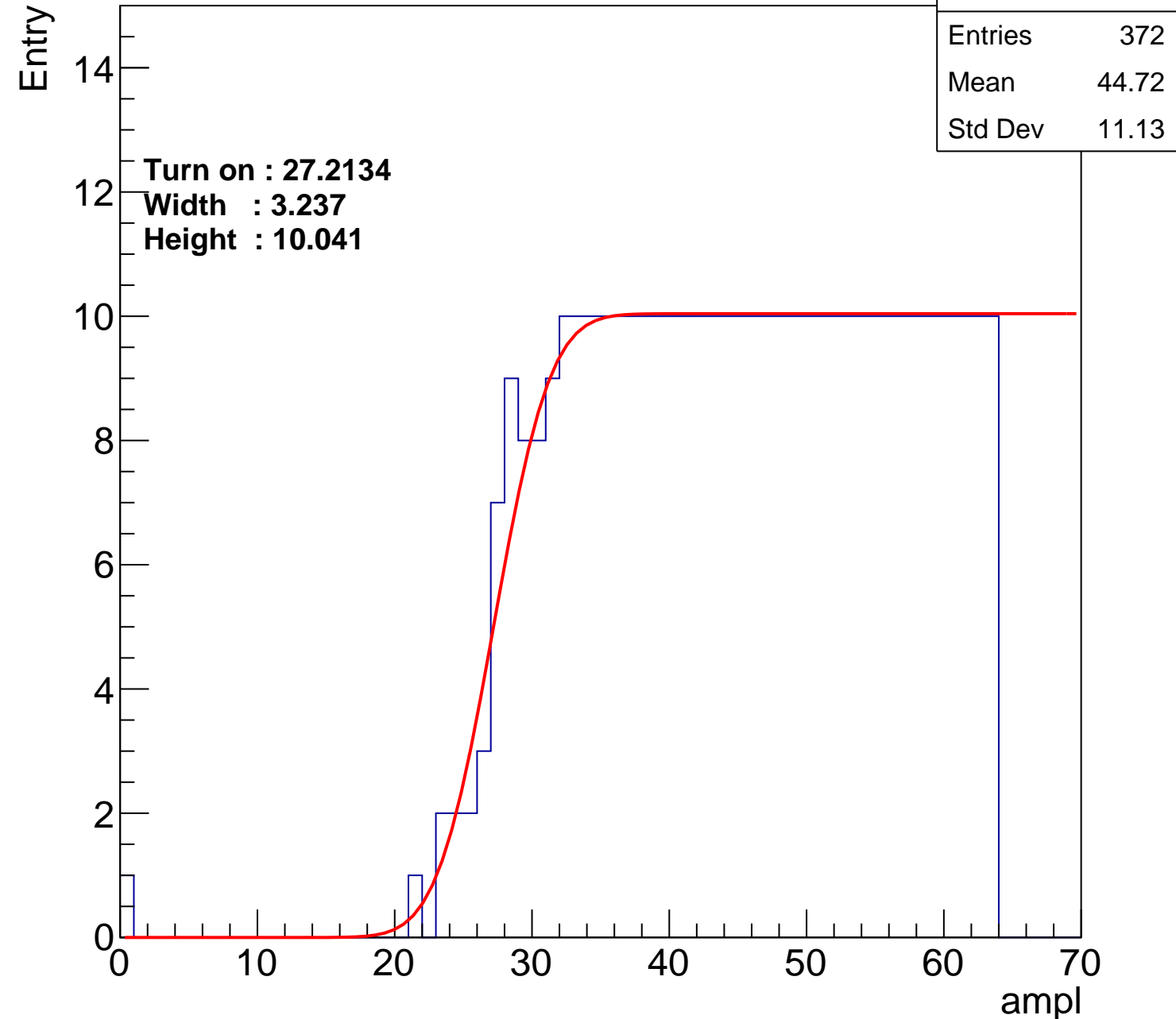
Width : 3.237

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.05
Std Dev	12.06

Turn on : 26.5238

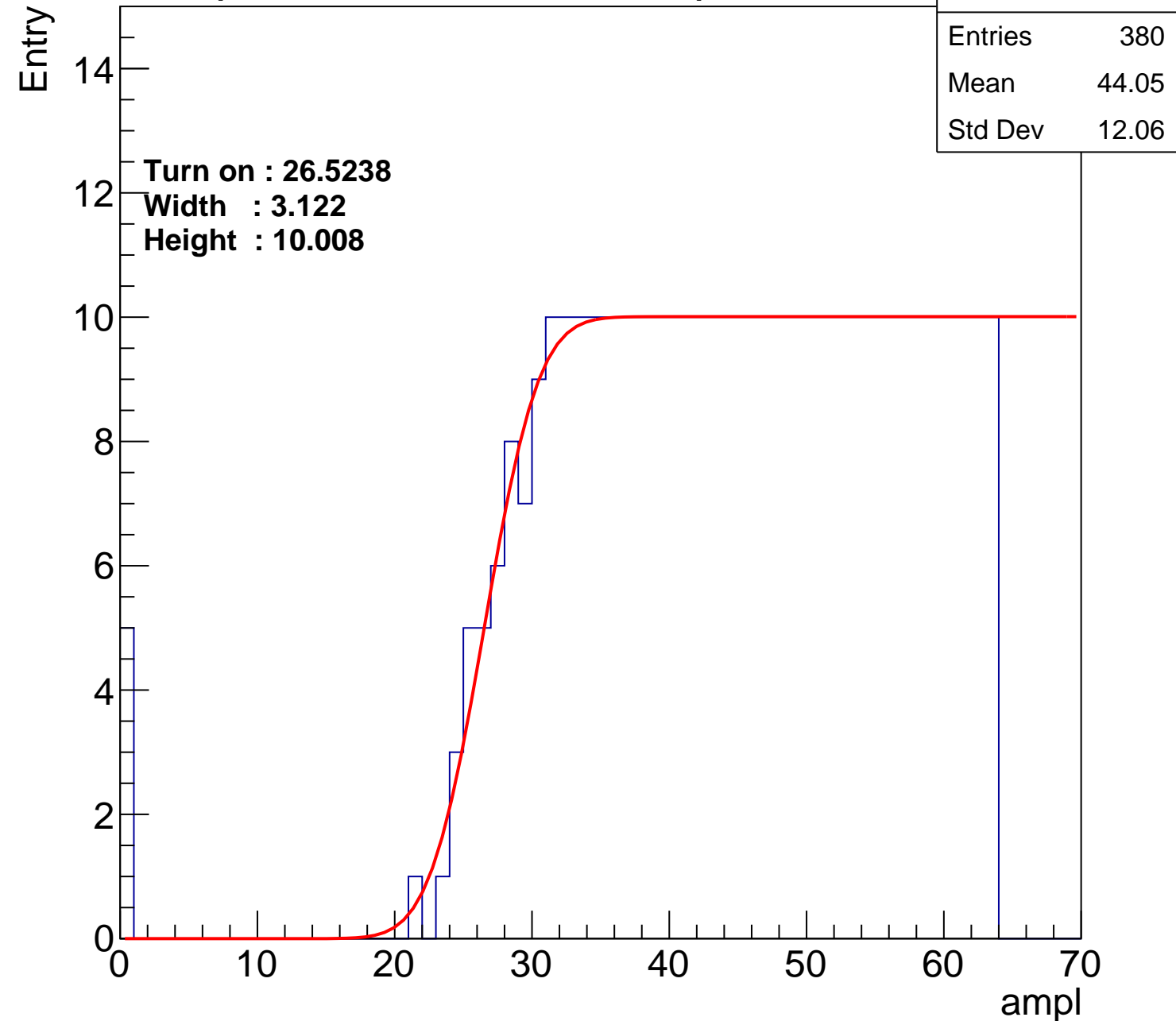
Width : 3.122

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 28.2890

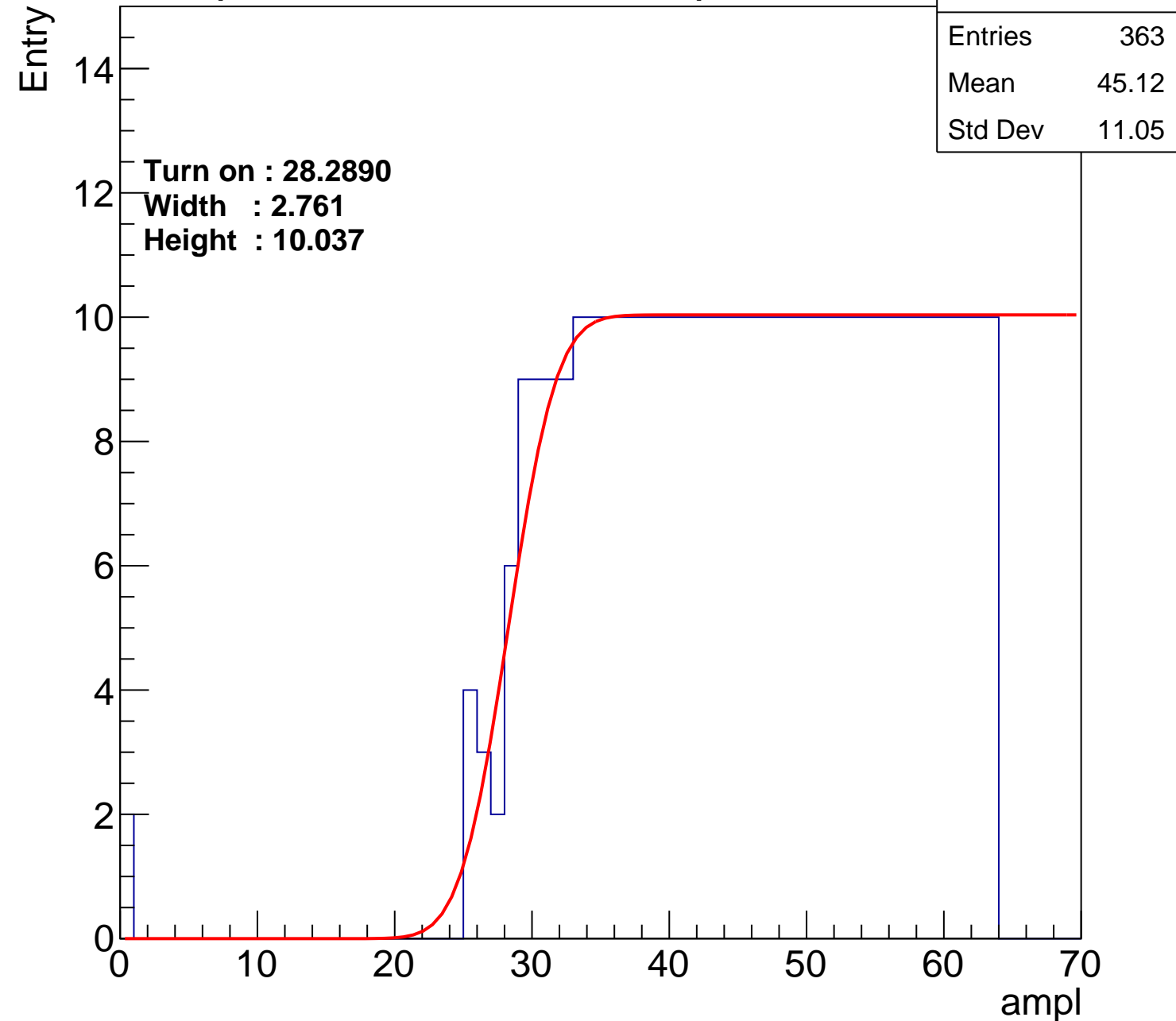
Width : 2.761

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.95
Std Dev	10.96

Turn on : 27.7070

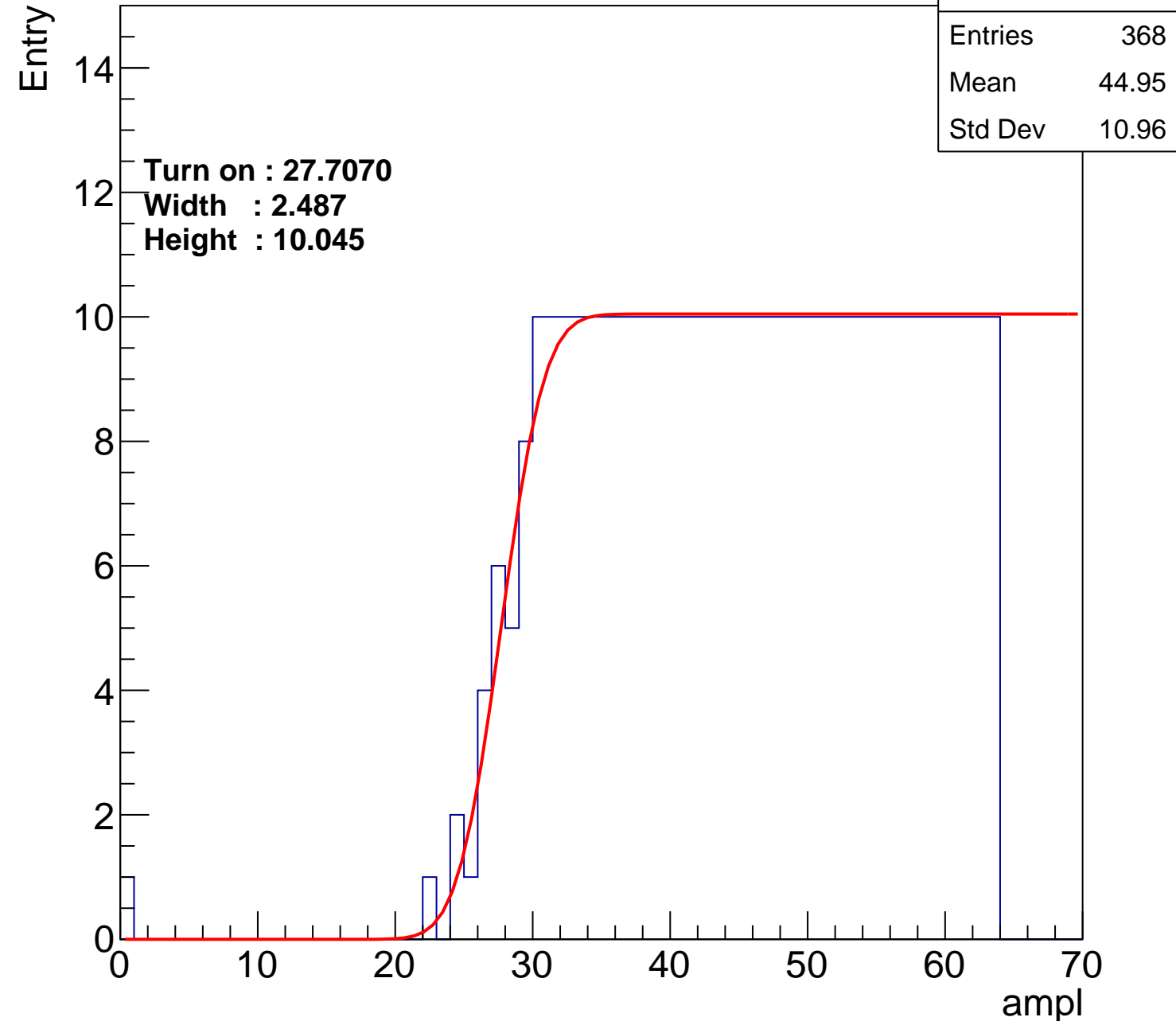
Width : 2.487

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.52

Turn on : 27.3363

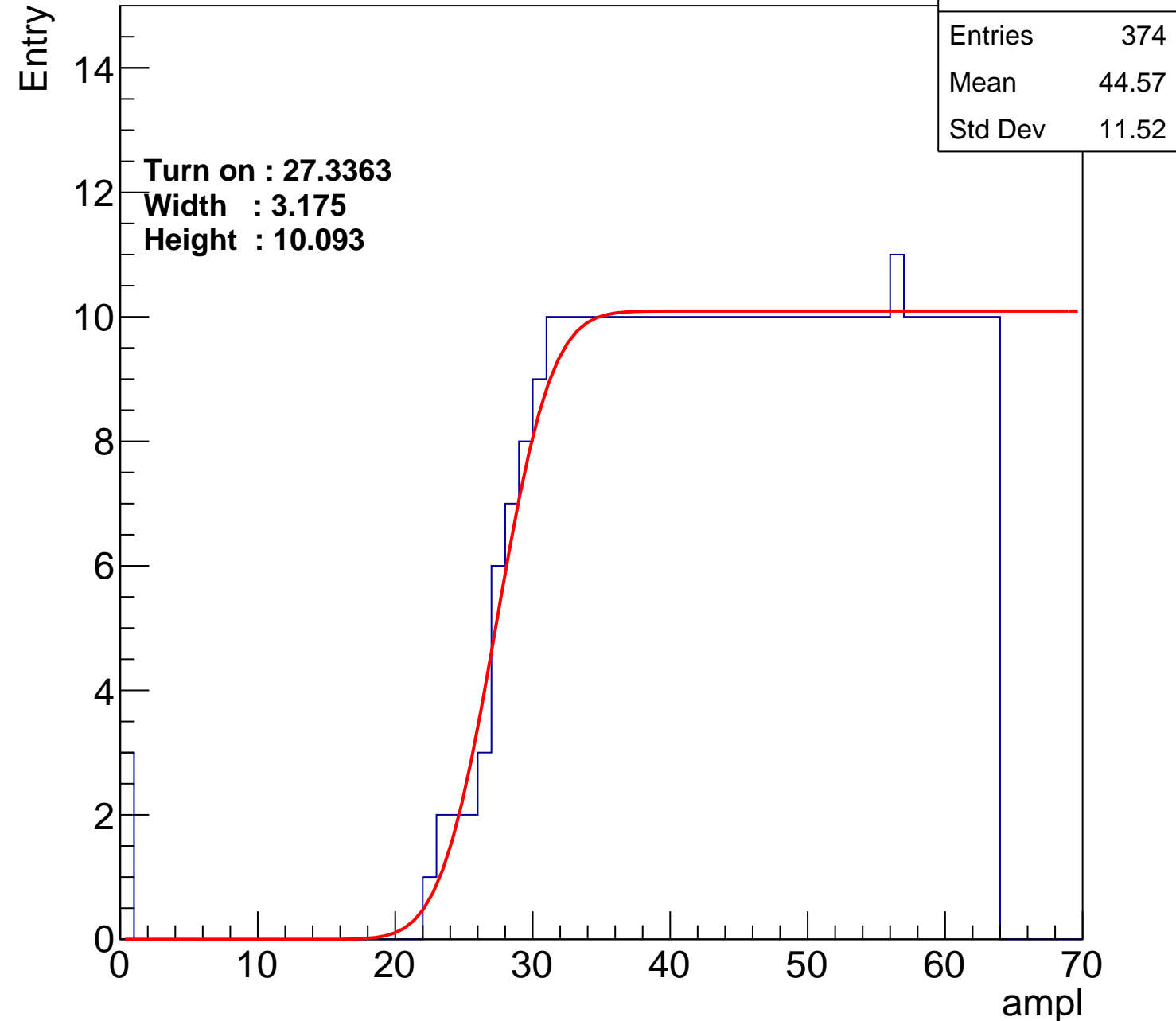
Width : 3.175

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.8
Std Dev	10.71

Turn on : 30.1367

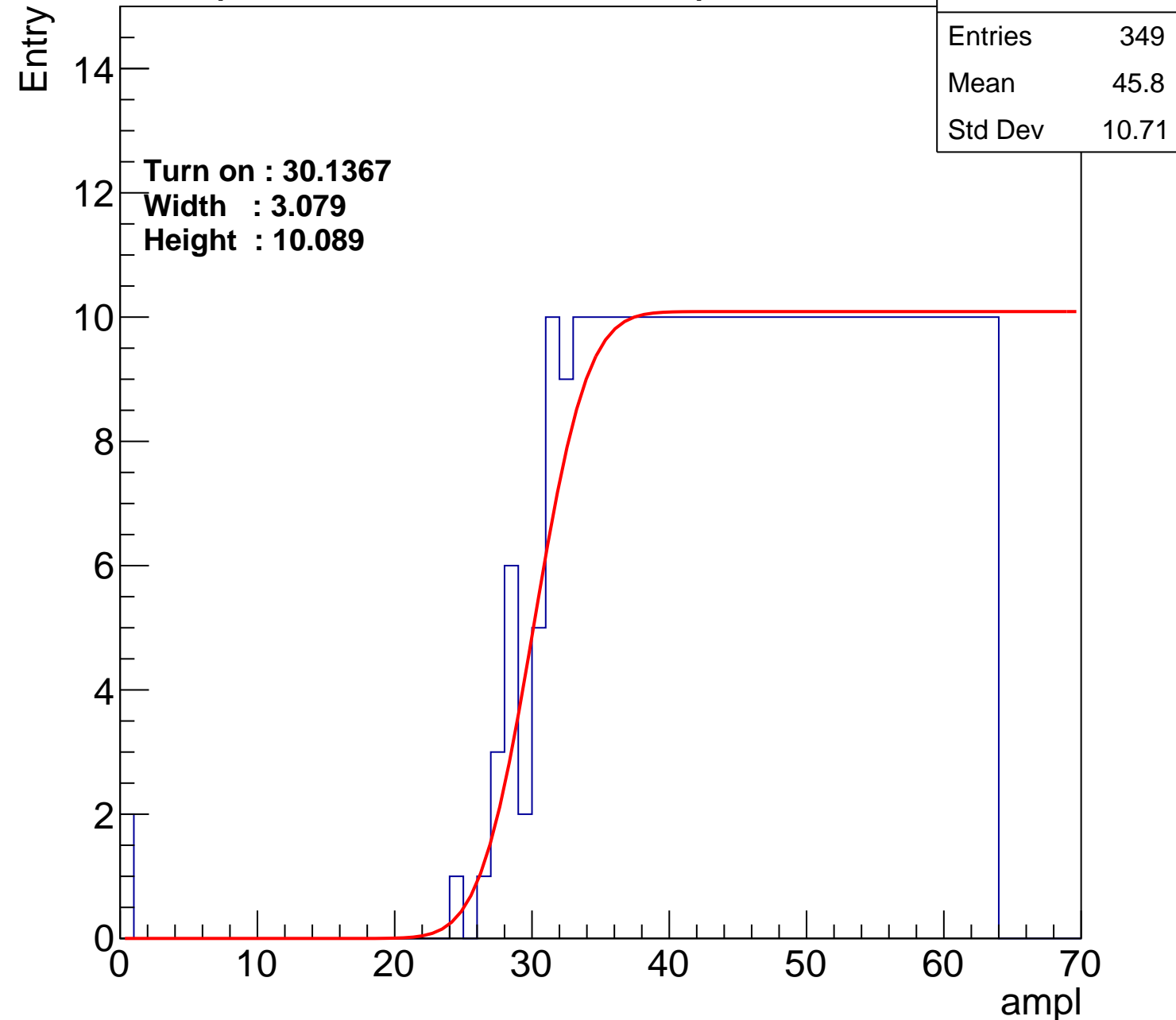
Width : 3.079

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 26.1675

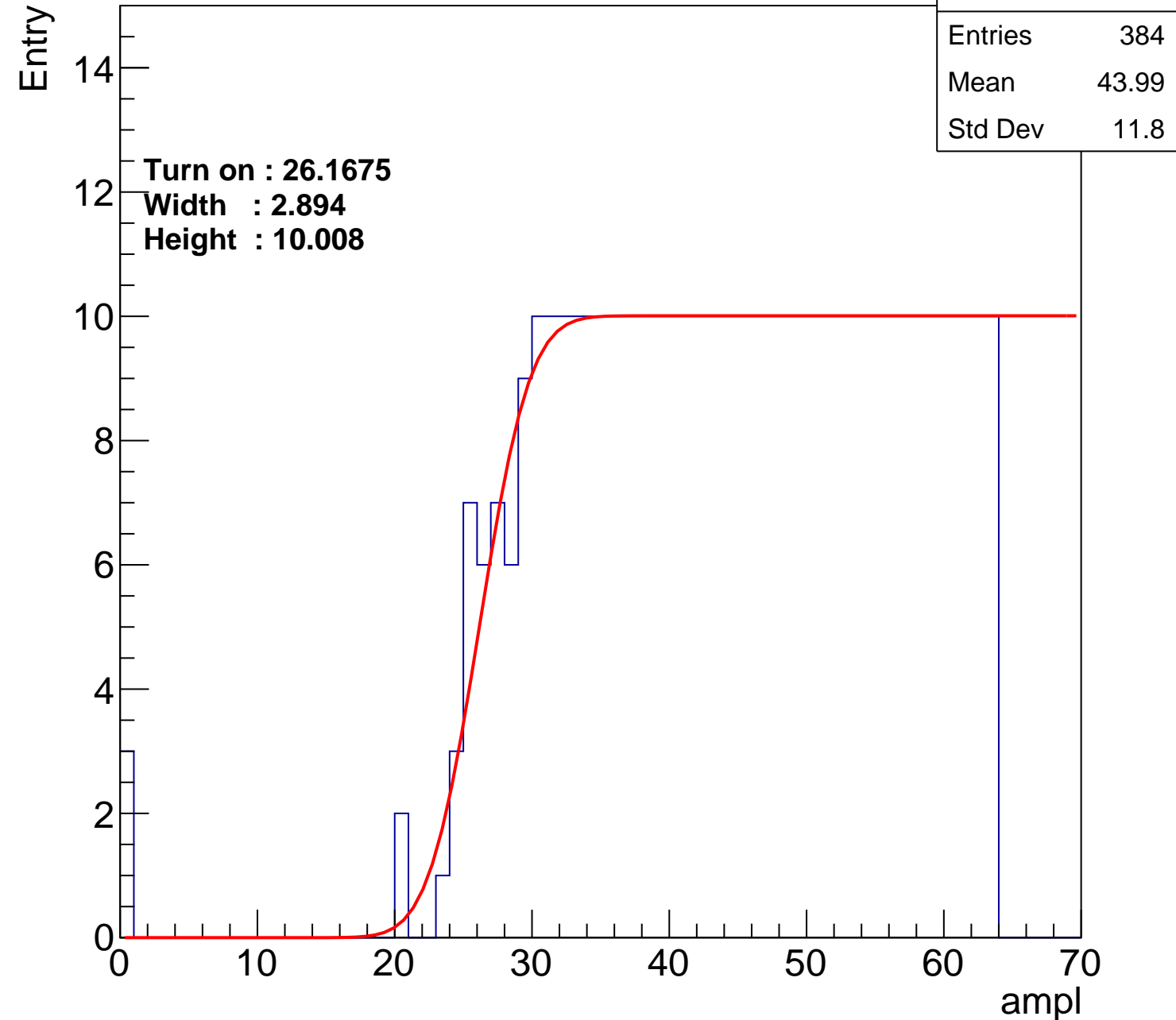
Width : 2.894

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.42
Std Dev	11.55

Turn on : 26.5222

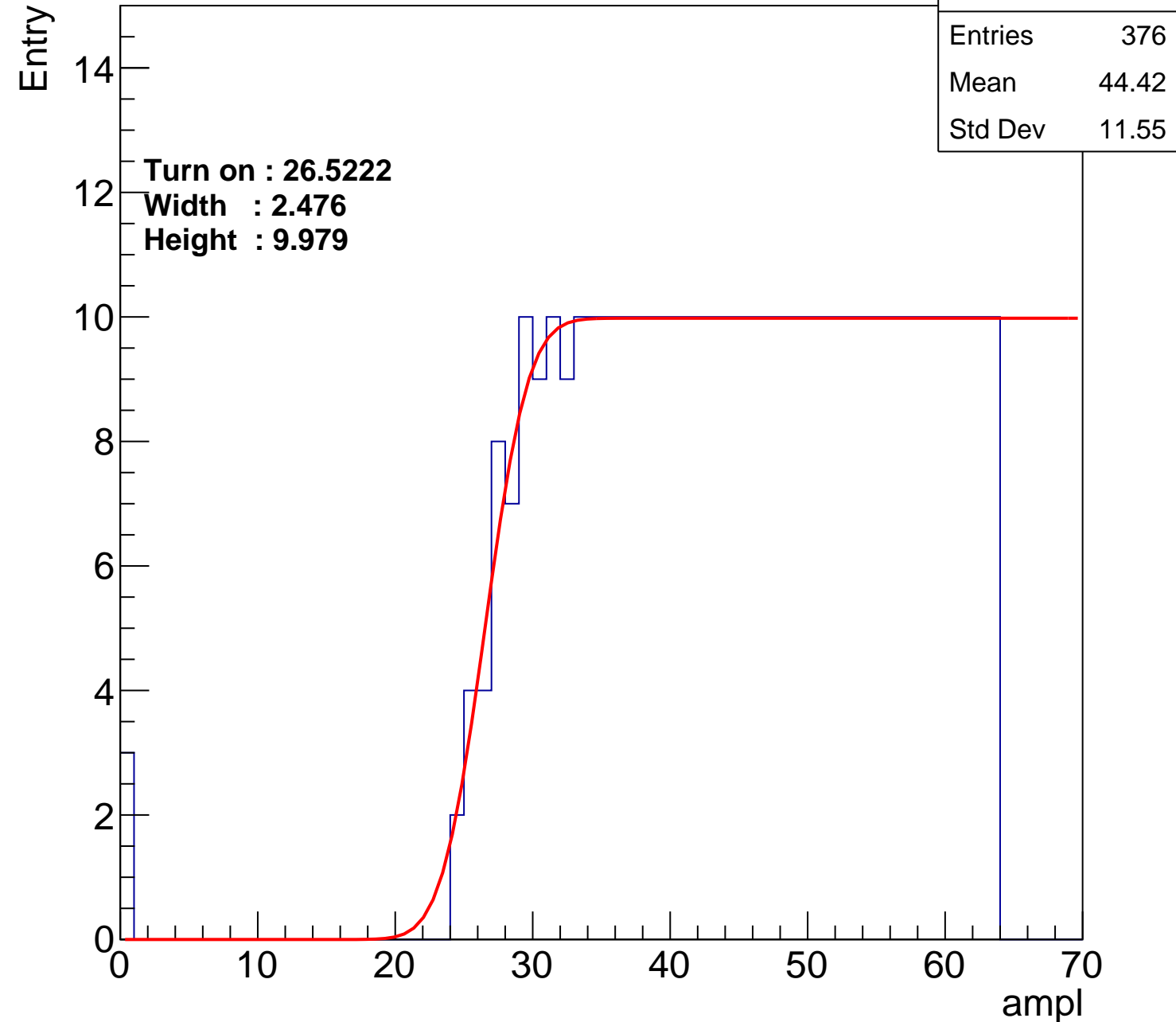
Width : 2.476

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch126

calib_packv5_042523_0143.root, FC#8, port C1

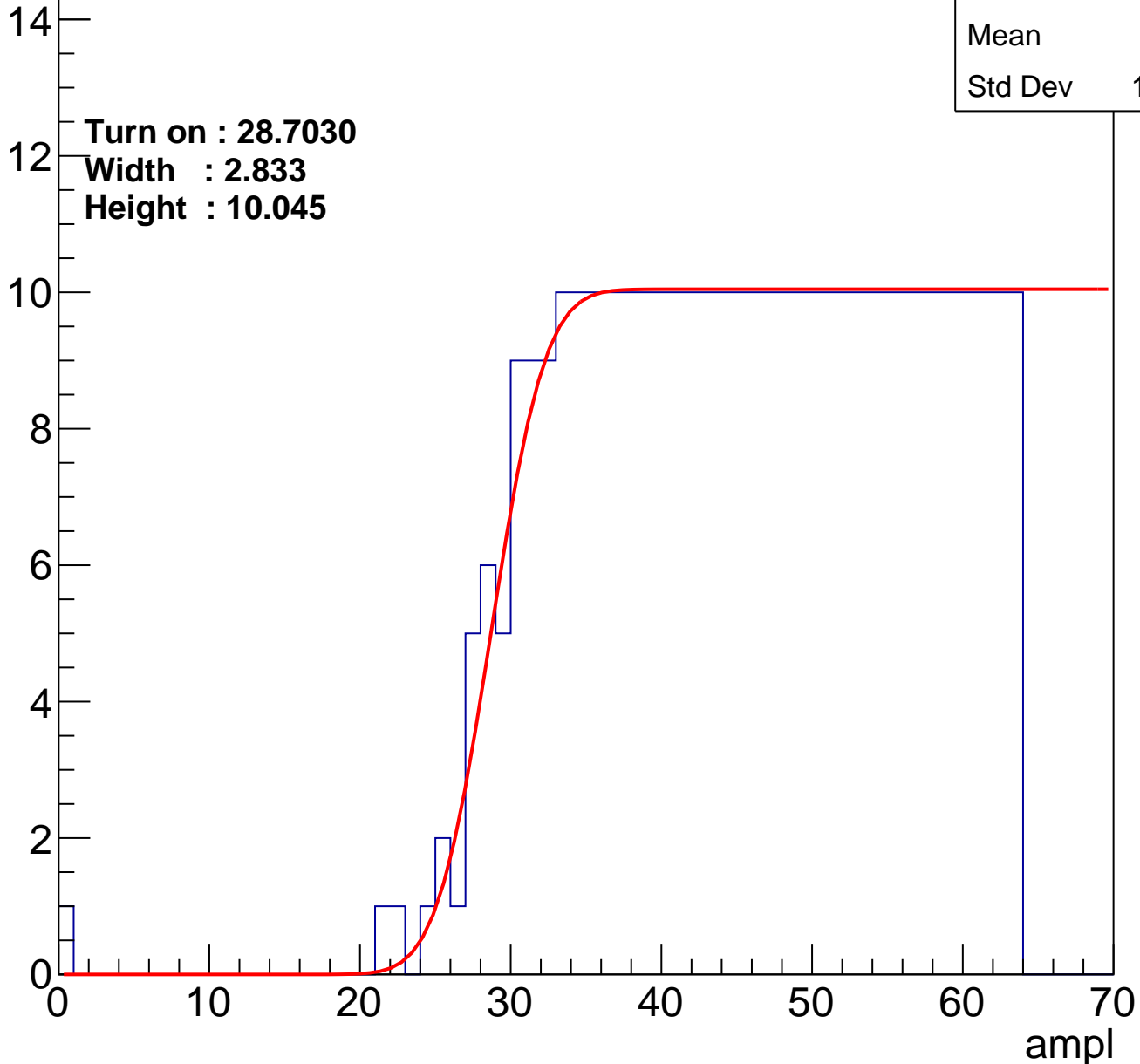
Entries	360
Mean	45.3
Std Dev	10.83

Turn on : 28.7030

Width : 2.833

Height : 10.045

Entry



B0L002S, U21-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.45
Std Dev	11.1

Turn on : 28.8443

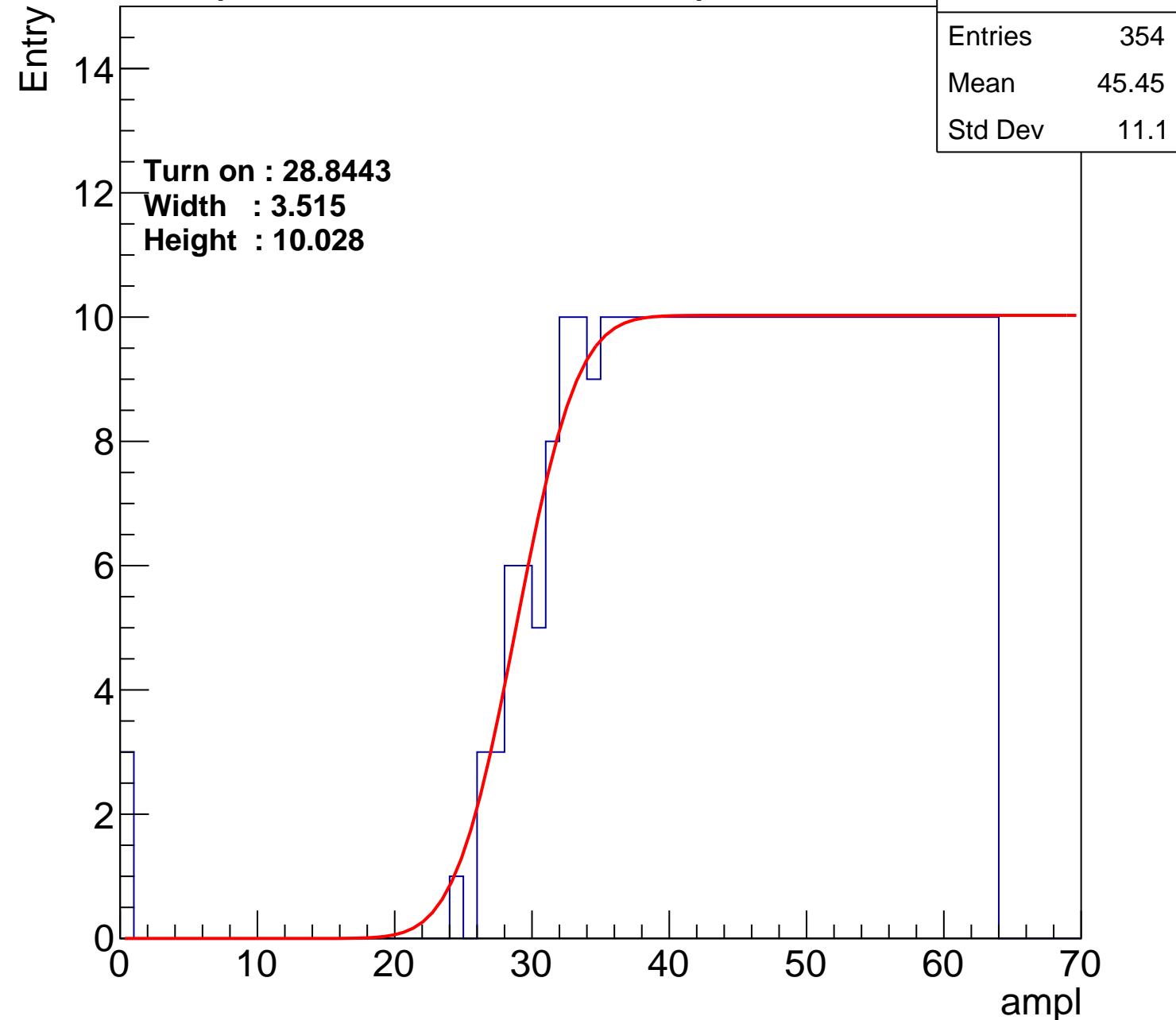
Width : 3.515

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U21-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.45
Std Dev	11.1

Turn on : 28.8443

Width : 3.515

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

