

B1L100S, U14-ch0

calib_packv5_042523_0143.root, FC#4, port A2

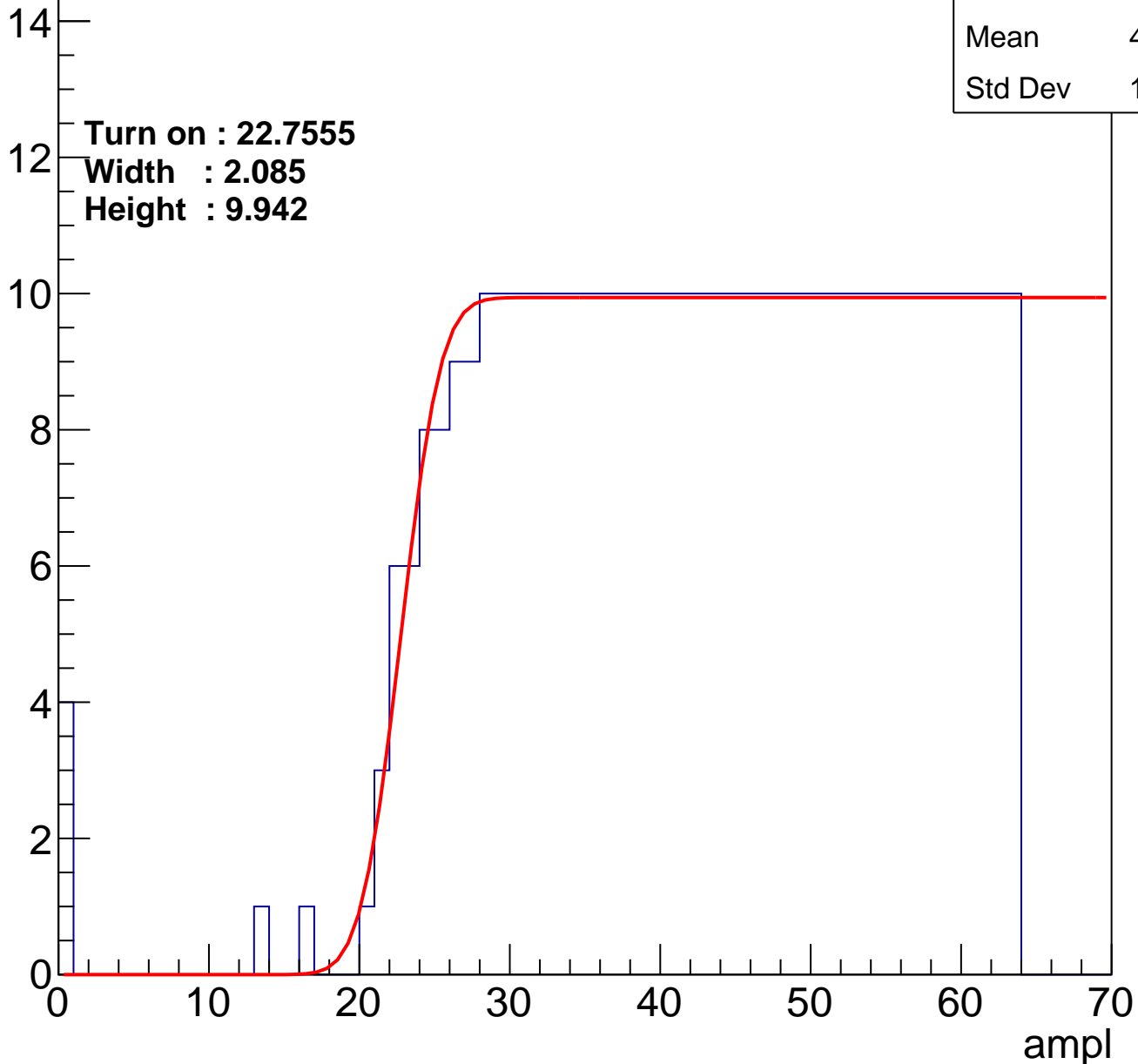
Entries	416
Mean	42.38
Std Dev	12.73

Turn on : 22.7555

Width : 2.085

Height : 9.942

Entry



B1L100S, U14-ch1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.55
Std Dev	12.14

Turn on : 25.1018

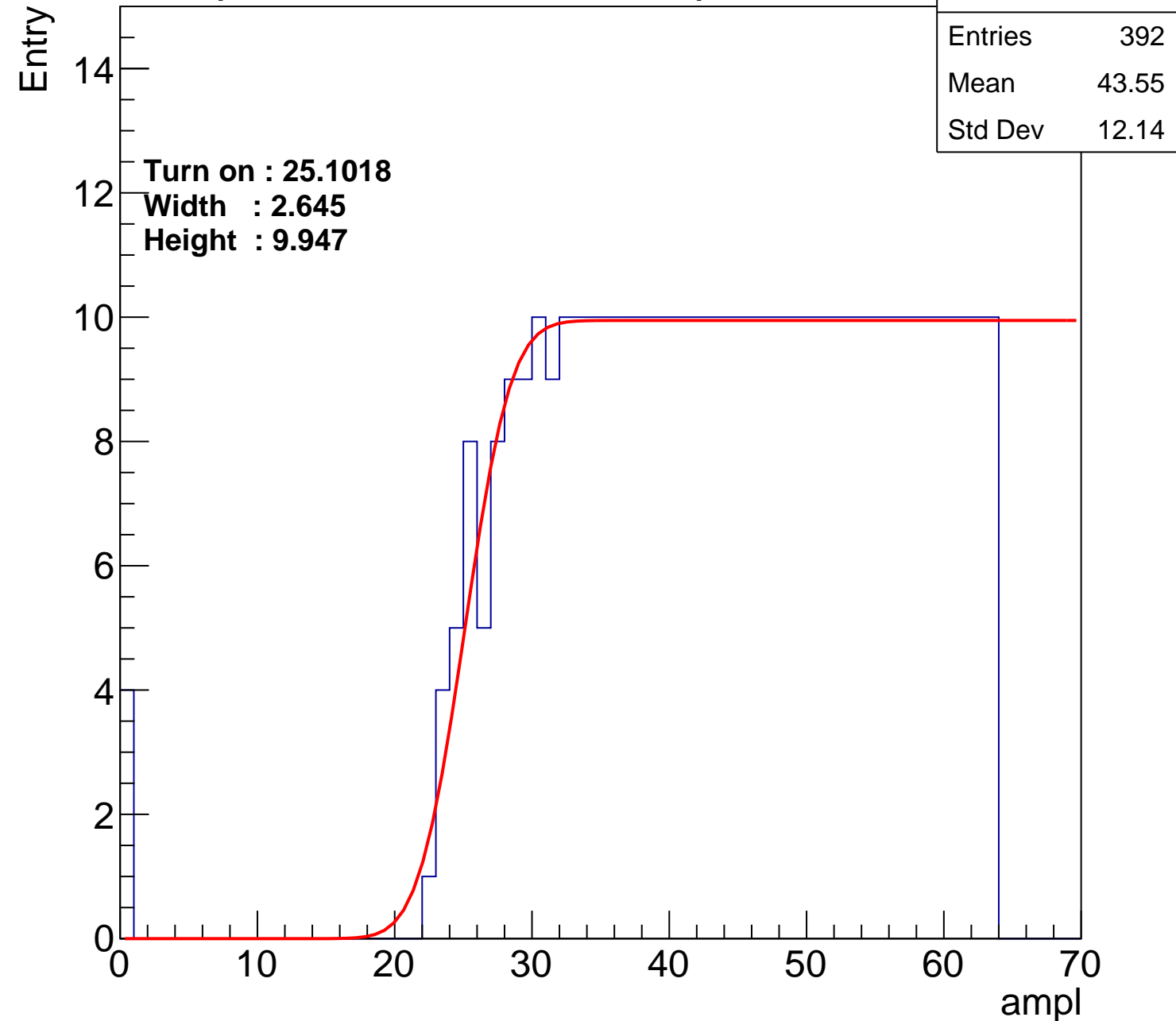
Width : 2.645

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	401
Mean	43.3
Std Dev	11.89

Turn on : 24.3046

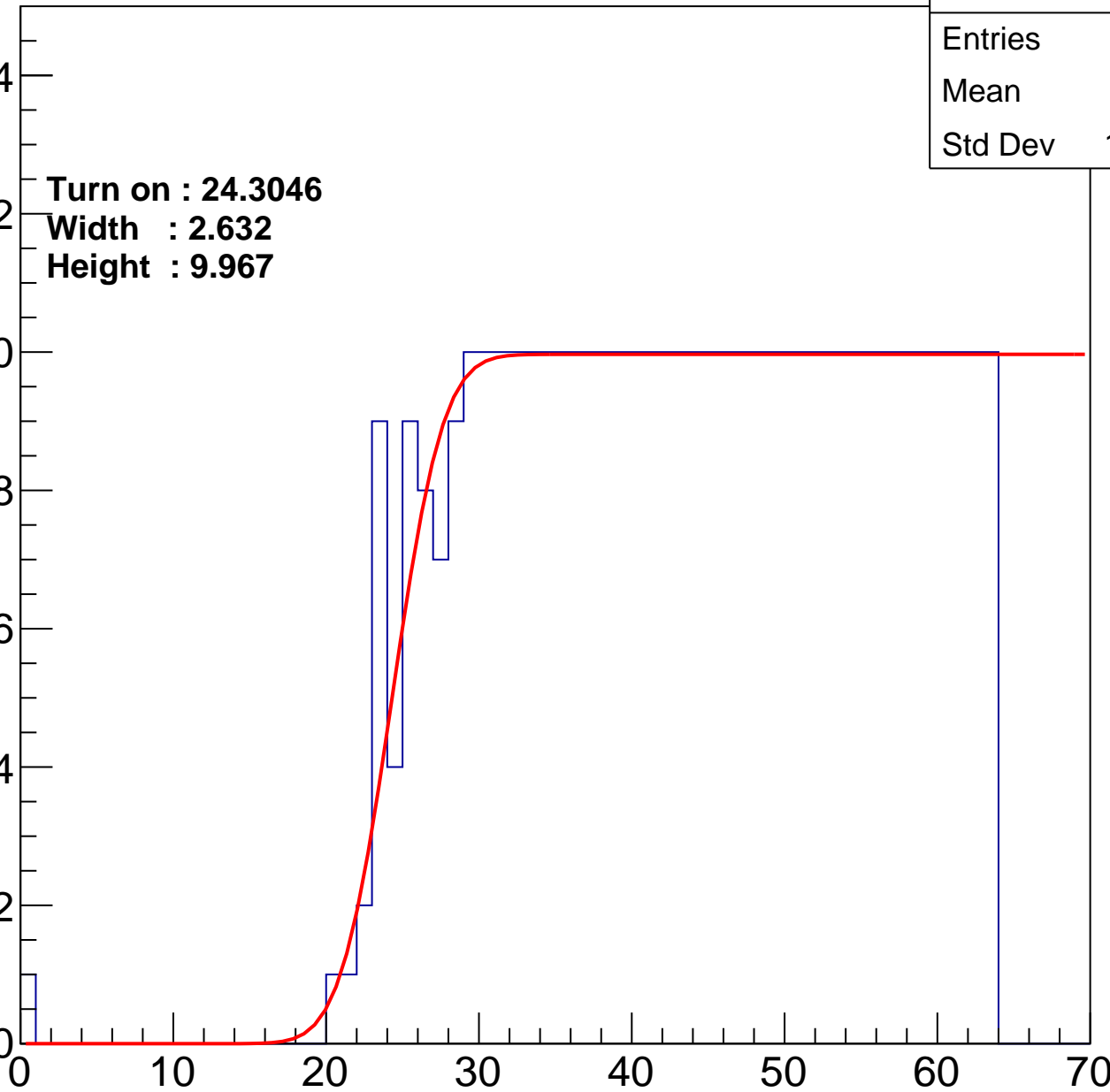
Width : 2.632

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.73
Std Dev	11.96

Turn on : 25.9472

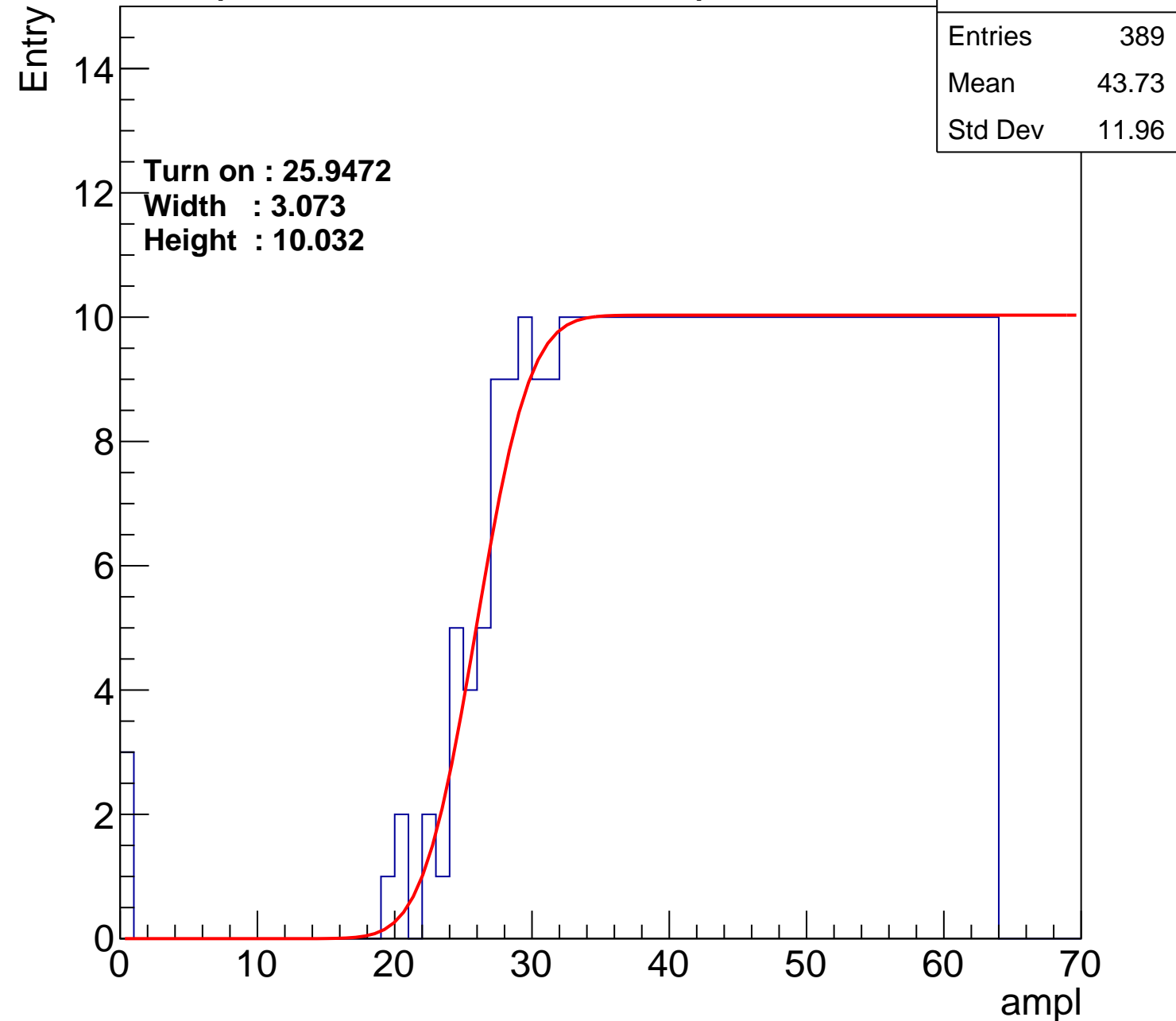
Width : 3.073

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.2872

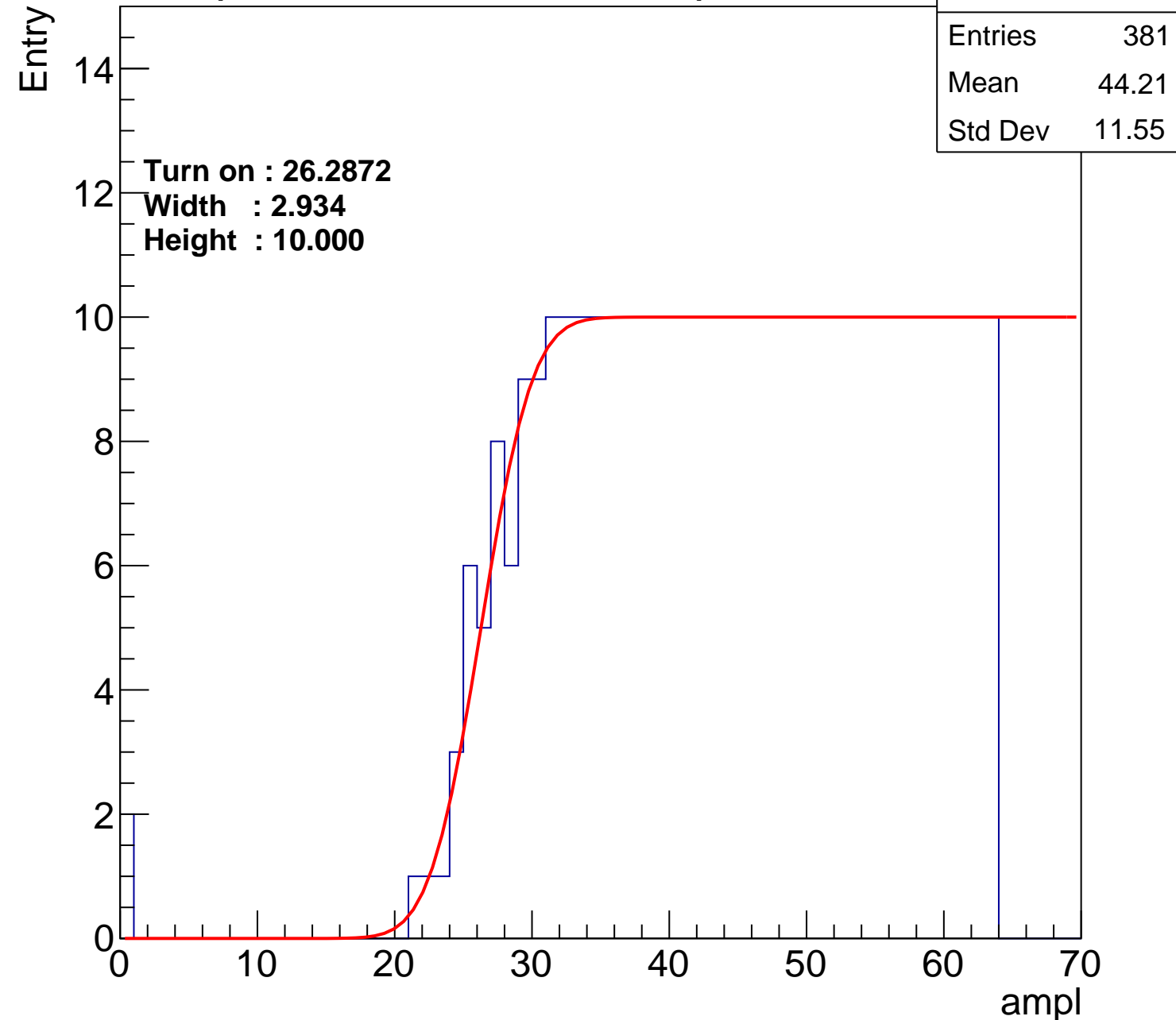
Width : 2.934

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.6275

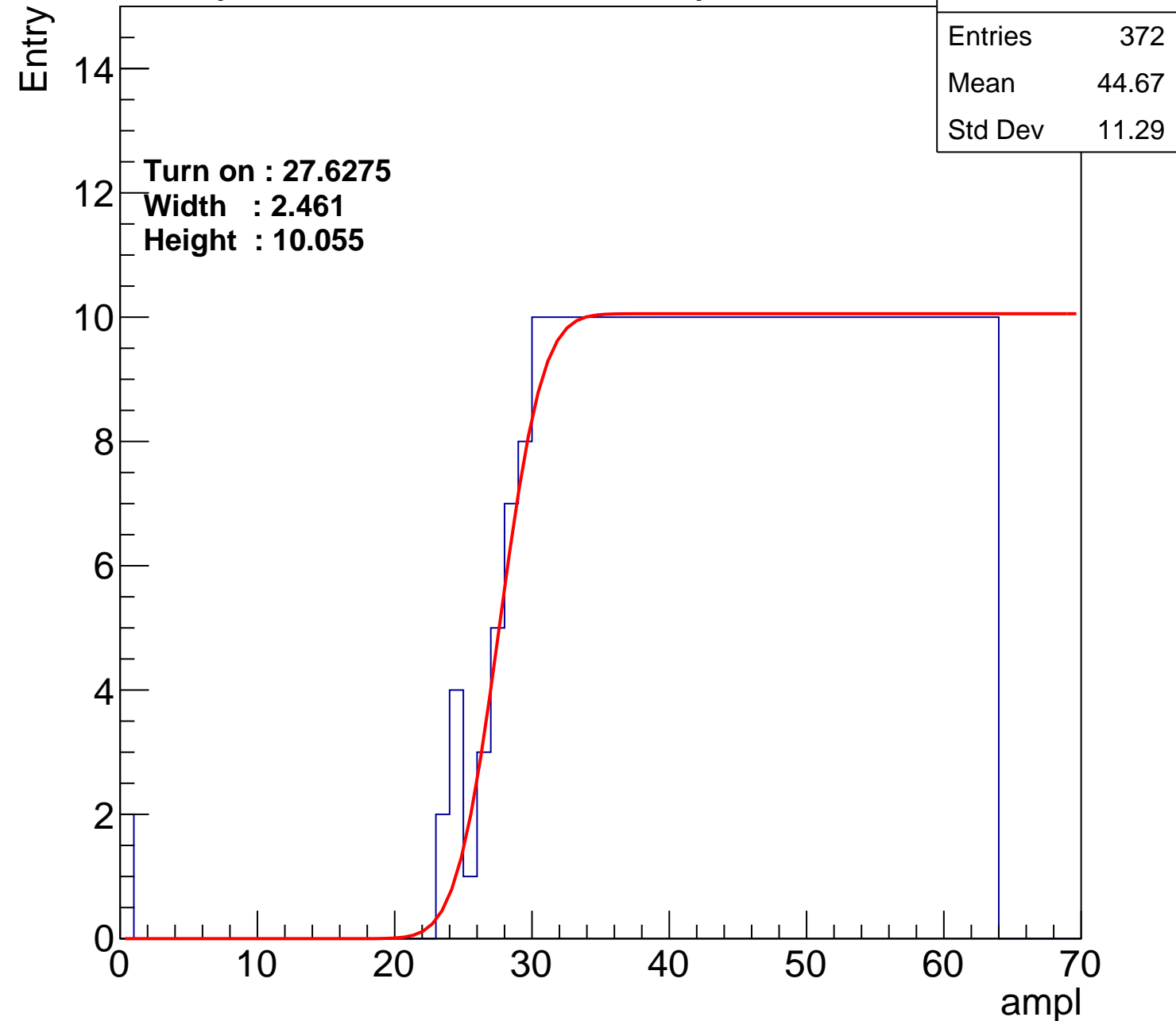
Width : 2.461

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch6

calib_packv5_042523_0143.root, FC#4, port A2

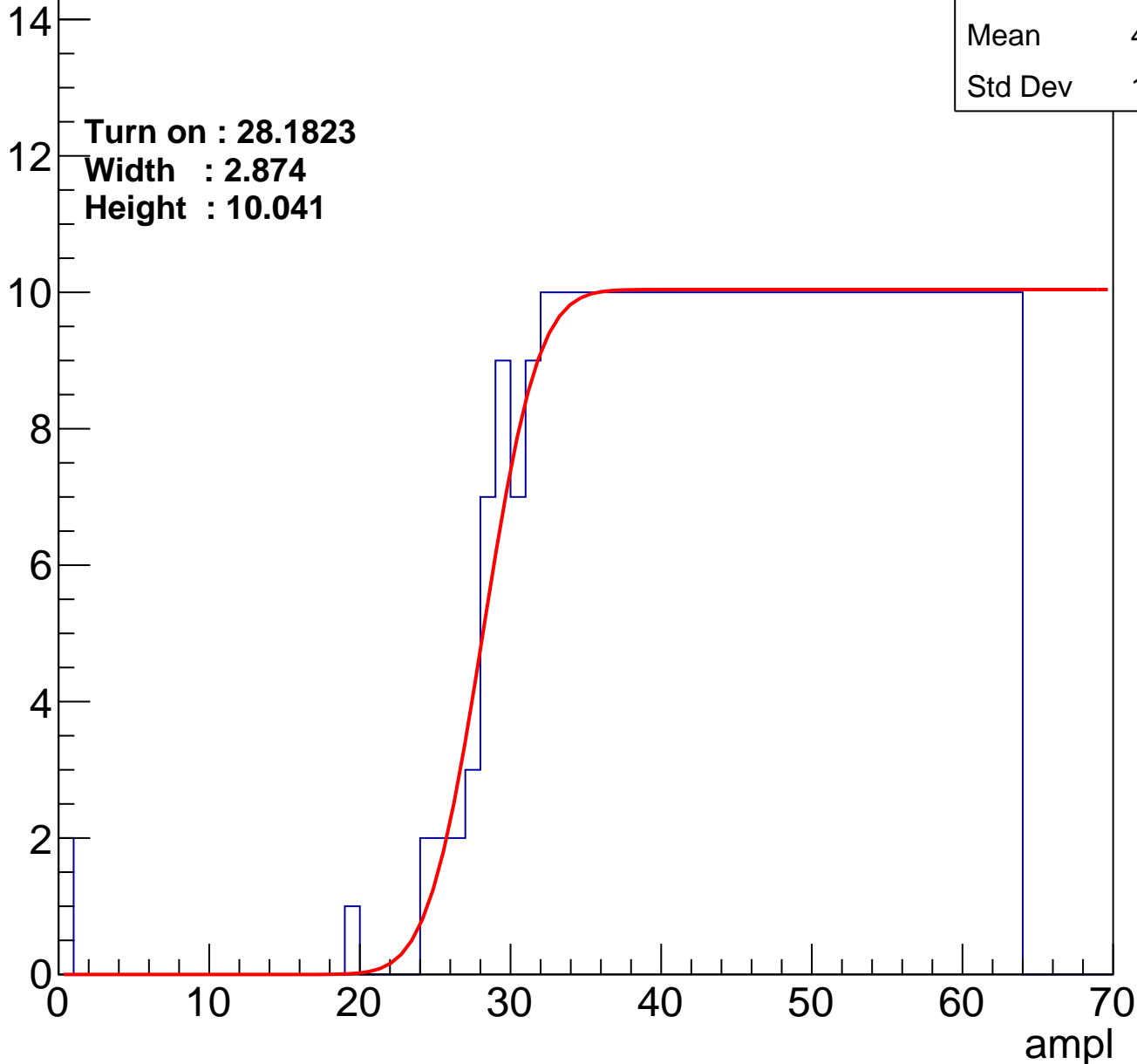
Entries	364
Mean	45.04
Std Dev	11.12

Turn on : 28.1823

Width : 2.874

Height : 10.041

Entry



B1L100S, U14-ch7

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.5
Std Dev	11.21

Turn on : 26.6506

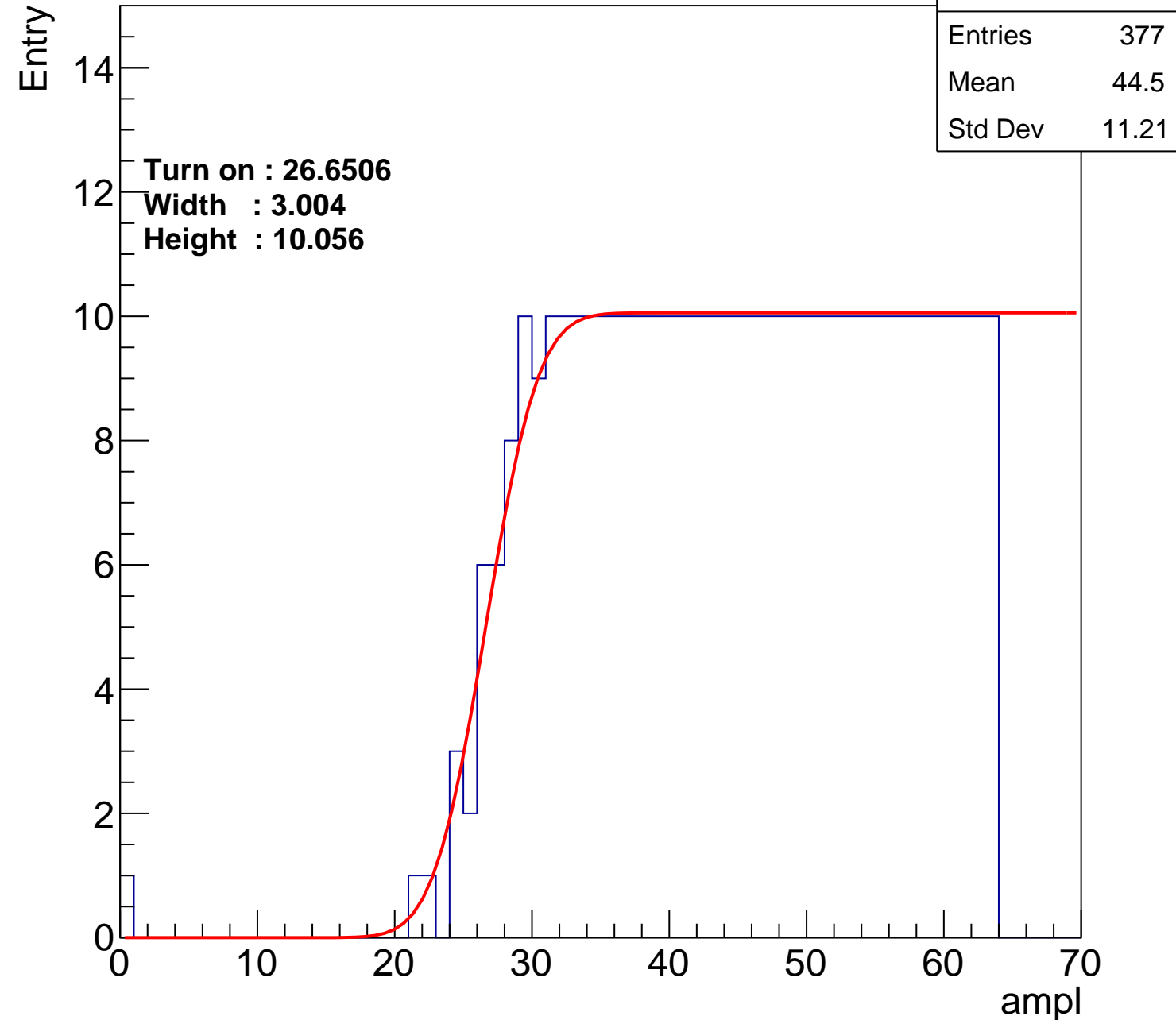
Width : 3.004

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch8

calib_packv5_042523_0143.root, FC#4, port A2

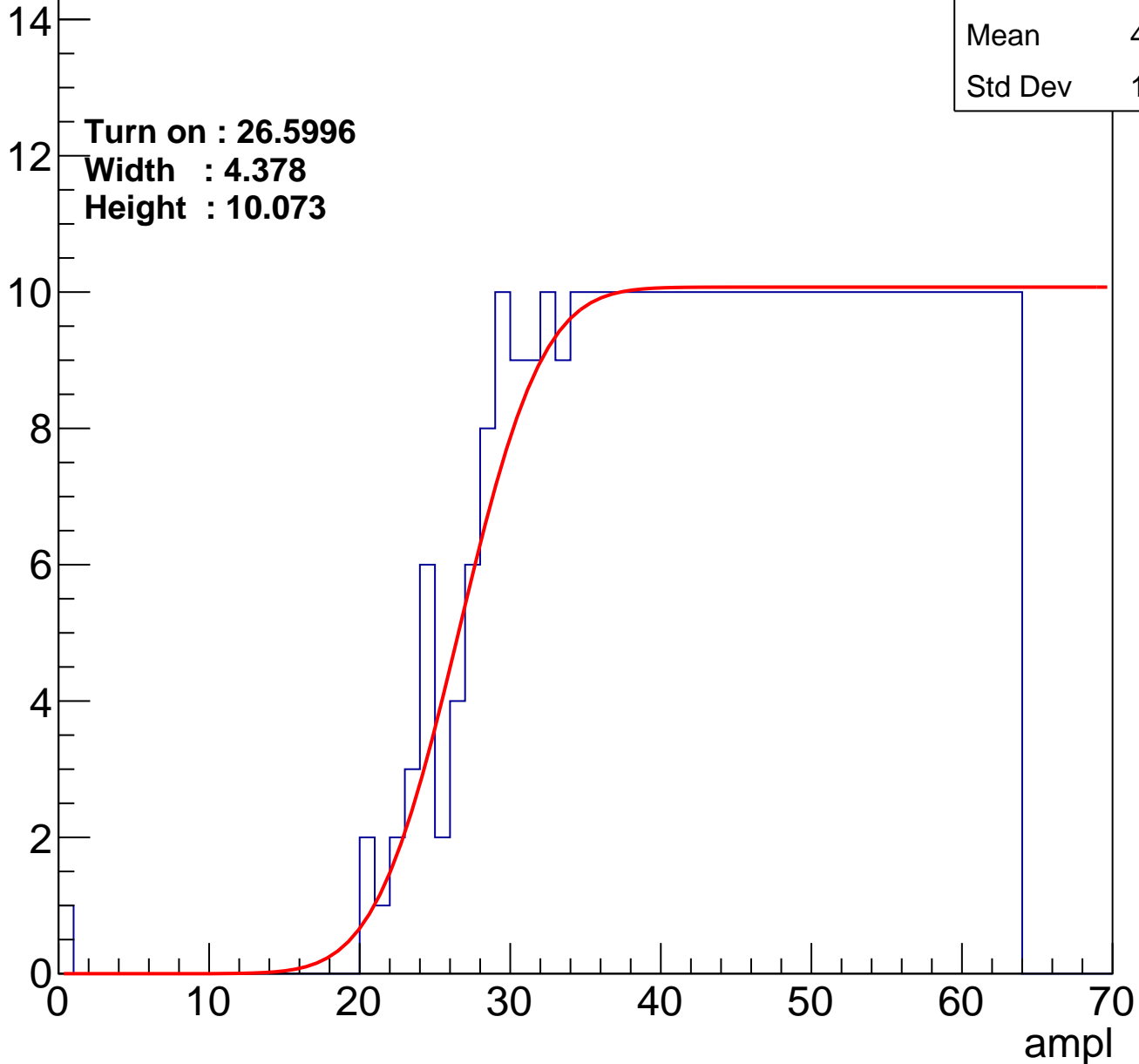
Entries	382
Mean	44.15
Std Dev	11.52

Turn on : 26.5996

Width : 4.378

Height : 10.073

Entry



B1L100S, U14-ch9

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.7790

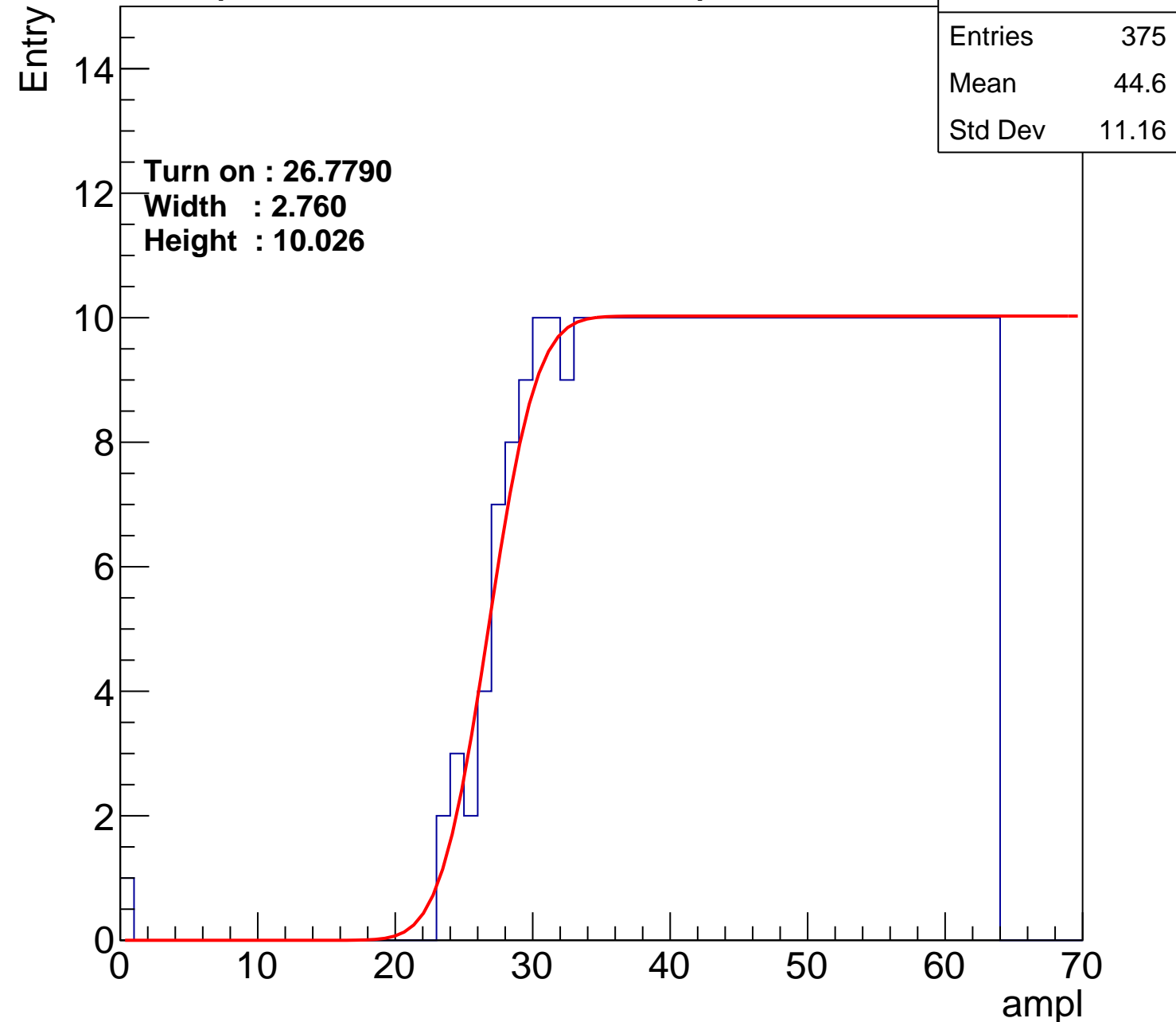
Width : 2.760

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch10

calib_packv5_042523_0143.root, FC#4, port A2

Entries	405
Mean	42.98
Std Dev	12.3

Turn on : 23.8499

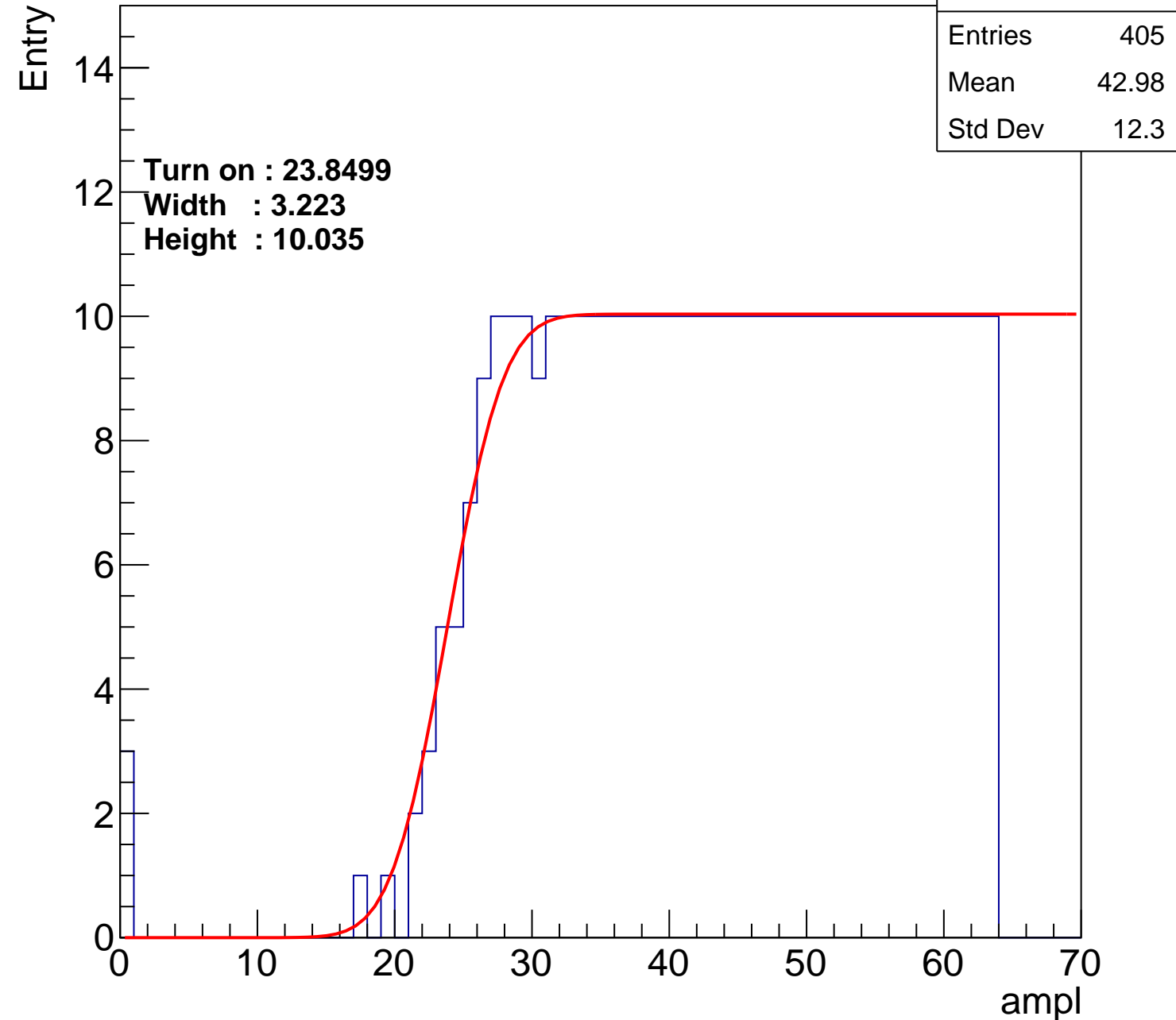
Width : 3.223

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch11

calib_packv5_042523_0143.root, FC#4, port A2

Entries	409
Mean	42.85
Std Dev	12.25

Turn on : 23.4760

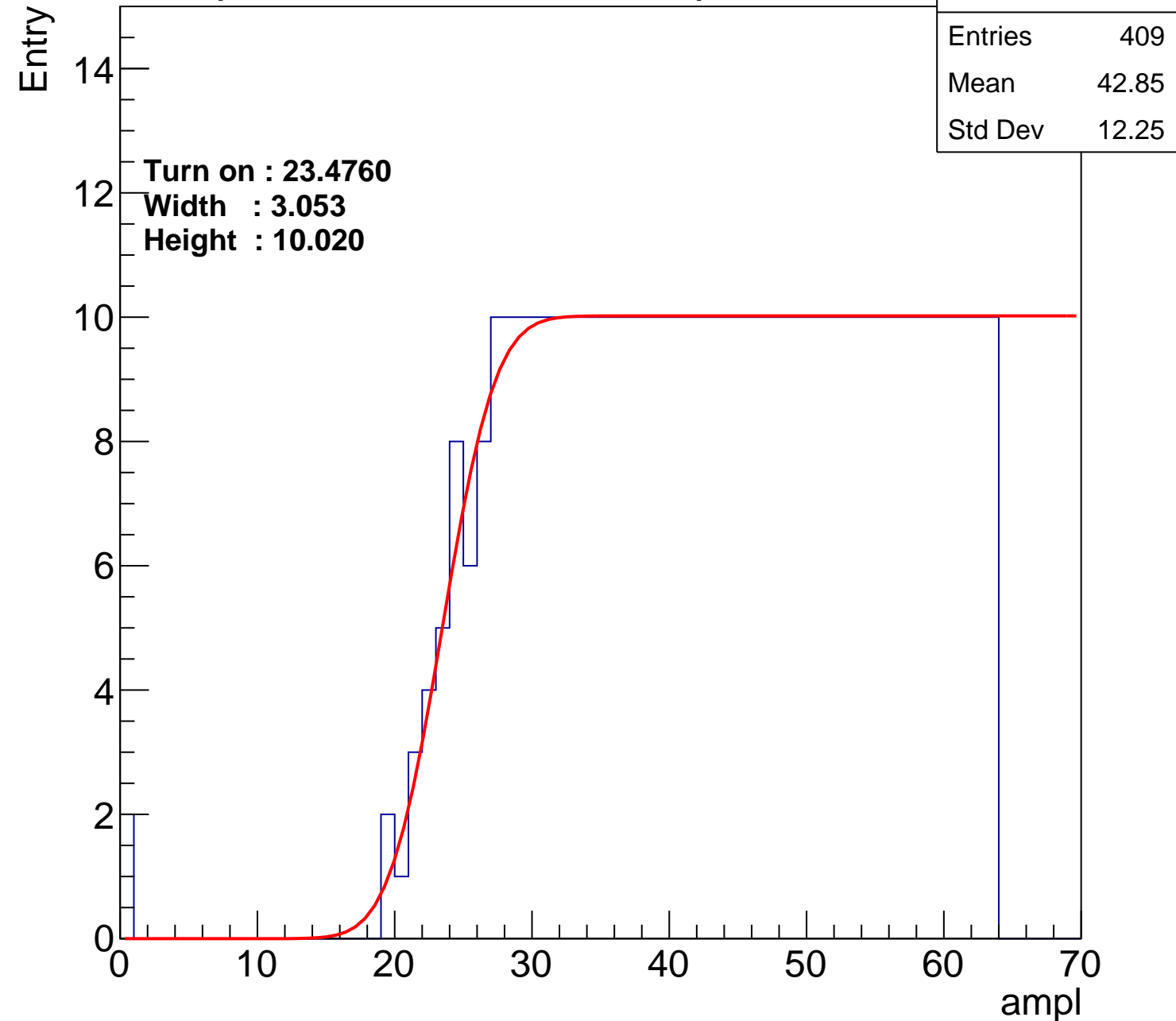
Width : 3.053

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch12

calib_packv5_042523_0143.root, FC#4, port A2

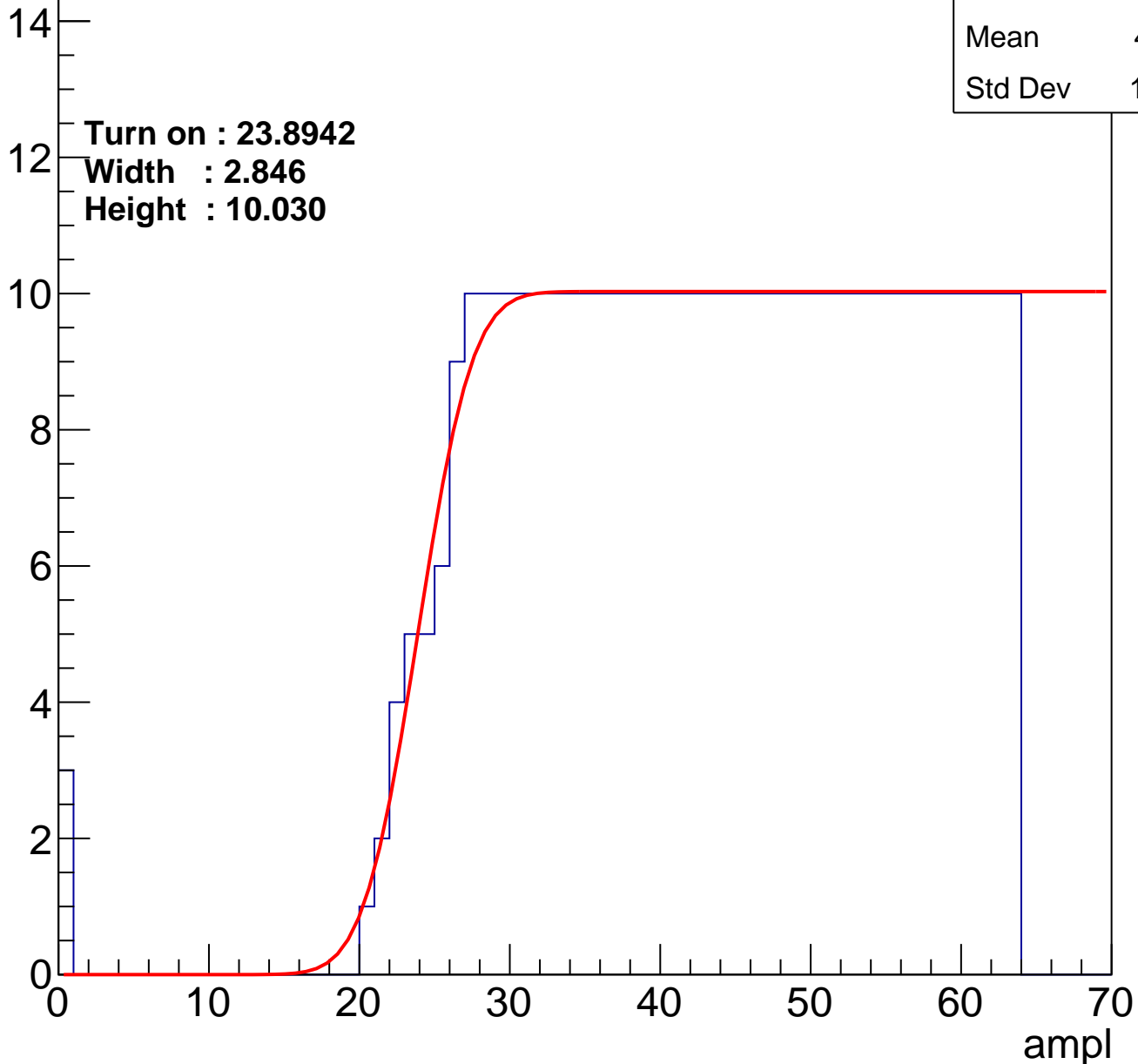
Entries	405
Mean	43.01
Std Dev	12.25

Turn on : 23.8942

Width : 2.846

Height : 10.030

Entry



B1L100S, U14-ch13

calib_packv5_042523_0143.root, FC#4, port A2

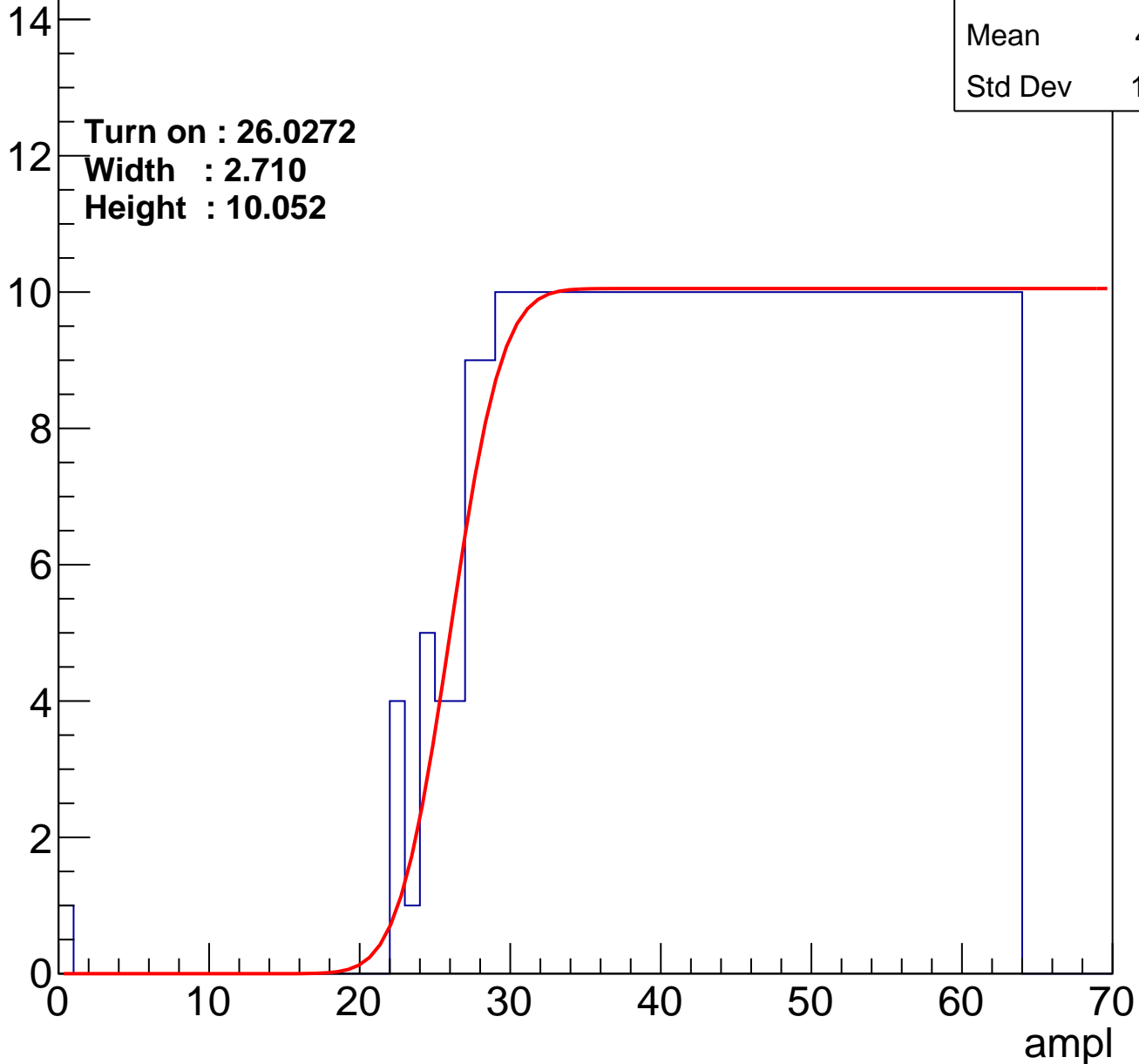
Entries	387
Mean	44.01
Std Dev	11.49

Turn on : 26.0272

Width : 2.710

Height : 10.052

Entry



B1L100S, U14-ch14

calib_packv5_042523_0143.root, FC#4, port A2

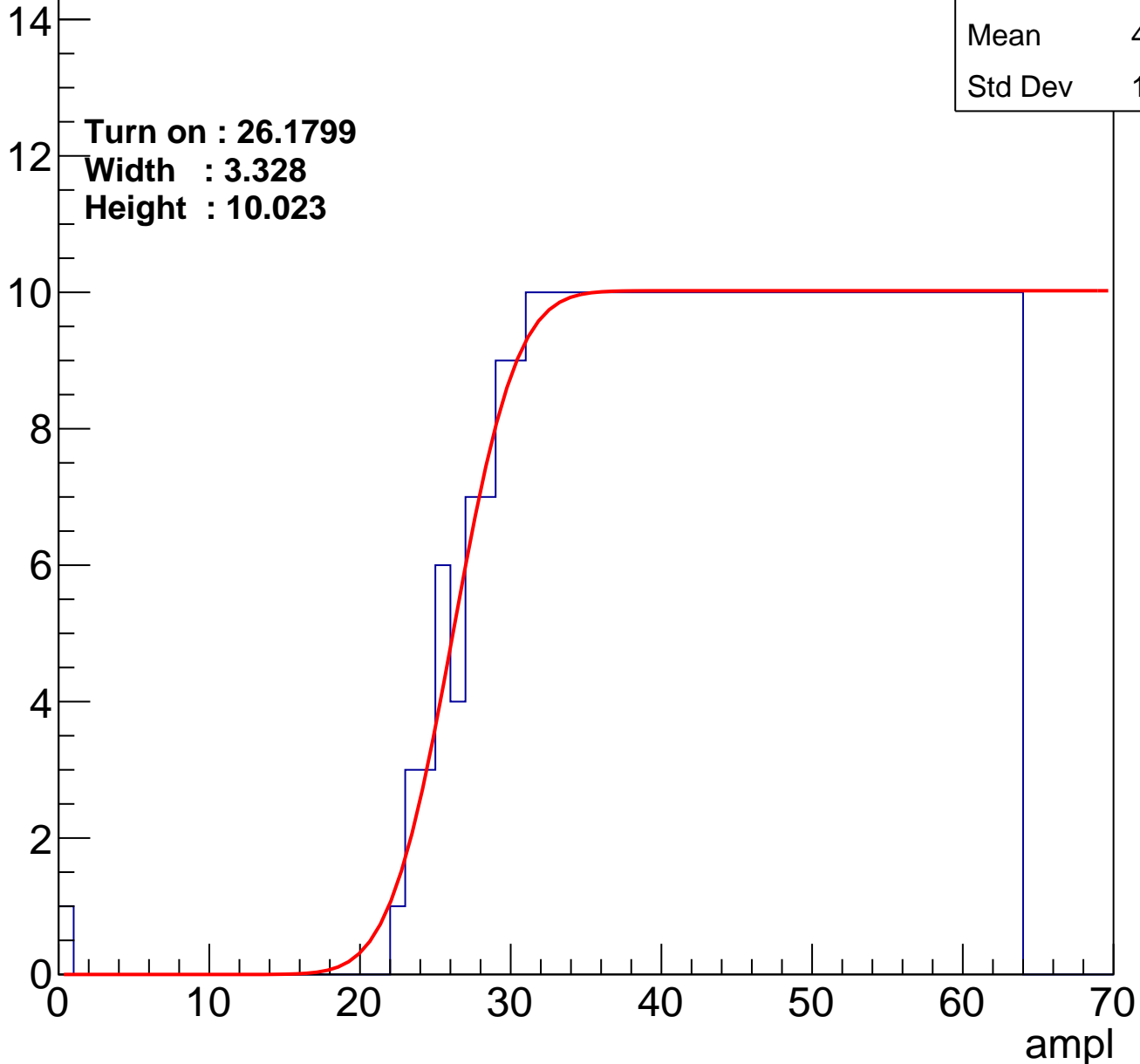
Entries	380
Mean	44.32
Std Dev	11.34

Turn on : 26.1799

Width : 3.328

Height : 10.023

Entry



B1L100S, U14-ch15

calib_packv5_042523_0143.root, FC#4, port A2

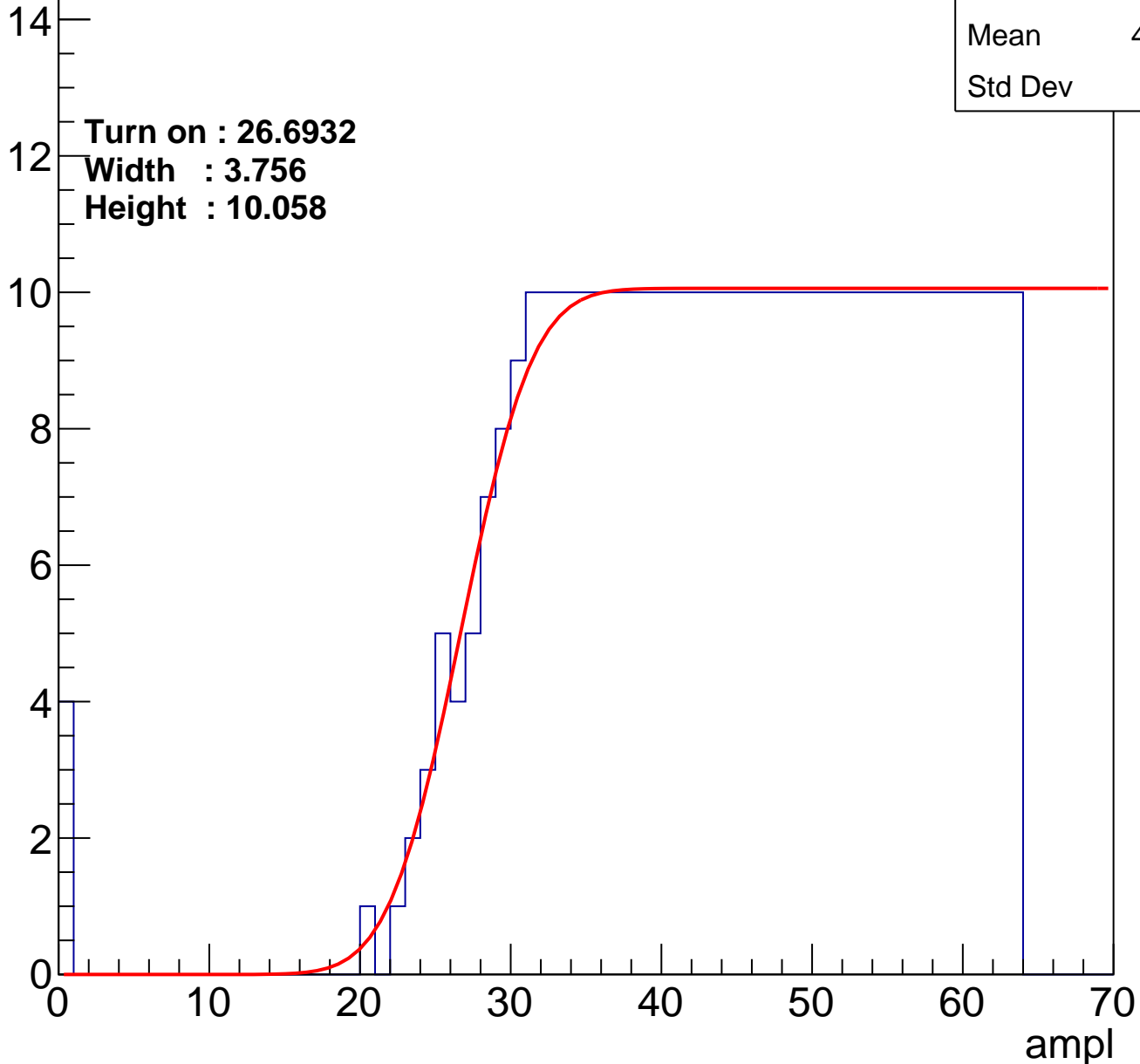
Entries	379
Mean	44.15
Std Dev	11.9

Turn on : 26.6932

Width : 3.756

Height : 10.058

Entry



B1L100S, U14-ch16

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.66
Std Dev	11.73

Turn on : 24.7191

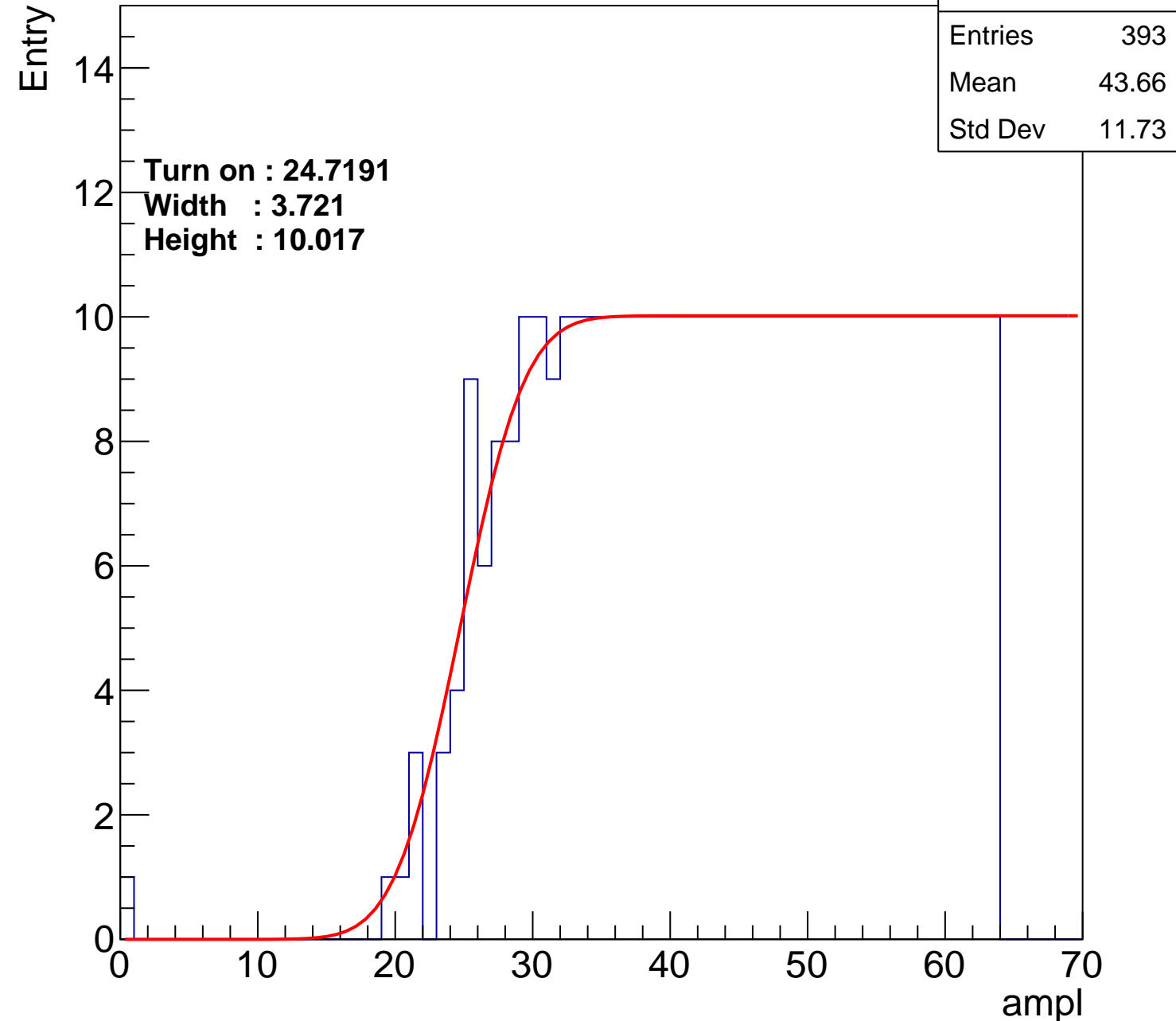
Width : 3.721

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch17

calib_packv5_042523_0143.root, FC#4, port A2

Entries	404
Mean	43.08
Std Dev	12.2

Turn on : 23.8010

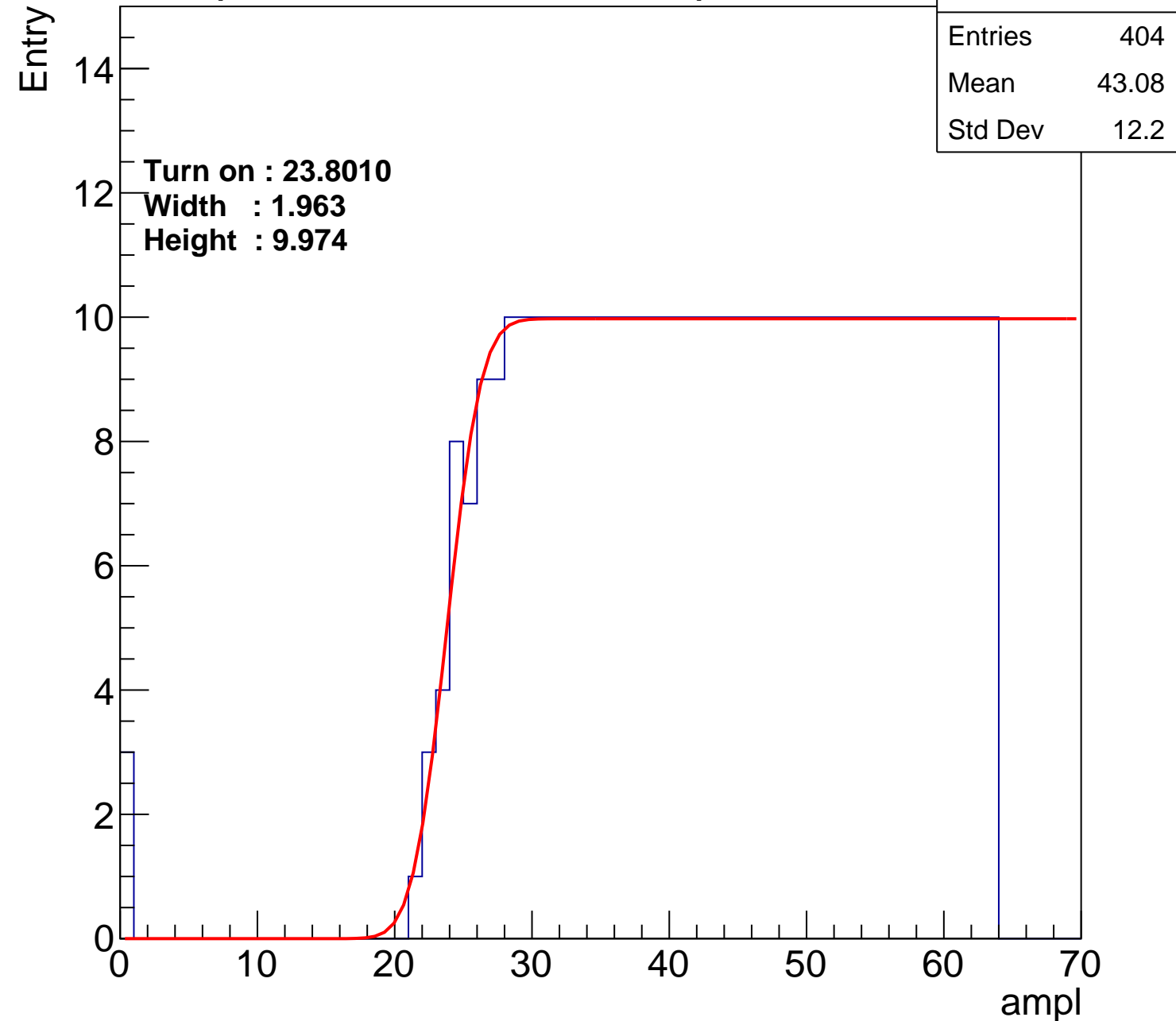
Width : 1.963

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch18

calib_packv5_042523_0143.root, FC#4, port A2

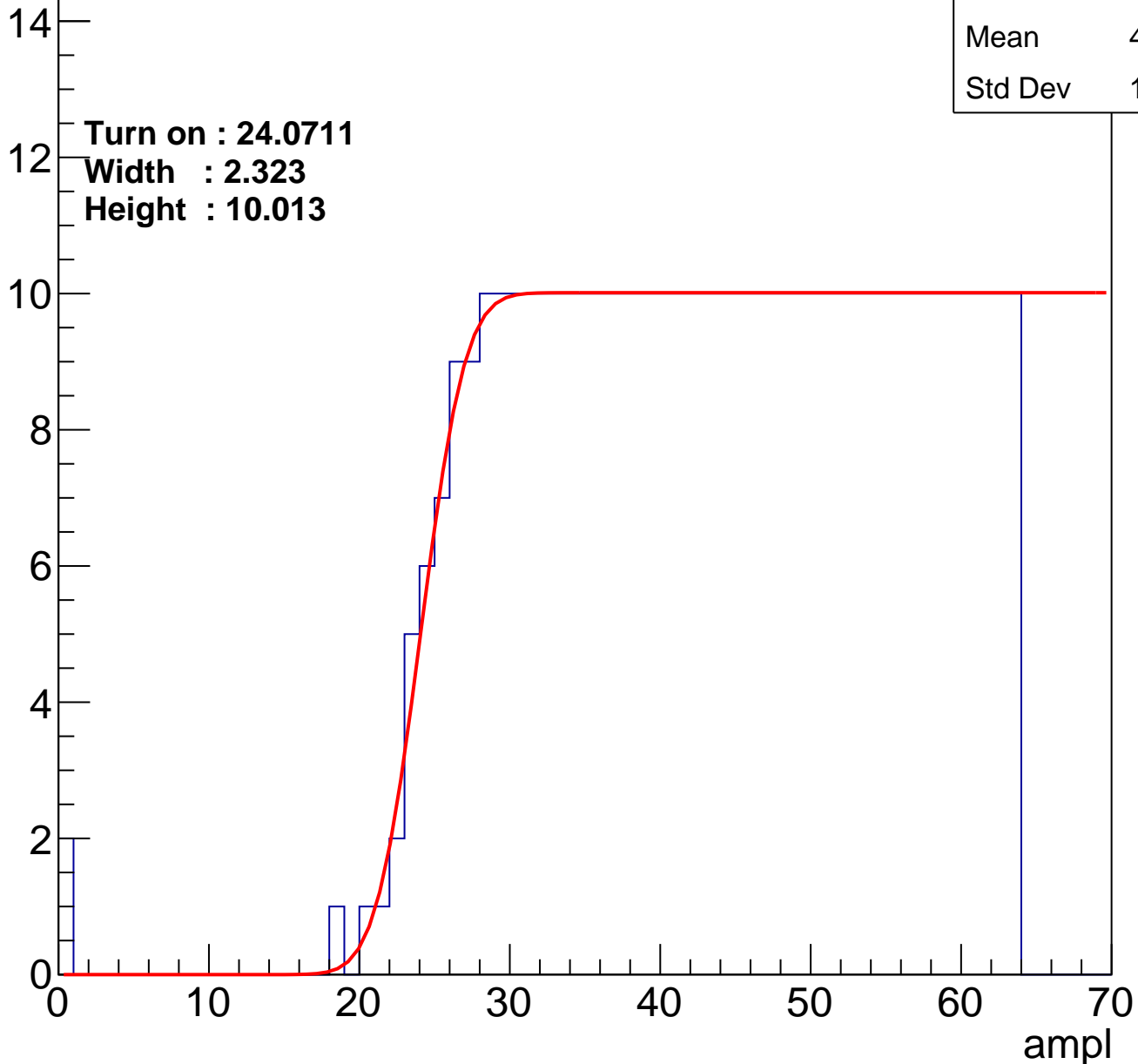
Entries	403
Mean	43.16
Std Dev	12.06

Turn on : 24.0711

Width : 2.323

Height : 10.013

Entry



B1L100S, U14-ch19

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.6
Std Dev	12.01

Turn on : 25.4467

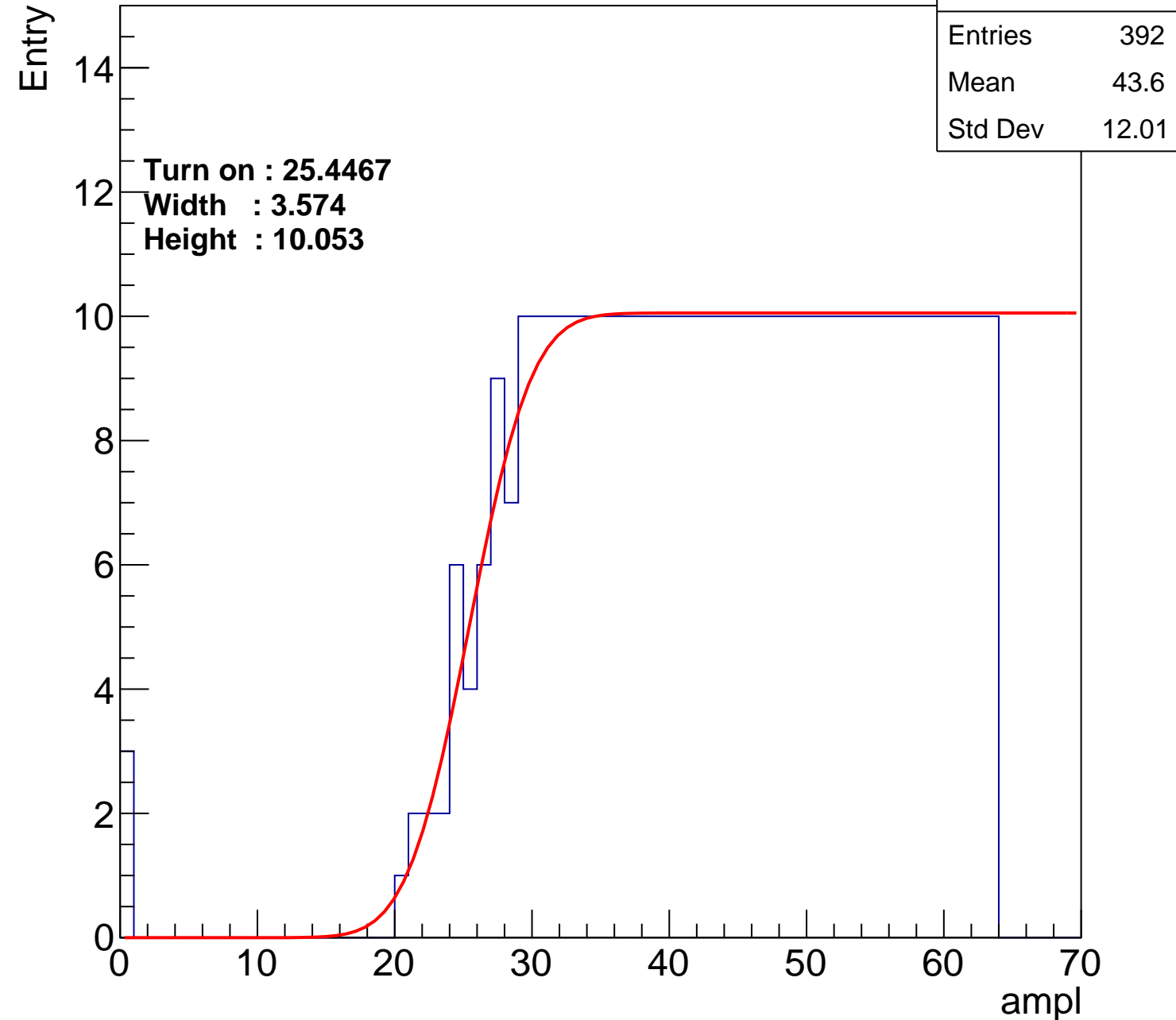
Width : 3.574

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch20

calib_packv5_042523_0143.root, FC#4, port A2

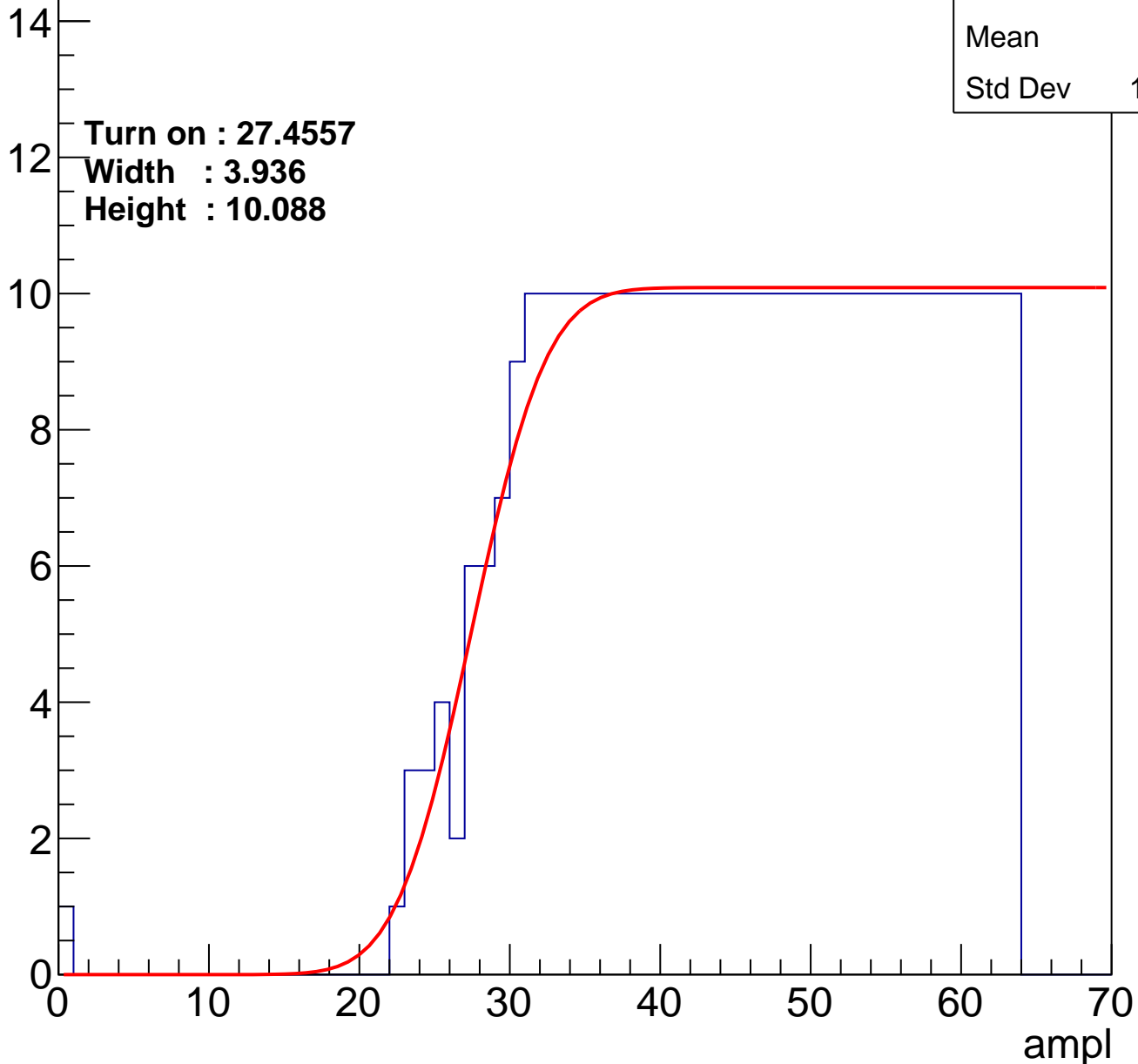
Entries	372
Mean	44.7
Std Dev	11.16

Turn on : 27.4557

Width : 3.936

Height : 10.088

Entry



B1L100S, U14-ch21

calib_packv5_042523_0143.root, FC#4, port A2

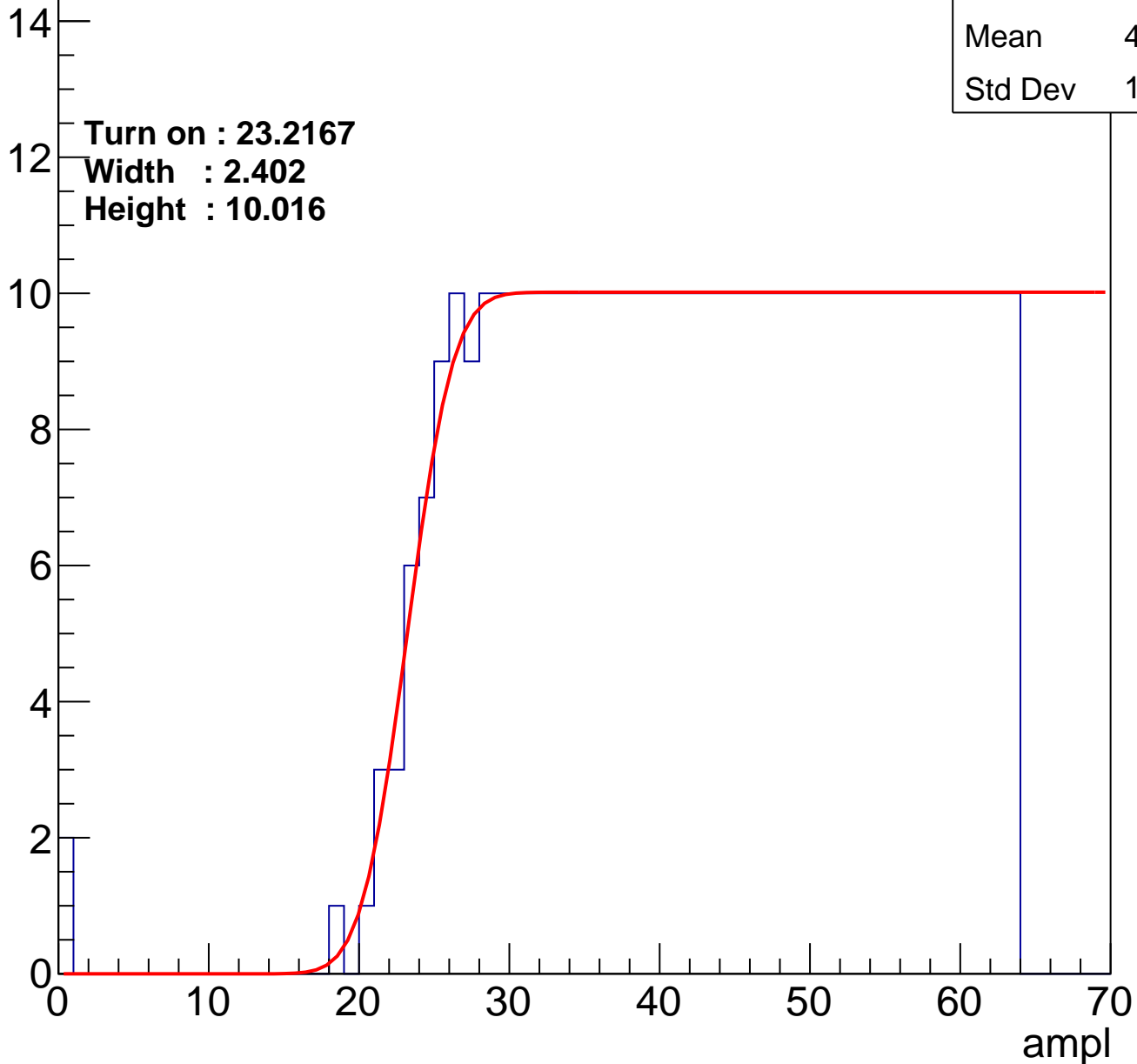
Entries	411
Mean	42.78
Std Dev	12.26

Turn on : 23.2167

Width : 2.402

Height : 10.016

Entry



B1L100S, U14-ch22

calib_packv5_042523_0143.root, FC#4, port A2

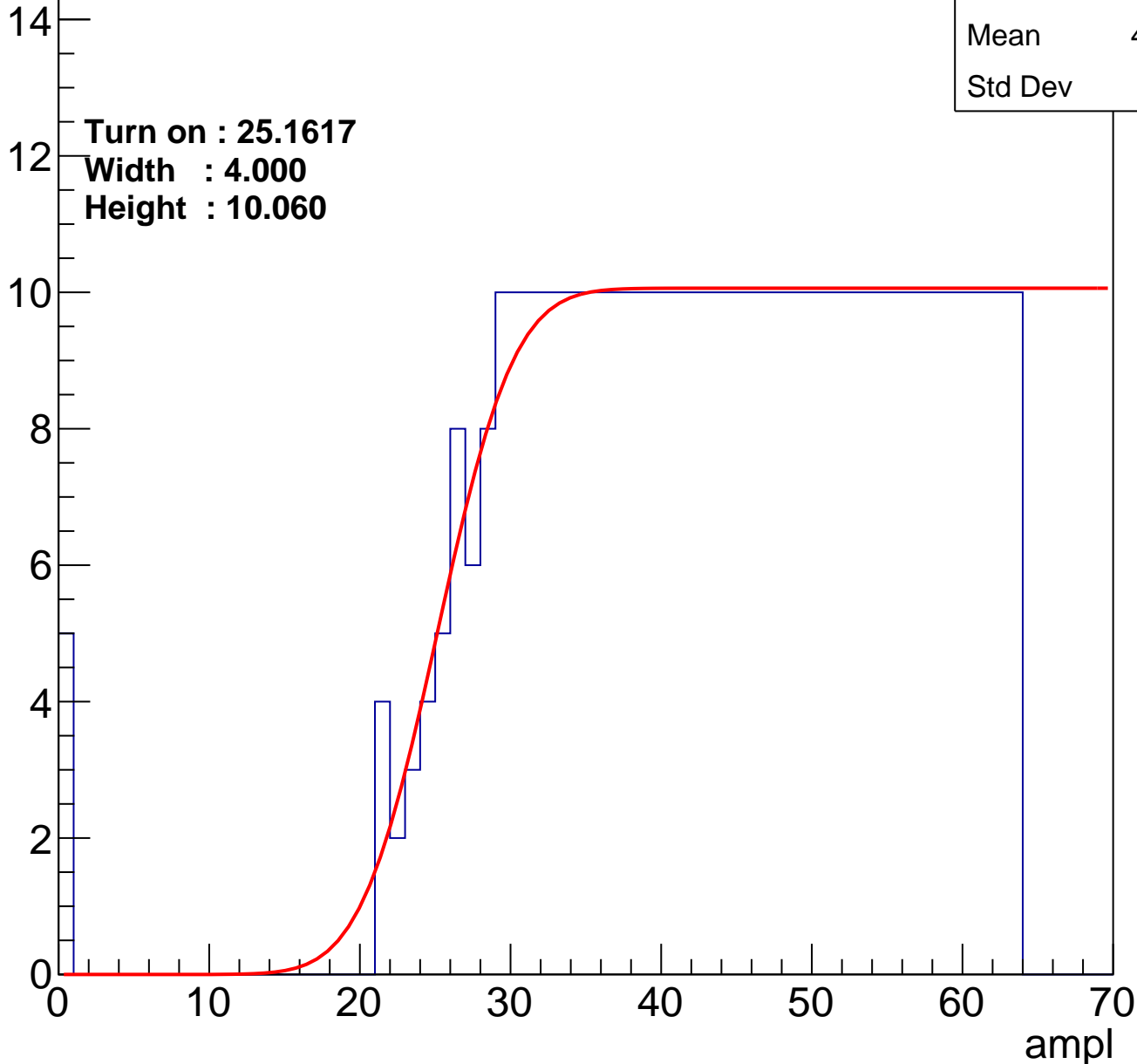
Entries	395
Mean	43.32
Std Dev	12.4

Turn on : 25.1617

Width : 4.000

Height : 10.060

Entry



B1L100S, U14-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.8175

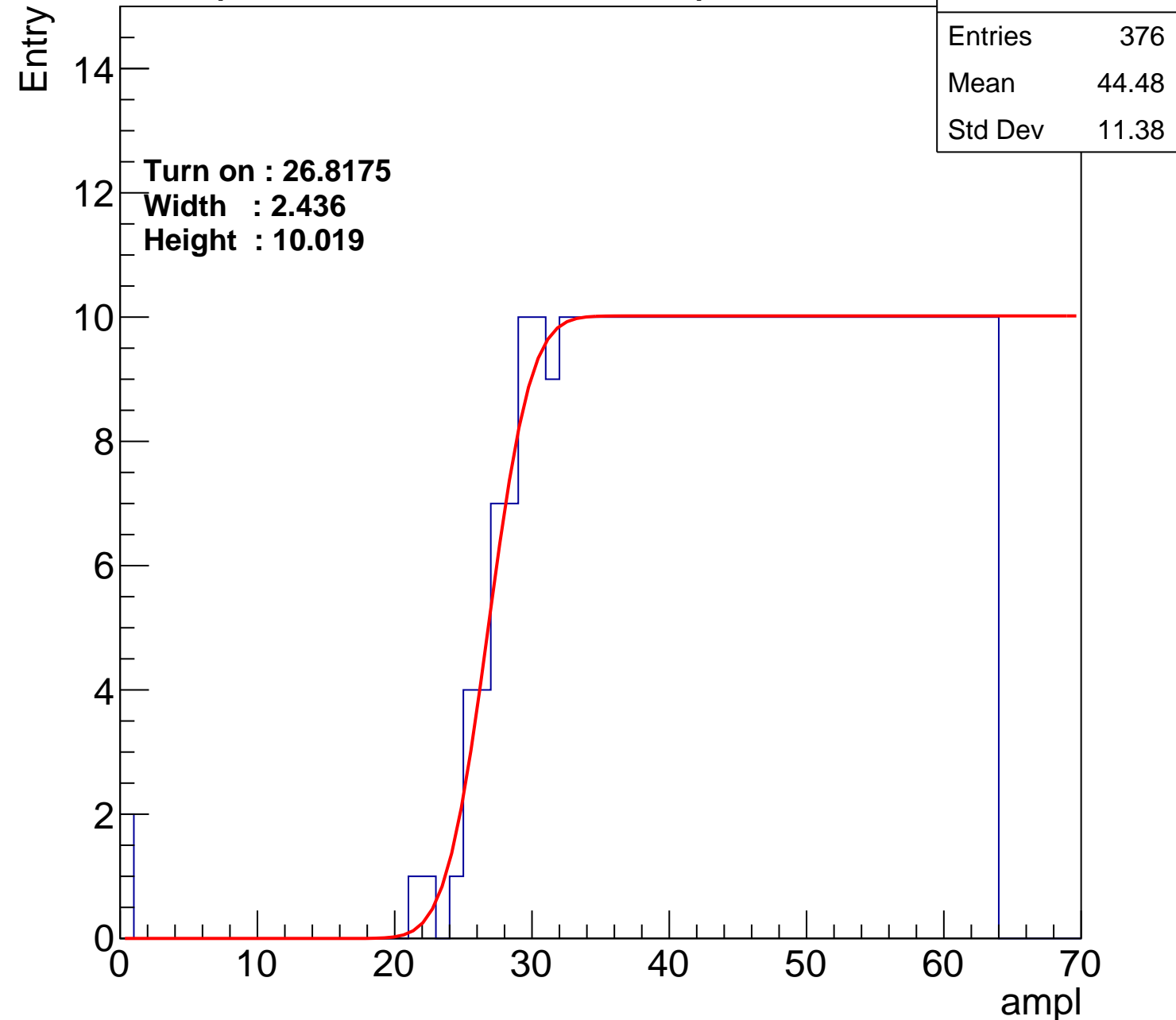
Width : 2.436

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch24

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.05
Std Dev	11.63

Turn on : 25.6607

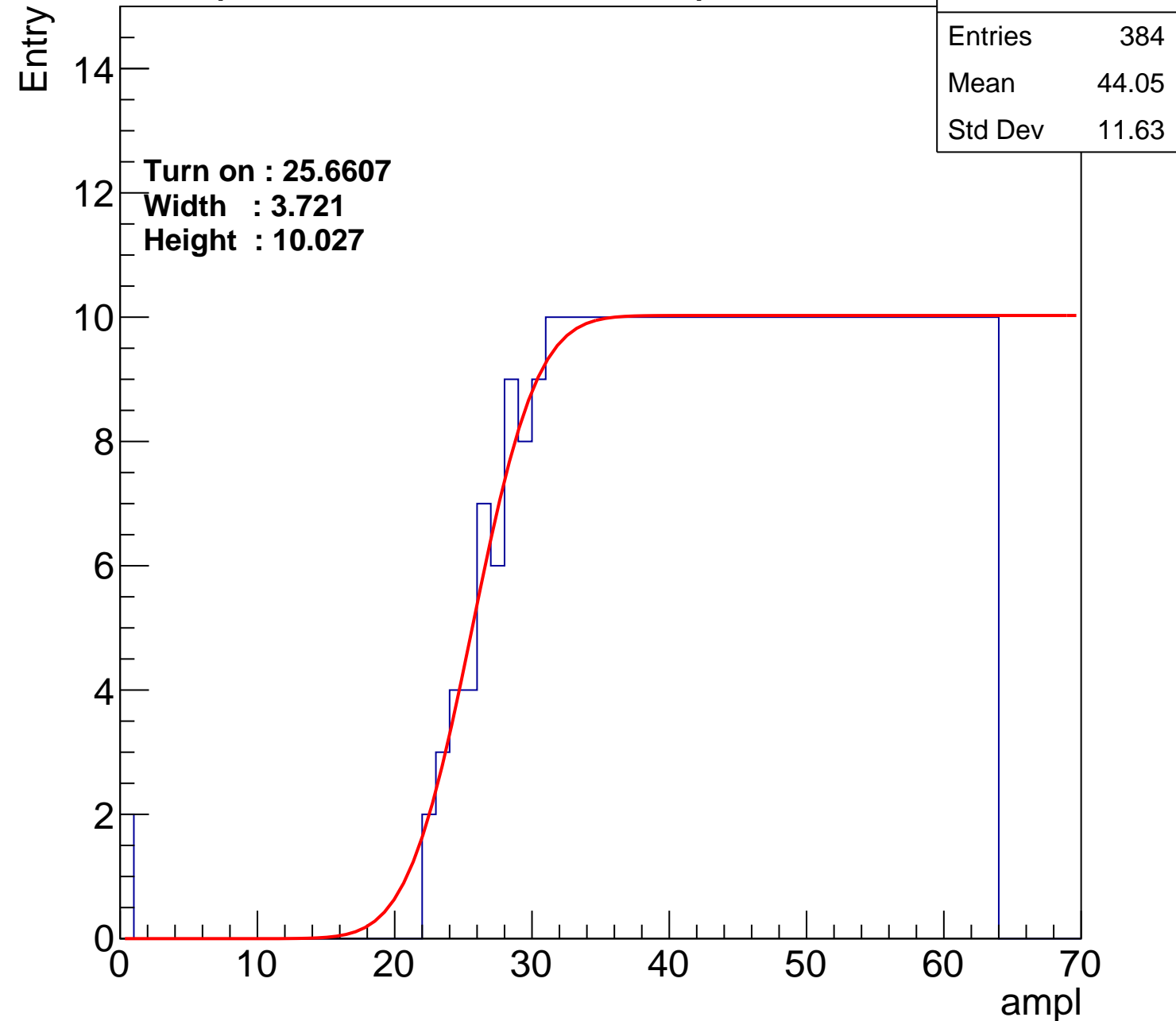
Width : 3.721

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch25

calib_packv5_042523_0143.root, FC#4, port A2

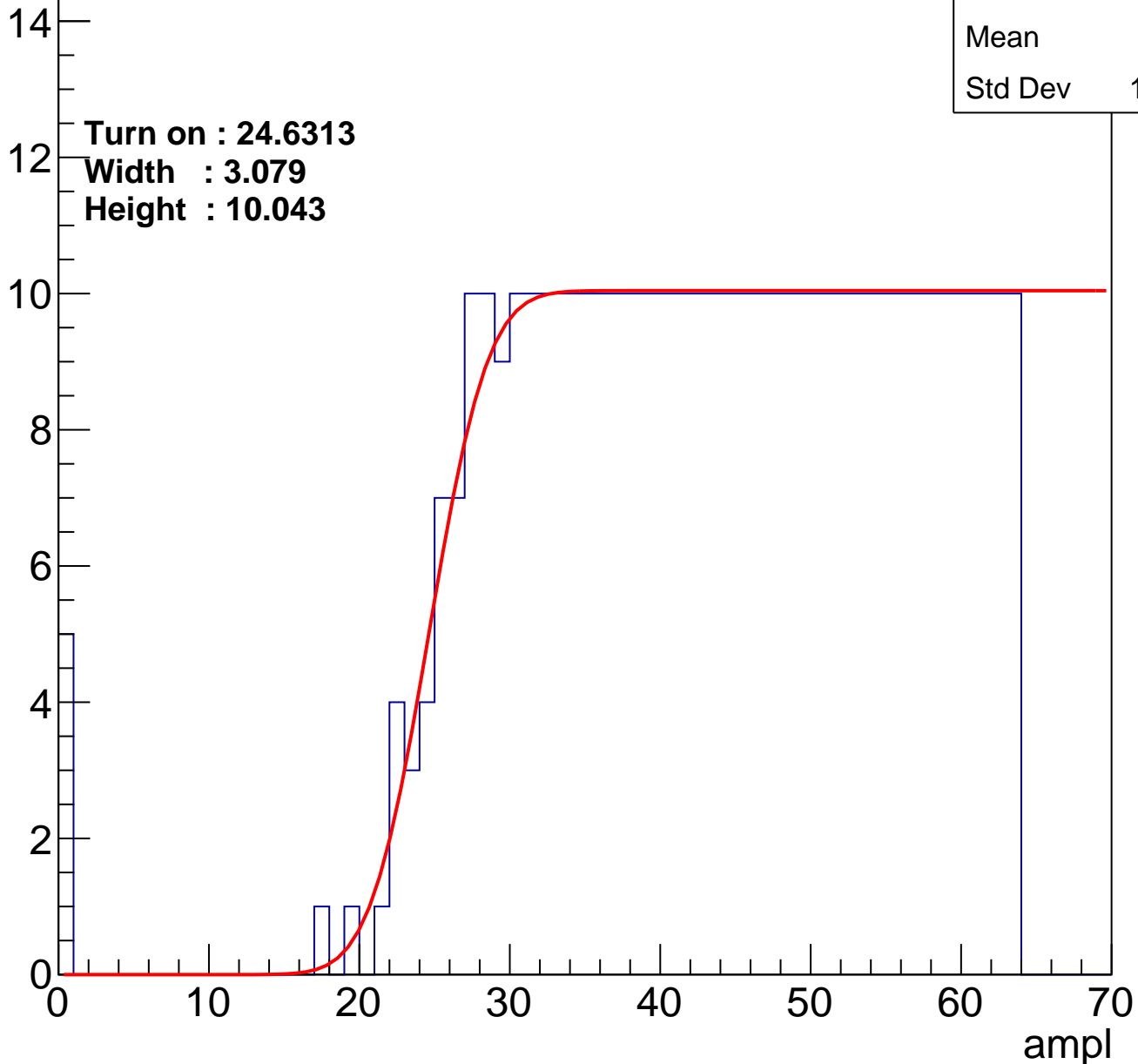
Entries	402
Mean	43
Std Dev	12.53

Turn on : 24.6313

Width : 3.079

Height : 10.043

Entry



B1L100S, U14-ch26

calib_packv5_042523_0143.root, FC#4, port A2

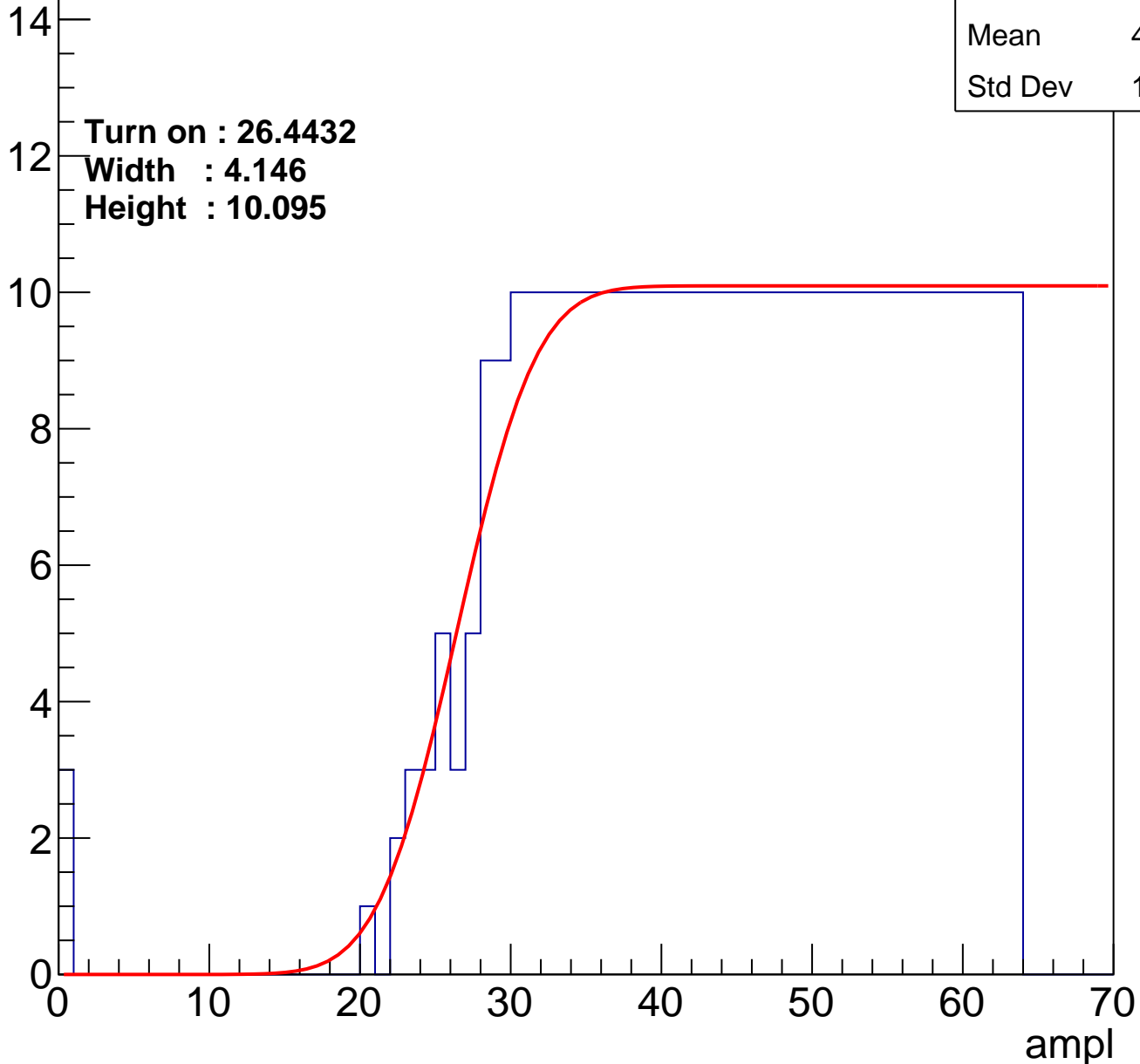
Entries	383
Mean	44.04
Std Dev	11.79

Turn on : 26.4432

Width : 4.146

Height : 10.095

Entry



B1L100S, U14-ch27

calib_packv5_042523_0143.root, FC#4, port A2

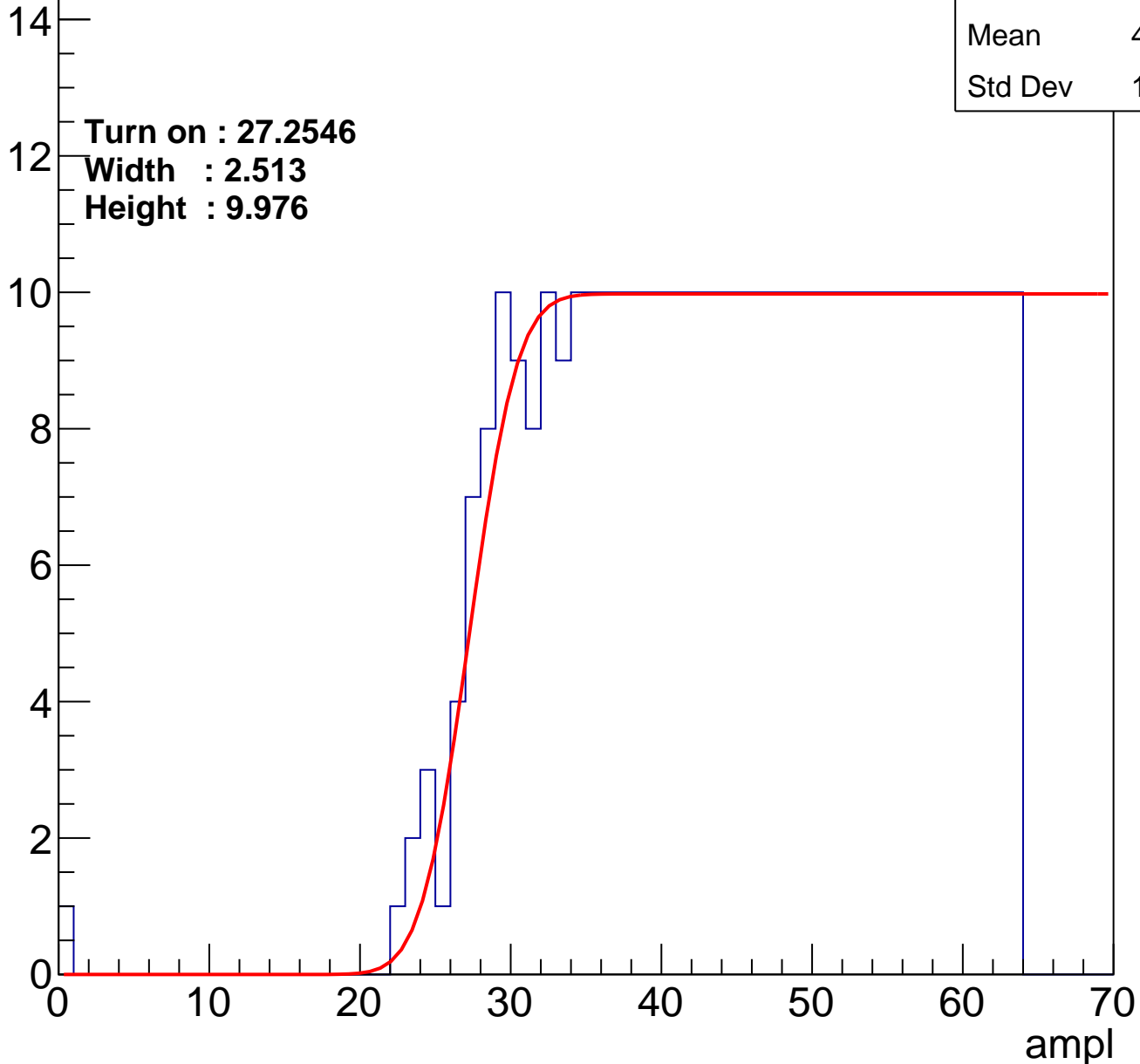
Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.2546

Width : 2.513

Height : 9.976

Entry



B1L100S, U14-ch28

calib_packv5_042523_0143.root, FC#4, port A2

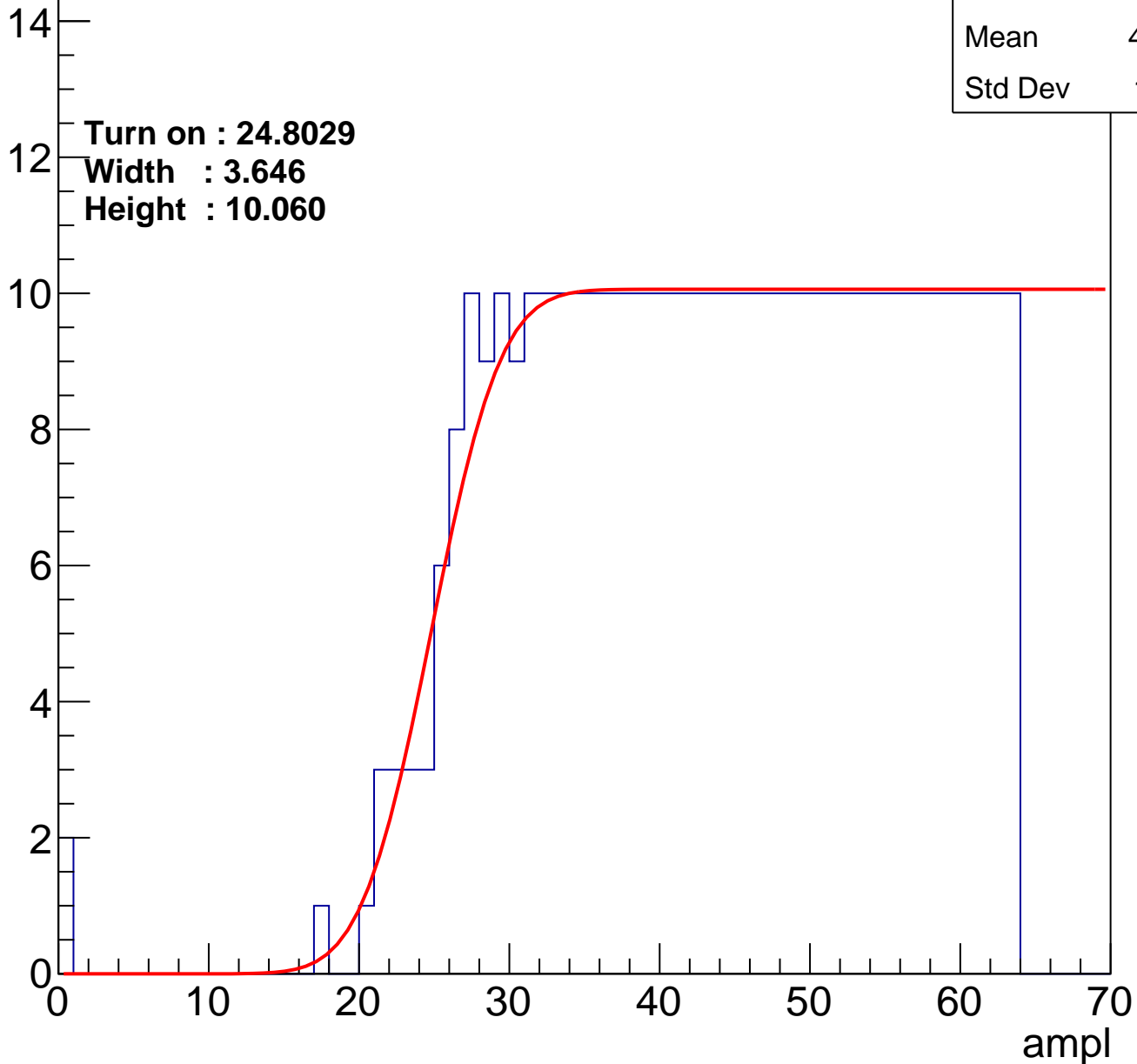
Entries	398
Mean	43.36
Std Dev	12.01

Turn on : 24.8029

Width : 3.646

Height : 10.060

Entry



B1L100S, U14-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 27.0162

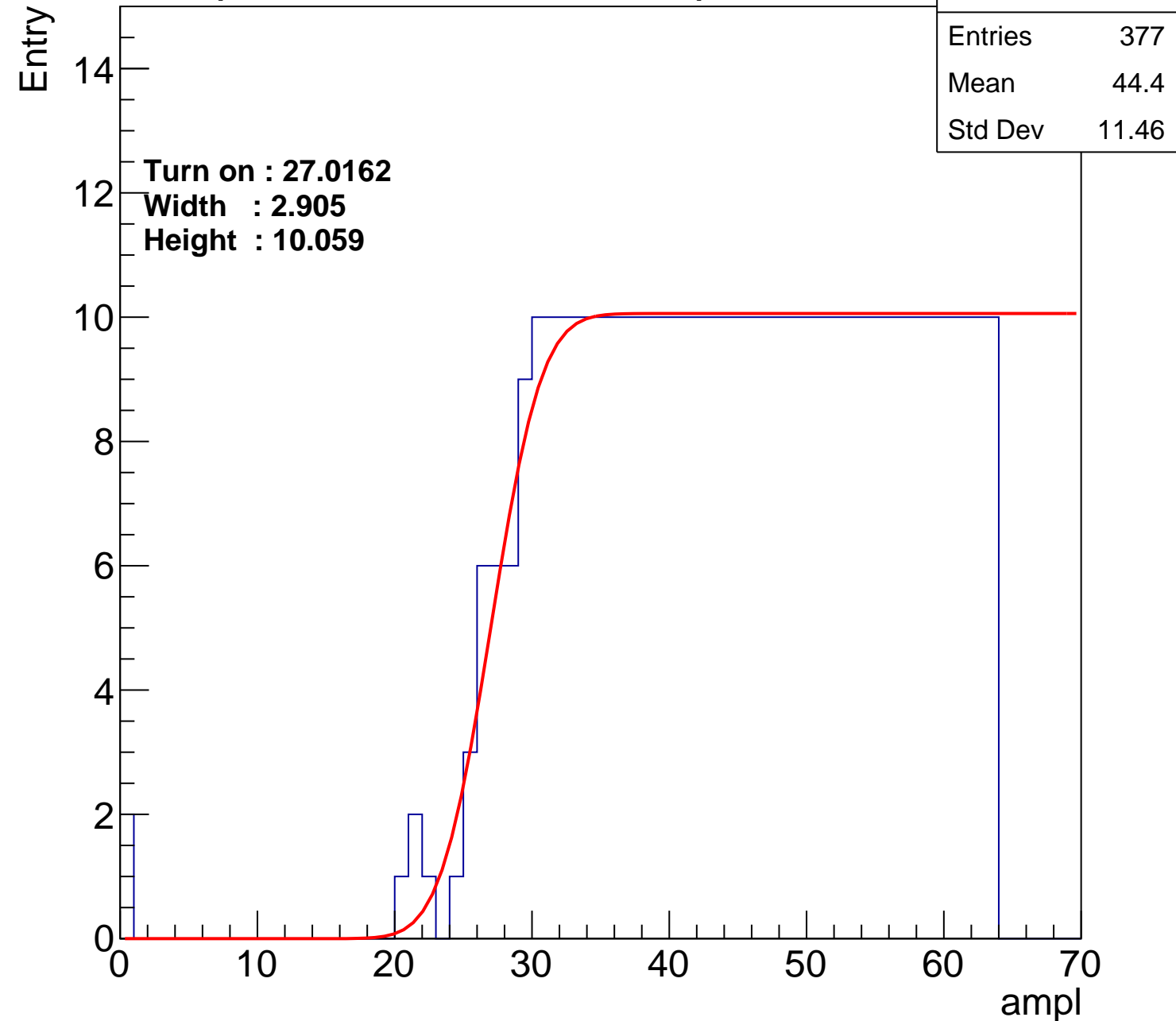
Width : 2.905

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch30

calib_packv5_042523_0143.root, FC#4, port A2

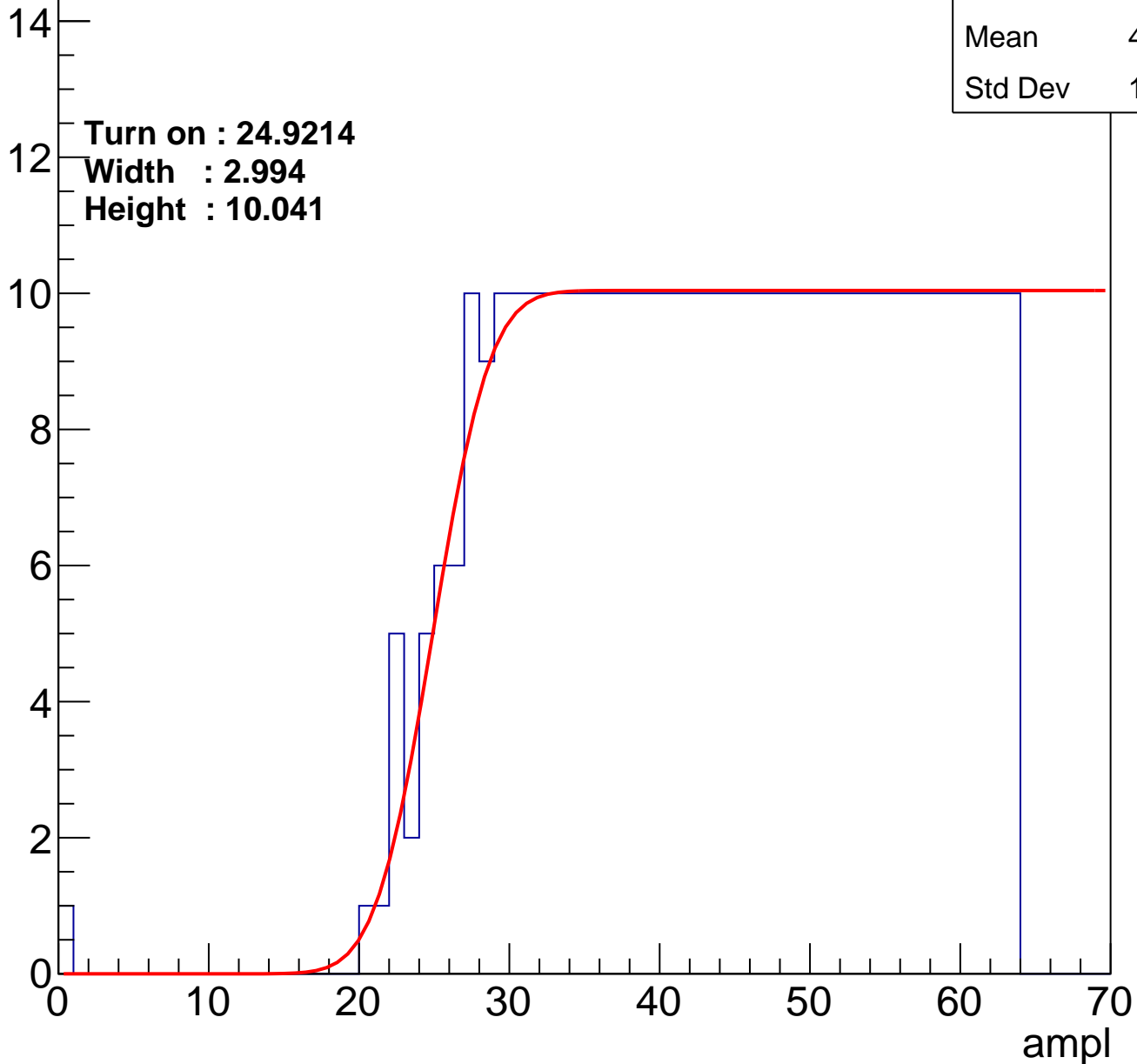
Entries	396
Mean	43.55
Std Dev	11.75

Turn on : 24.9214

Width : 2.994

Height : 10.041

Entry



B1L100S, U14-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.6244

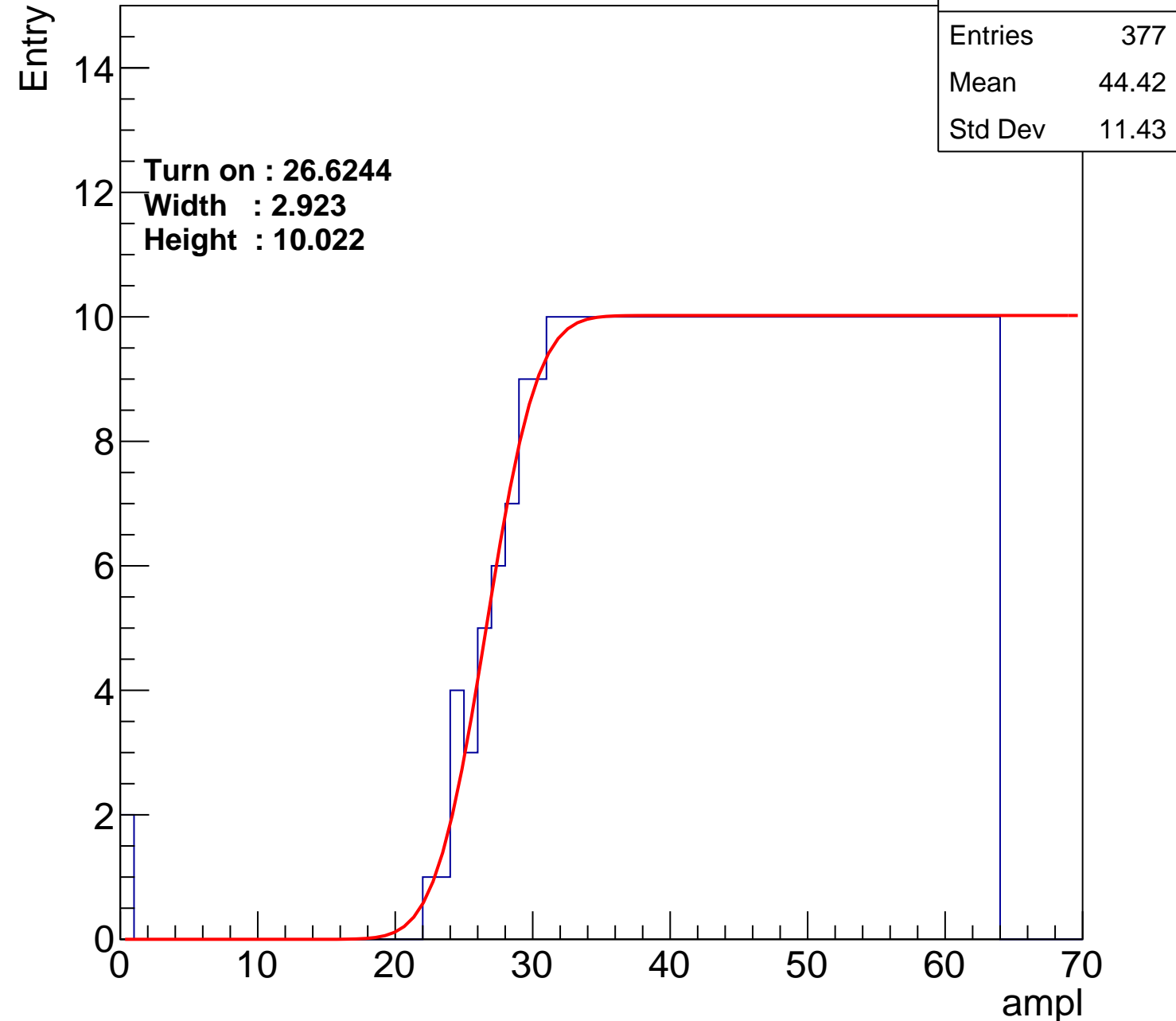
Width : 2.923

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch32

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.03
Std Dev	12.05

Turn on : 26.3161

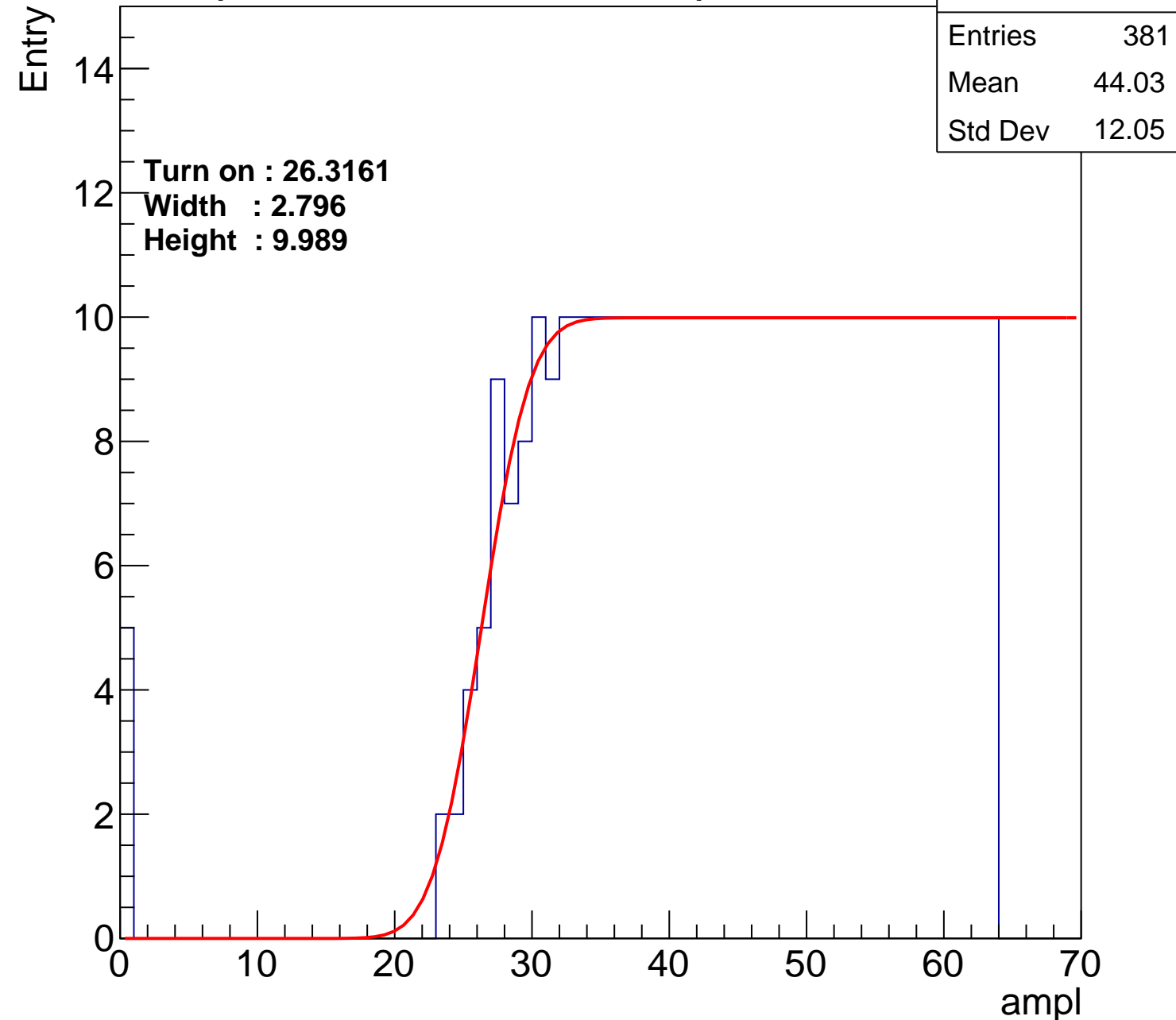
Width : 2.796

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.11
Std Dev	11.96

Turn on : 26.6248

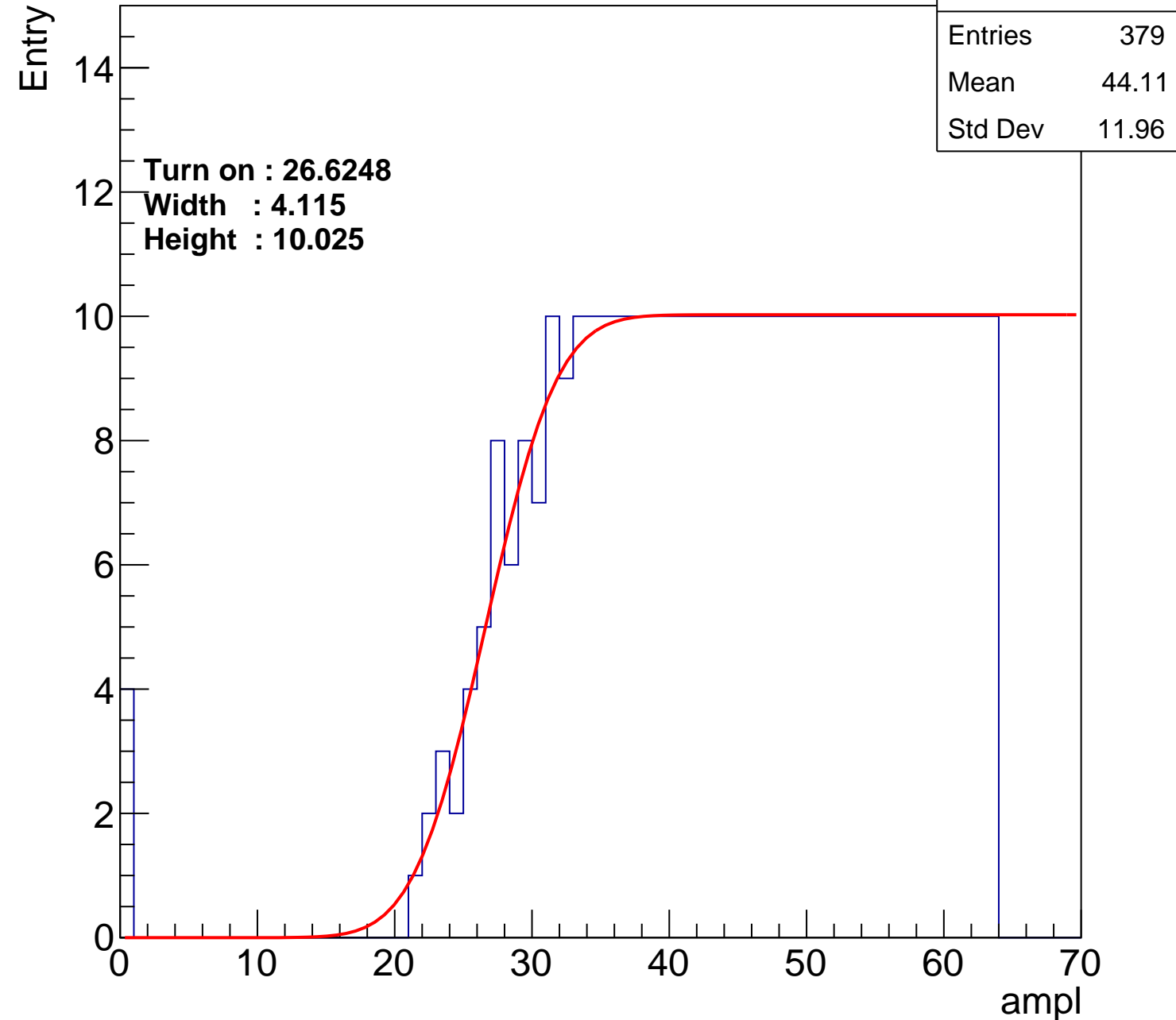
Width : 4.115

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch34

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.68
Std Dev	11.87

Turn on : 25.6415

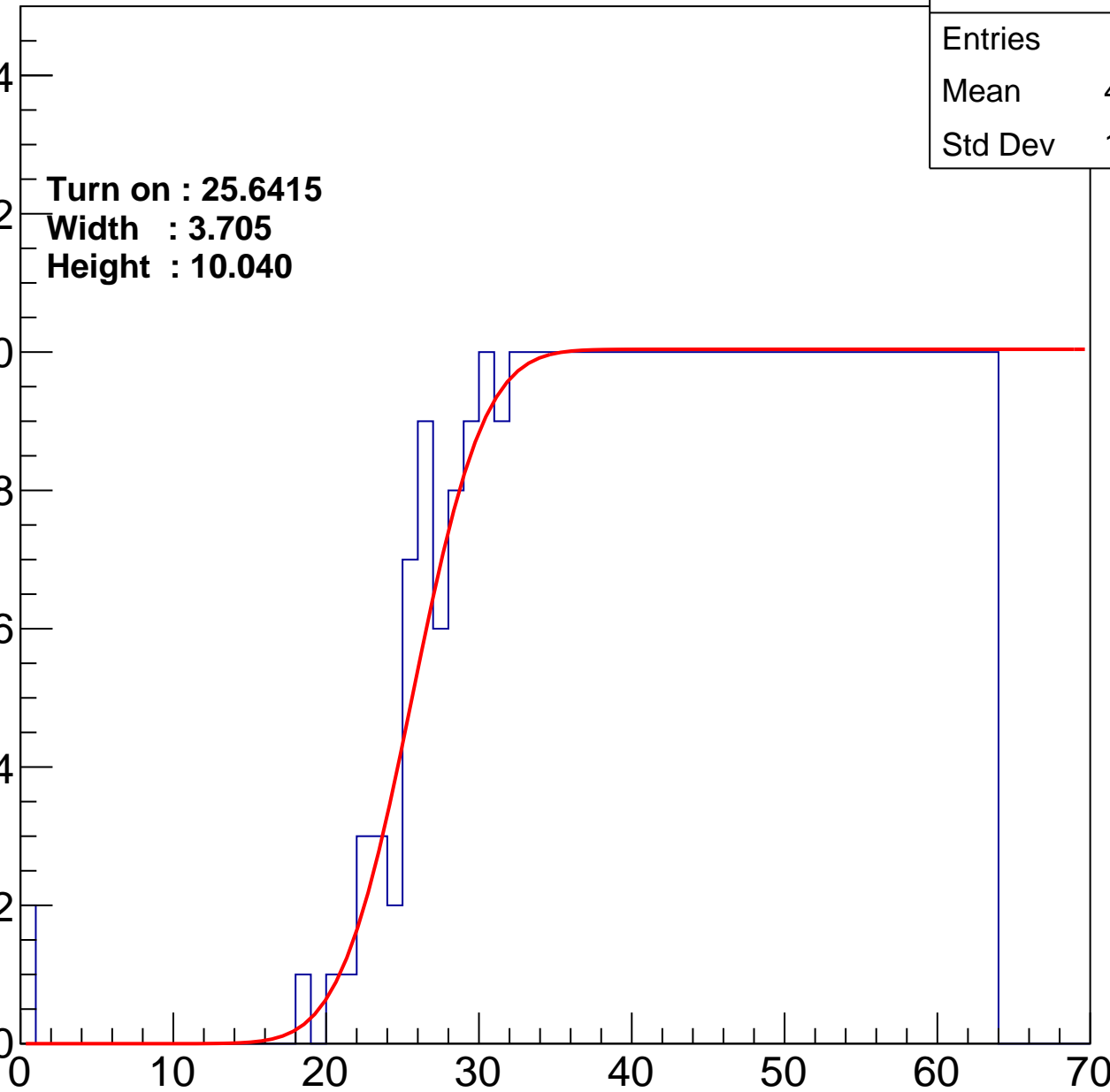
Width : 3.705

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.3
Std Dev	11.33

Turn on : 26.0185

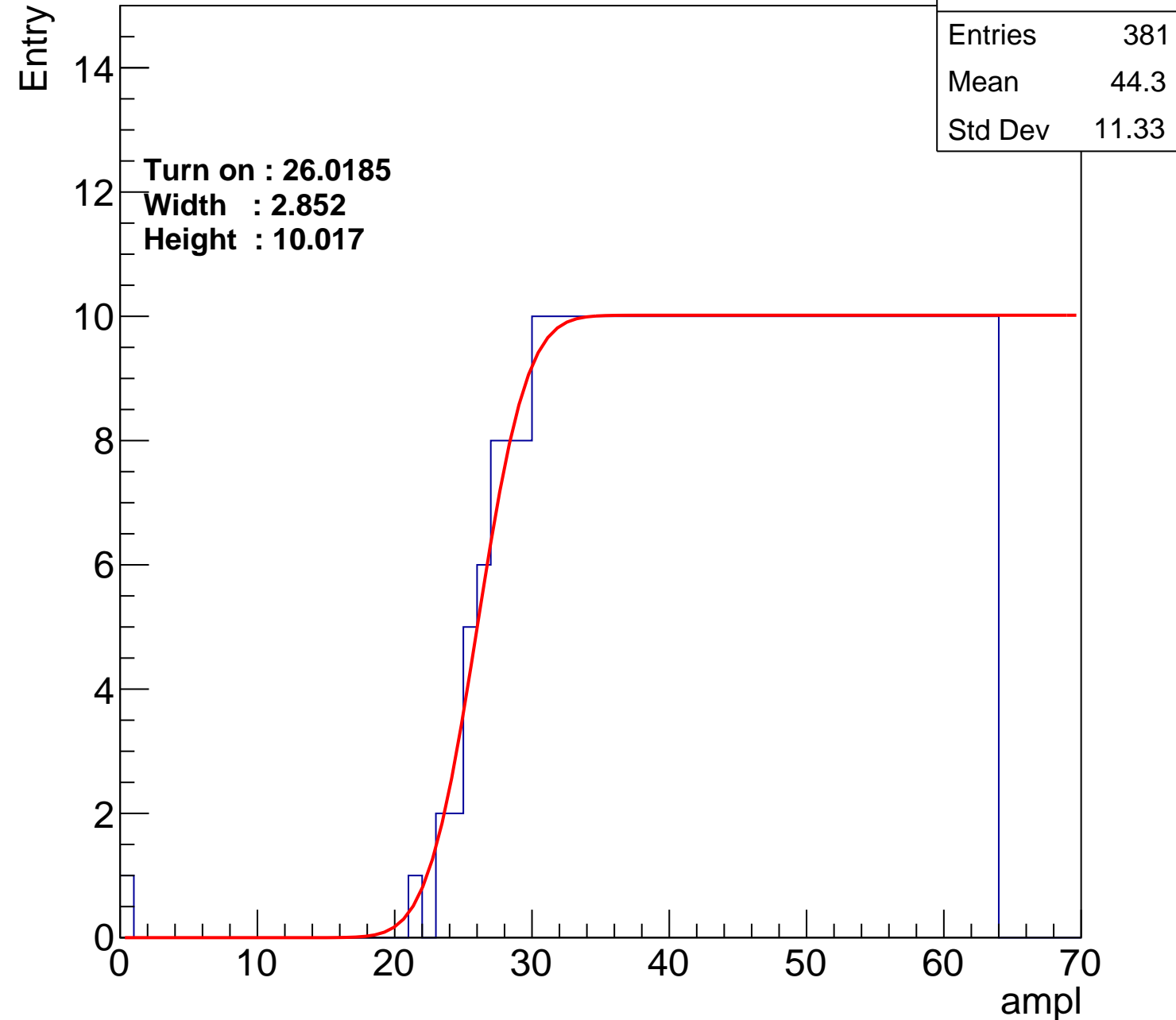
Width : 2.852

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch36

calib_packv5_042523_0143.root, FC#4, port A2

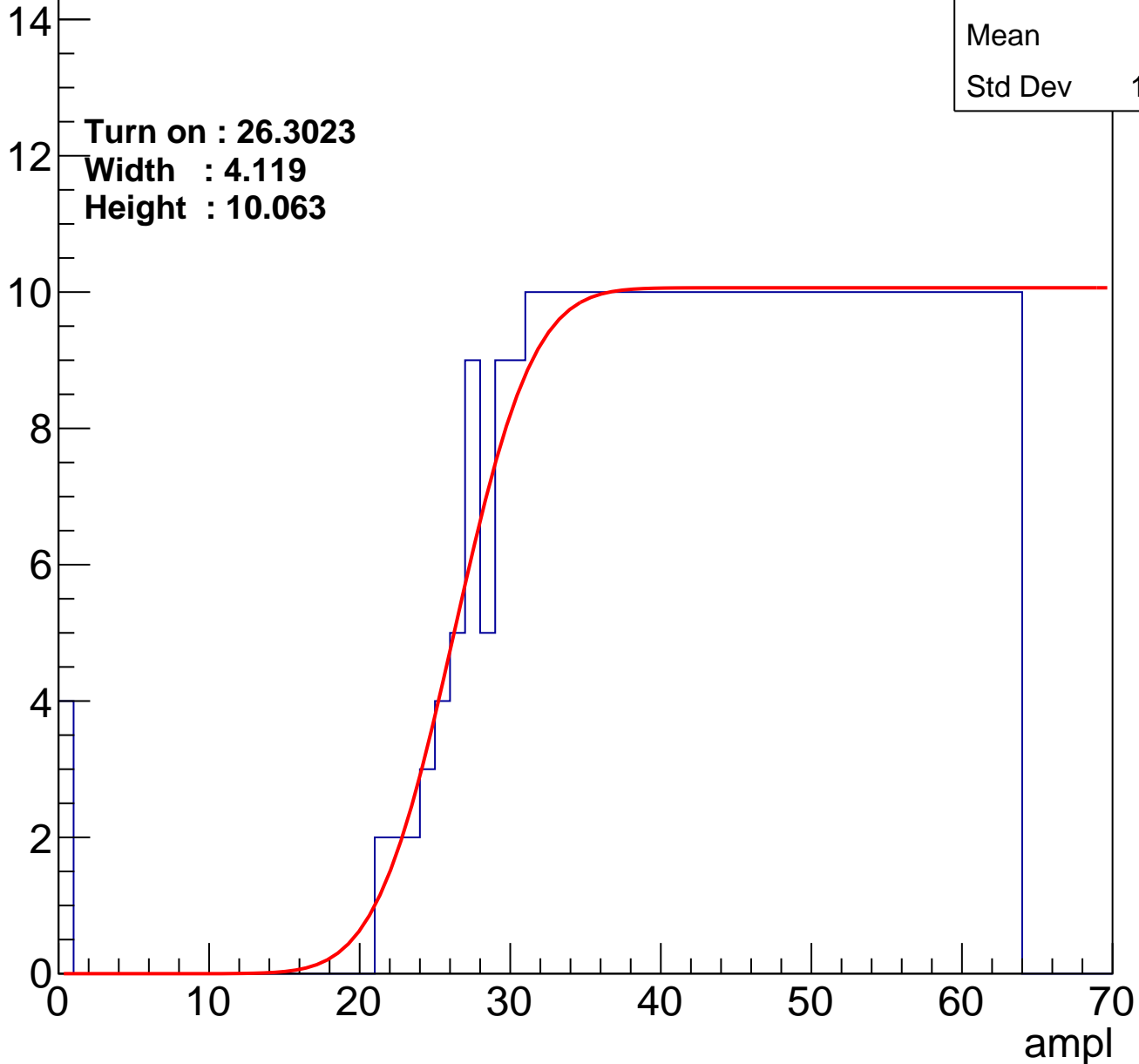
Entries	384
Mean	43.9
Std Dev	12.02

Turn on : 26.3023

Width : 4.119

Height : 10.063

Entry



B1L100S, U14-ch37

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 24.9438

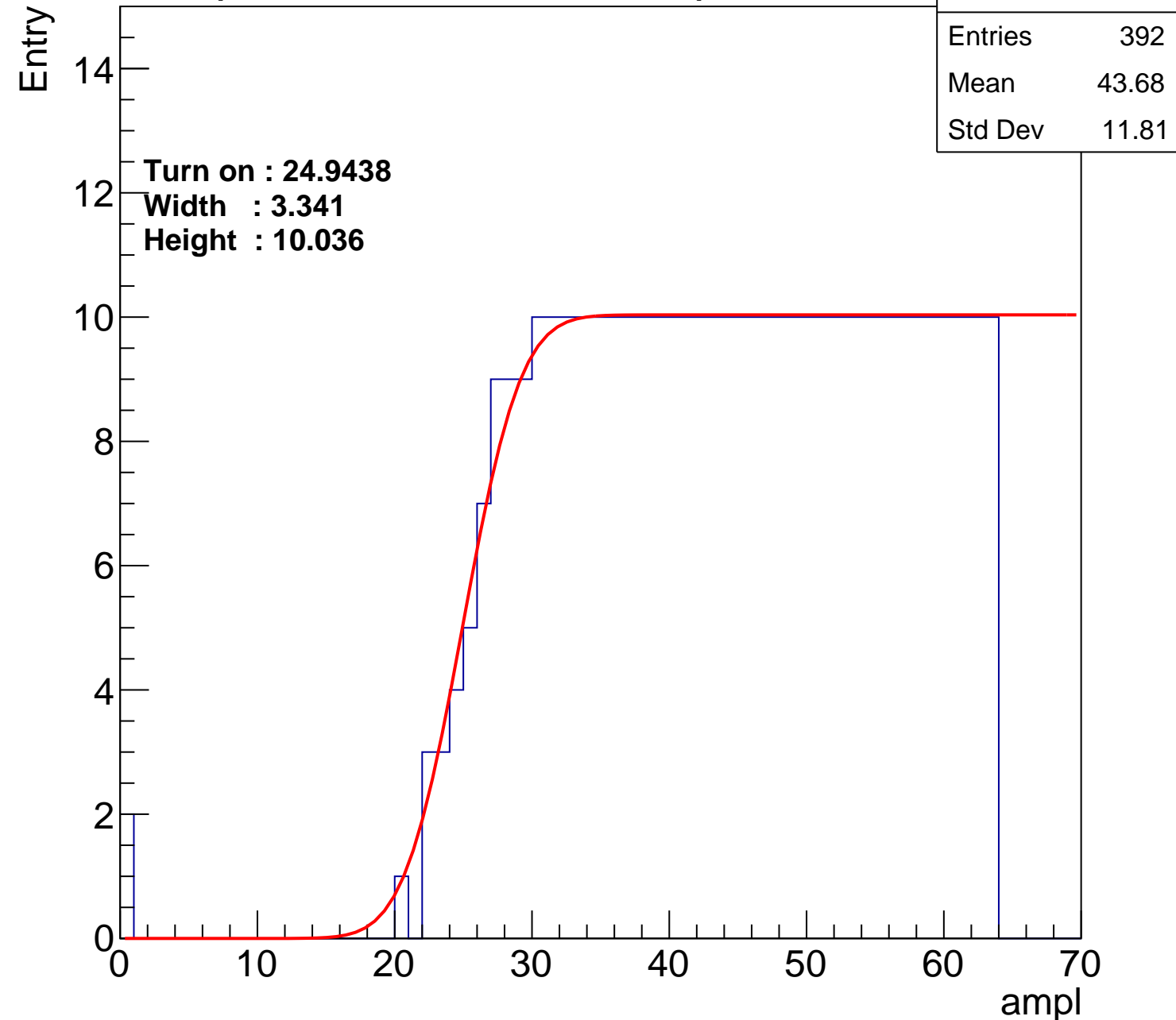
Width : 3.341

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.07
Std Dev	11.79

Turn on : 26.0824

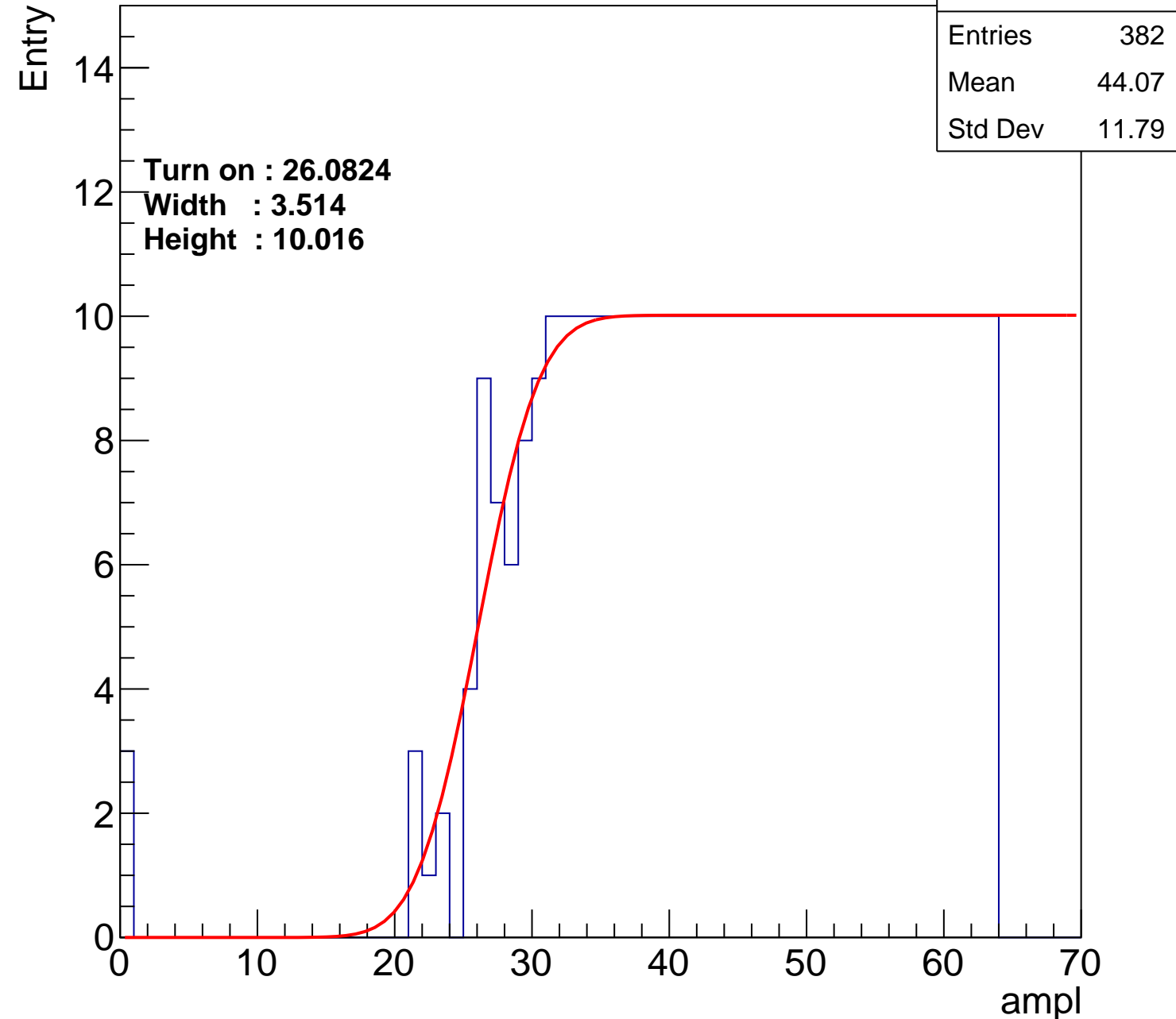
Width : 3.514

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.46
Std Dev	11.96

Turn on : 26.1252

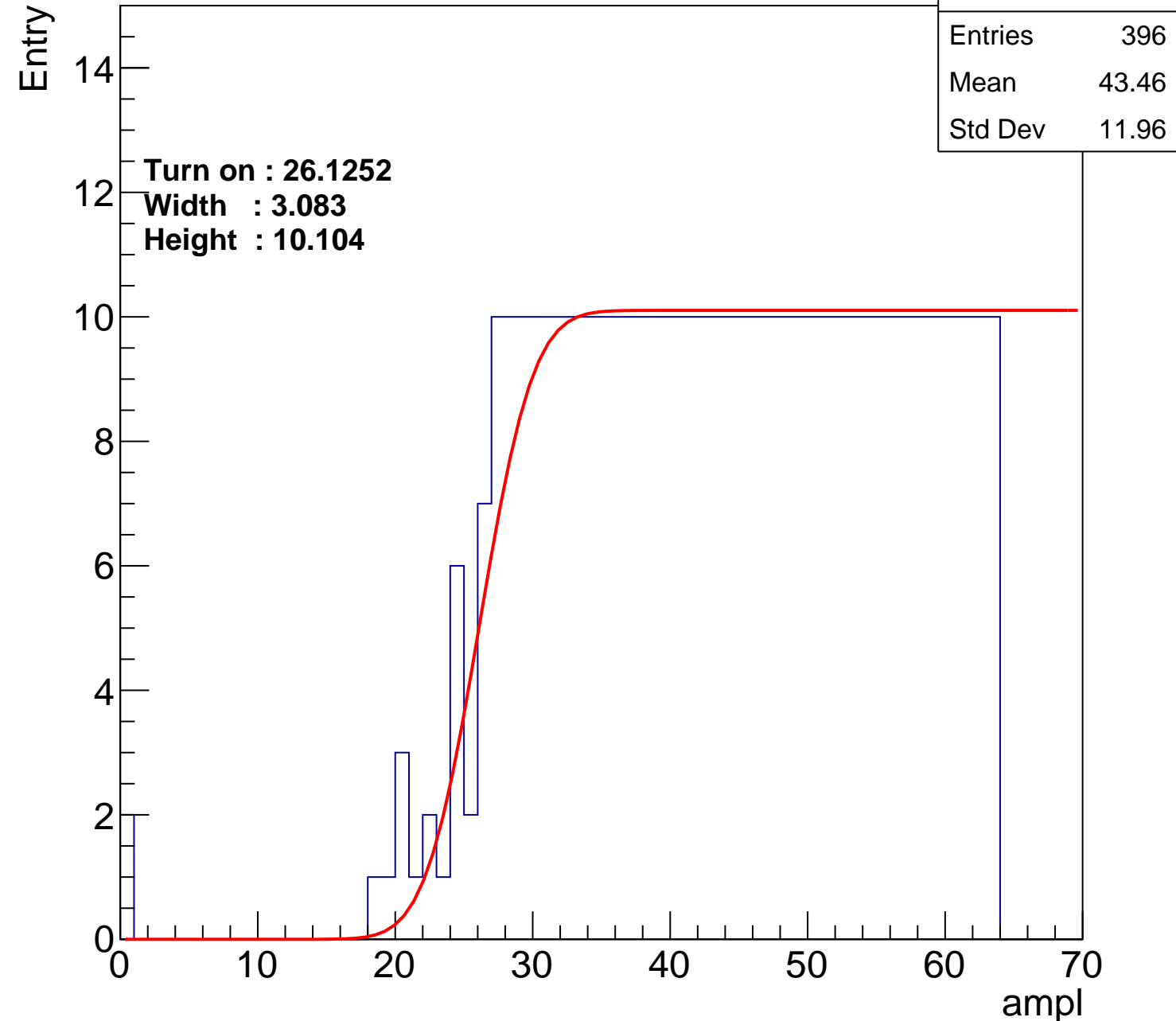
Width : 3.083

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch40

calib_packv5_042523_0143.root, FC#4, port A2

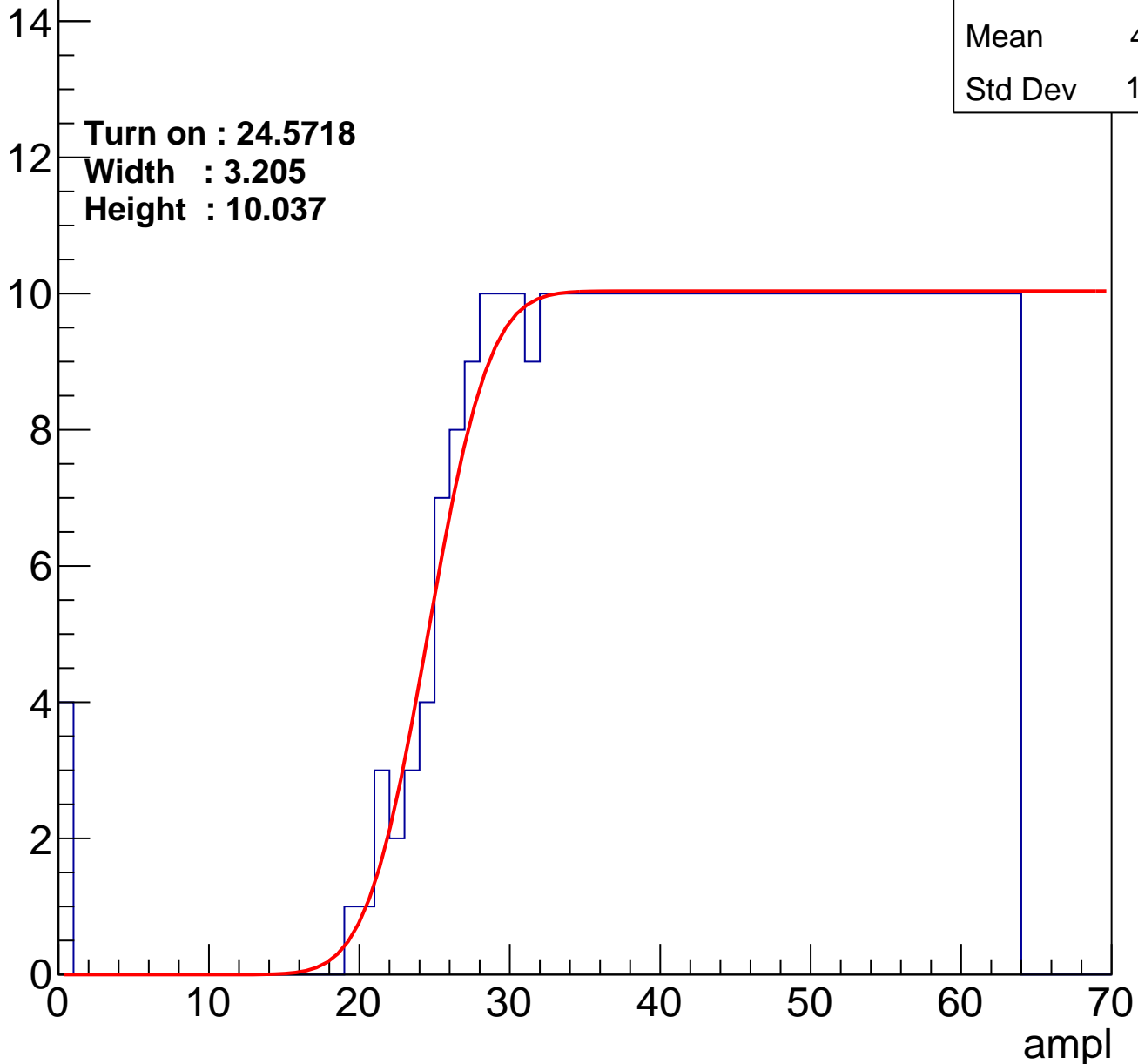
Entries	401
Mean	43.11
Std Dev	12.37

Turn on : 24.5718

Width : 3.205

Height : 10.037

Entry



B1L100S, U14-ch41

calib_packv5_042523_0143.root, FC#4, port A2

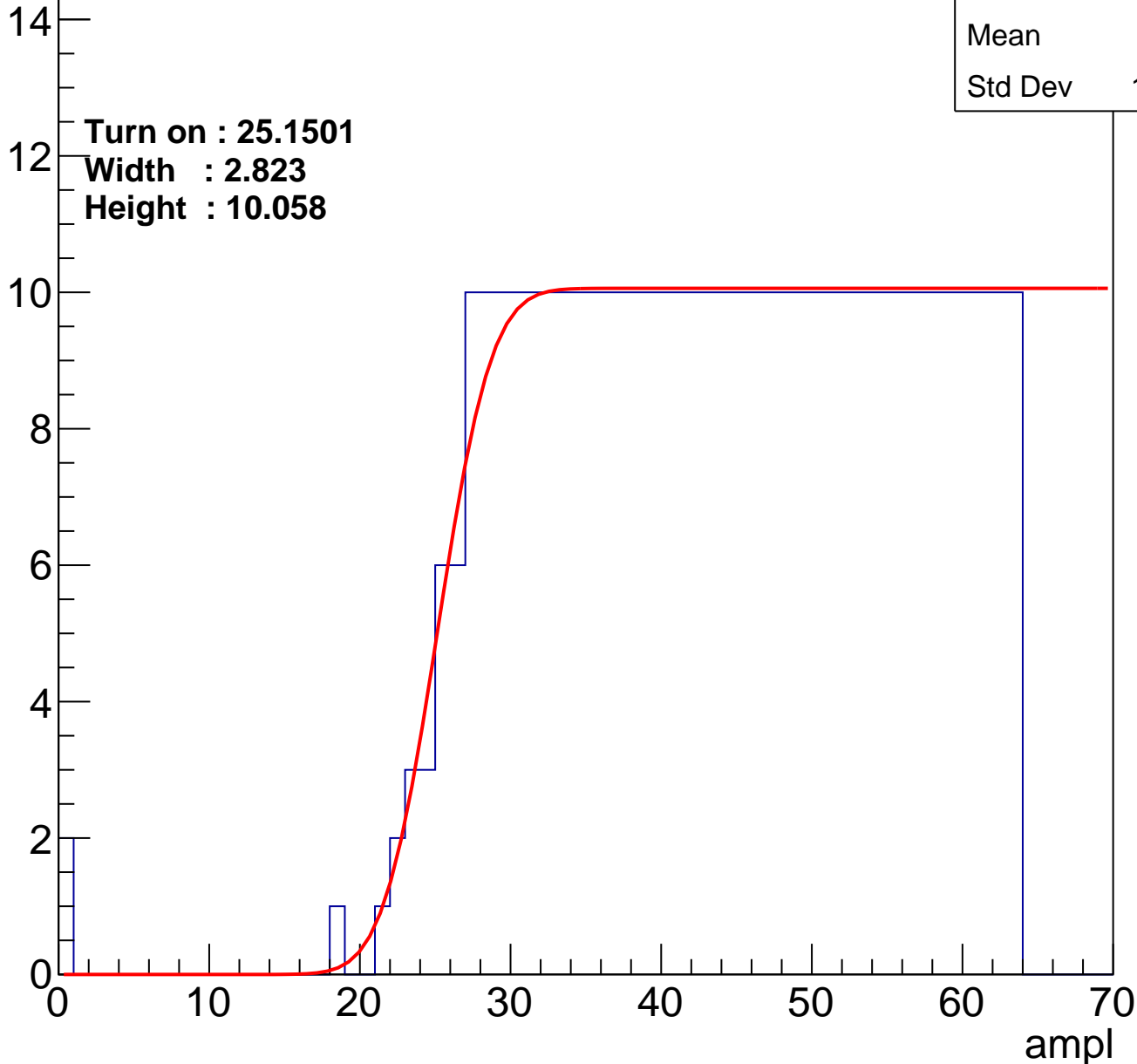
Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.1501

Width : 2.823

Height : 10.058

Entry



B1L100S, U14-ch42

calib_packv5_042523_0143.root, FC#4, port A2

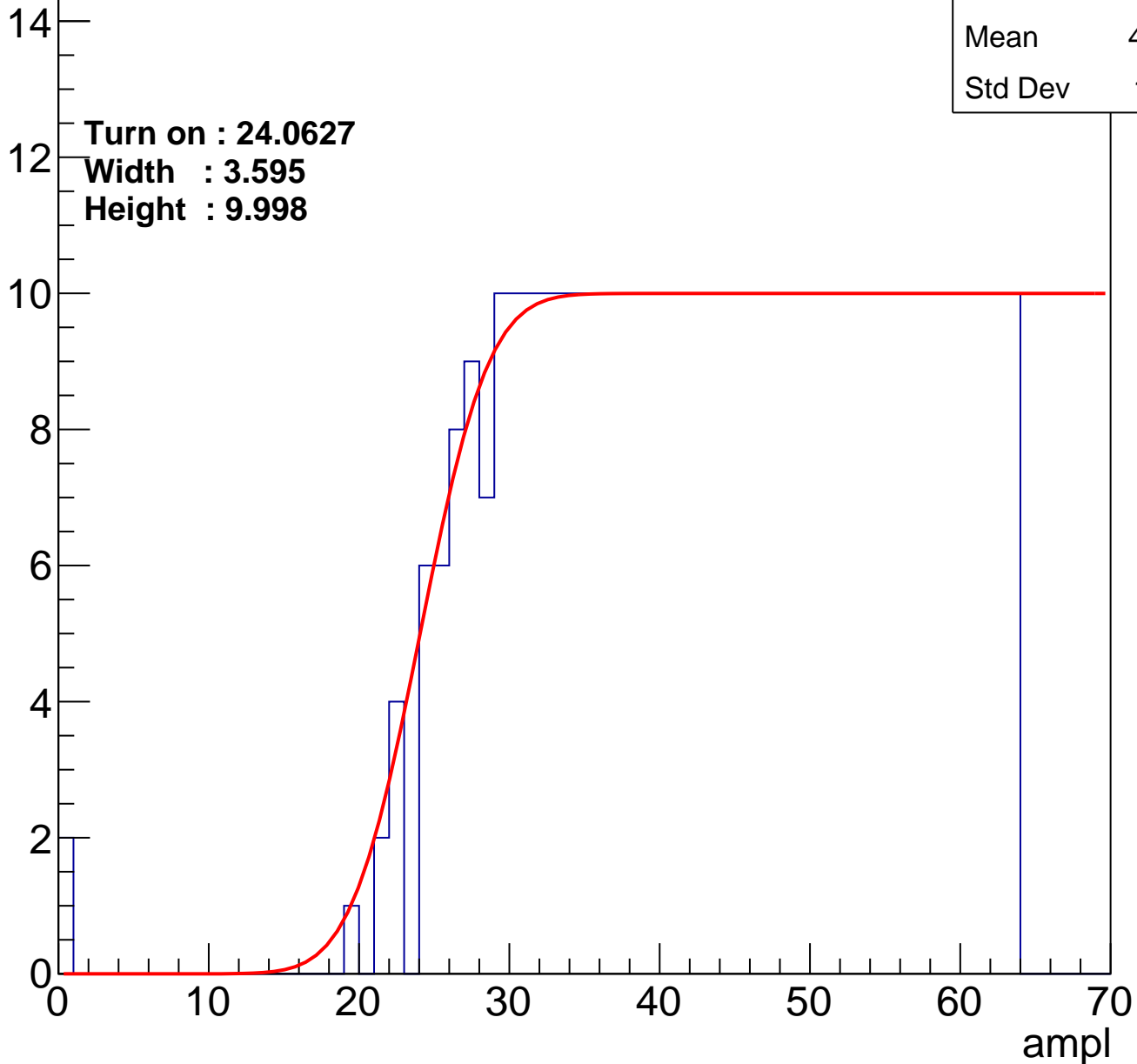
Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.0627

Width : 3.595

Height : 9.998

Entry



B1L100S, U14-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 25.9211

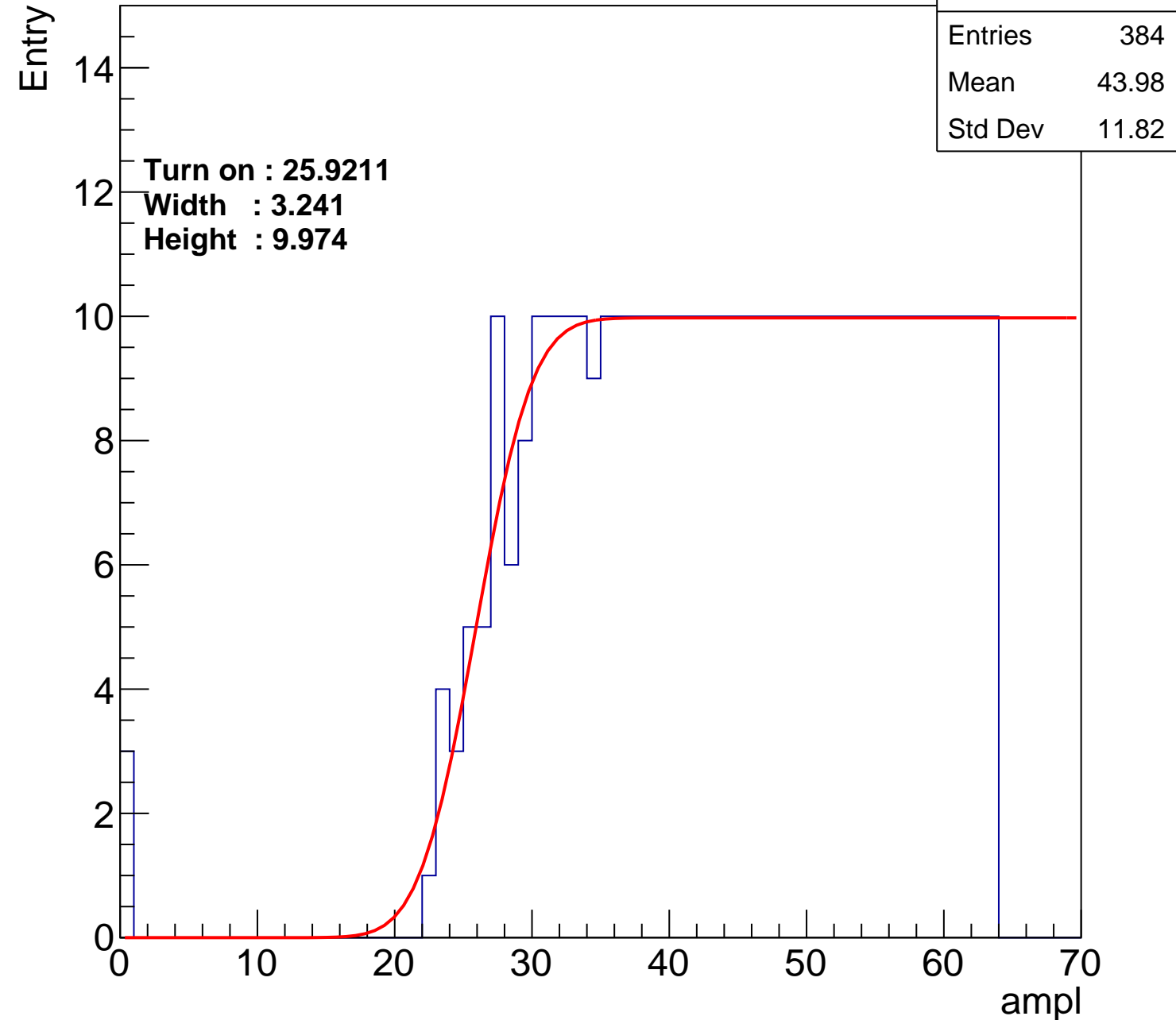
Width : 3.241

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch44

calib_packv5_042523_0143.root, FC#4, port A2

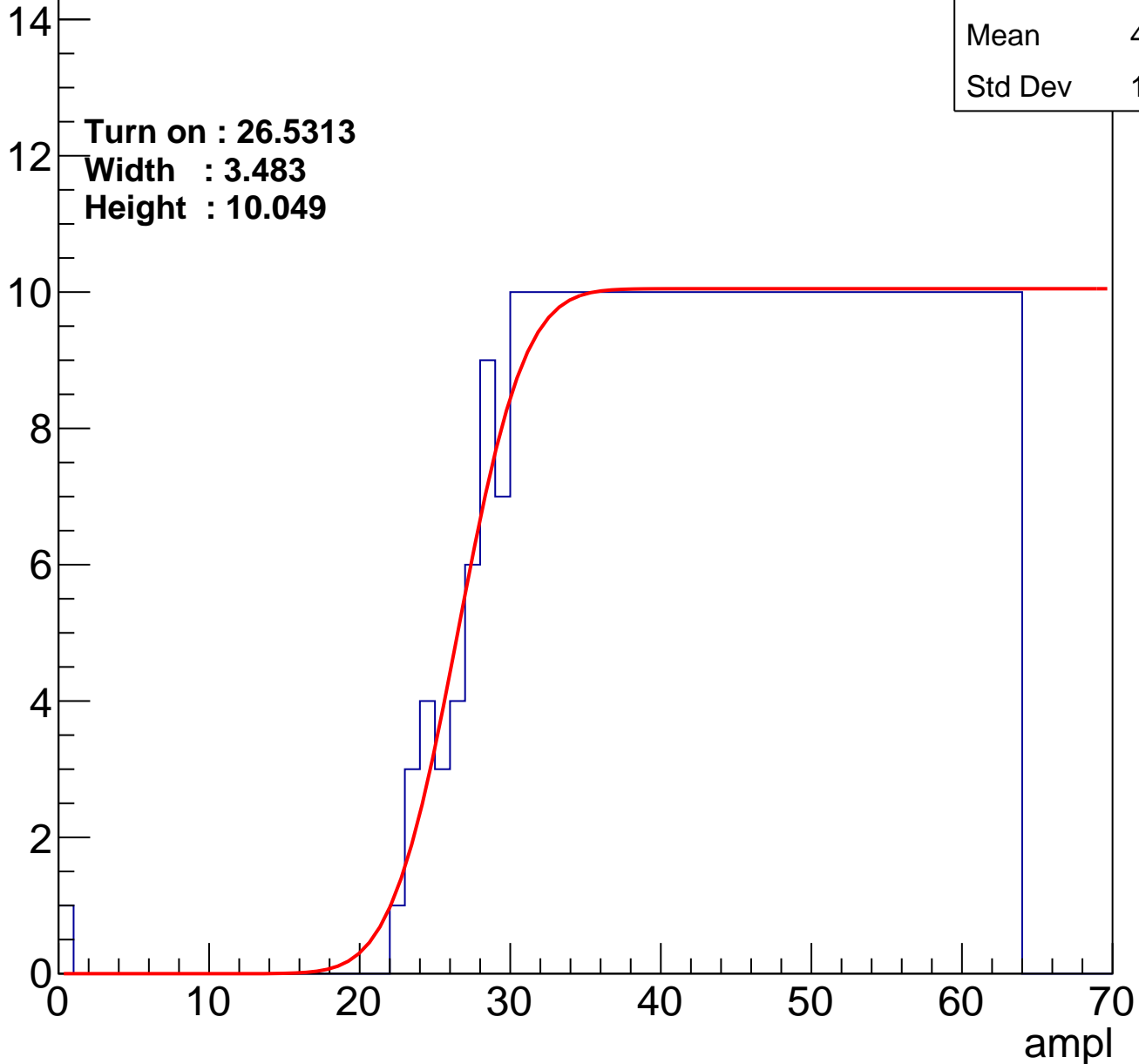
Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.5313

Width : 3.483

Height : 10.049

Entry



B1L100S, U14-ch45

calib_packv5_042523_0143.root, FC#4, port A2

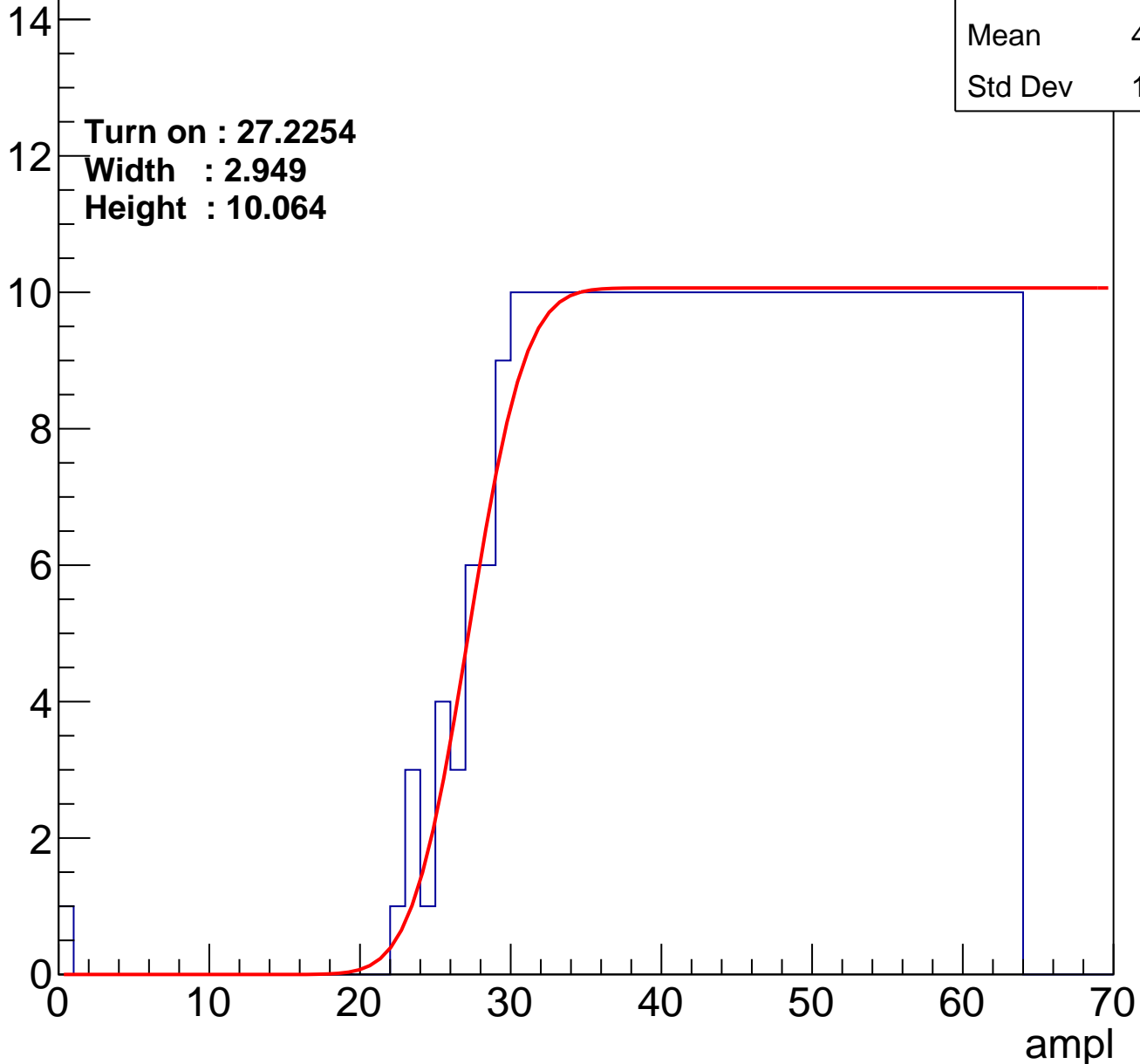
Entries	374
Mean	44.64
Std Dev	11.16

Turn on : 27.2254

Width : 2.949

Height : 10.064

Entry



B1L100S, U14-ch46

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.13
Std Dev	11.49

Turn on : 25.8408

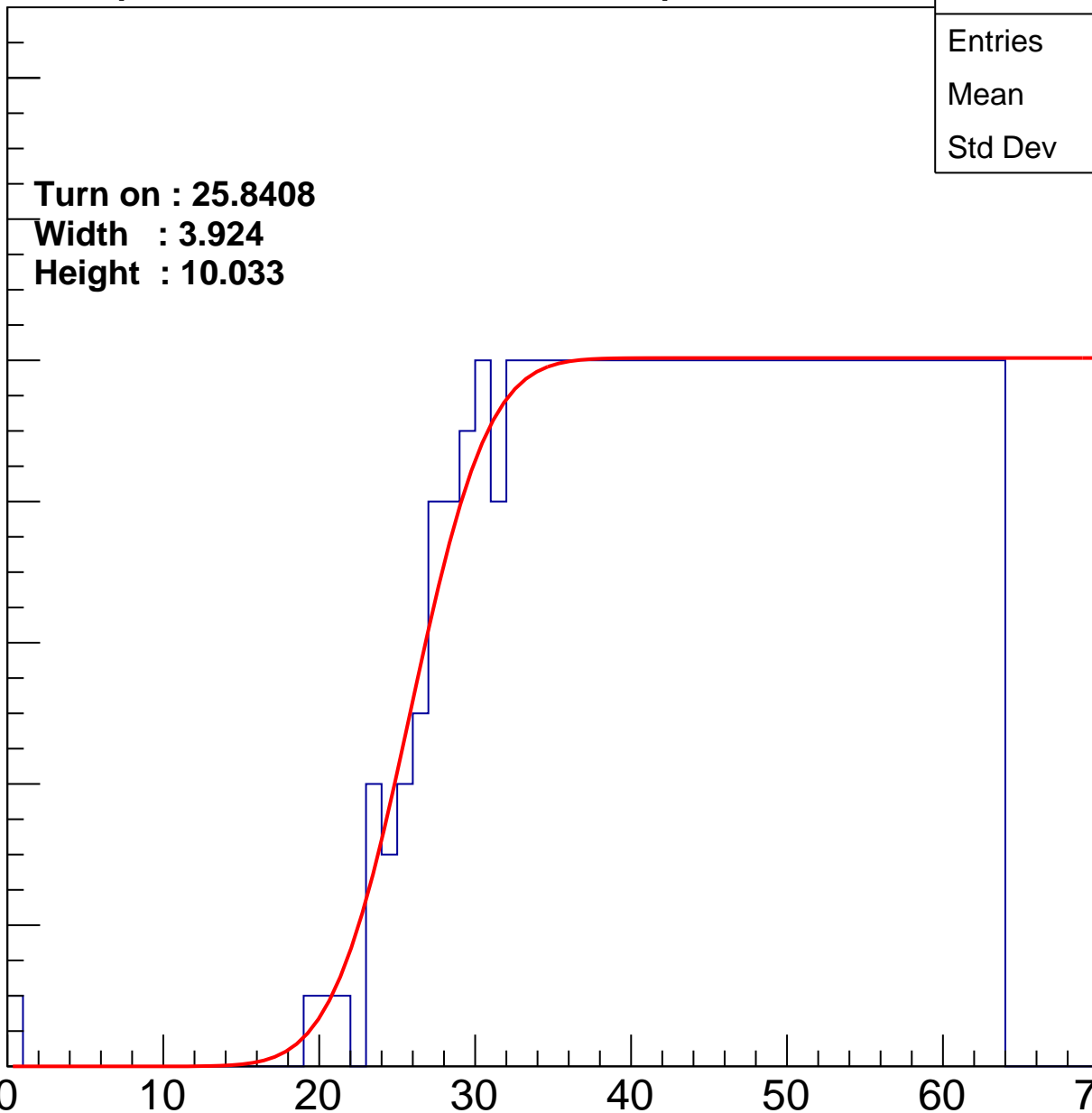
Width : 3.924

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch47

calib_packv5_042523_0143.root, FC#4, port A2

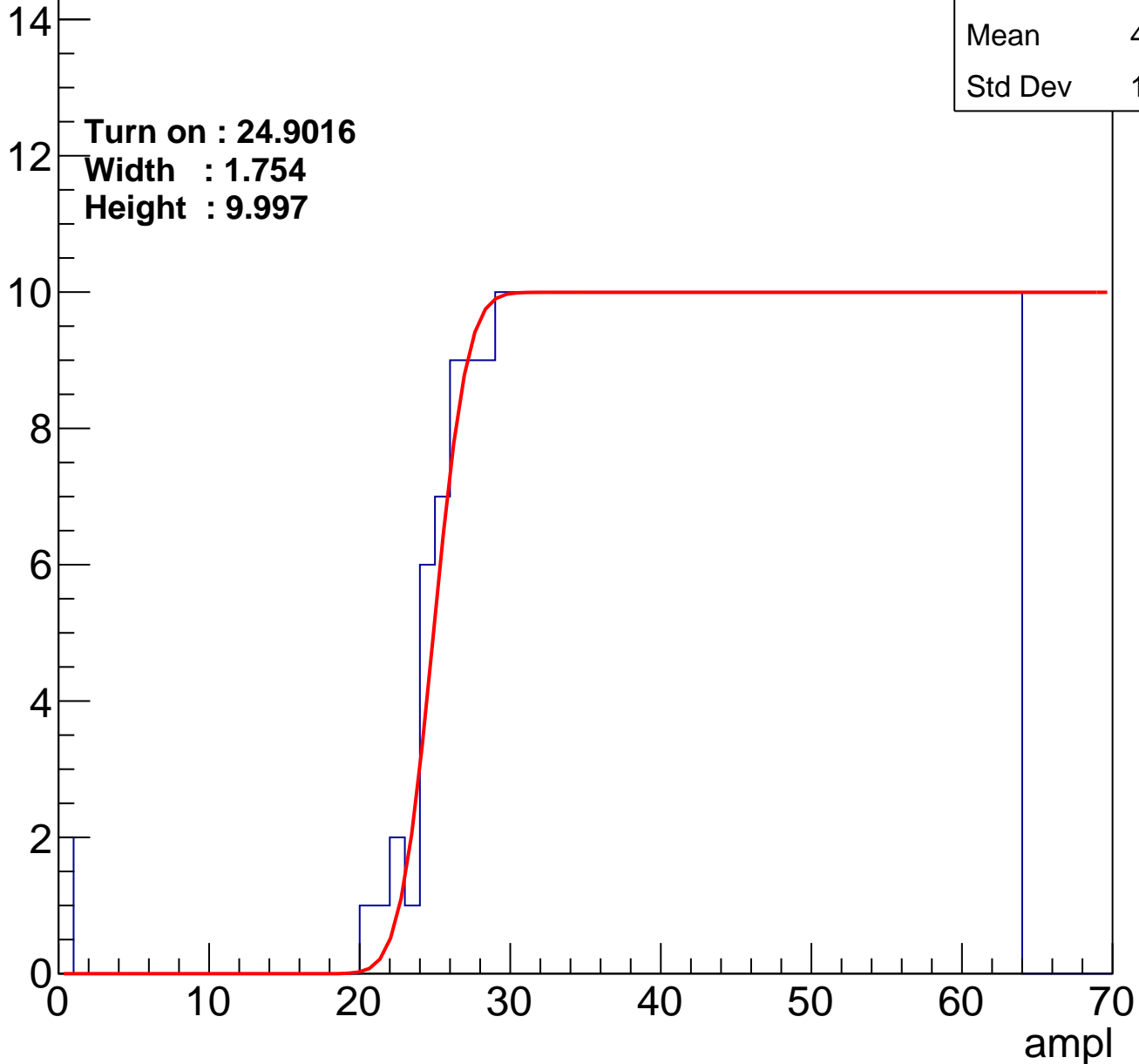
Entries	397
Mean	43.47
Std Dev	11.89

Turn on : 24.9016

Width : 1.754

Height : 9.997

Entry



B1L100S, U14-ch48

calib_packv5_042523_0143.root, FC#4, port A2

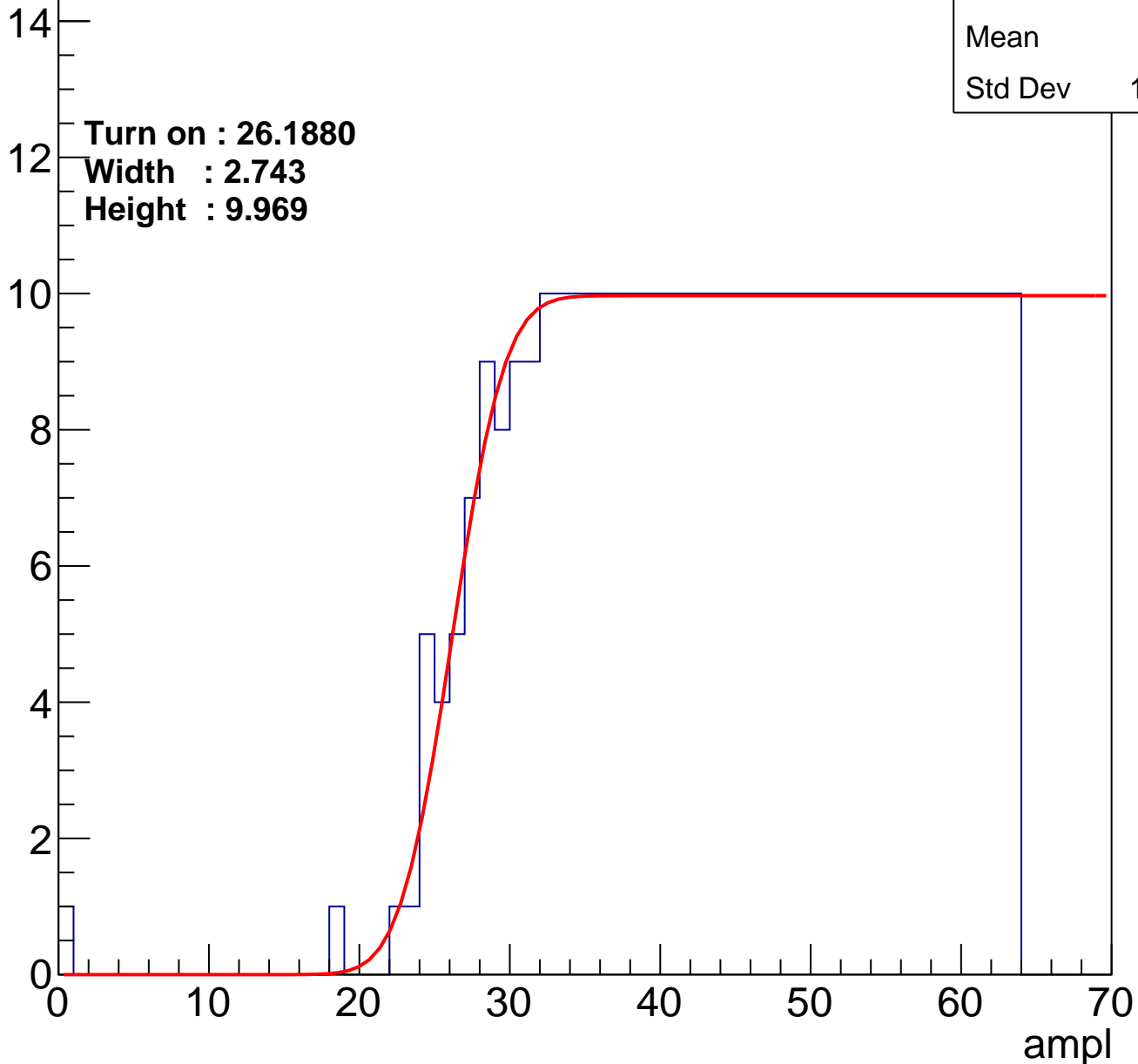
Entries	380
Mean	44.3
Std Dev	11.38

Turn on : 26.1880

Width : 2.743

Height : 9.969

Entry



B1L100S, U14-ch49

calib_packv5_042523_0143.root, FC#4, port A2

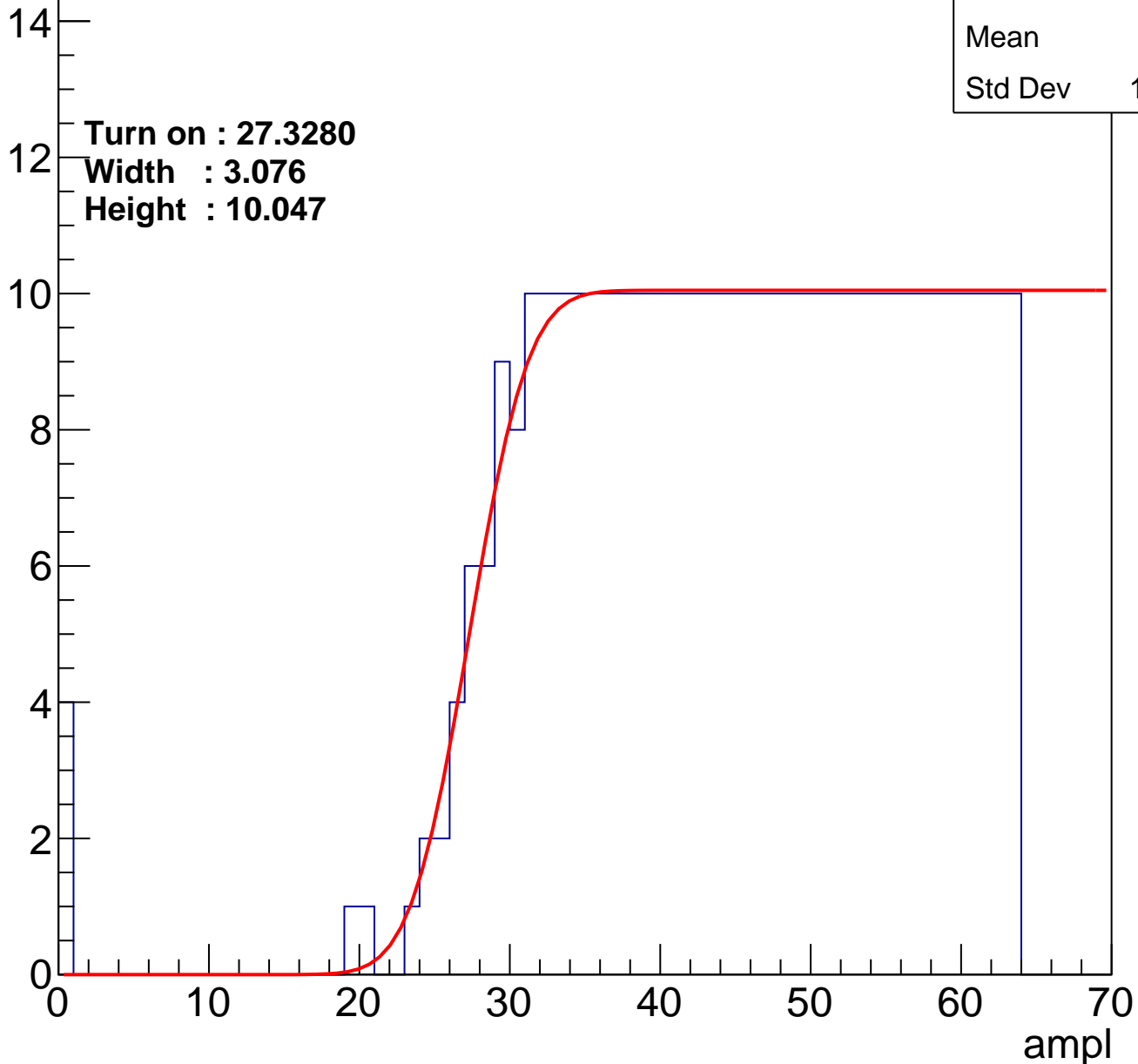
Entries	374
Mean	44.4
Std Dev	11.78

Turn on : 27.3280

Width : 3.076

Height : 10.047

Entry



B1L100S, U14-ch50

calib_packv5_042523_0143.root, FC#4, port A2

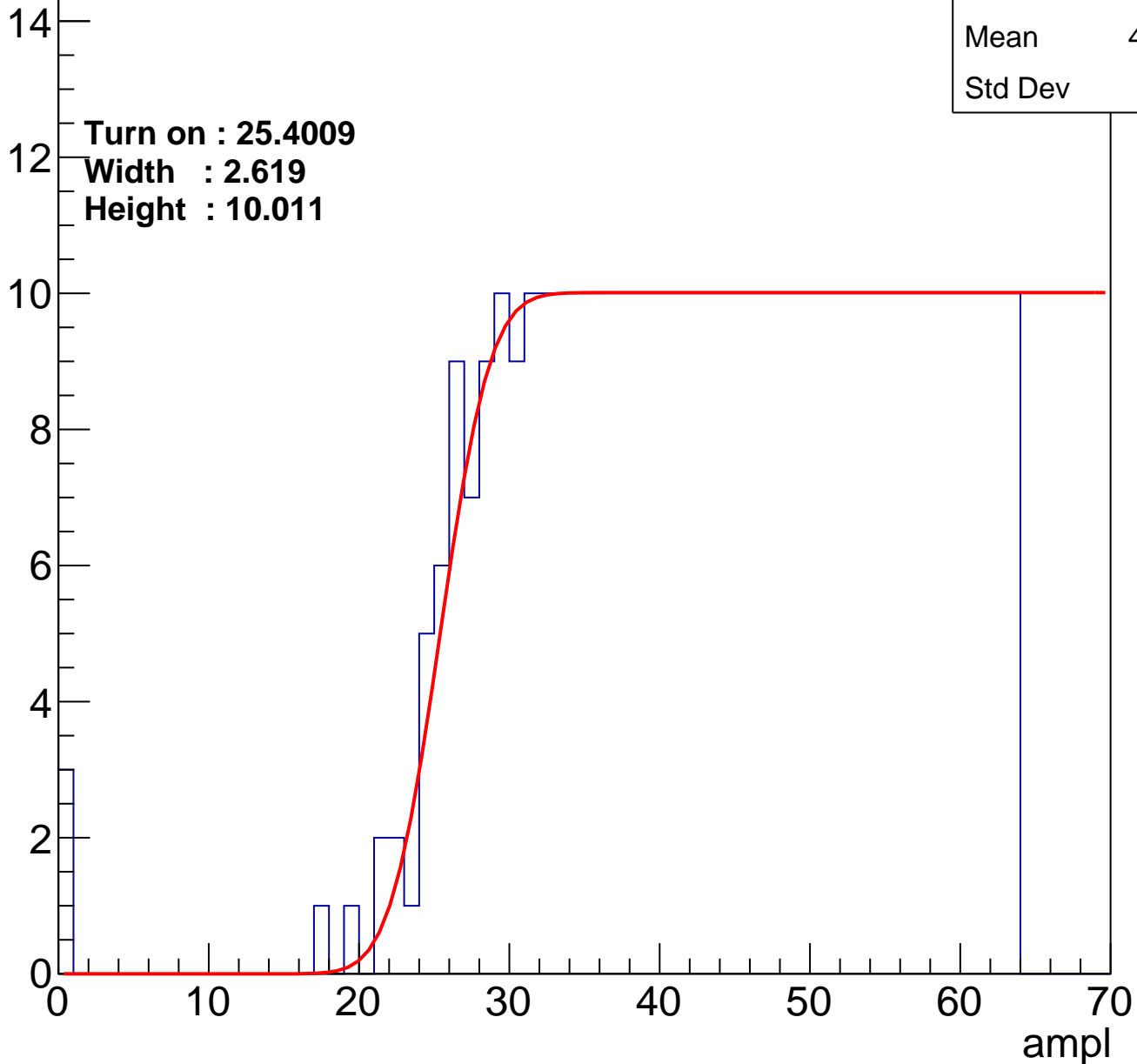
Entries	395
Mean	43.44
Std Dev	12.1

Turn on : 25.4009

Width : 2.619

Height : 10.011

Entry



B1L100S, U14-ch51

calib_packv5_042523_0143.root, FC#4, port A2

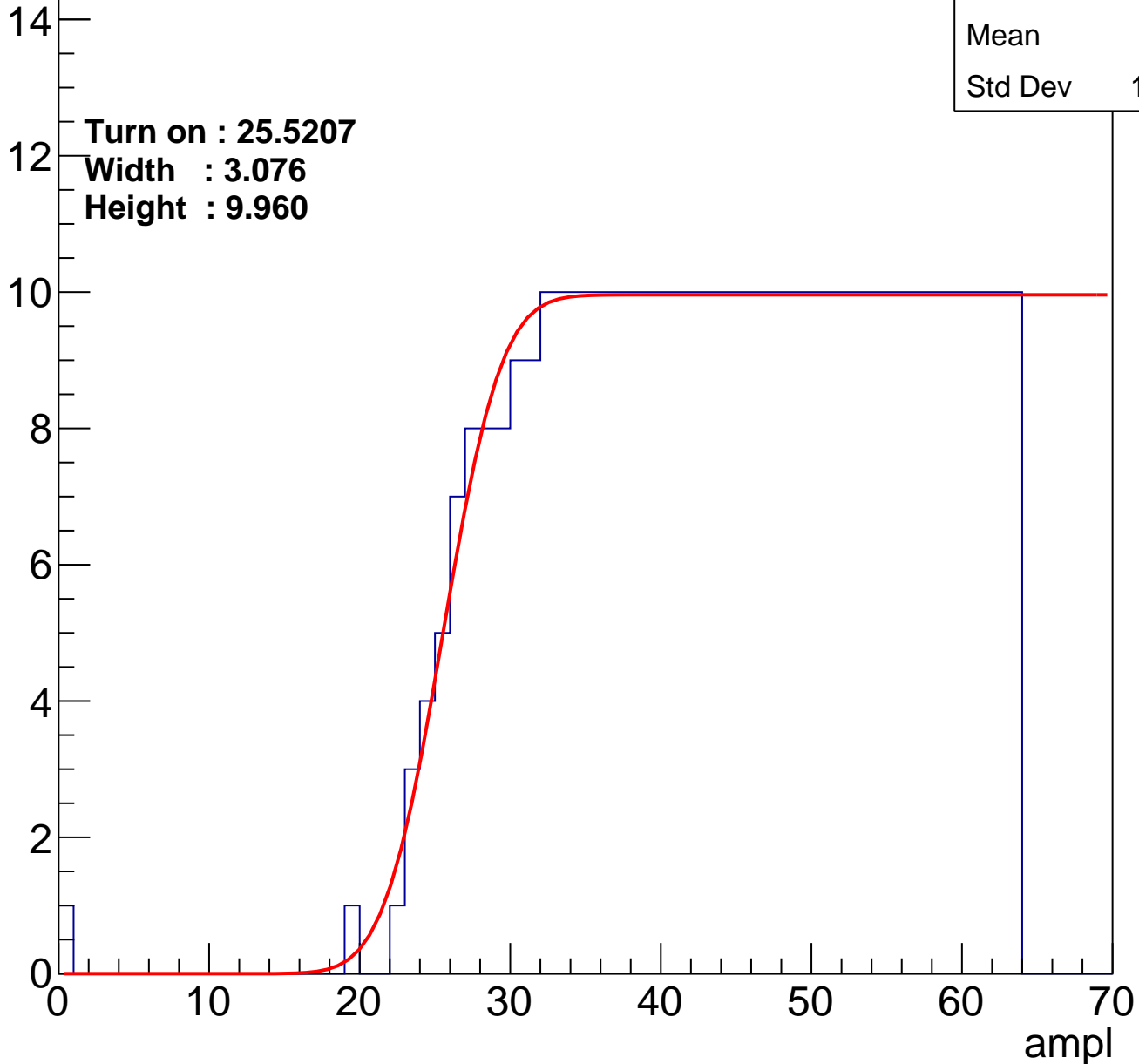
Entries	384
Mean	44.1
Std Dev	11.49

Turn on : 25.5207

Width : 3.076

Height : 9.960

Entry



B1L100S, U14-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.46
Std Dev	12.06

Turn on : 24.5163

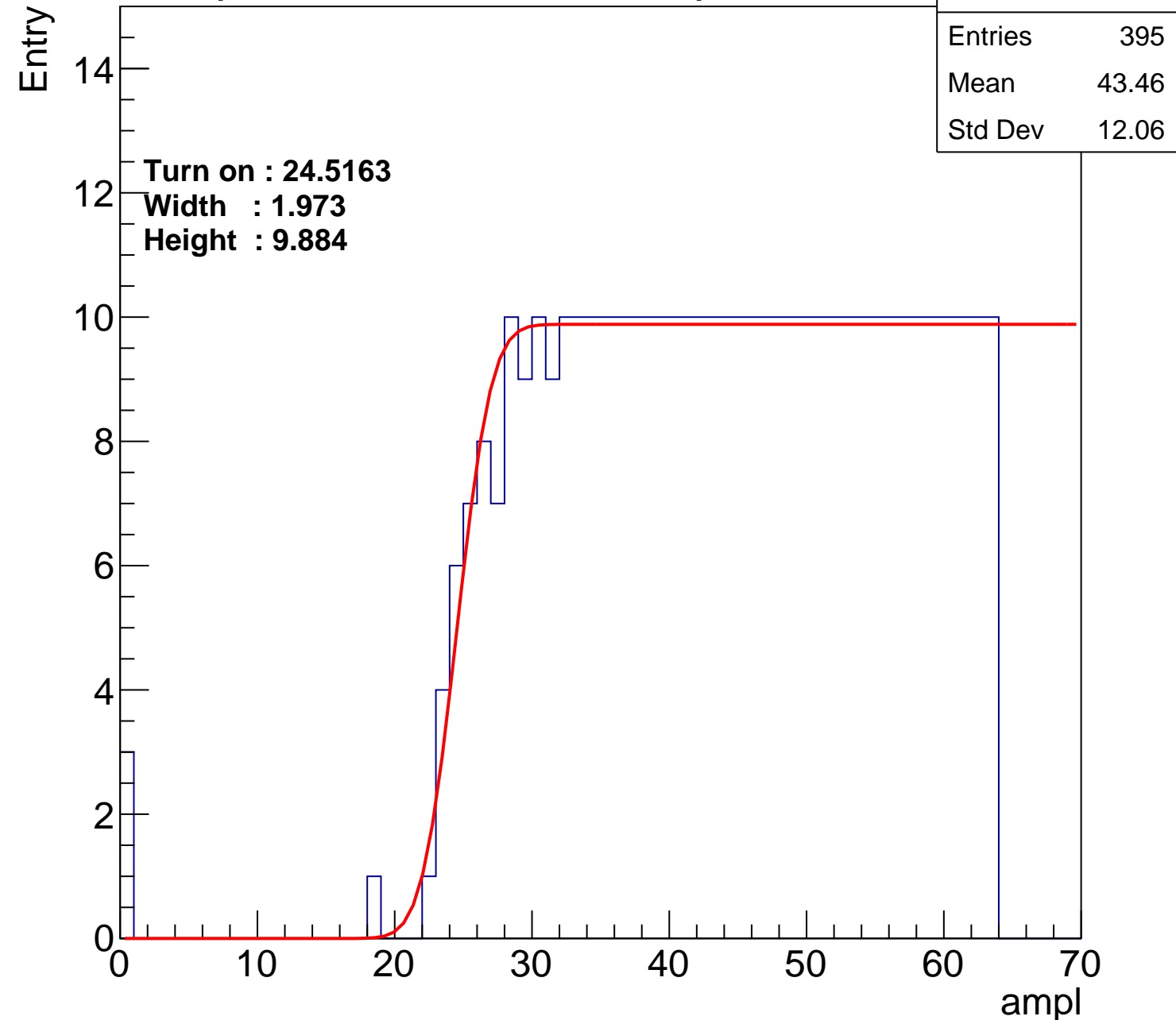
Width : 1.973

Height : 9.884

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.72
Std Dev	11.81

Turn on : 25.1828

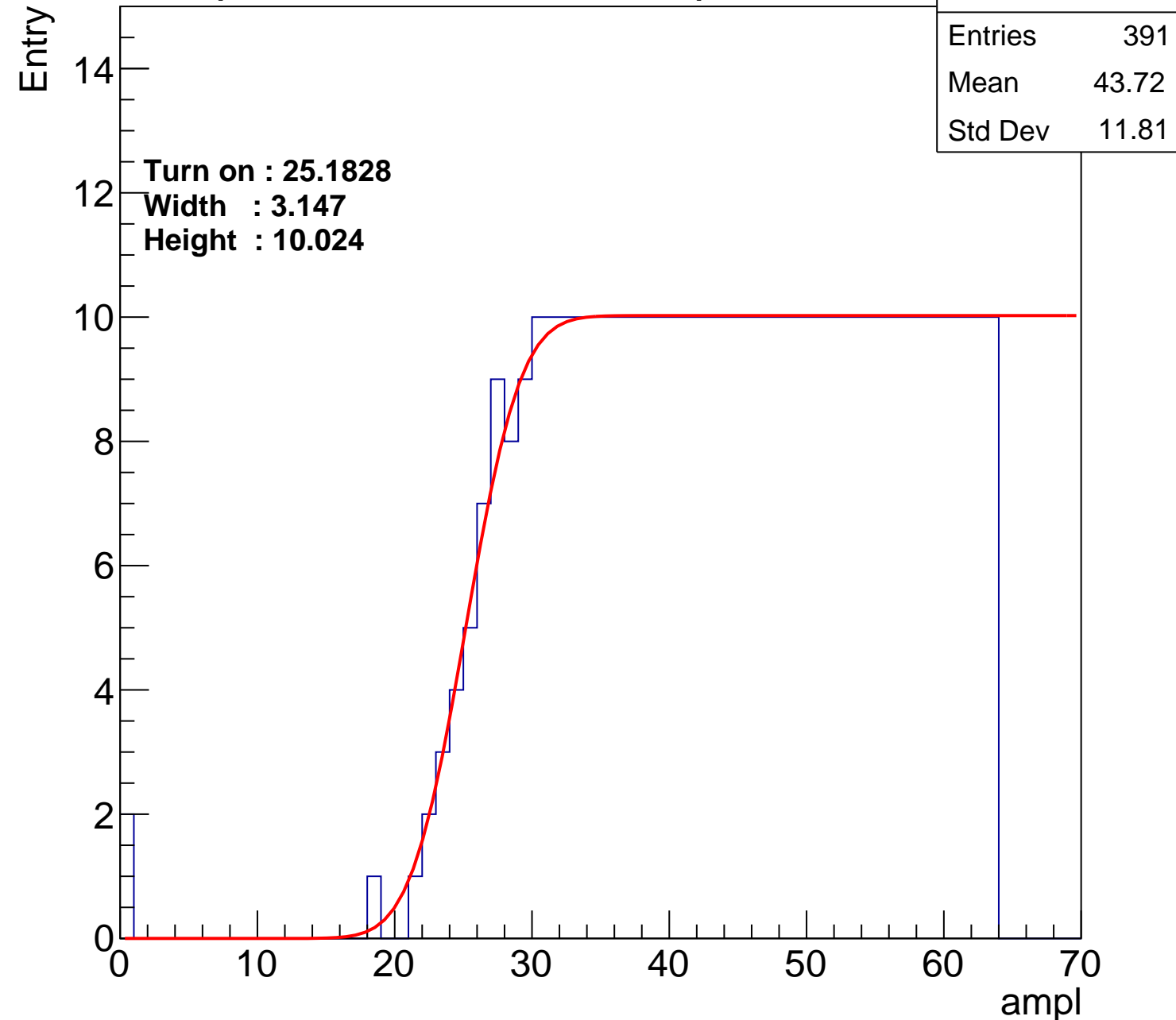
Width : 3.147

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch54

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.34
Std Dev	11.41

Turn on : 26.5755

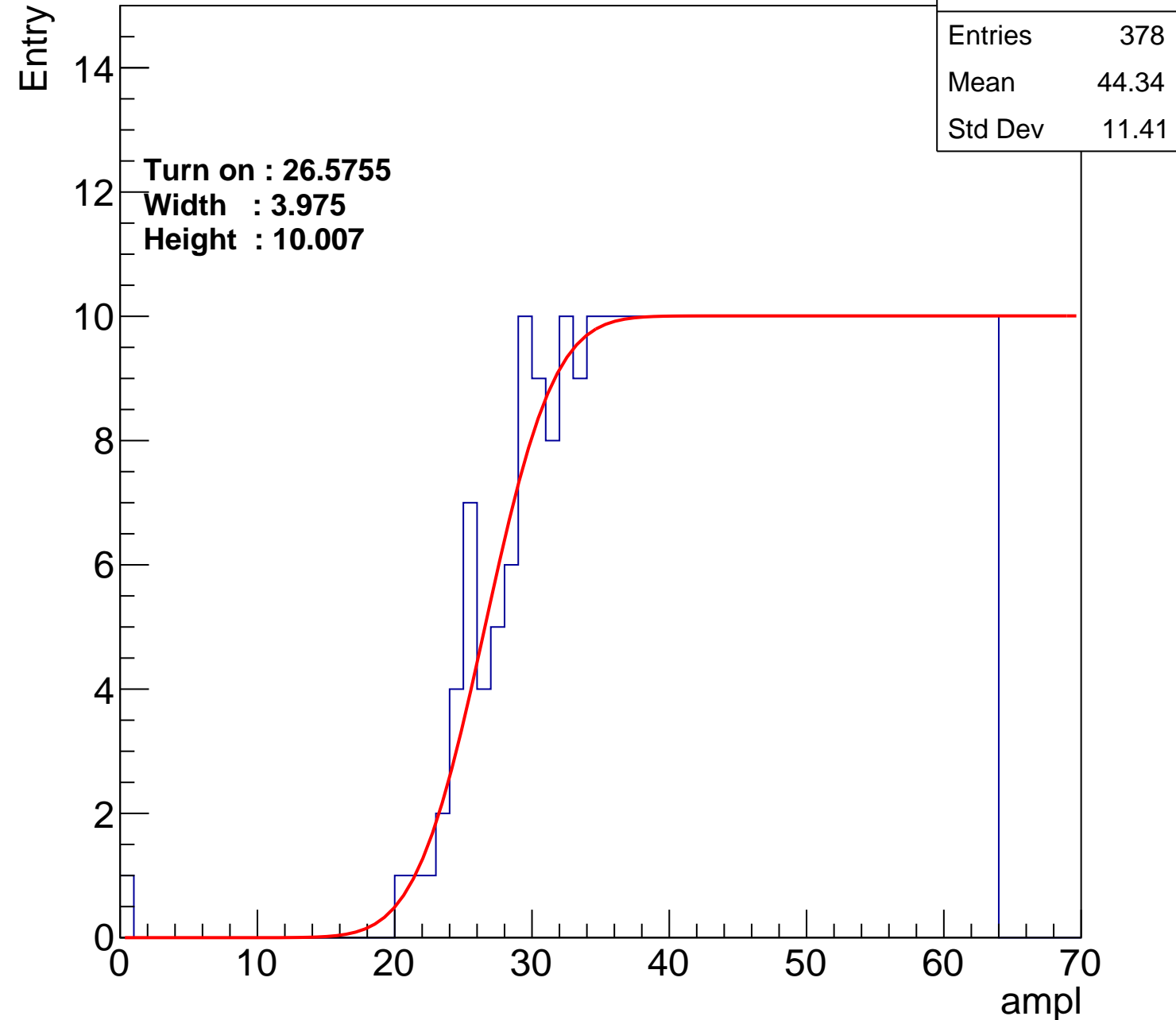
Width : 3.975

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch55

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.05
Std Dev	10.96

Turn on : 28.1098

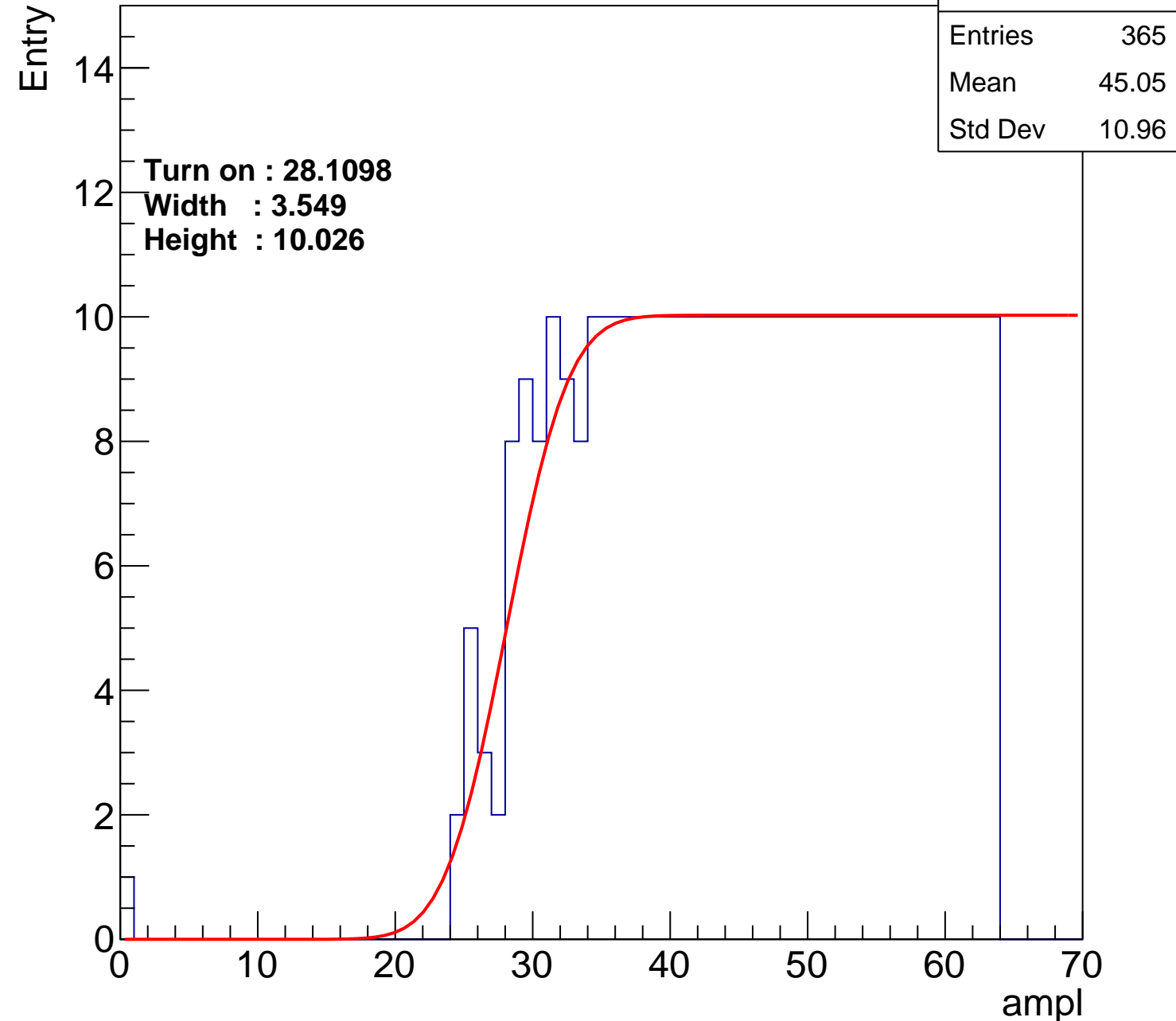
Width : 3.549

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch56

calib_packv5_042523_0143.root, FC#4, port A2

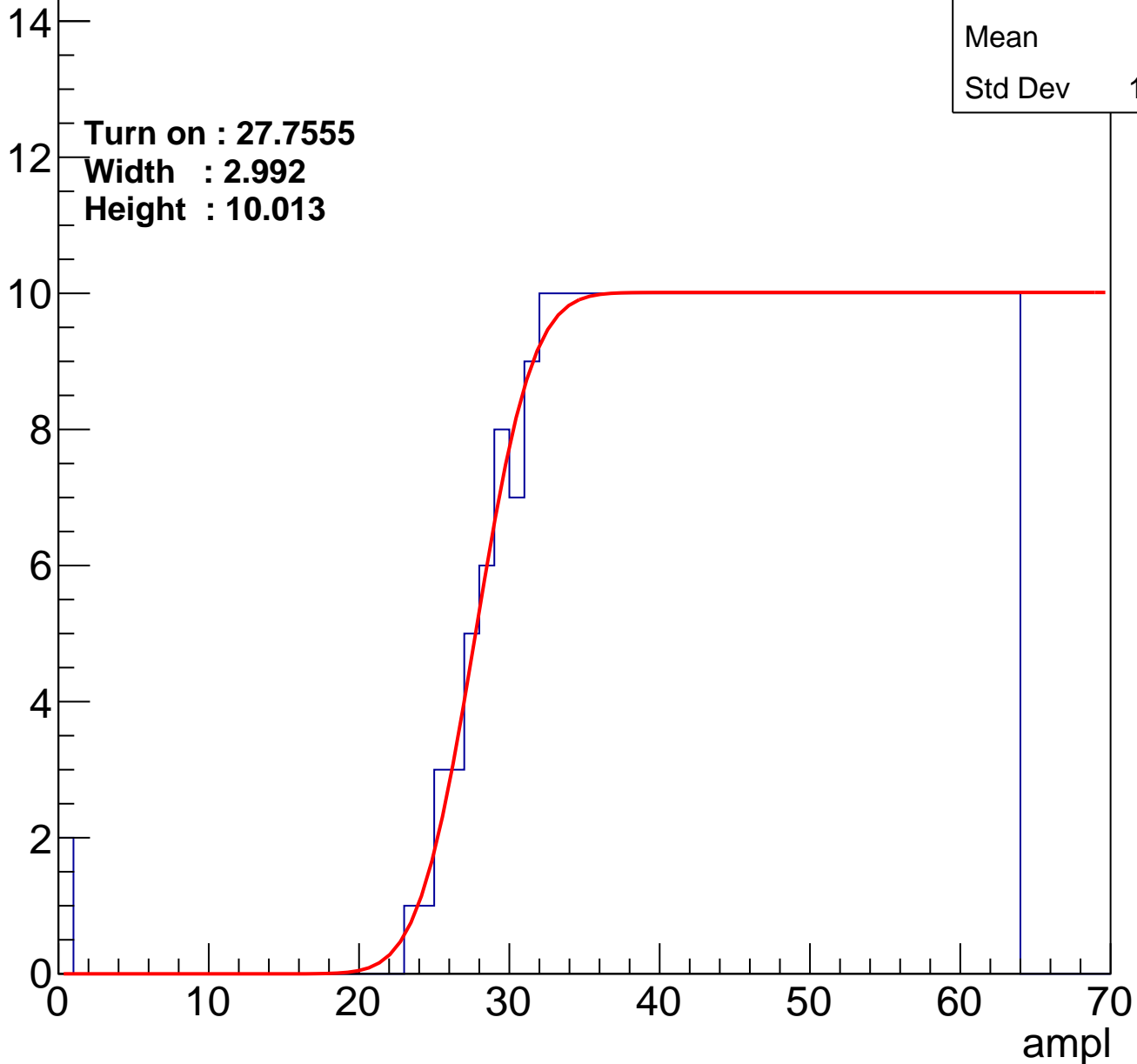
Entries	365
Mean	45
Std Dev	11.14

Turn on : 27.7555

Width : 2.992

Height : 10.013

Entry



B1L100S, U14-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.04
Std Dev	11.12

Turn on : 28.3410

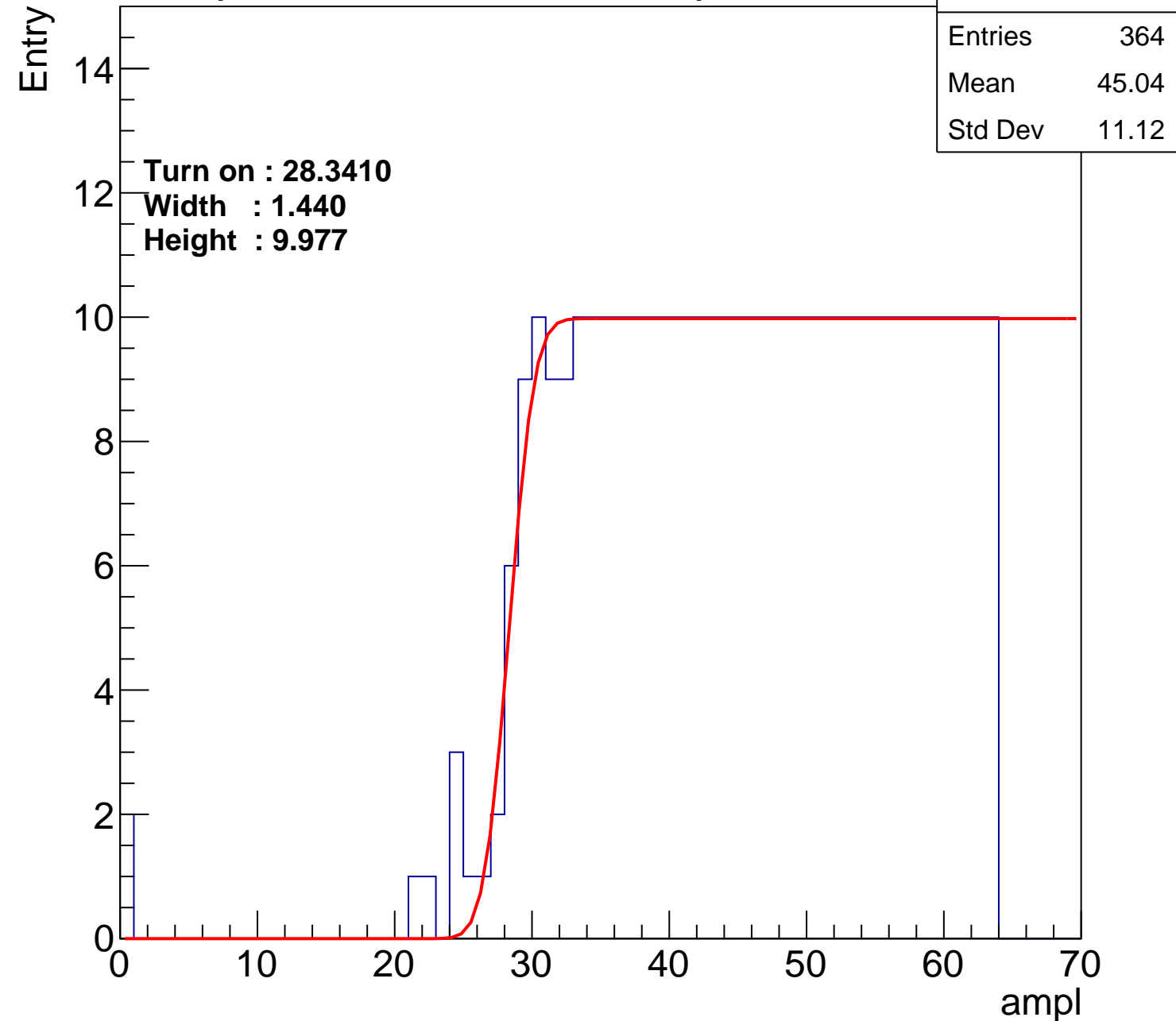
Width : 1.440

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch58

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.69
Std Dev	12.13

Turn on : 25.8997

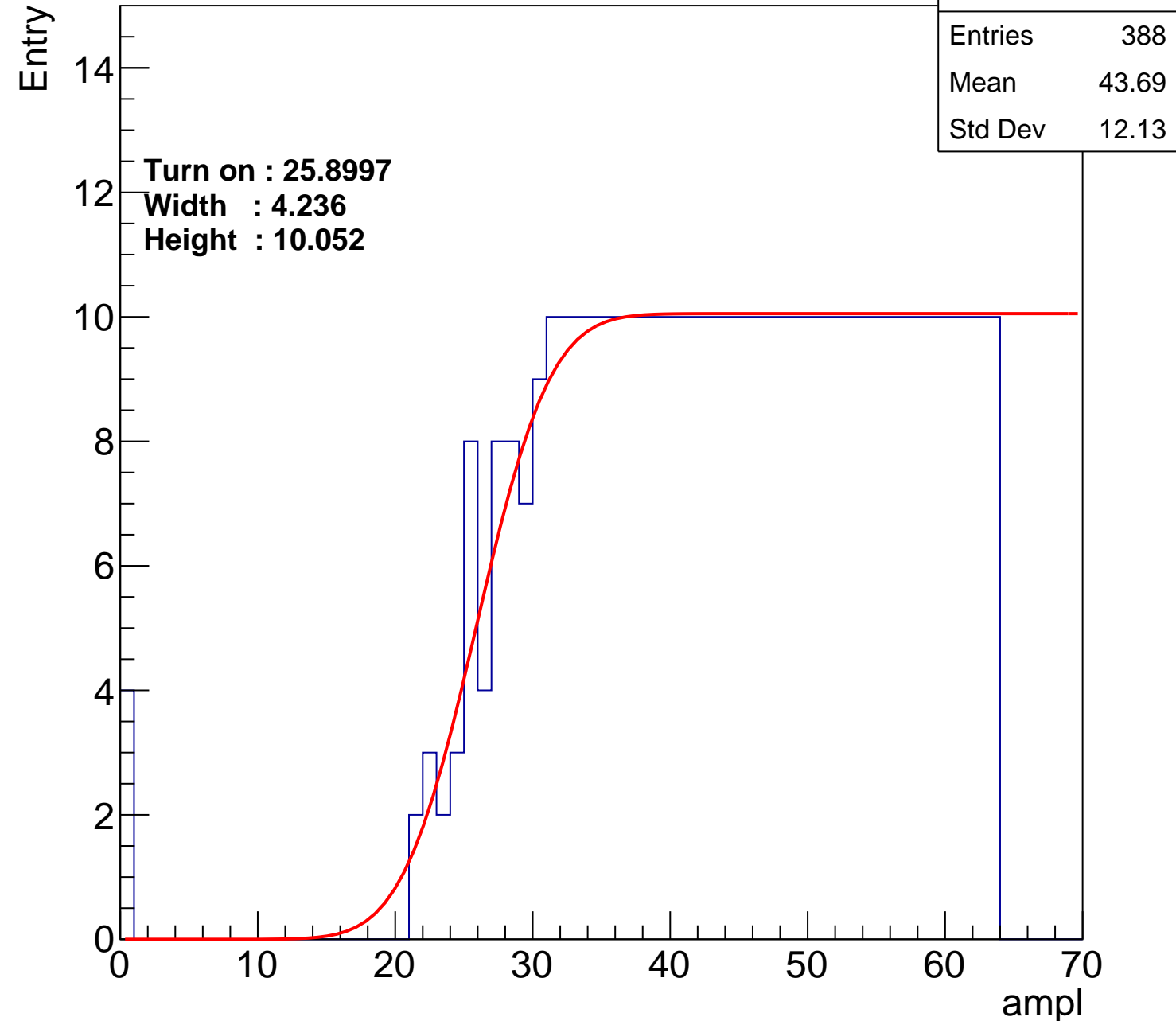
Width : 4.236

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch59

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.76
Std Dev	12.32

Turn on : 26.1168

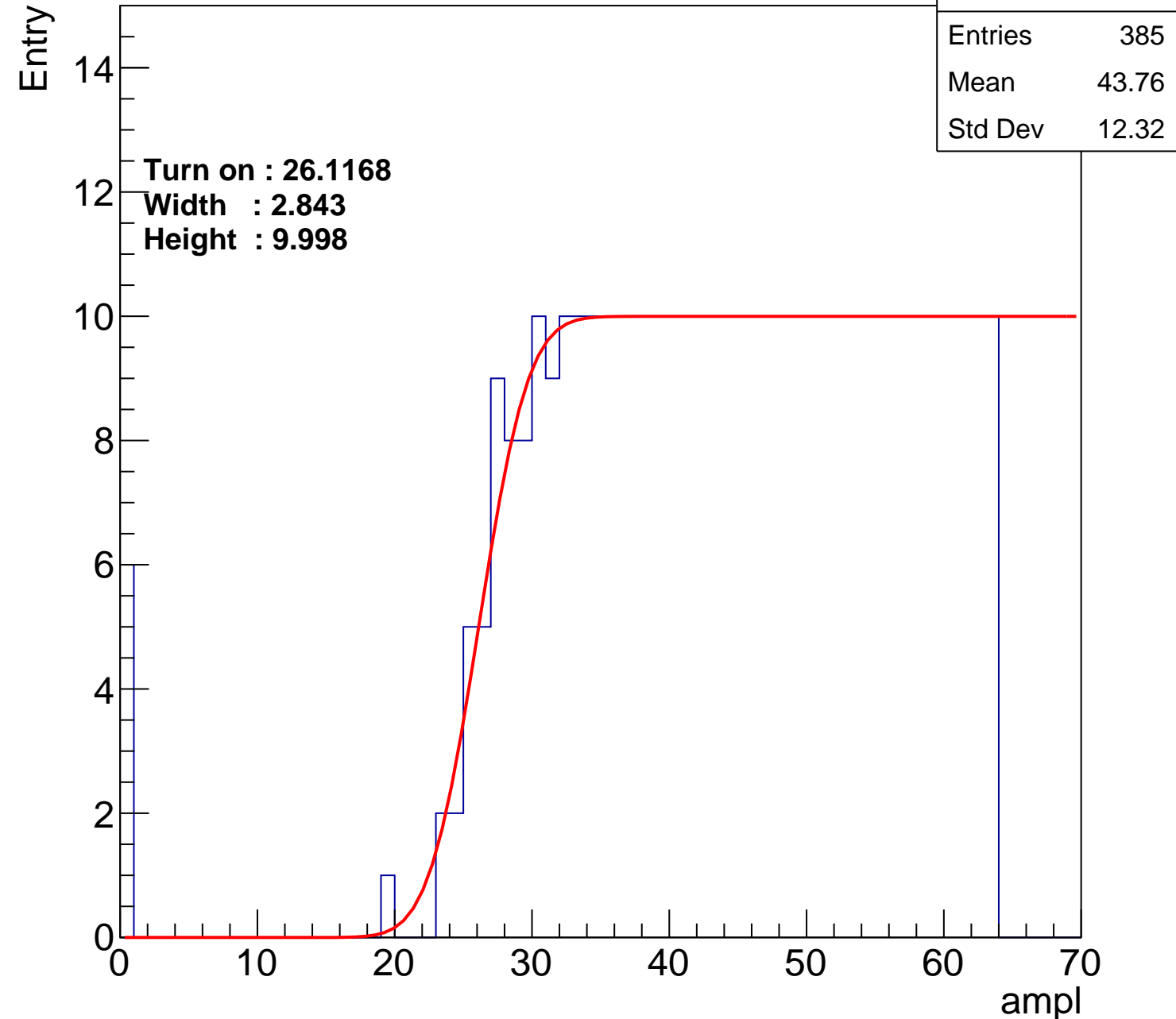
Width : 2.843

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch60

calib_packv5_042523_0143.root, FC#4, port A2

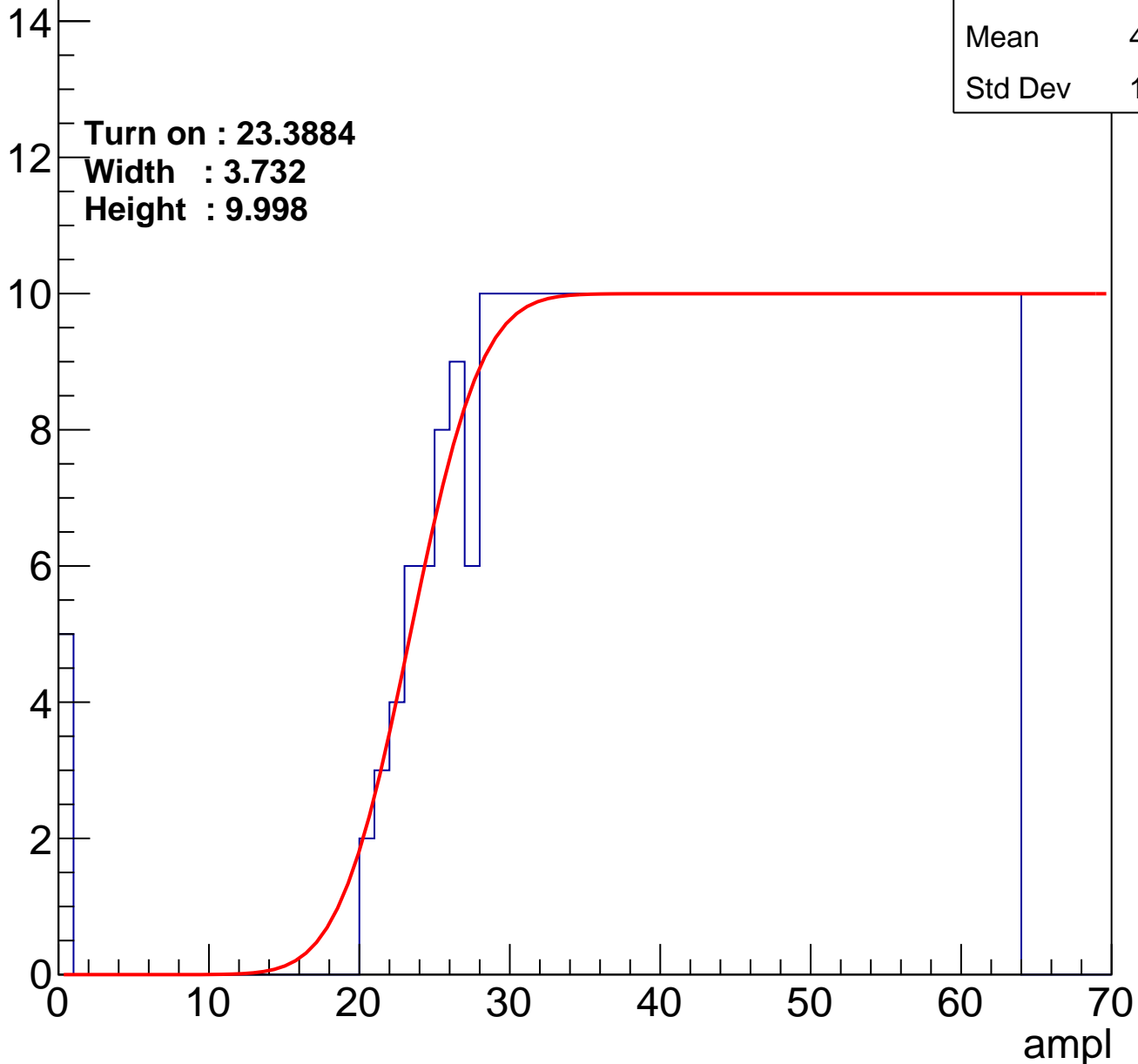
Entries	409
Mean	42.66
Std Dev	12.69

Turn on : 23.3884

Width : 3.732

Height : 9.998

Entry



B1L100S, U14-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.88
Std Dev	11.69

Turn on : 25.1375

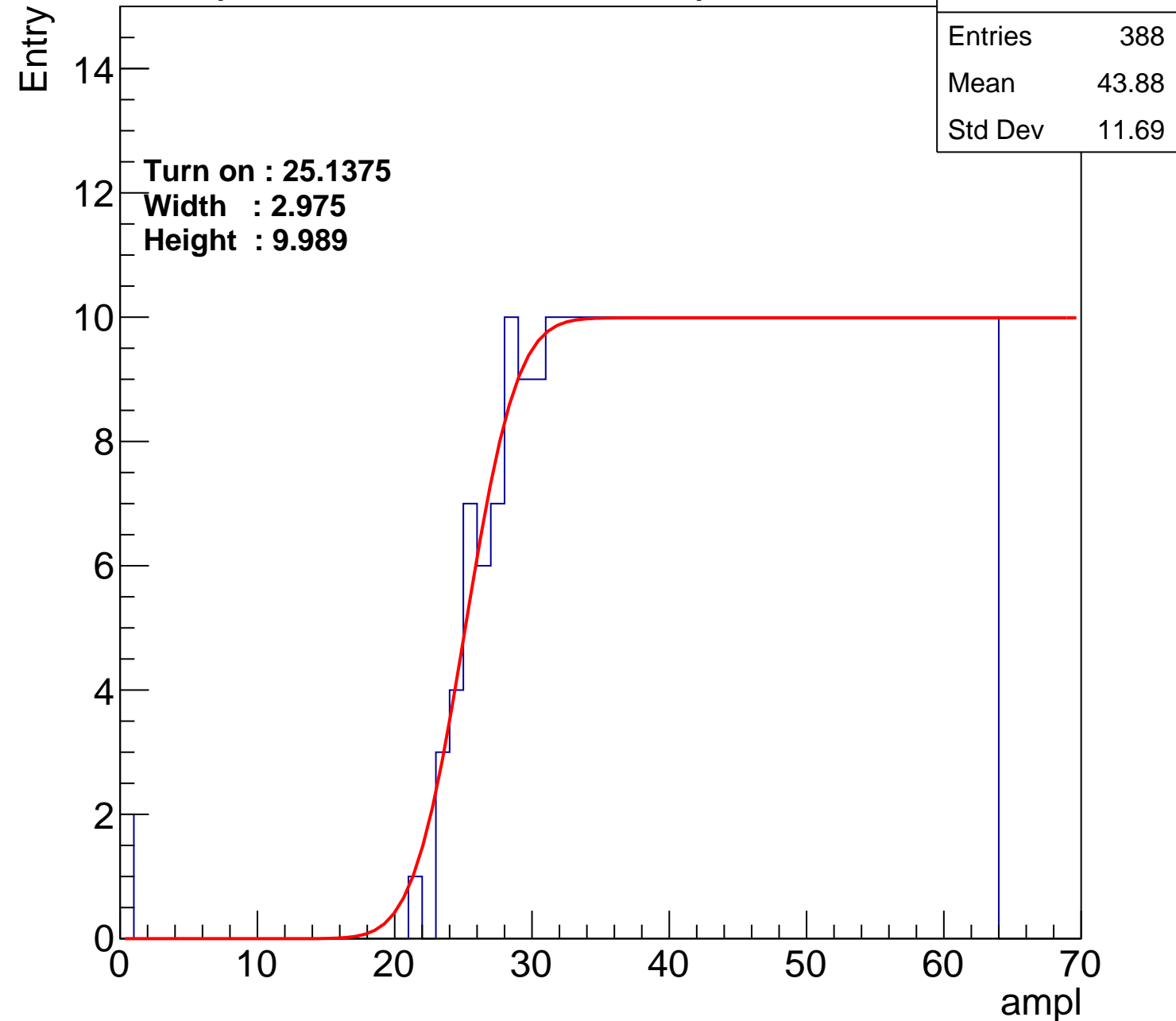
Width : 2.975

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch62

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.77
Std Dev	11.23

Turn on : 27.0828

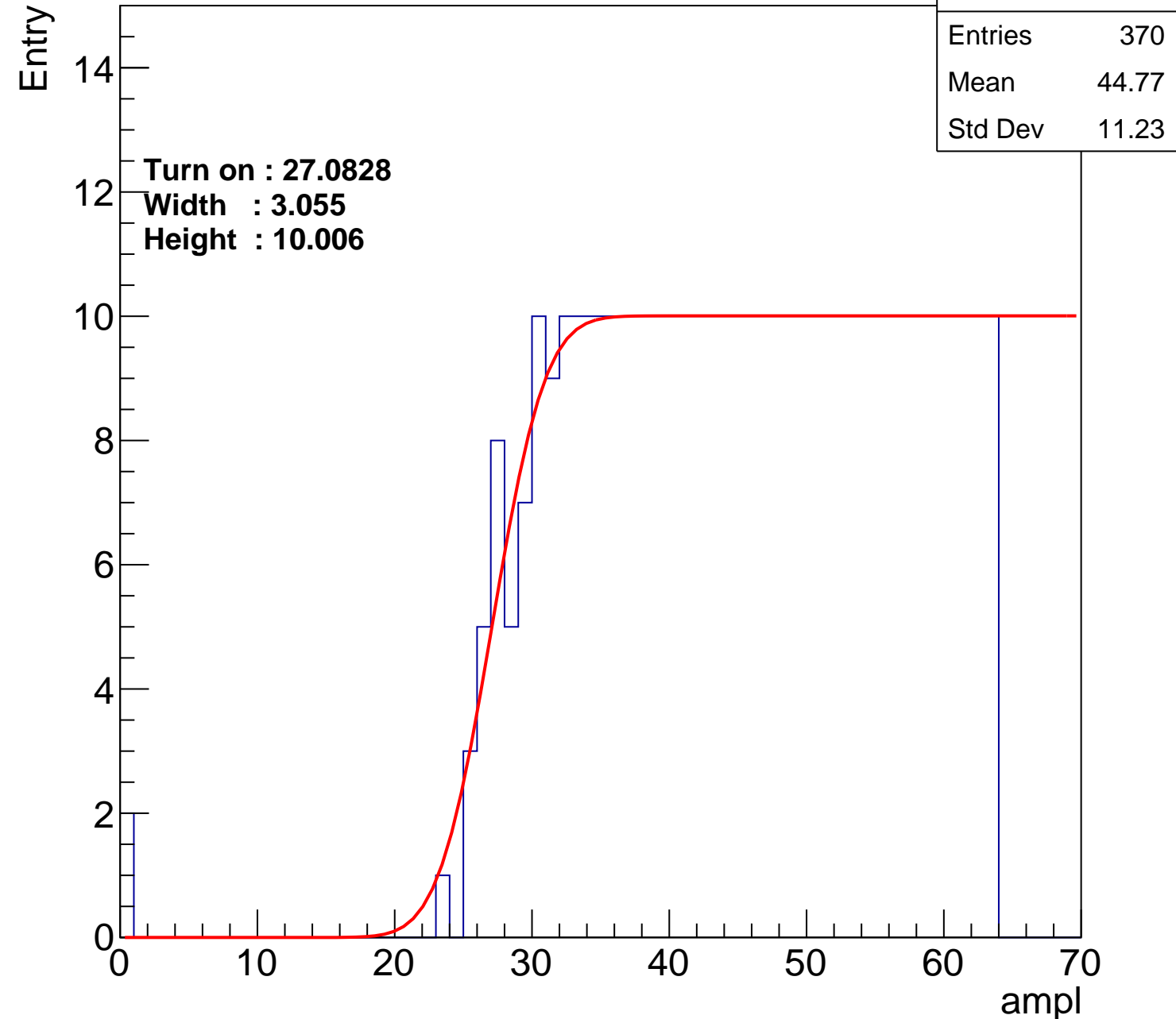
Width : 3.055

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl

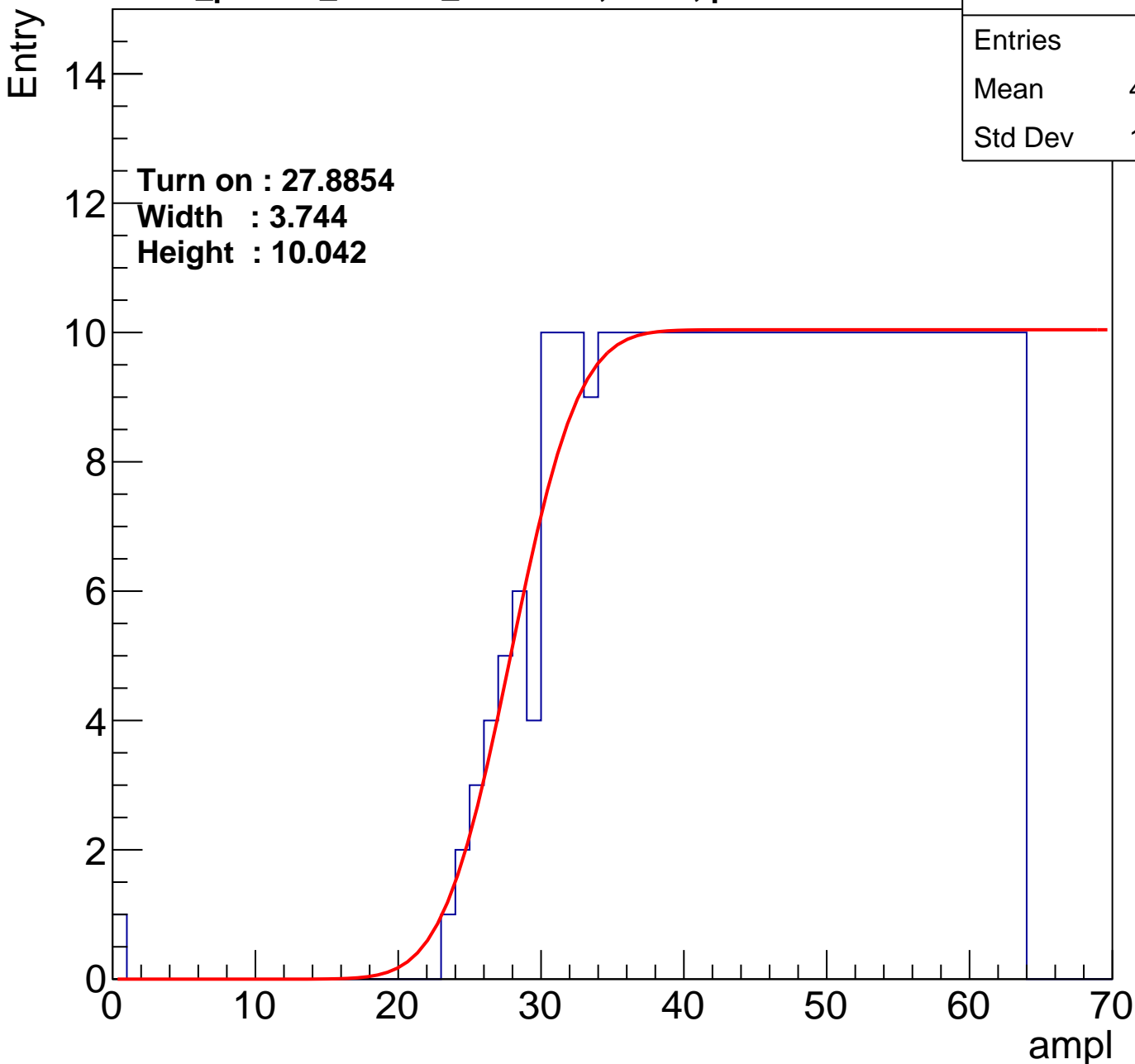


calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.06
Std Dev	10.95

Height : 10.042



B1L100S, U14-ch64

calib_packv5_042523_0143.root, FC#4, port A2

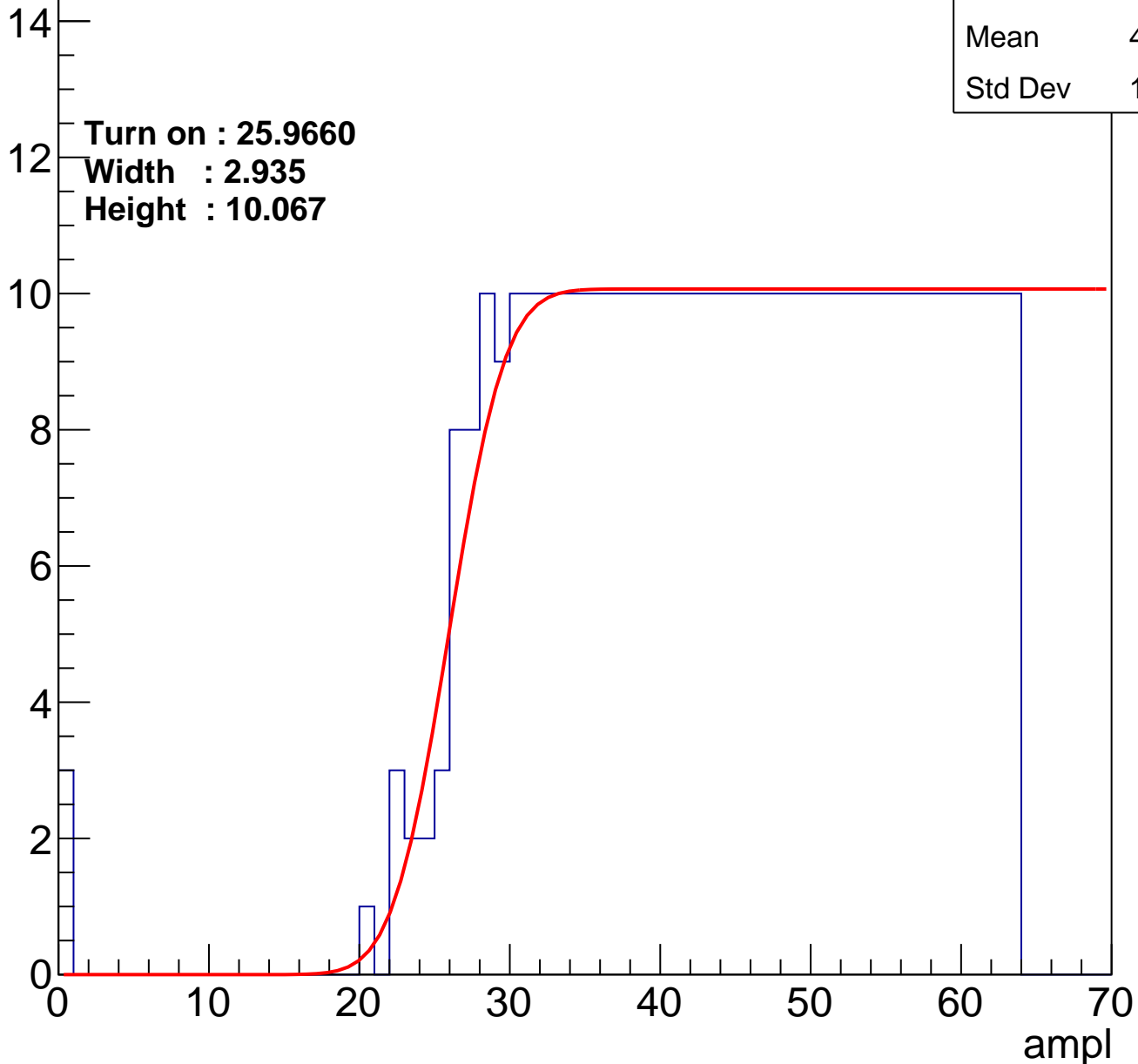
Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.9660

Width : 2.935

Height : 10.067

Entry



B1L100S, U14-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.82
Std Dev	11.64

Turn on : 26.2972

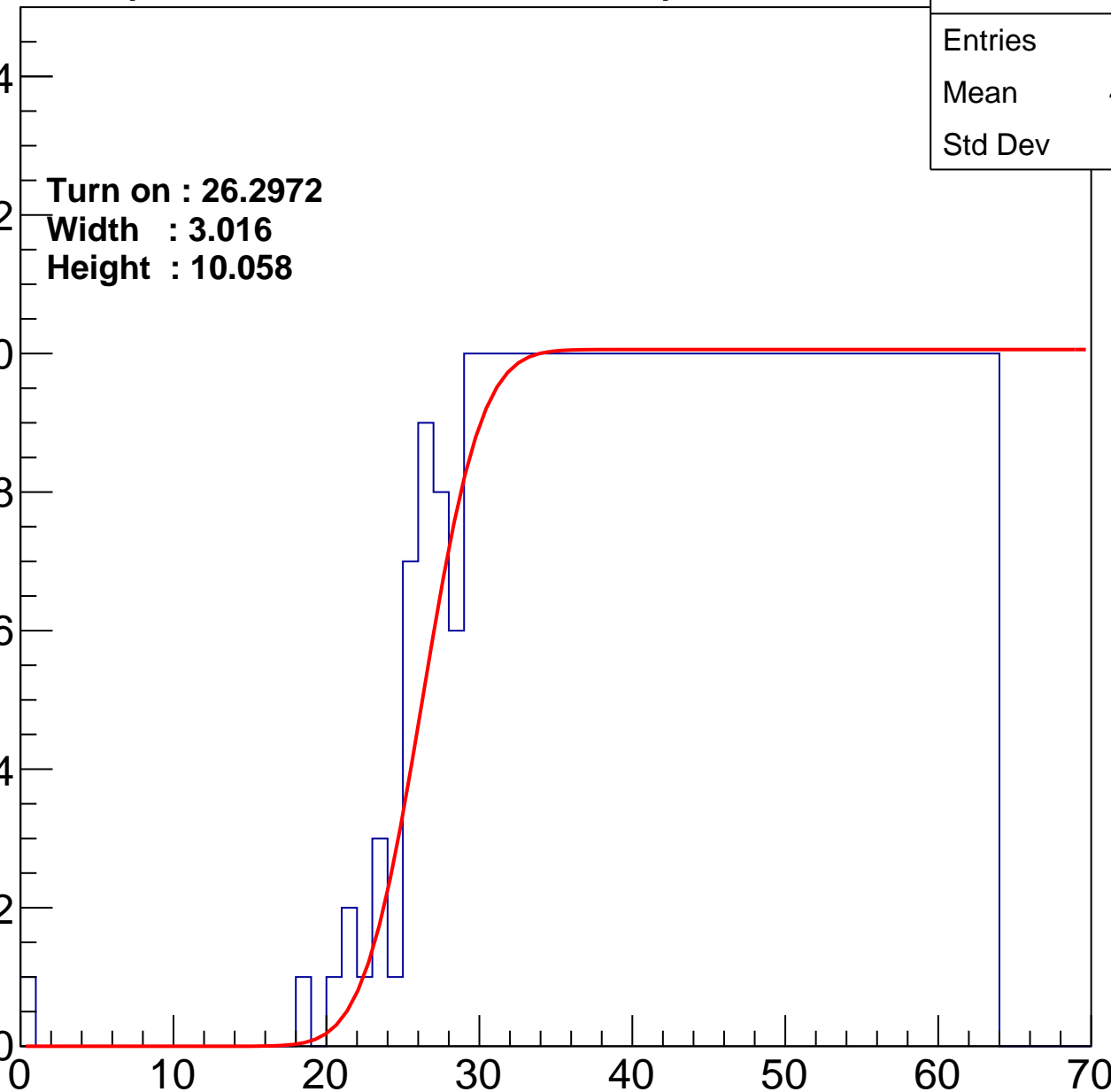
Width : 3.016

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch66

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.74
Std Dev	11.83

Turn on : 25.8710

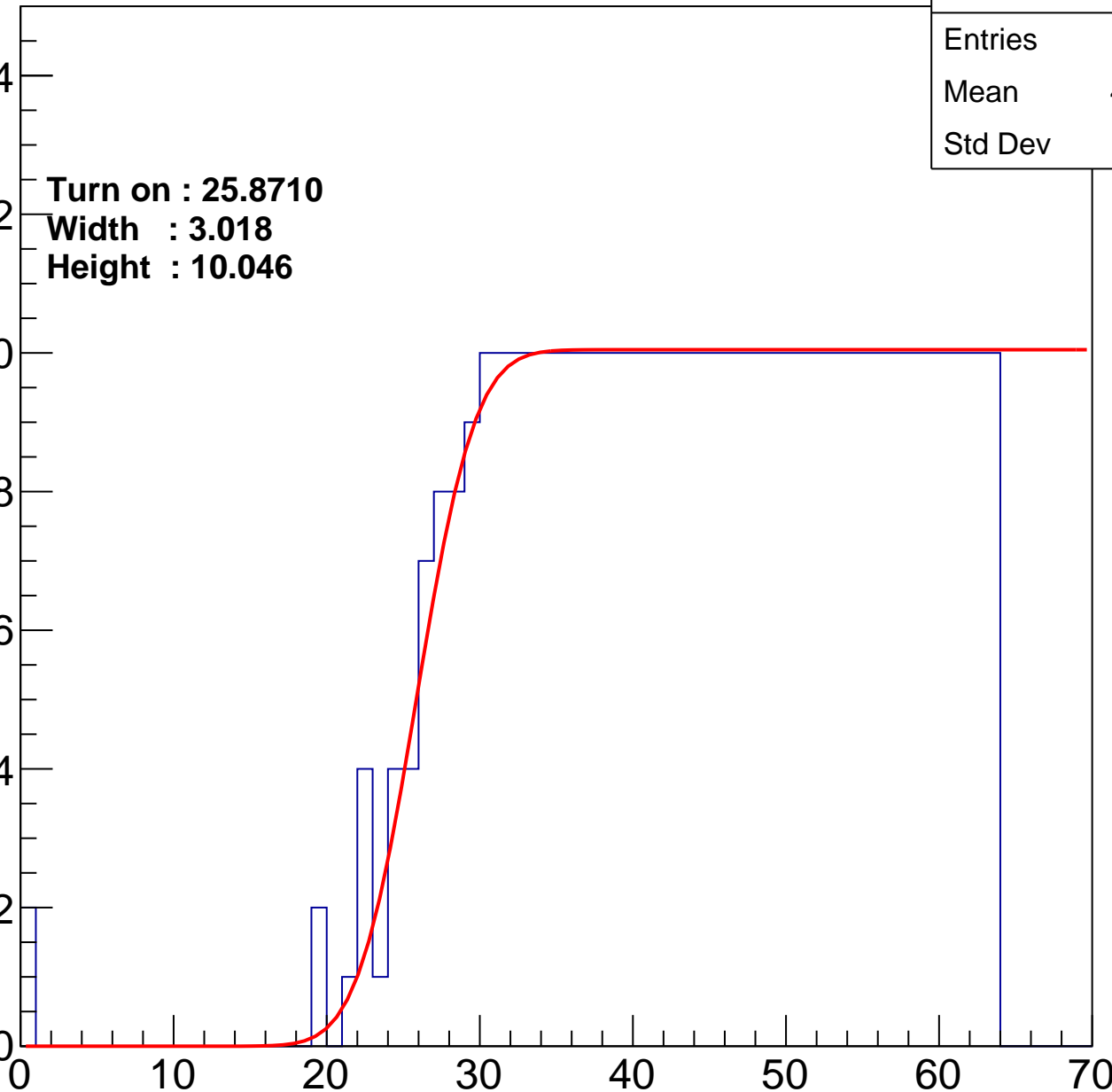
Width : 3.018

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch67

calib_packv5_042523_0143.root, FC#4, port A2

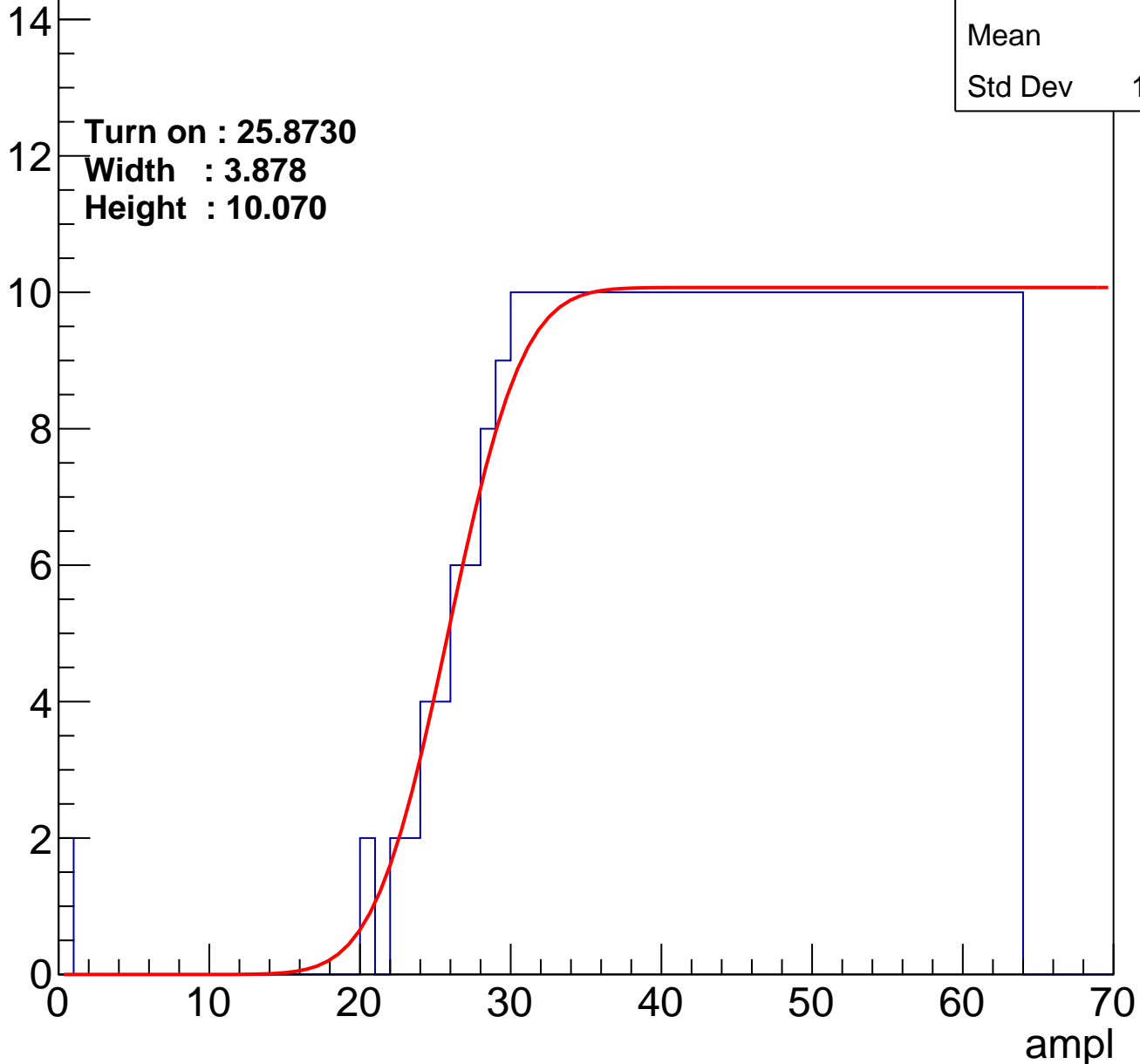
Entries	385
Mean	44
Std Dev	11.68

Turn on : 25.8730

Width : 3.878

Height : 10.070

Entry



B1L100S, U14-ch68

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.71
Std Dev	11.87

Turn on : 26.1176

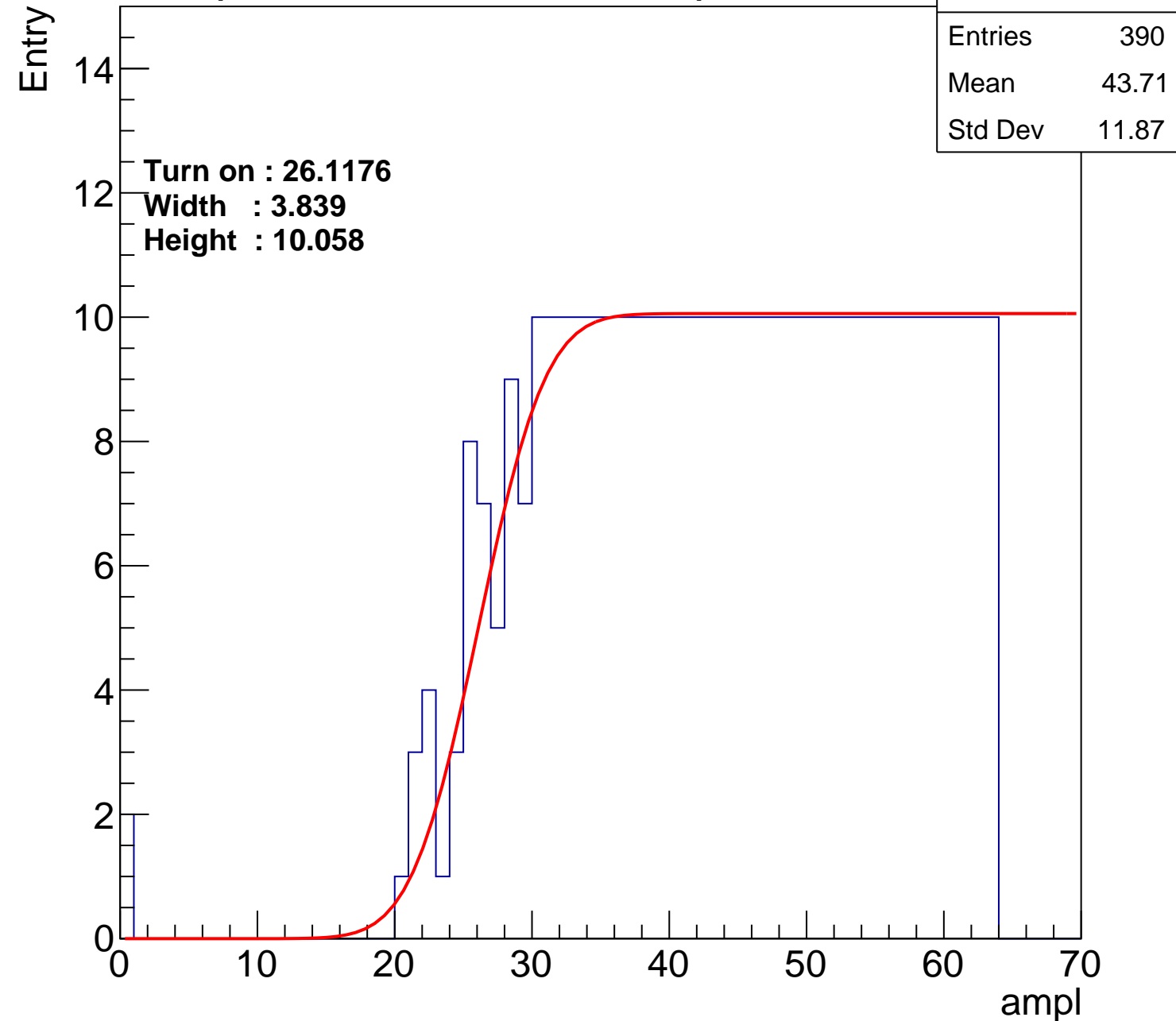
Width : 3.839

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch69

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.45
Std Dev	11.25

Turn on : 26.0973

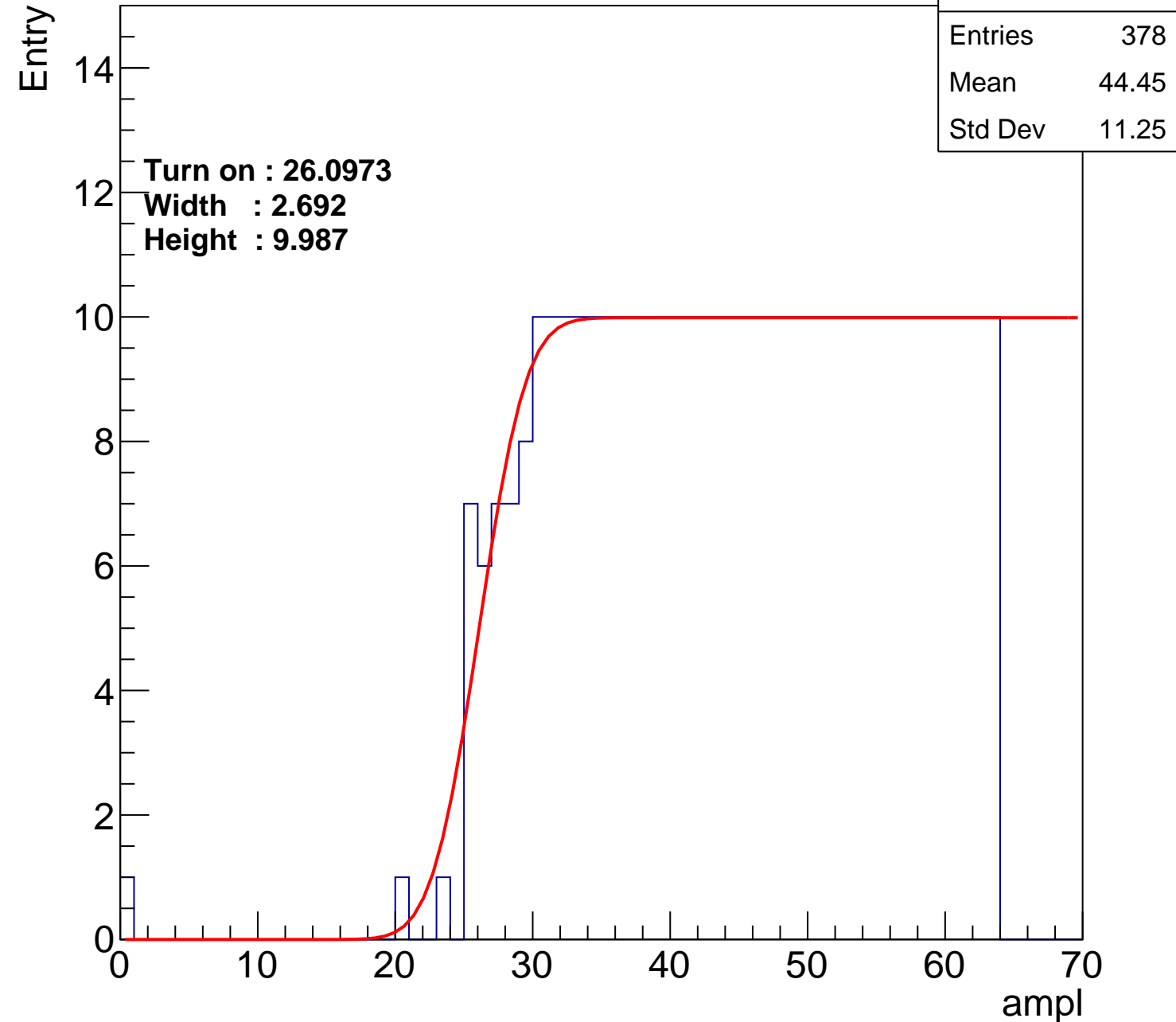
Width : 2.692

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch70

calib_packv5_042523_0143.root, FC#4, port A2

Entries	407
Mean	42.85
Std Dev	12.49

Turn on : 23.7701

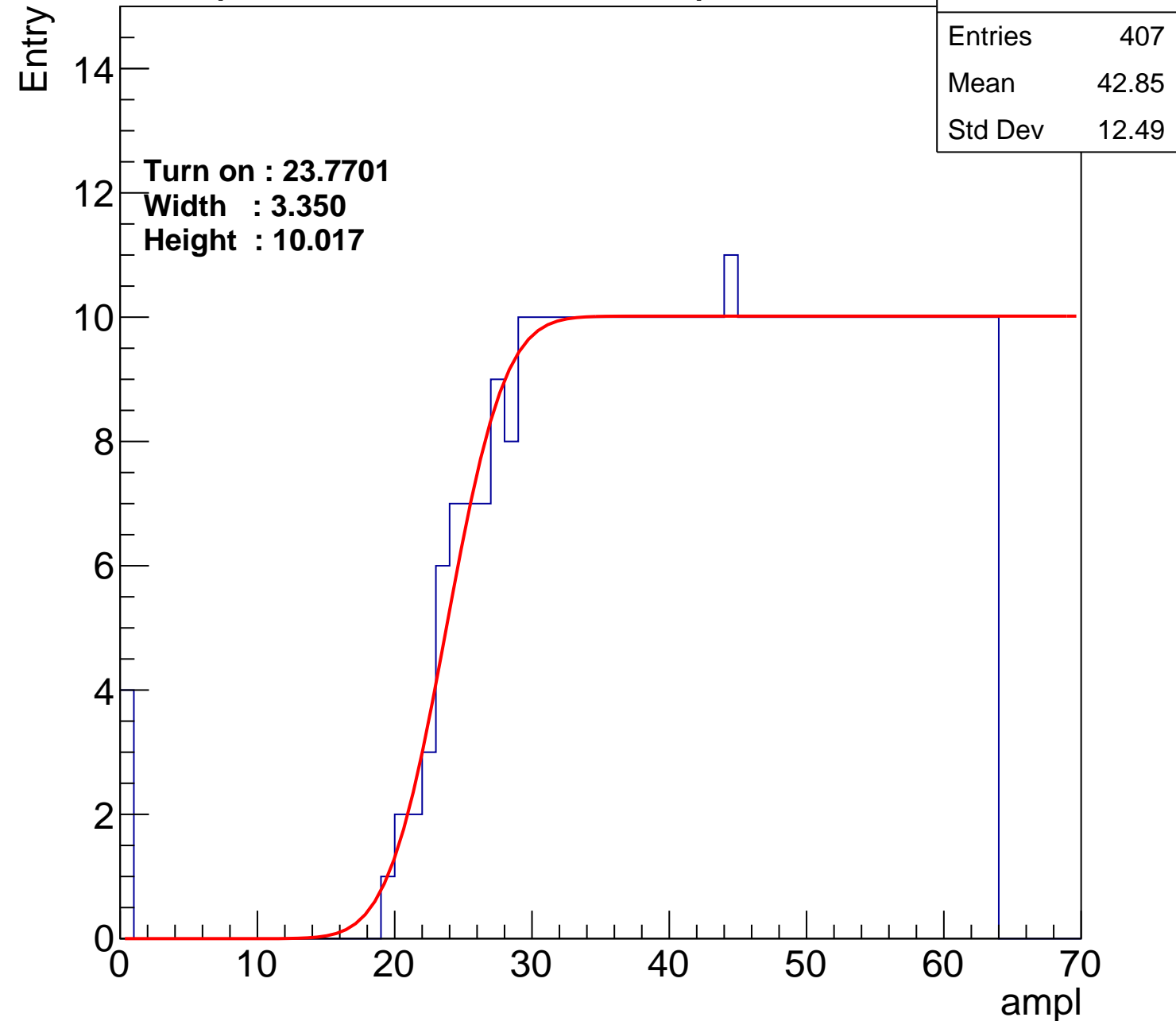
Width : 3.350

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch71

calib_packv5_042523_0143.root, FC#4, port A2

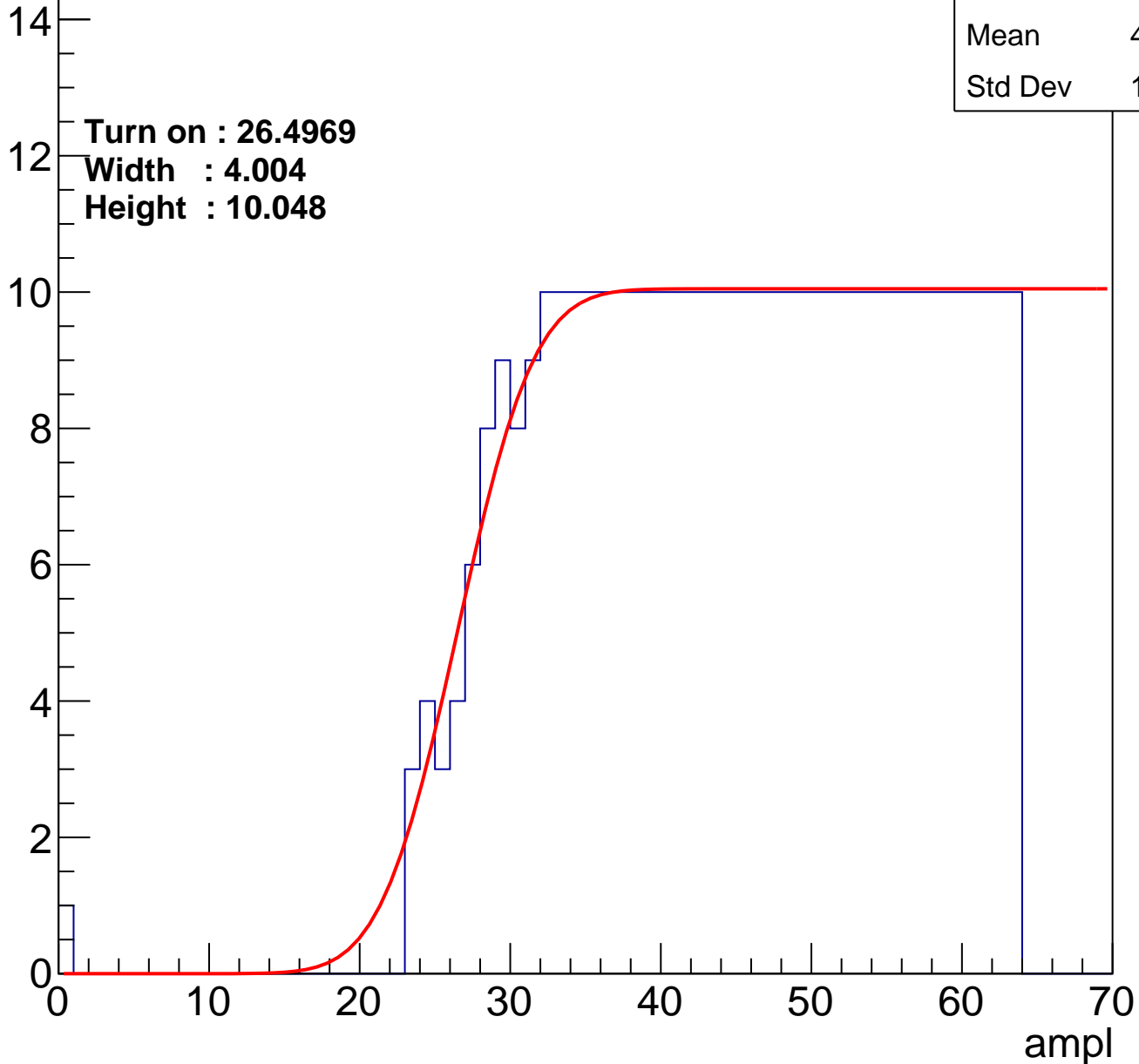
Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.4969

Width : 4.004

Height : 10.048

Entry



B1L100S, U14-ch72

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.36
Std Dev	11.59

Turn on : 26.7867

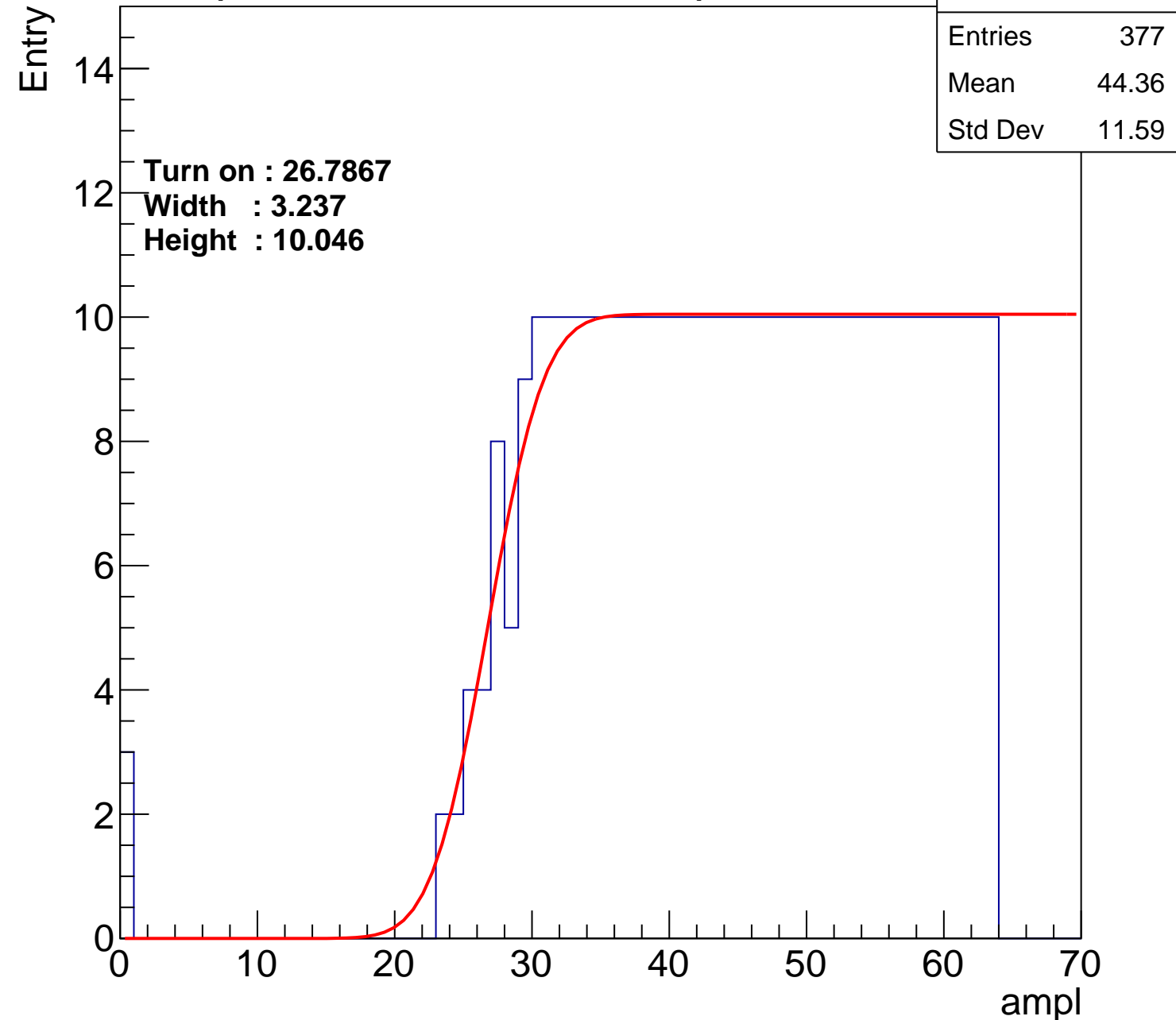
Width : 3.237

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch73

calib_packv5_042523_0143.root, FC#4, port A2

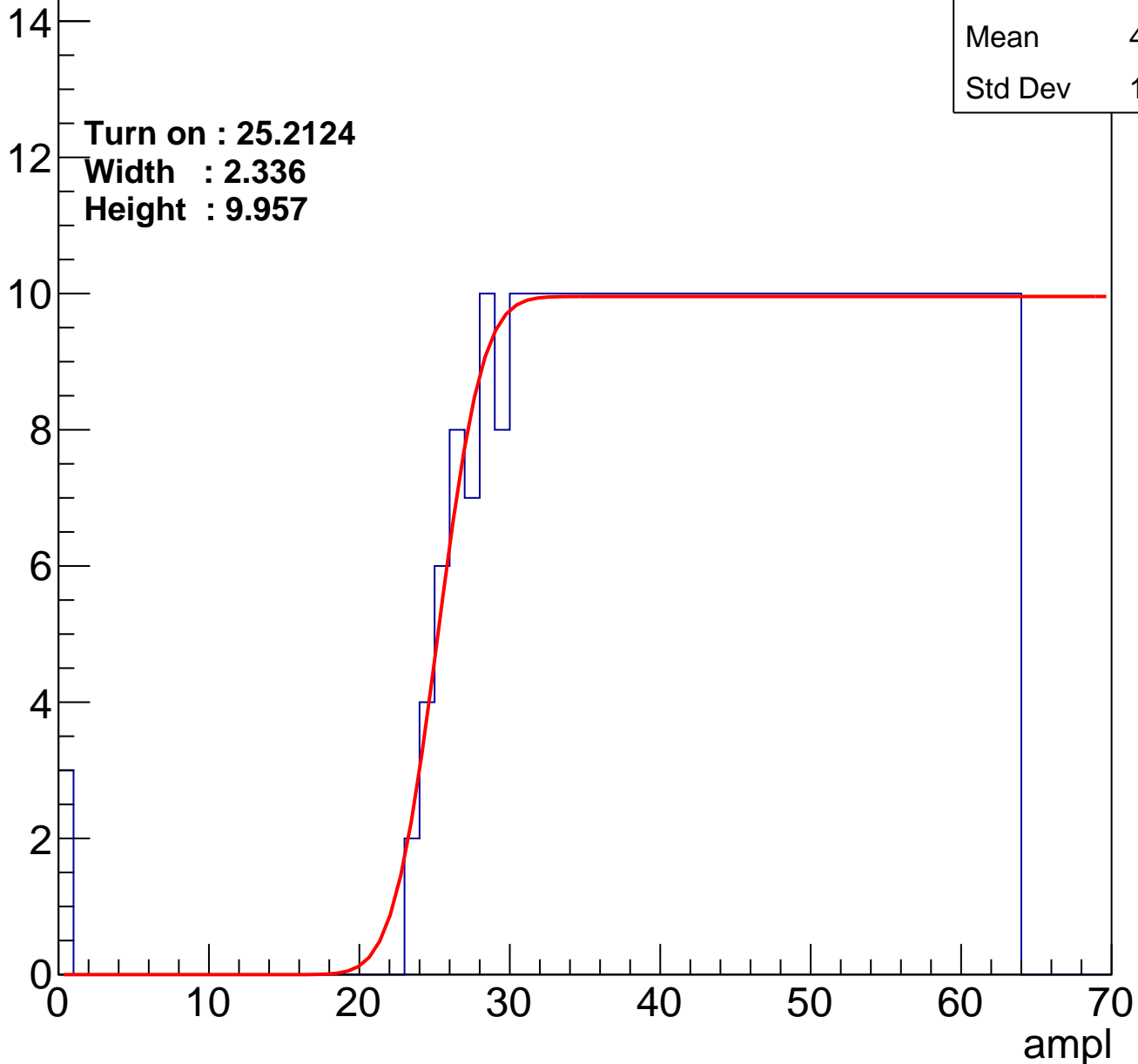
Entries	388
Mean	43.84
Std Dev	11.83

Turn on : 25.2124

Width : 2.336

Height : 9.957

Entry



B1L100S, U14-ch74

calib_packv5_042523_0143.root, FC#4, port A2

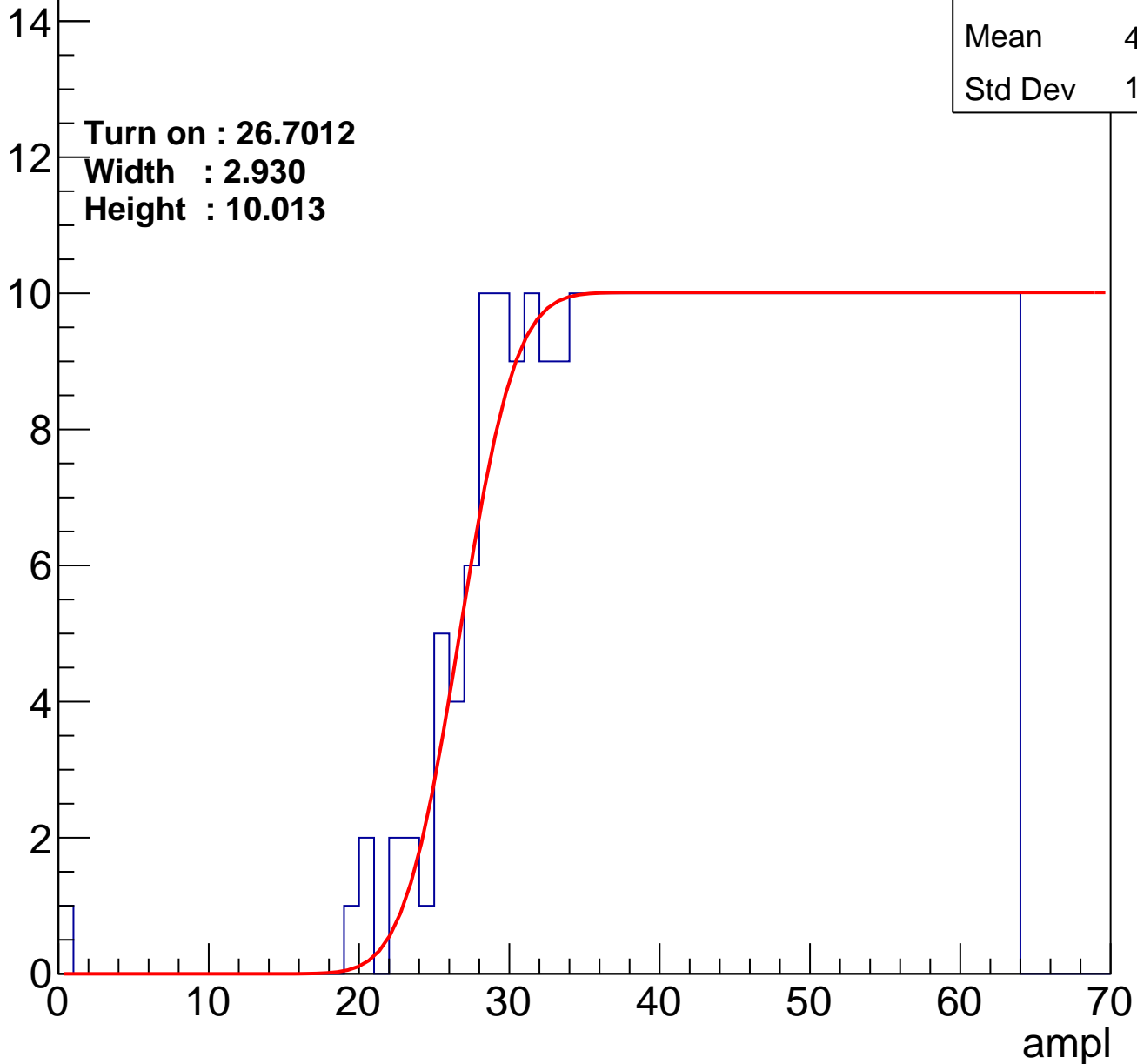
Entries	381
Mean	44.22
Std Dev	11.45

Turn on : 26.7012

Width : 2.930

Height : 10.013

Entry



B1L100S, U14-ch75

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.7648

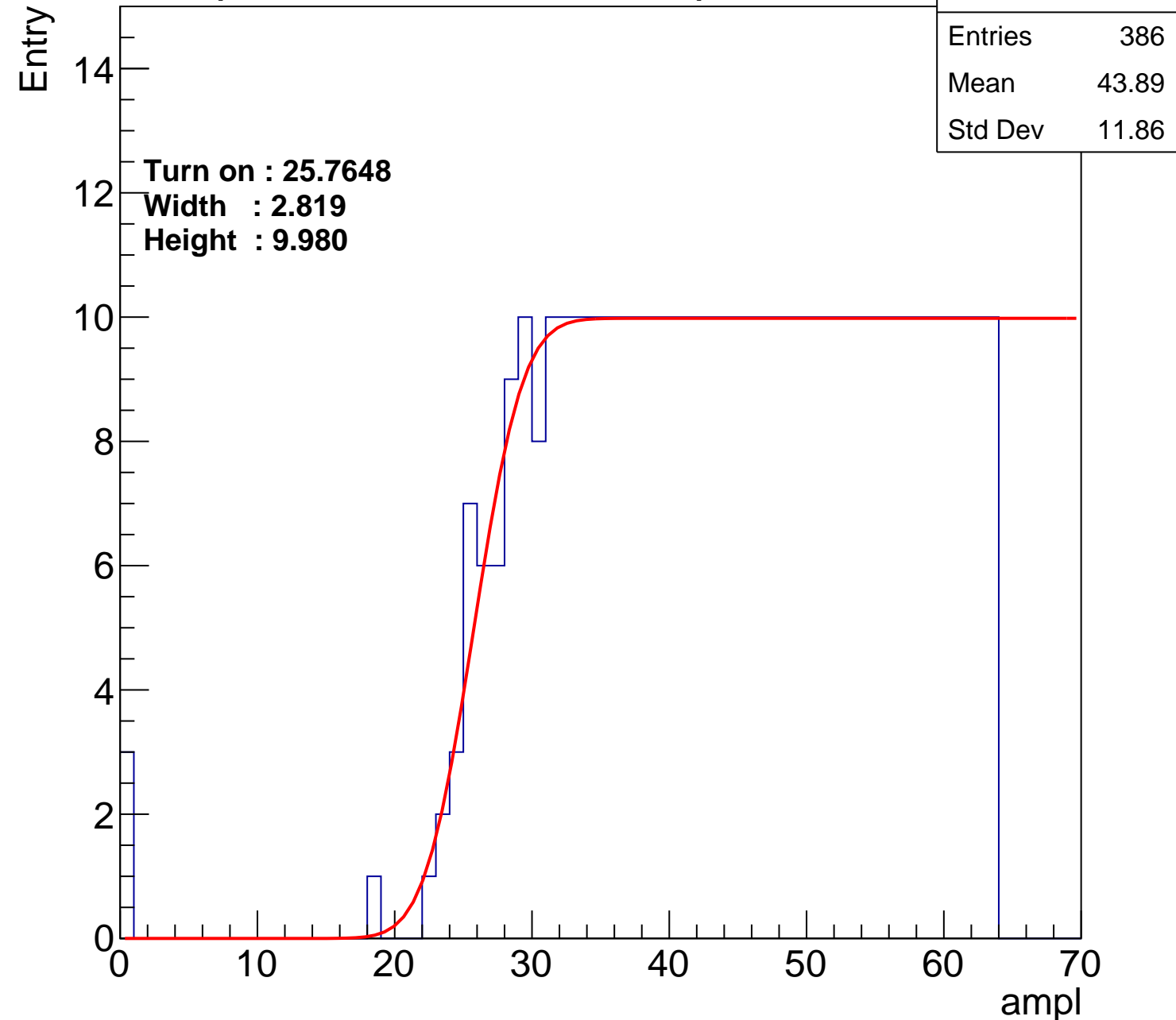
Width : 2.819

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch76

calib_packv5_042523_0143.root, FC#4, port A2

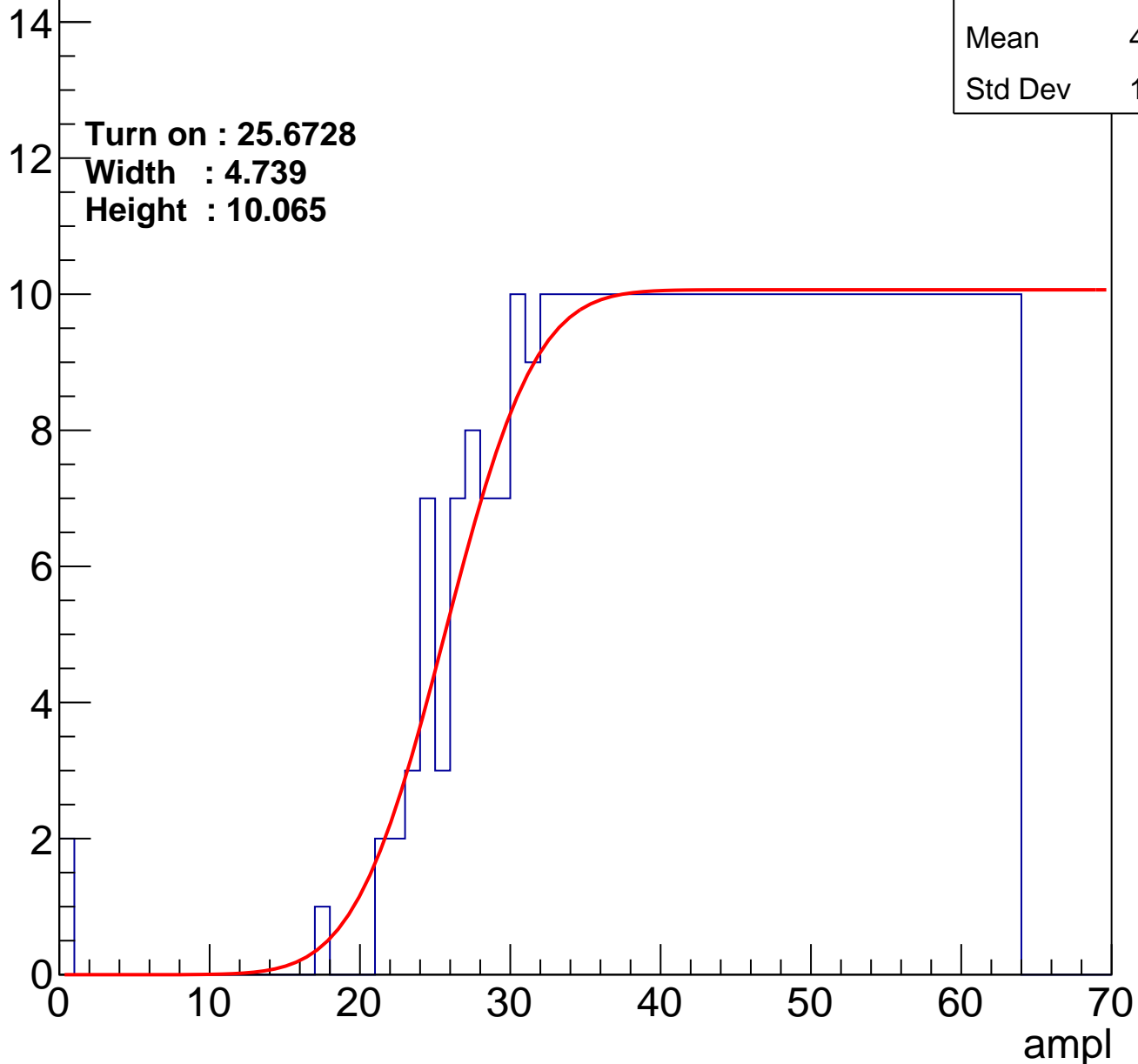
Entries	388
Mean	43.79
Std Dev	11.85

Turn on : 25.6728

Width : 4.739

Height : 10.065

Entry



B1L100S, U14-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.74
Std Dev	11.93

Turn on : 25.7308

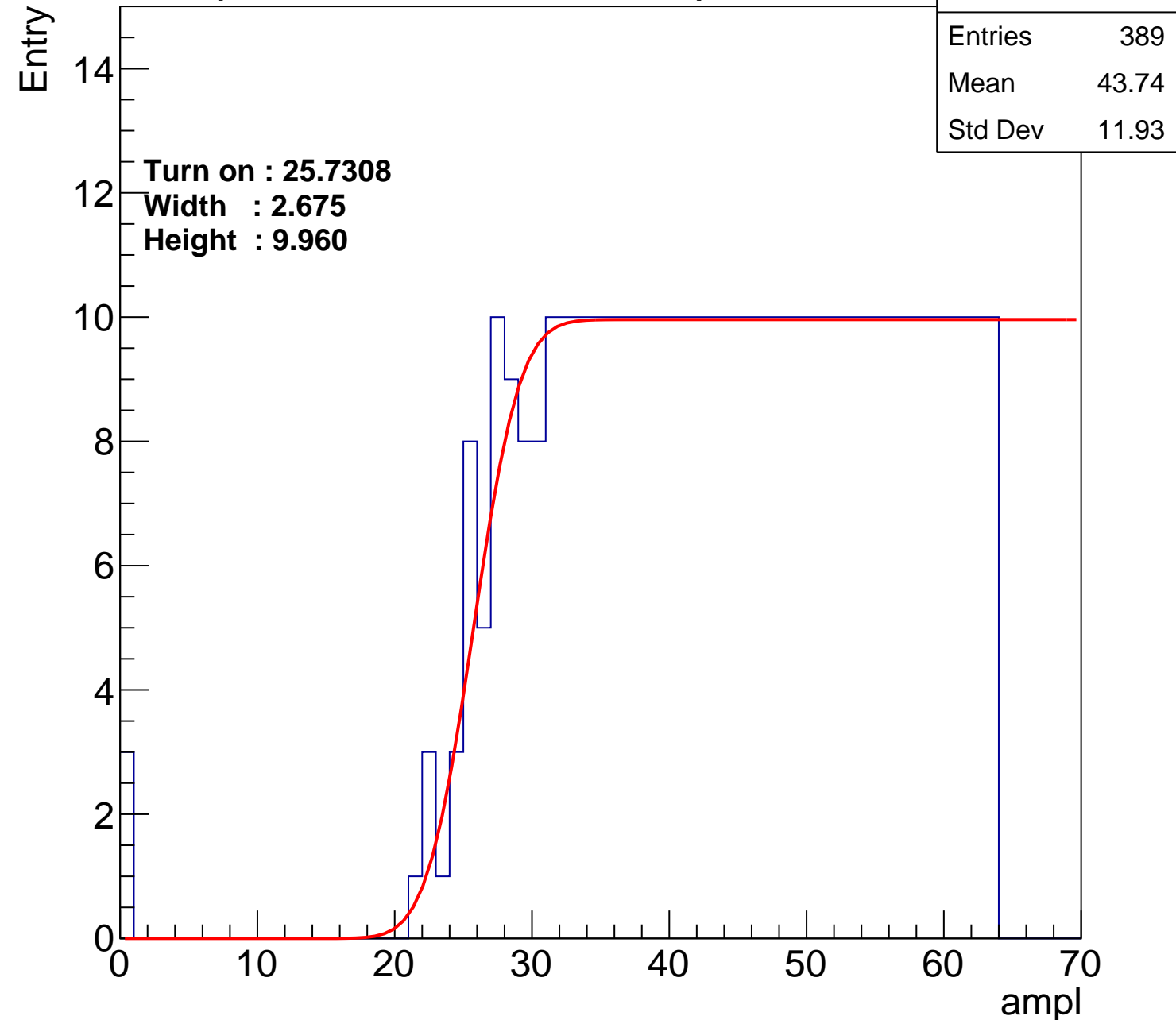
Width : 2.675

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch78

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	44.01
Std Dev	11.53

Turn on : 25.6819

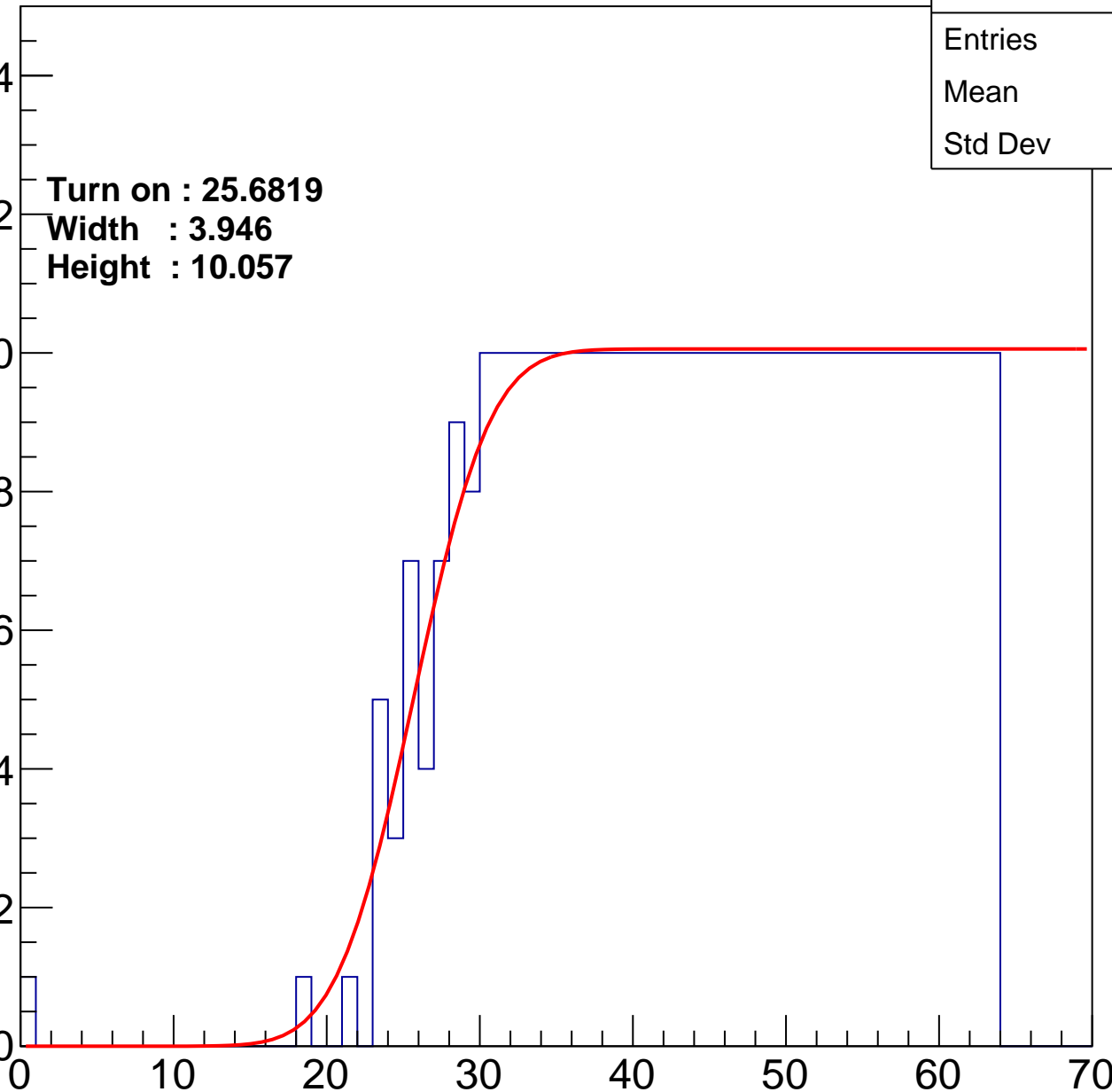
Width : 3.946

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch79

calib_packv5_042523_0143.root, FC#4, port A2

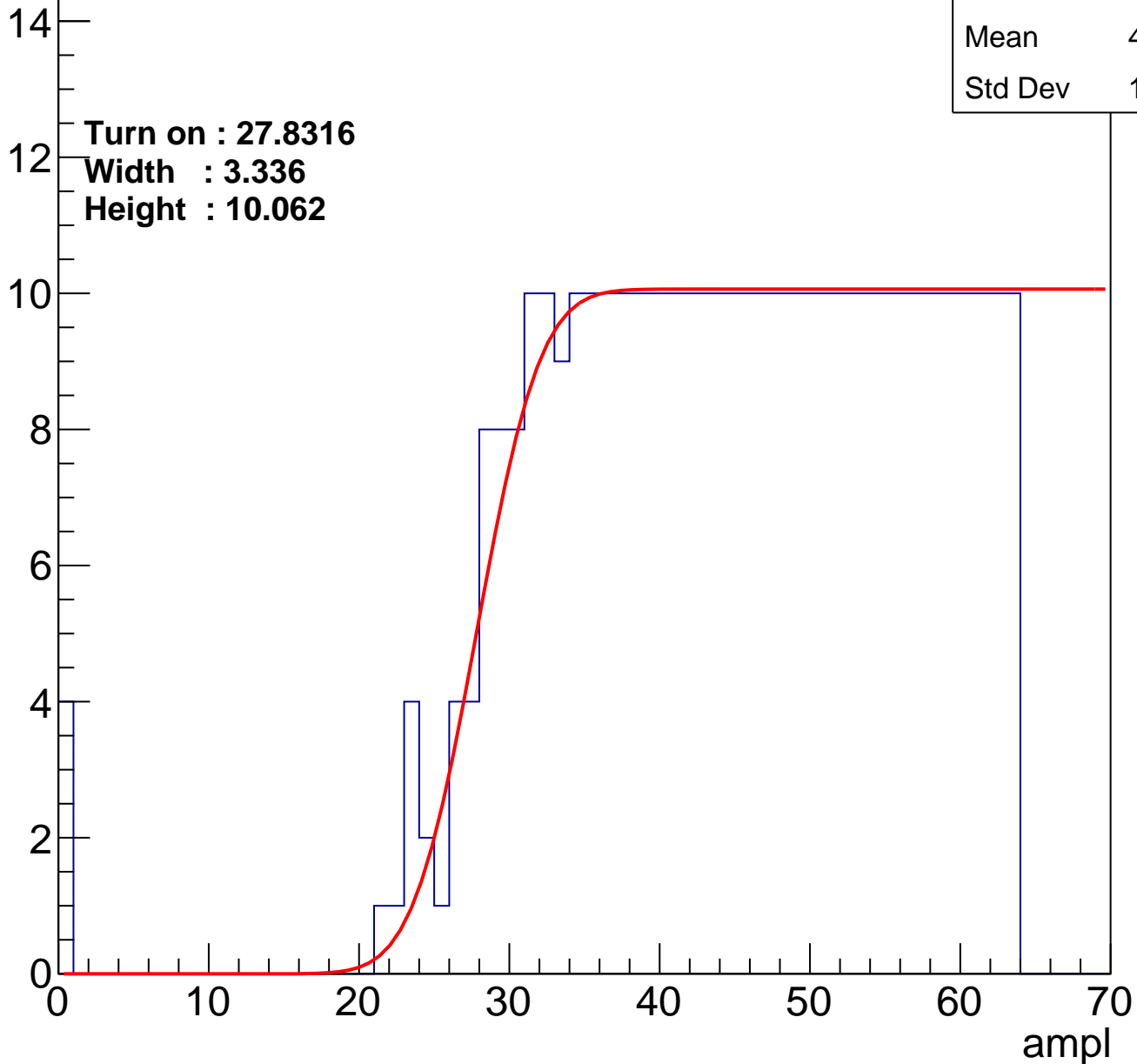
Entries	374
Mean	44.37
Std Dev	11.82

Turn on : 27.8316

Width : 3.336

Height : 10.062

Entry



B1L100S, U14-ch80

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.95
Std Dev	11.57

Turn on : 25.6117

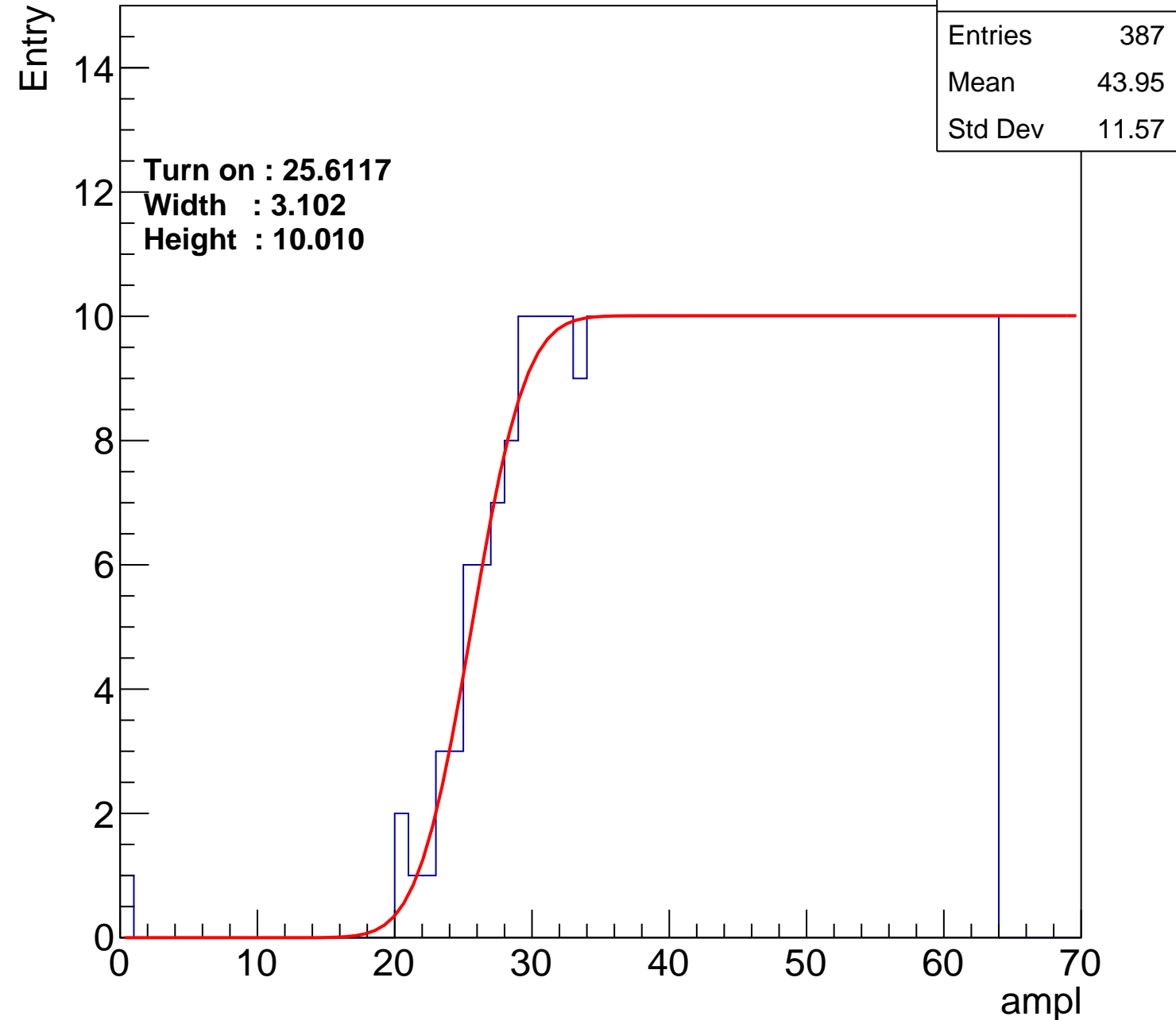
Width : 3.102

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch81

calib_packv5_042523_0143.root, FC#4, port A2

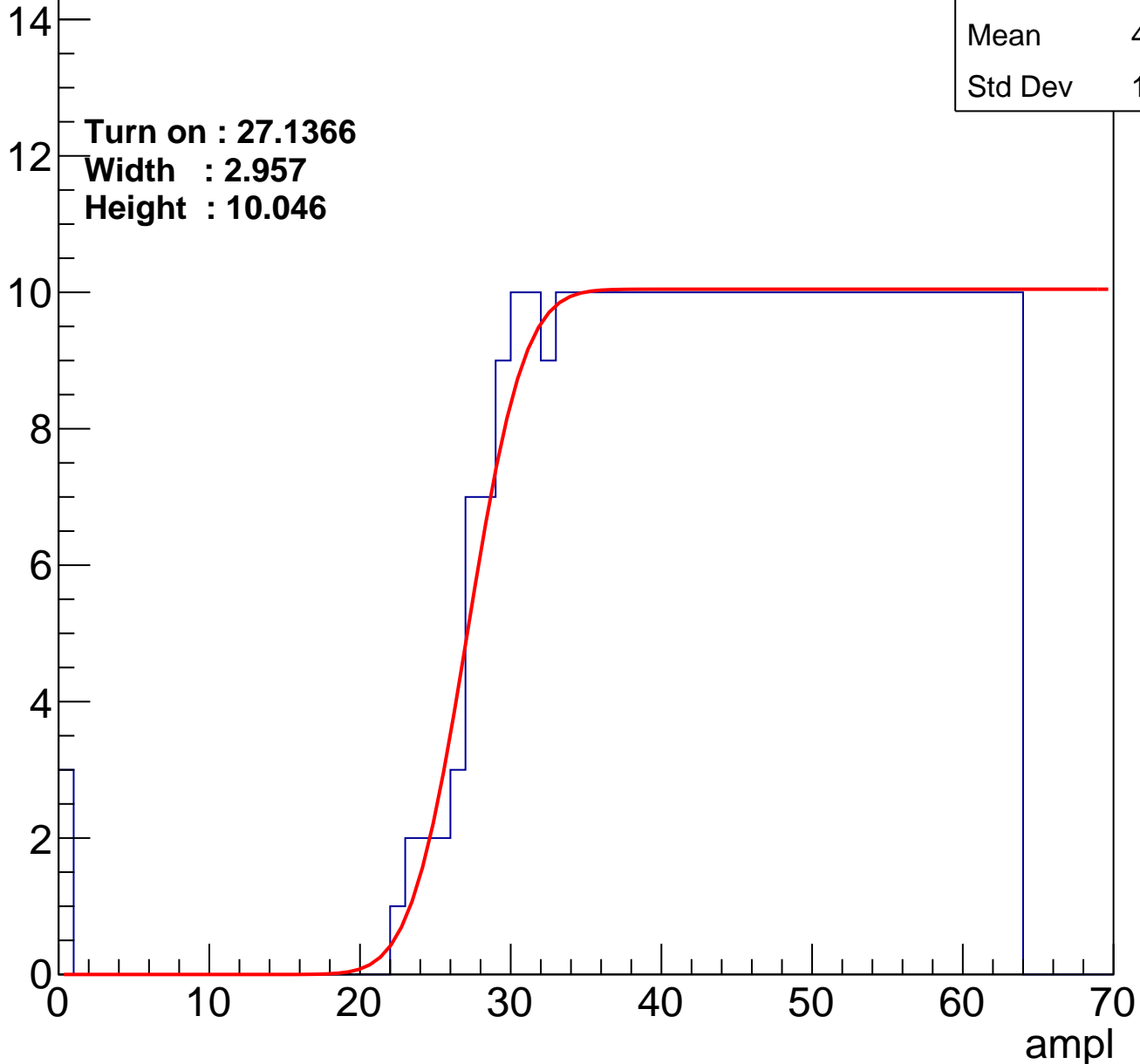
Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 27.1366

Width : 2.957

Height : 10.046

Entry



B1L100S, U14-ch82

calib_packv5_042523_0143.root, FC#4, port A2

Entries	399
Mean	43.28
Std Dev	12.14

Turn on : 24.6316

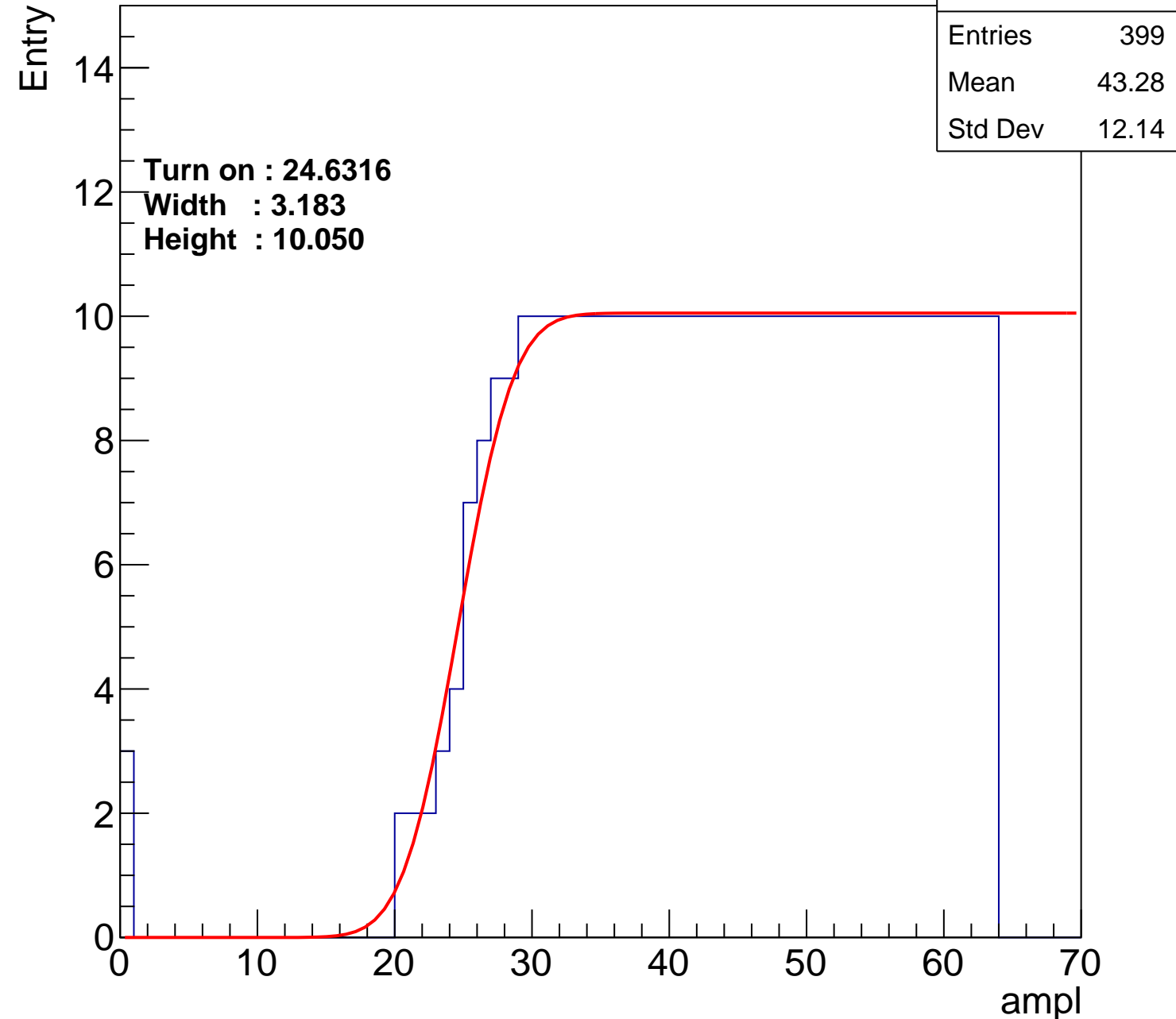
Width : 3.183

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch83

calib_packv5_042523_0143.root, FC#4, port A2

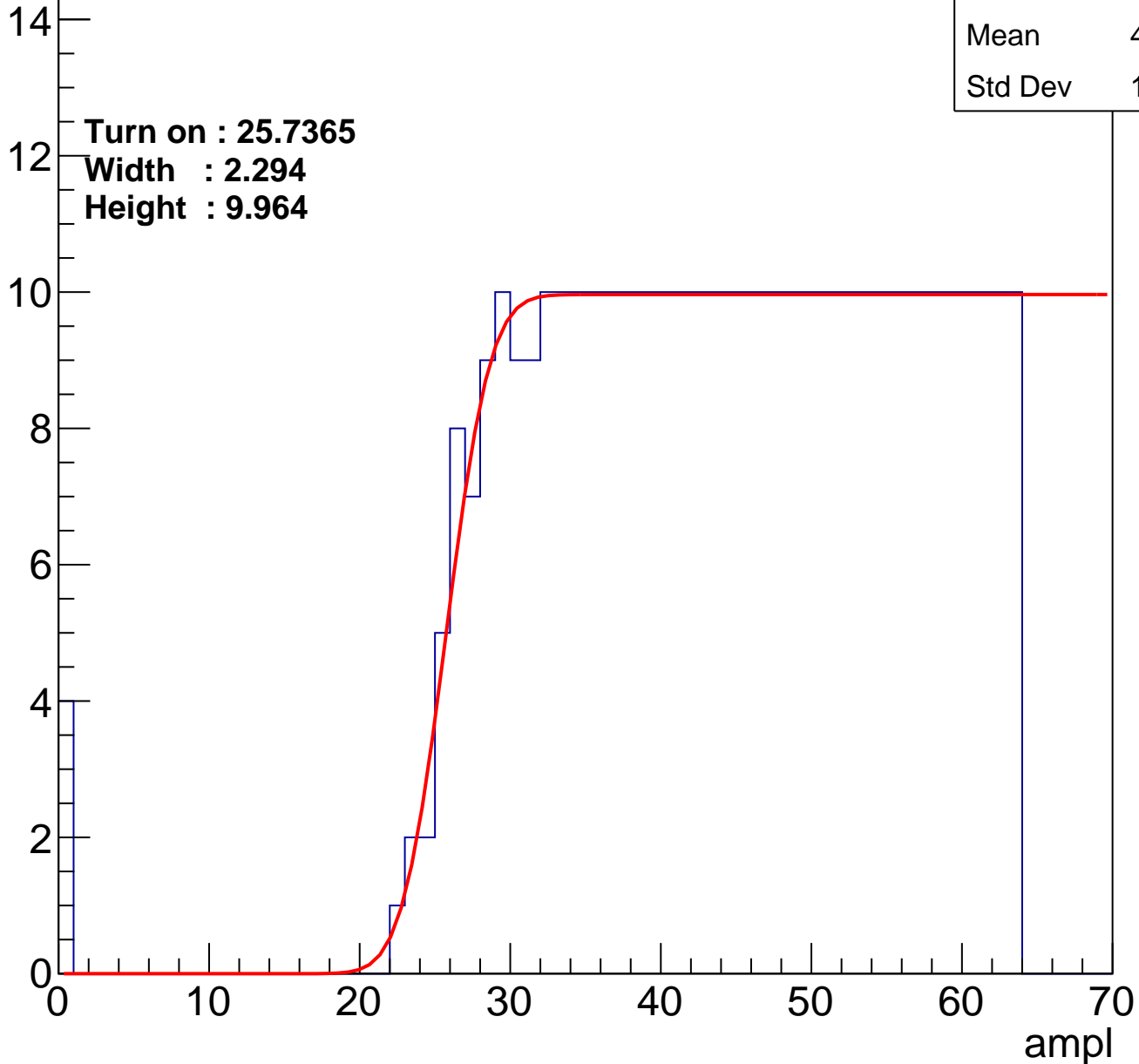
Entries	386
Mean	43.86
Std Dev	11.98

Turn on : 25.7365

Width : 2.294

Height : 9.964

Entry



B1L100S, U14-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 25.9668

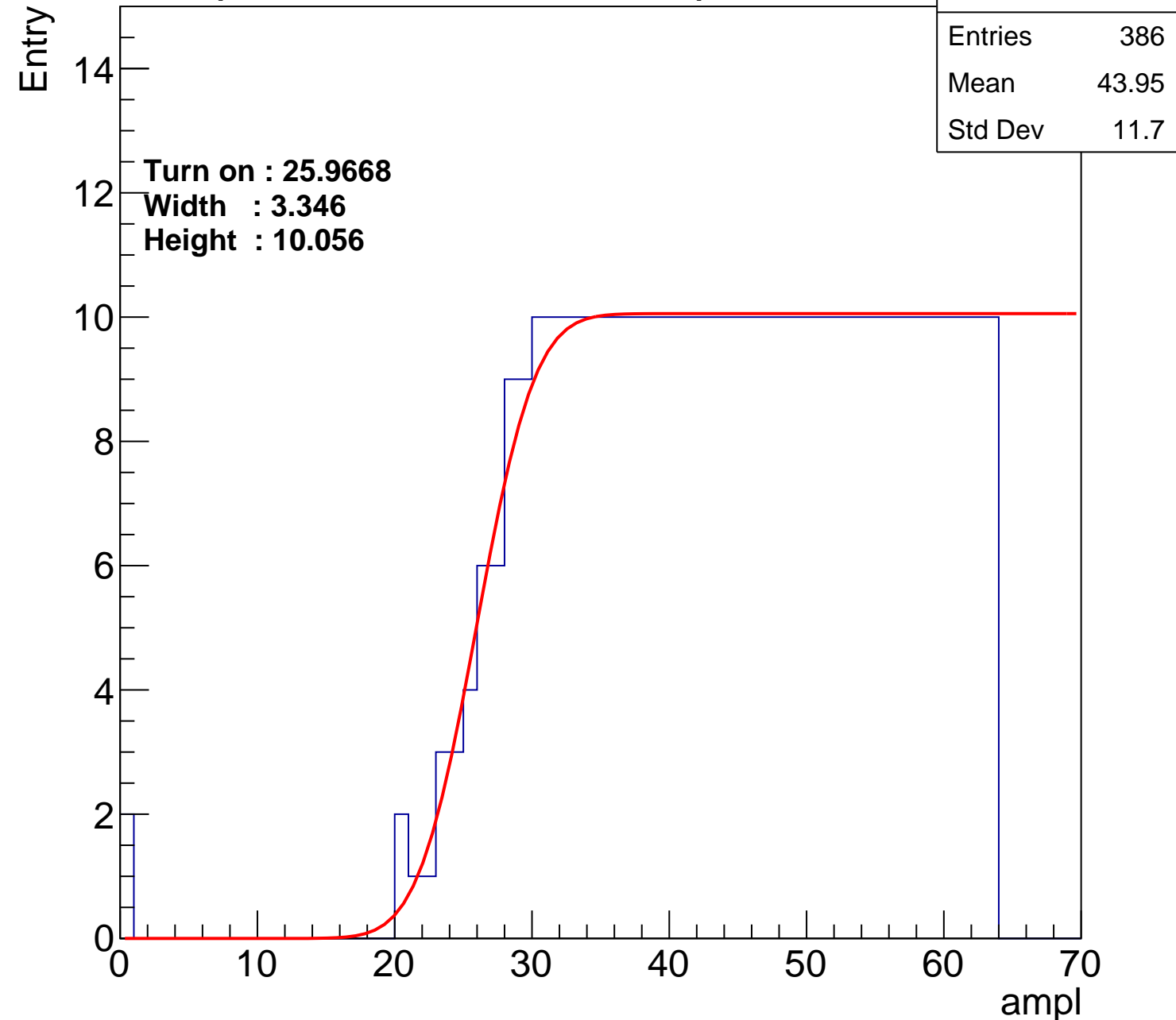
Width : 3.346

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch85

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	44.97
Std Dev	11.18

Turn on : 27.7615

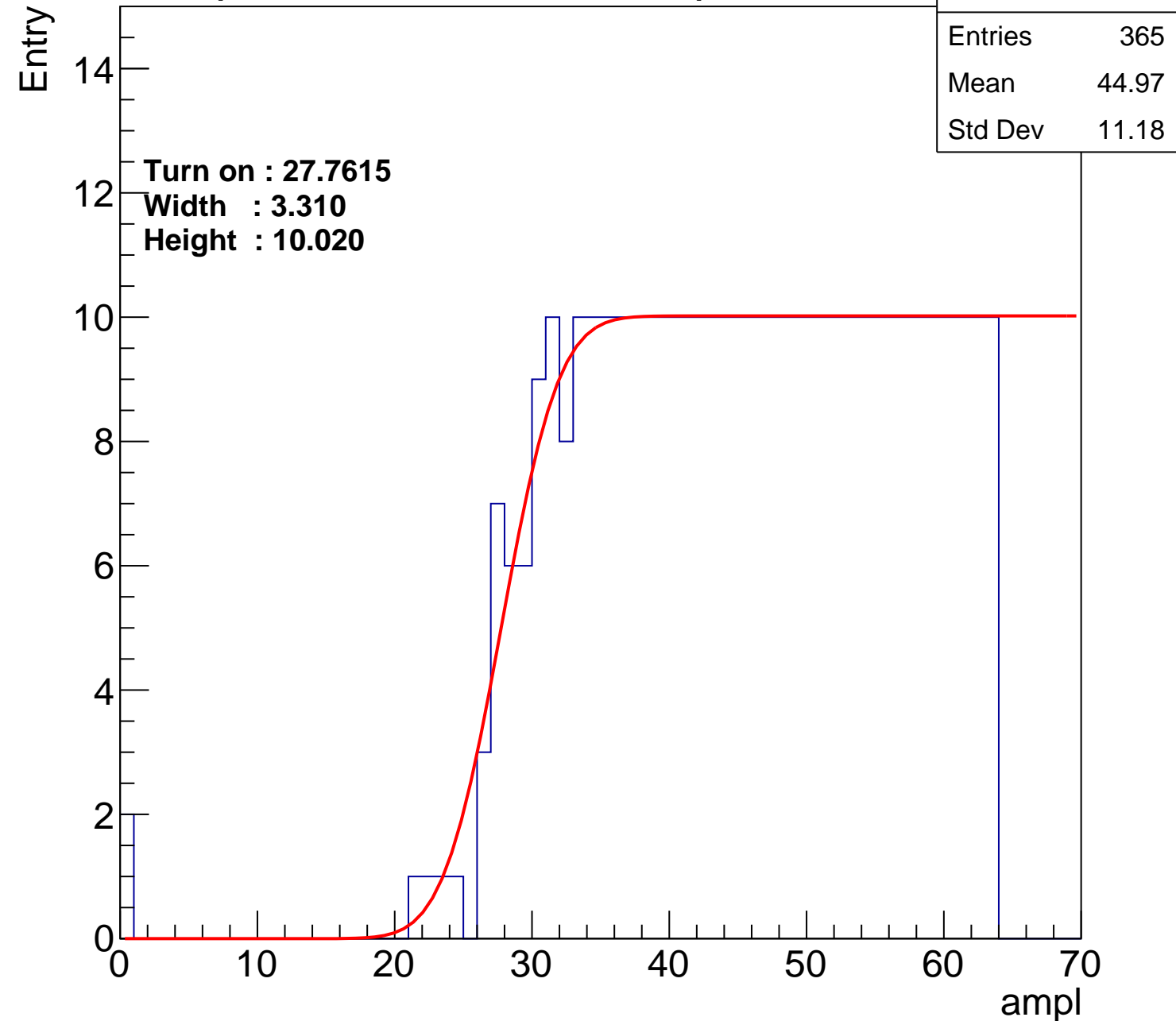
Width : 3.310

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch86

calib_packv5_042523_0143.root, FC#4, port A2

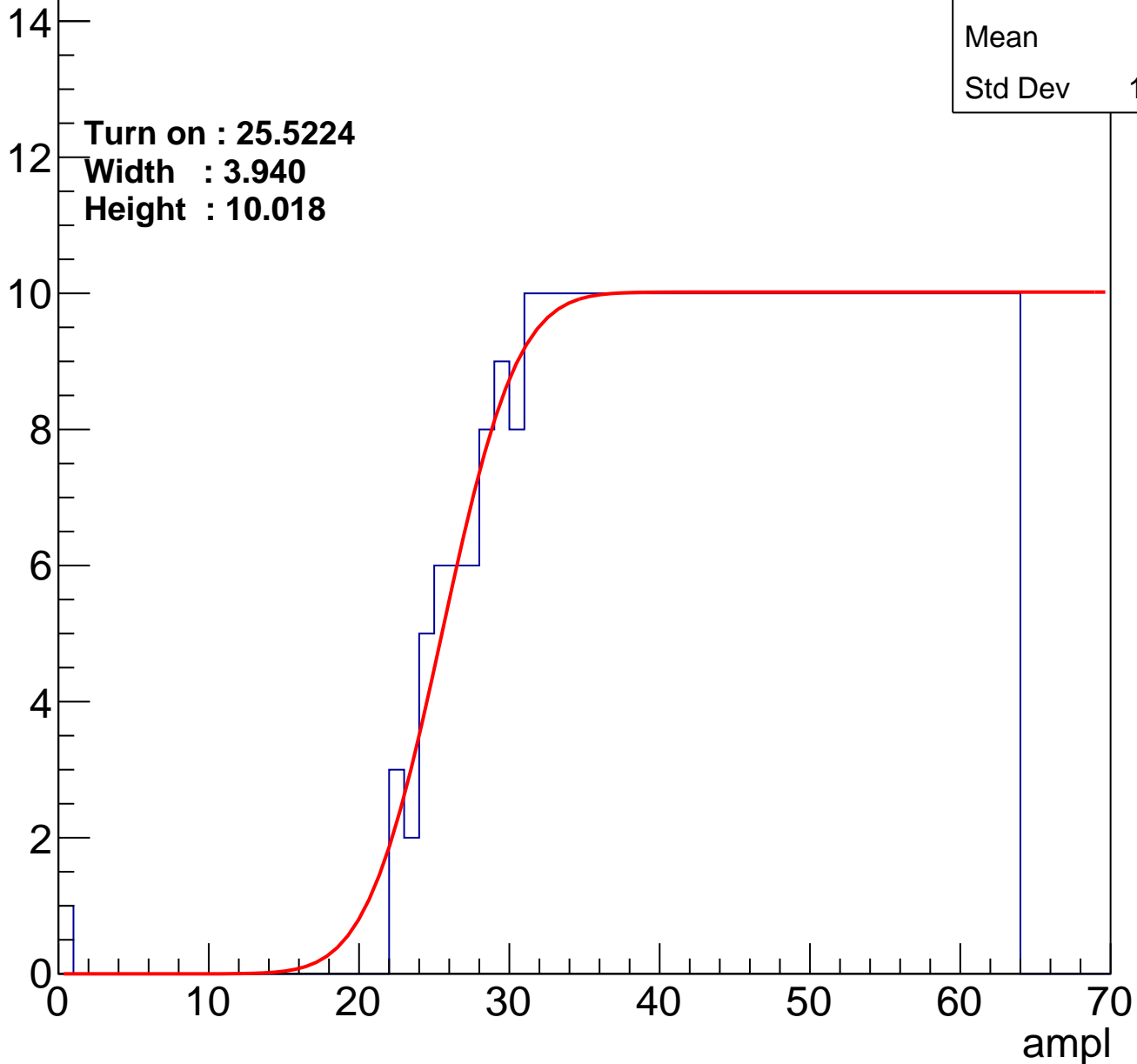
Entries	384
Mean	44.1
Std Dev	11.48

Turn on : 25.5224

Width : 3.940

Height : 10.018

Entry



B1L100S, U14-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 26.1604

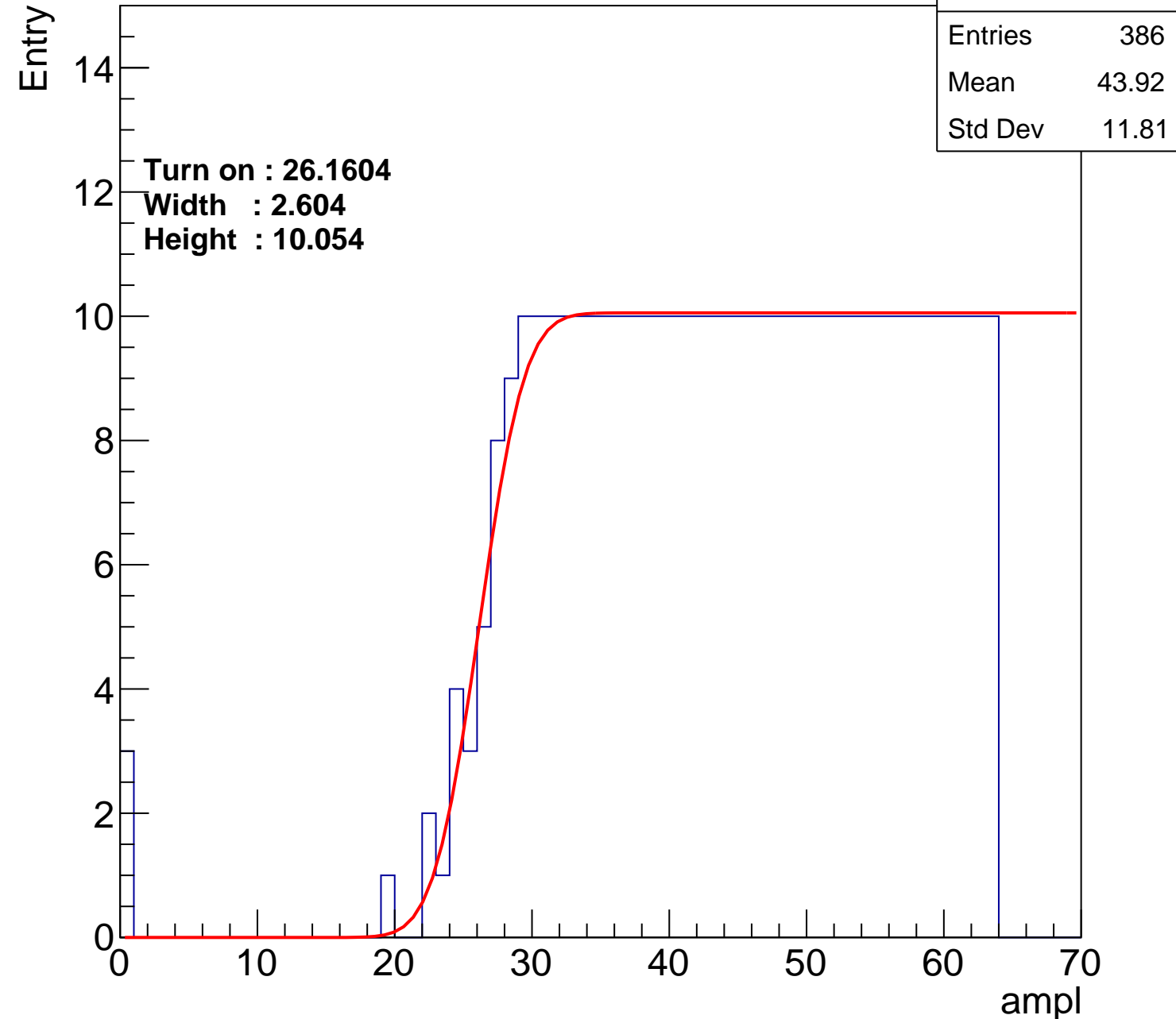
Width : 2.604

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch88

calib_packv5_042523_0143.root, FC#4, port A2

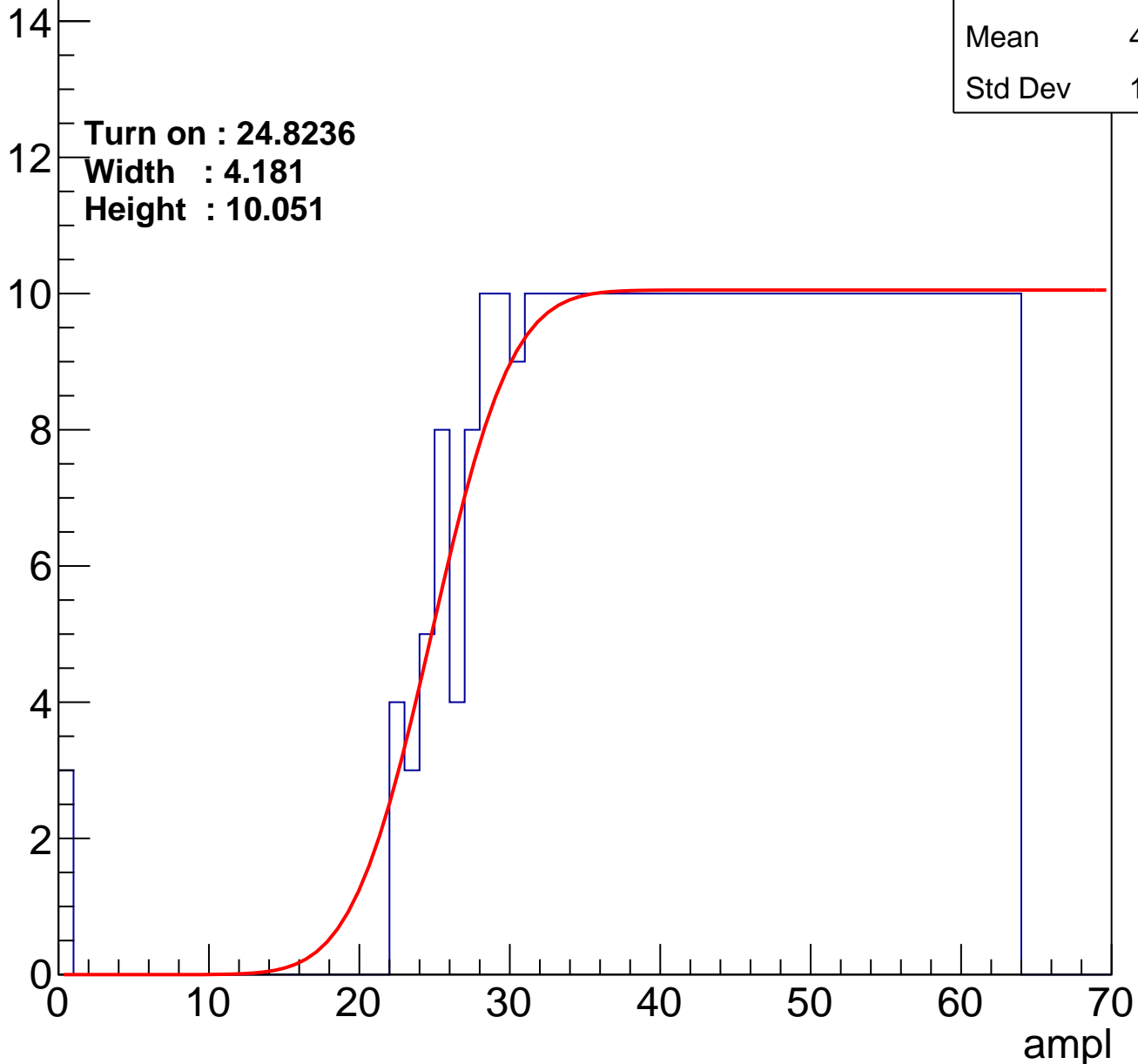
Entries	394
Mean	43.52
Std Dev	12.02

Turn on : 24.8236

Width : 4.181

Height : 10.051

Entry



B1L100S, U14-ch89

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.98
Std Dev	11.71

Turn on : 26.2773

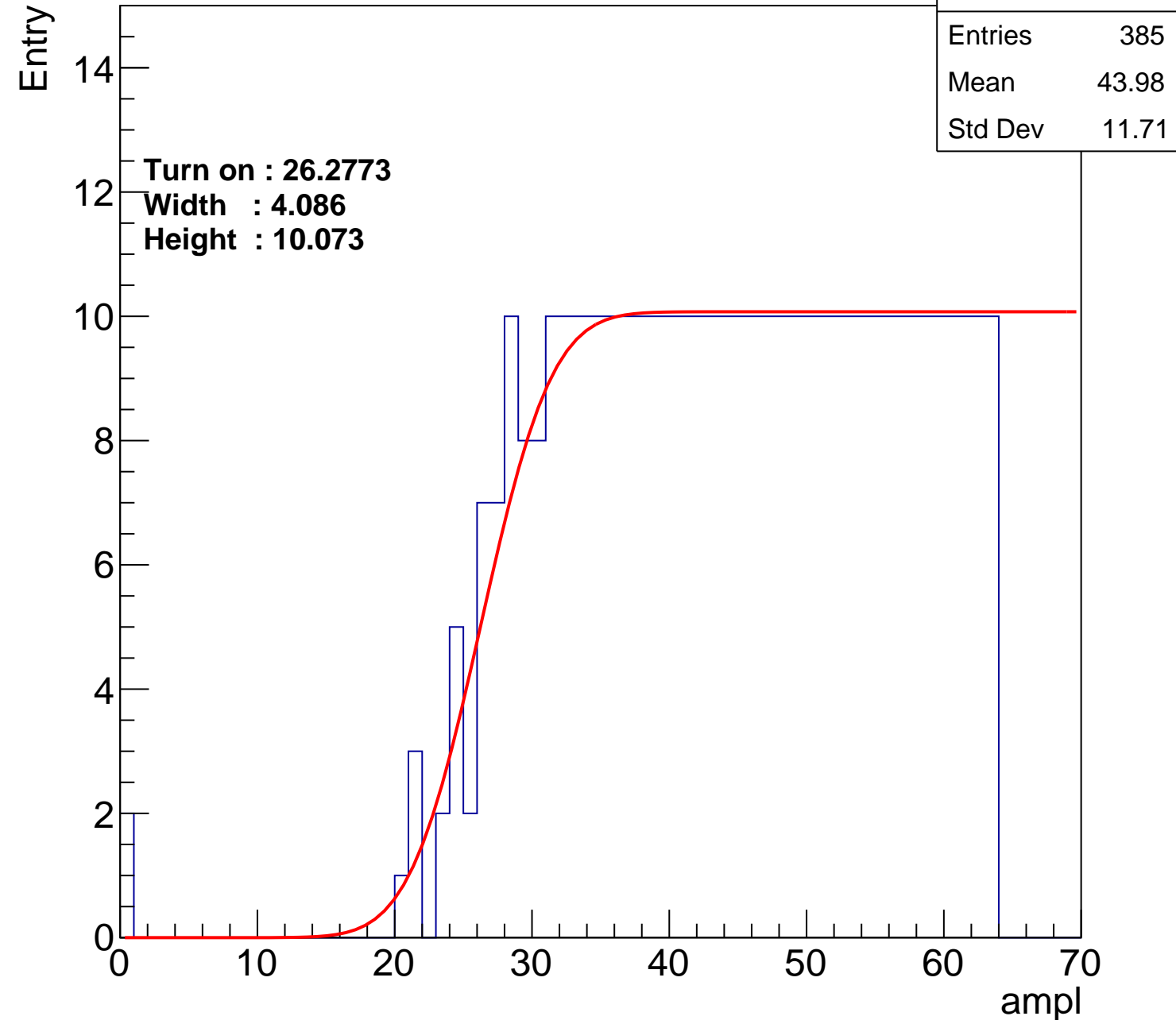
Width : 4.086

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch90

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.82
Std Dev	12.04

Turn on : 26.0885

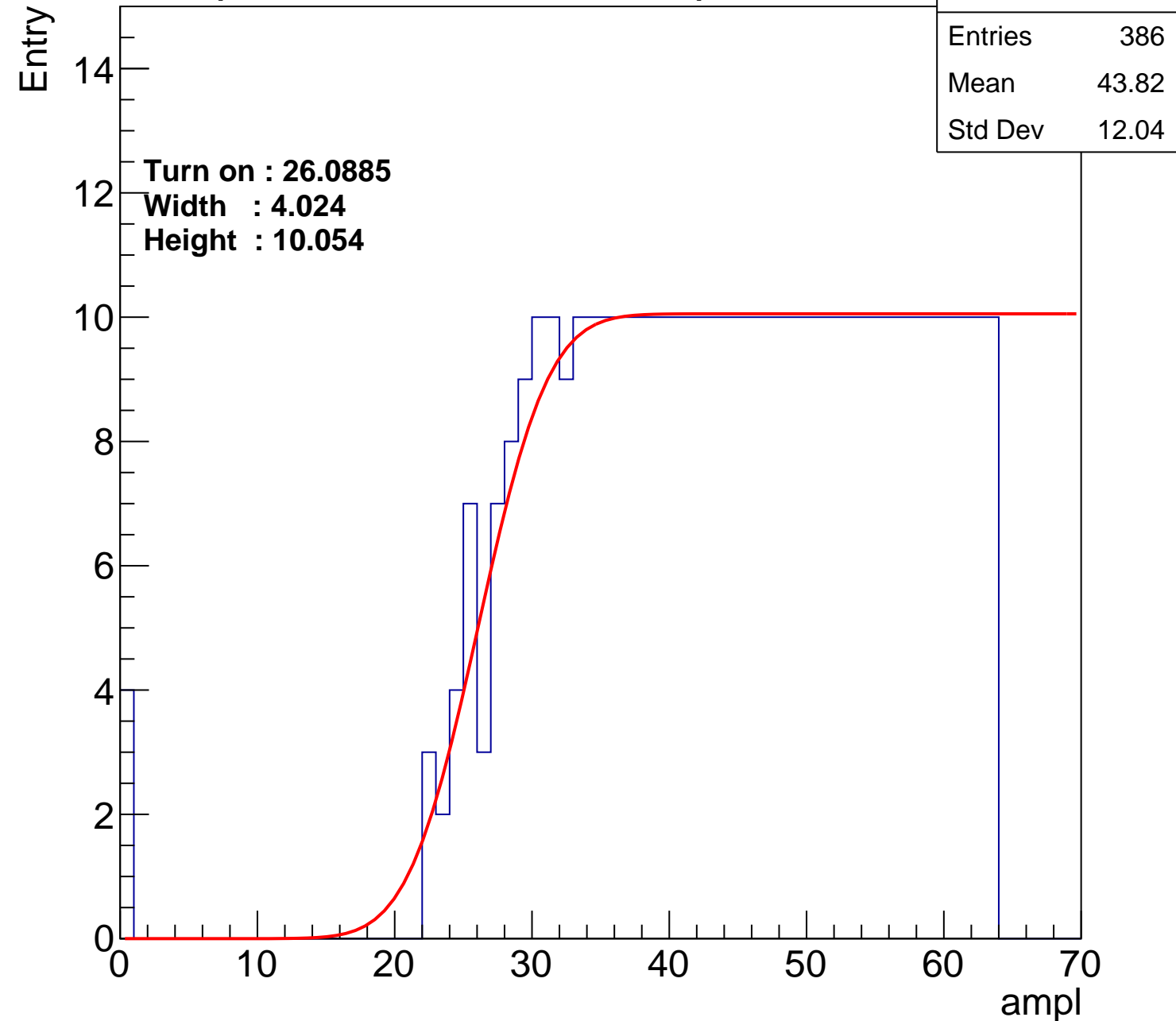
Width : 4.024

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch91

calib_packv5_042523_0143.root, FC#4, port A2

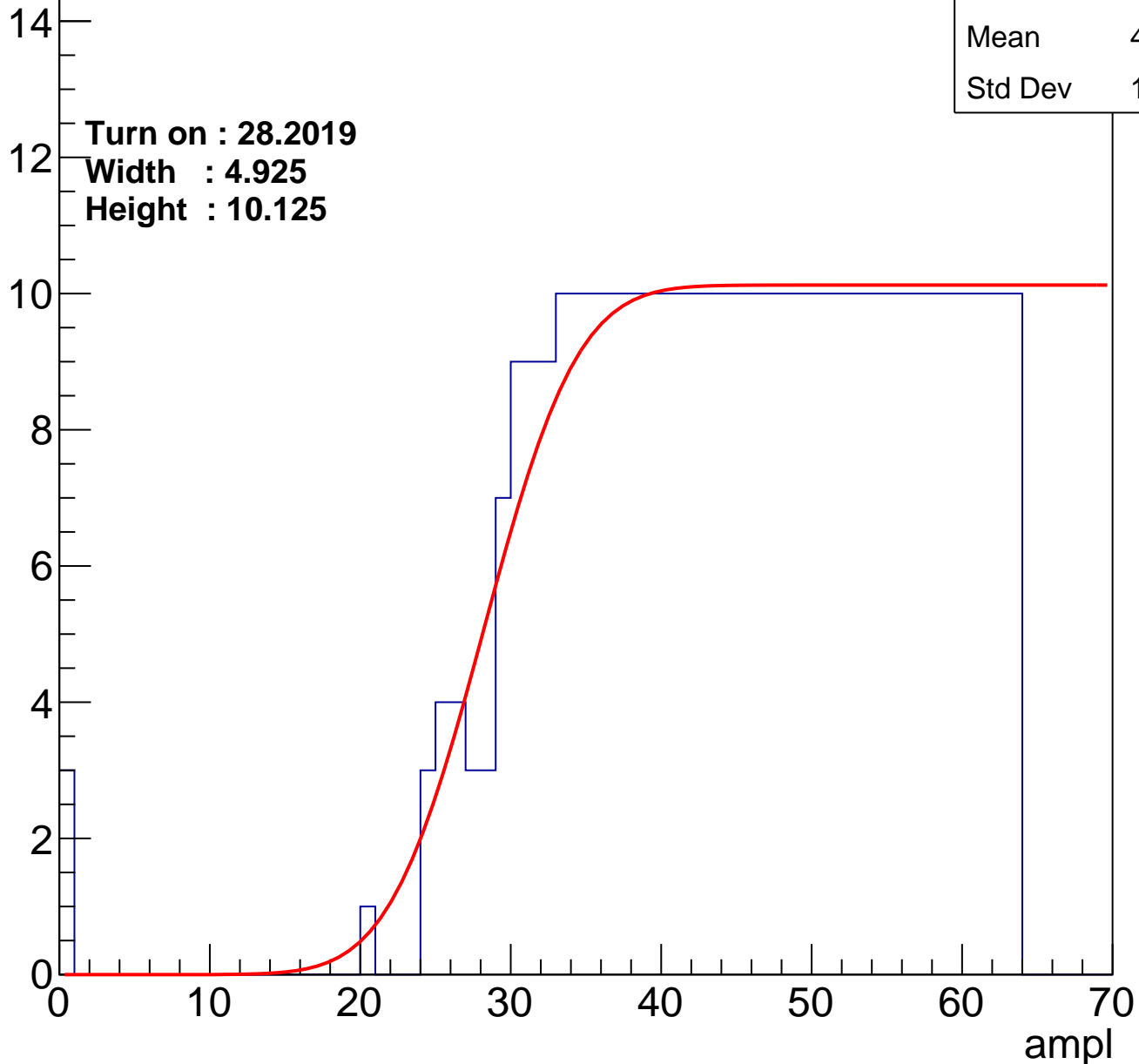
Entries	365
Mean	44.88
Std Dev	11.42

Turn on : 28.2019

Width : 4.925

Height : 10.125

Entry



B1L100S, U14-ch92

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.64
Std Dev	12.26

Turn on : 25.9542

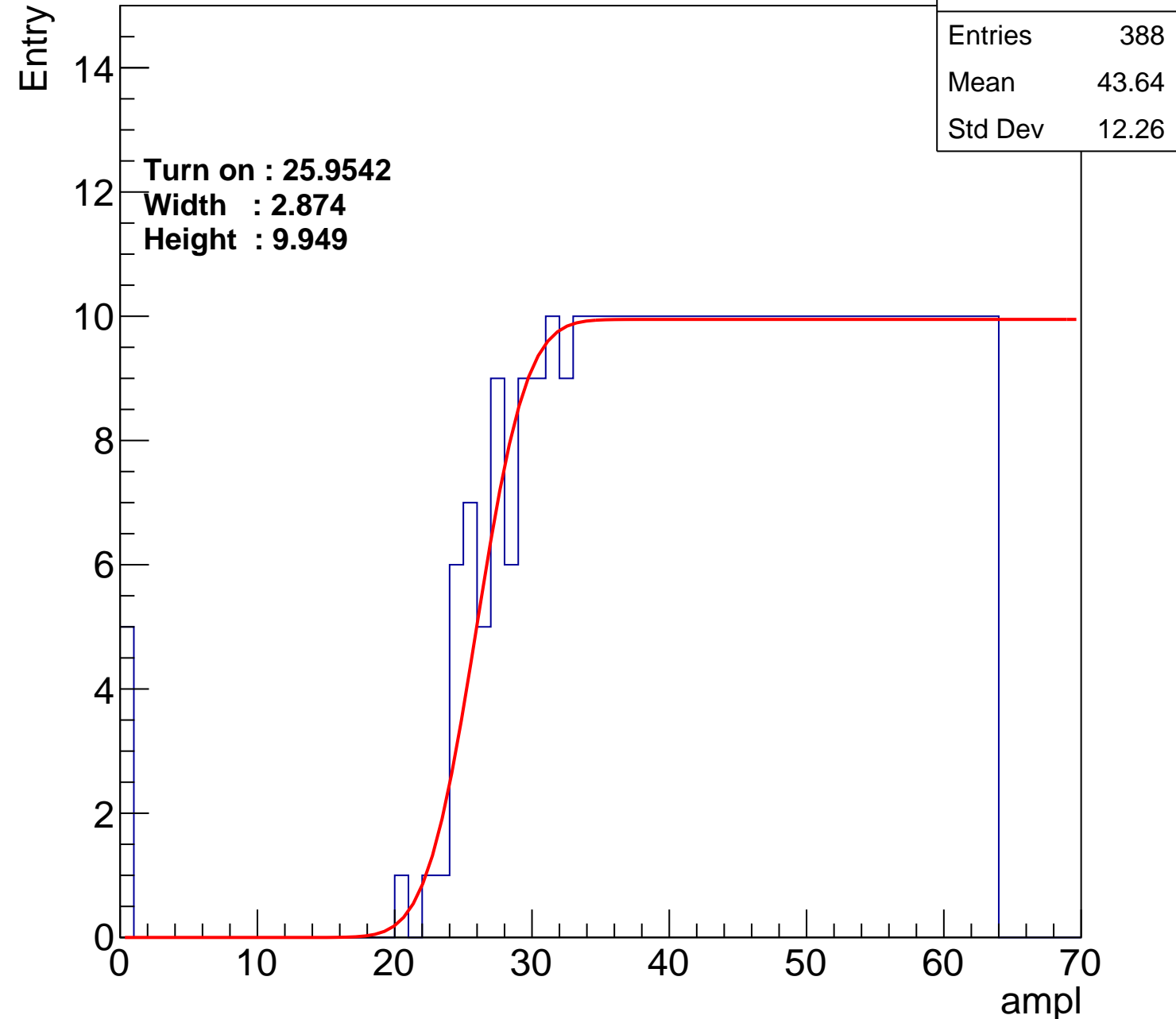
Width : 2.874

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.2751

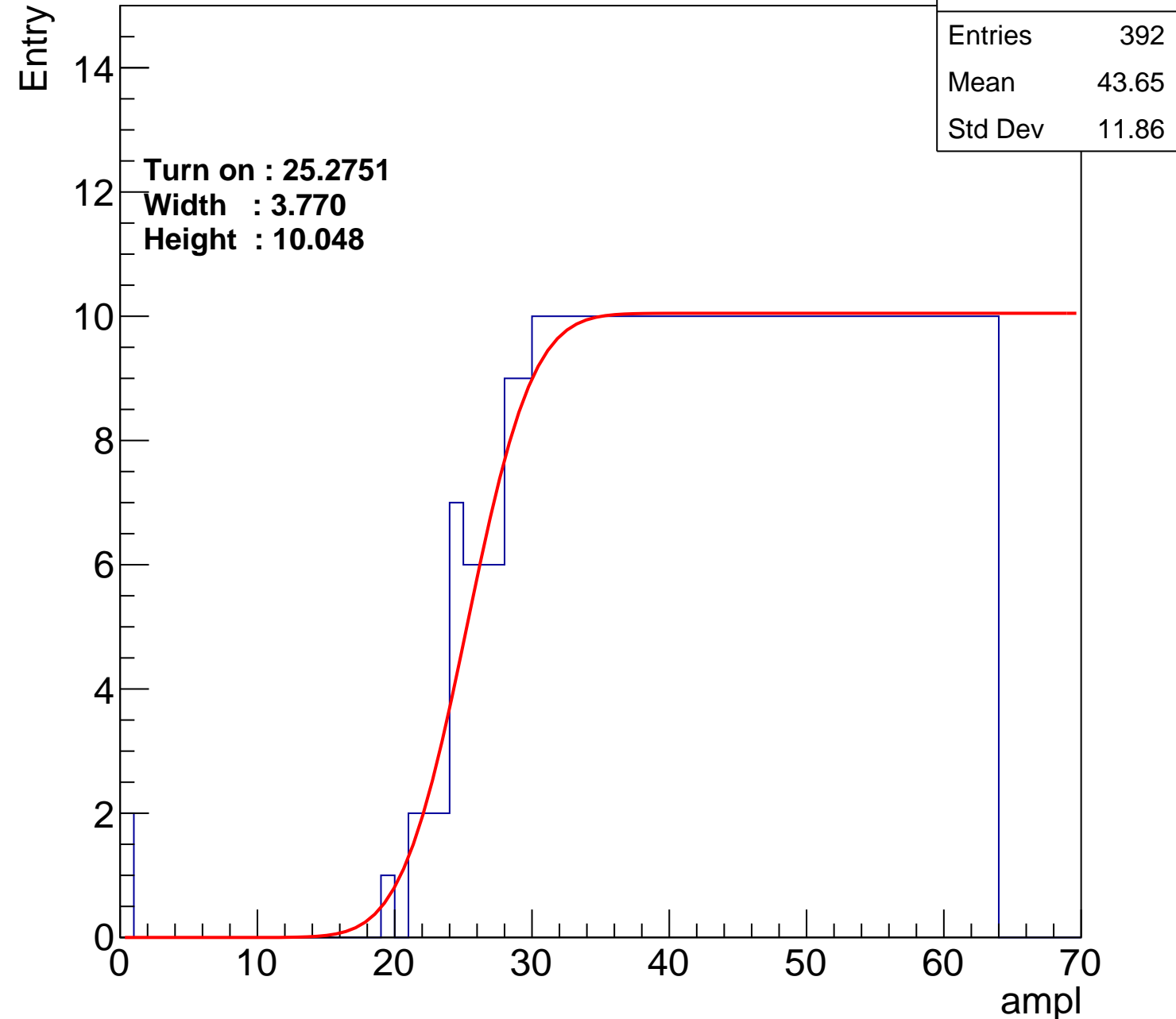
Width : 3.770

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.44
Std Dev	12.05

Turn on : 24.7444

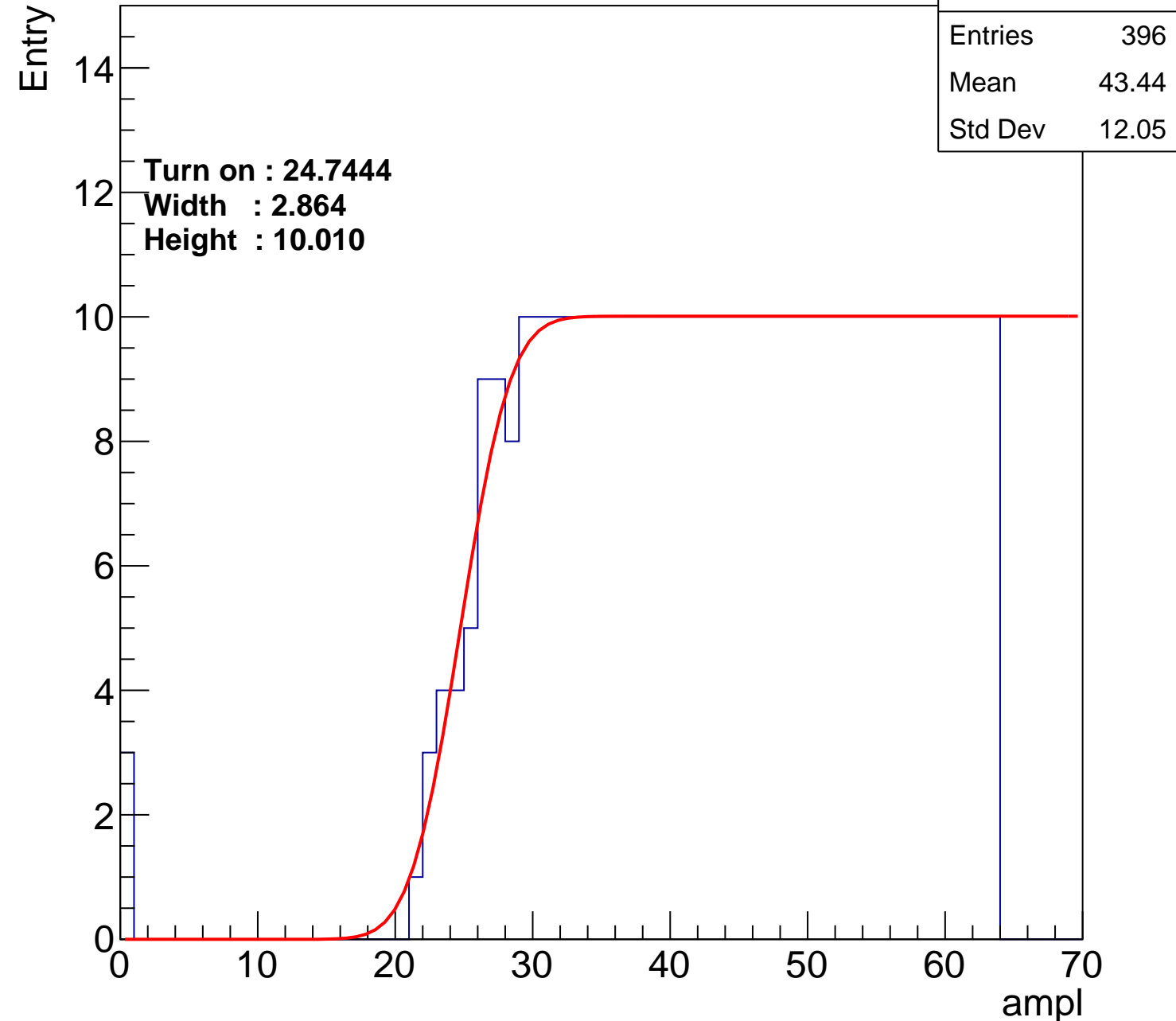
Width : 2.864

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch95

calib_packv5_042523_0143.root, FC#4, port A2

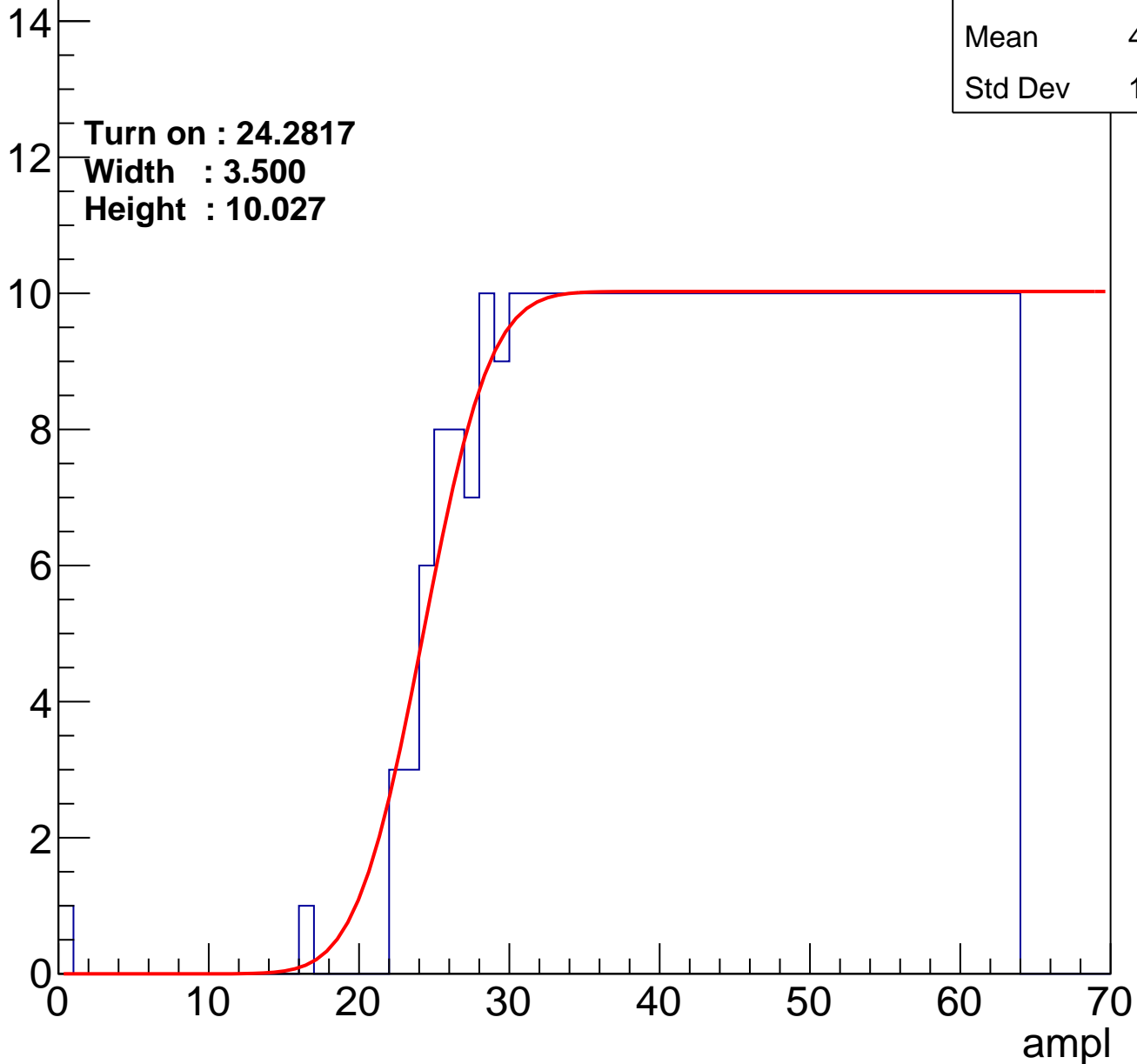
Entries	396
Mean	43.54
Std Dev	11.76

Turn on : 24.2817

Width : 3.500

Height : 10.027

Entry



B1L100S, U14-ch96

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.63
Std Dev	11.9

Turn on : 25.4653

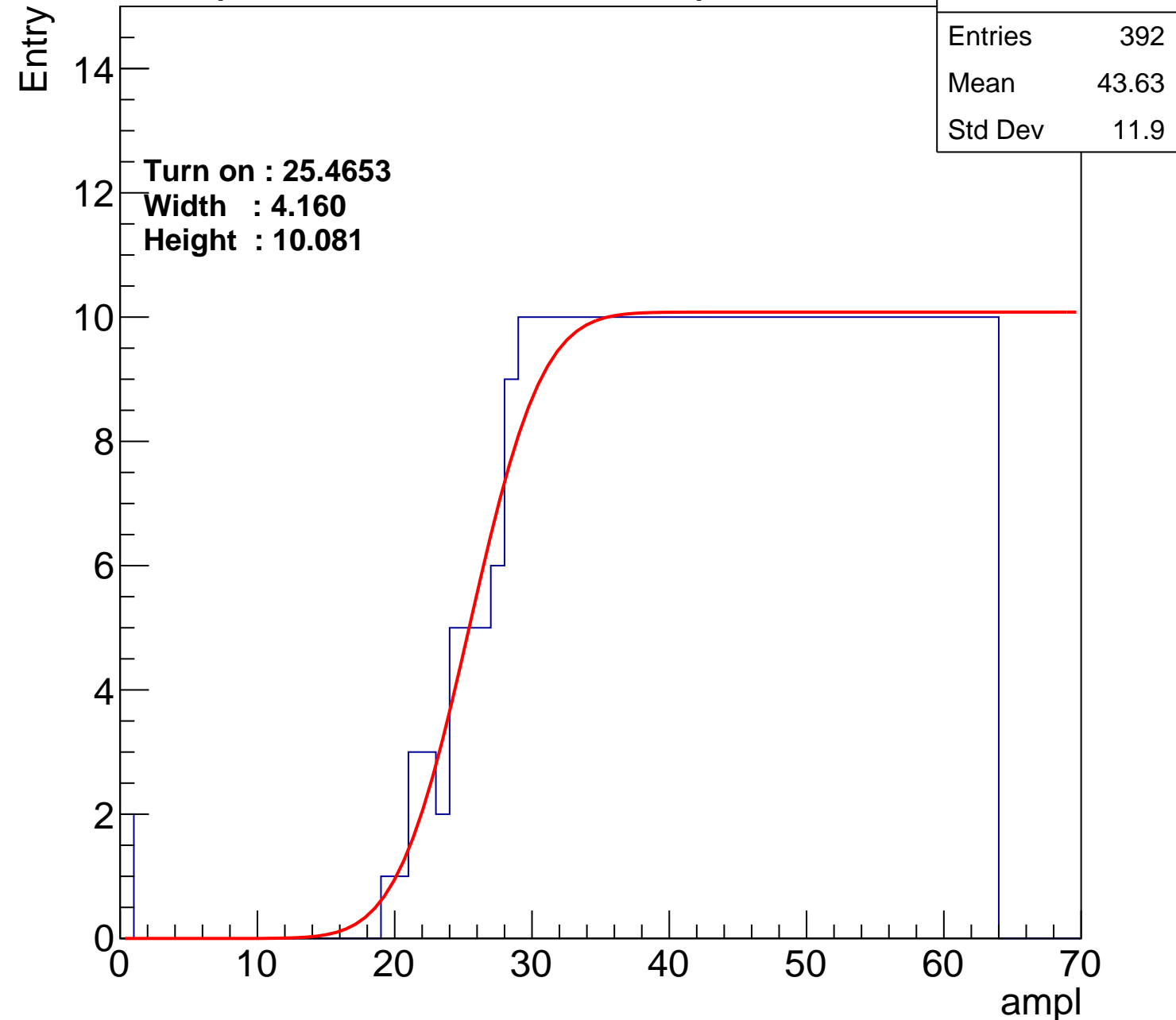
Width : 4.160

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
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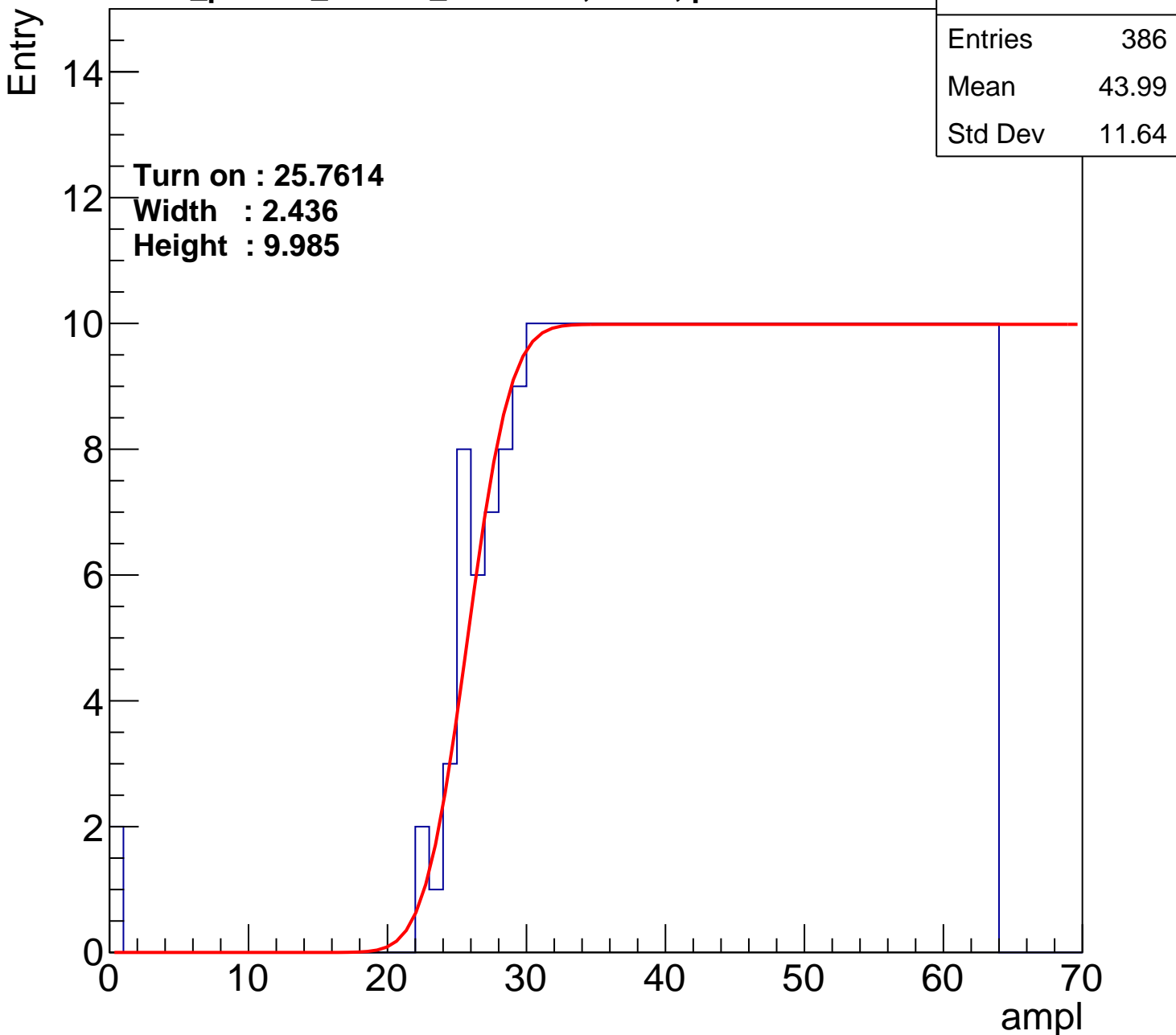
Mean	43.99
------	-------

Std Dev	11.64
---------	-------

Turn on : 25.7614

Width : 2.436

Height : 9.985



B1L100S, U14-ch98

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 25.7825

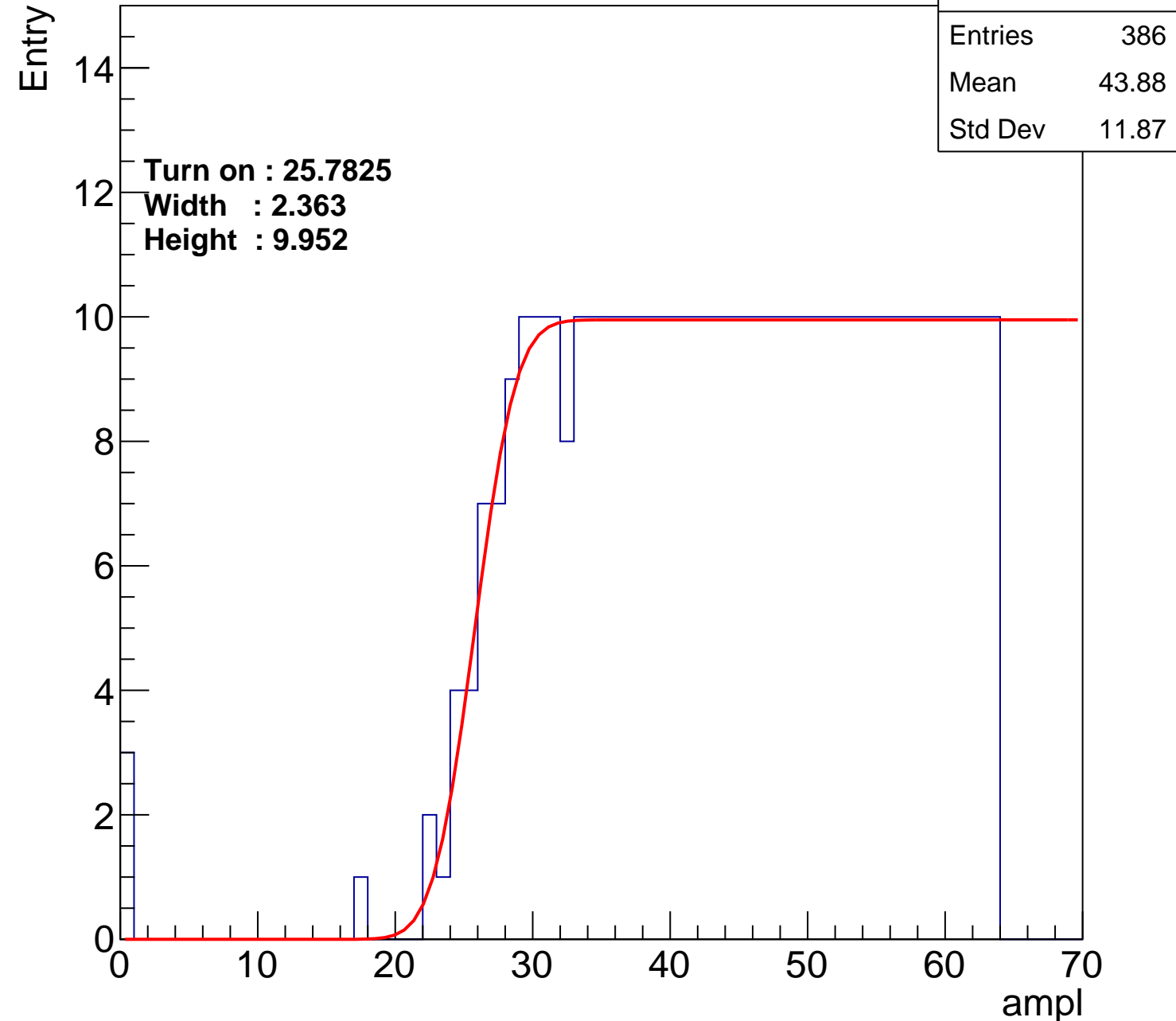
Width : 2.363

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch99

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.13
Std Dev	11.08

Turn on : 28.0560

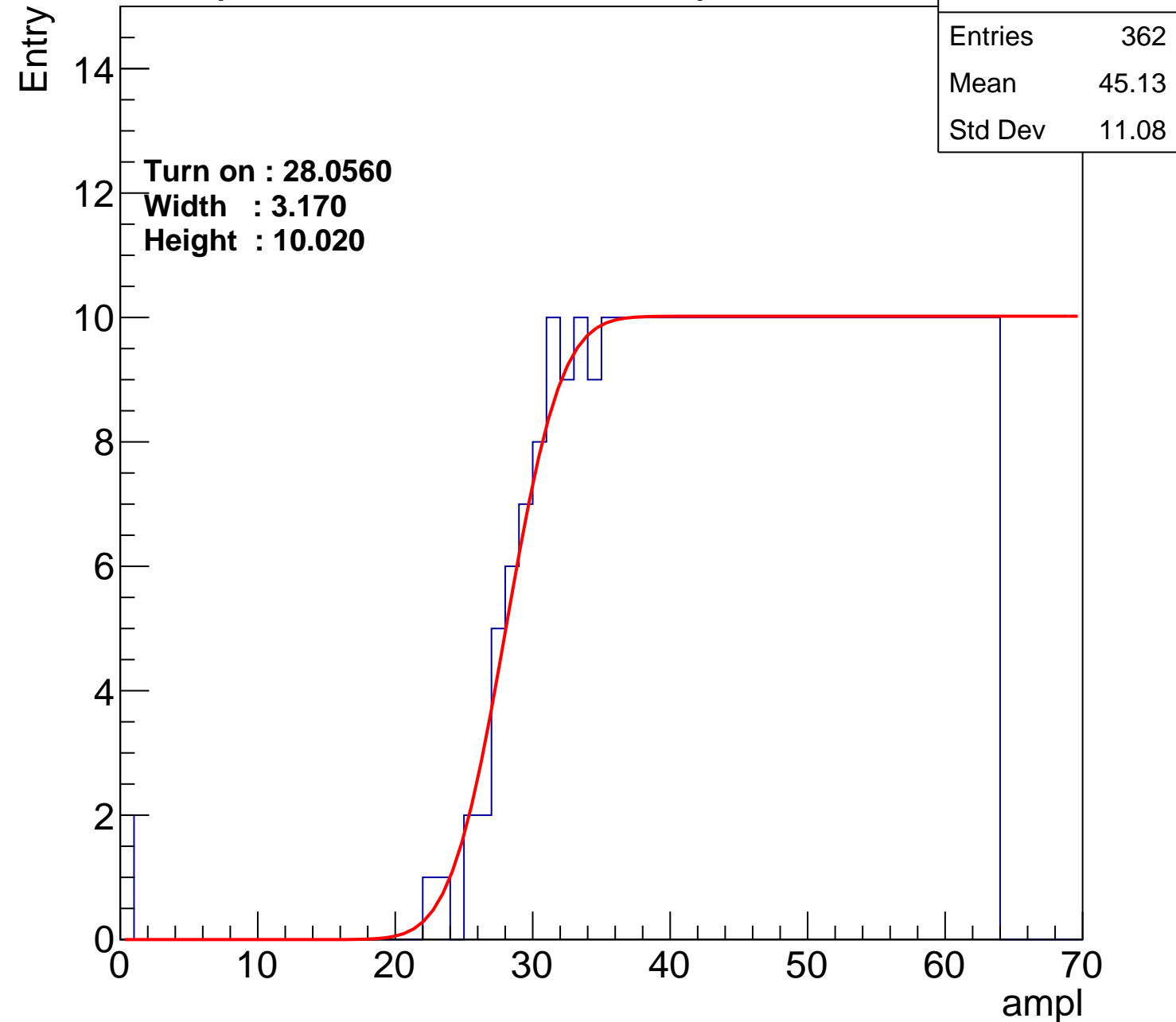
Width : 3.170

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch100

calib_packv5_042523_0143.root, FC#4, port A2

Entries	399
Mean	43.21
Std Dev	12.31

Turn on : 24.6249

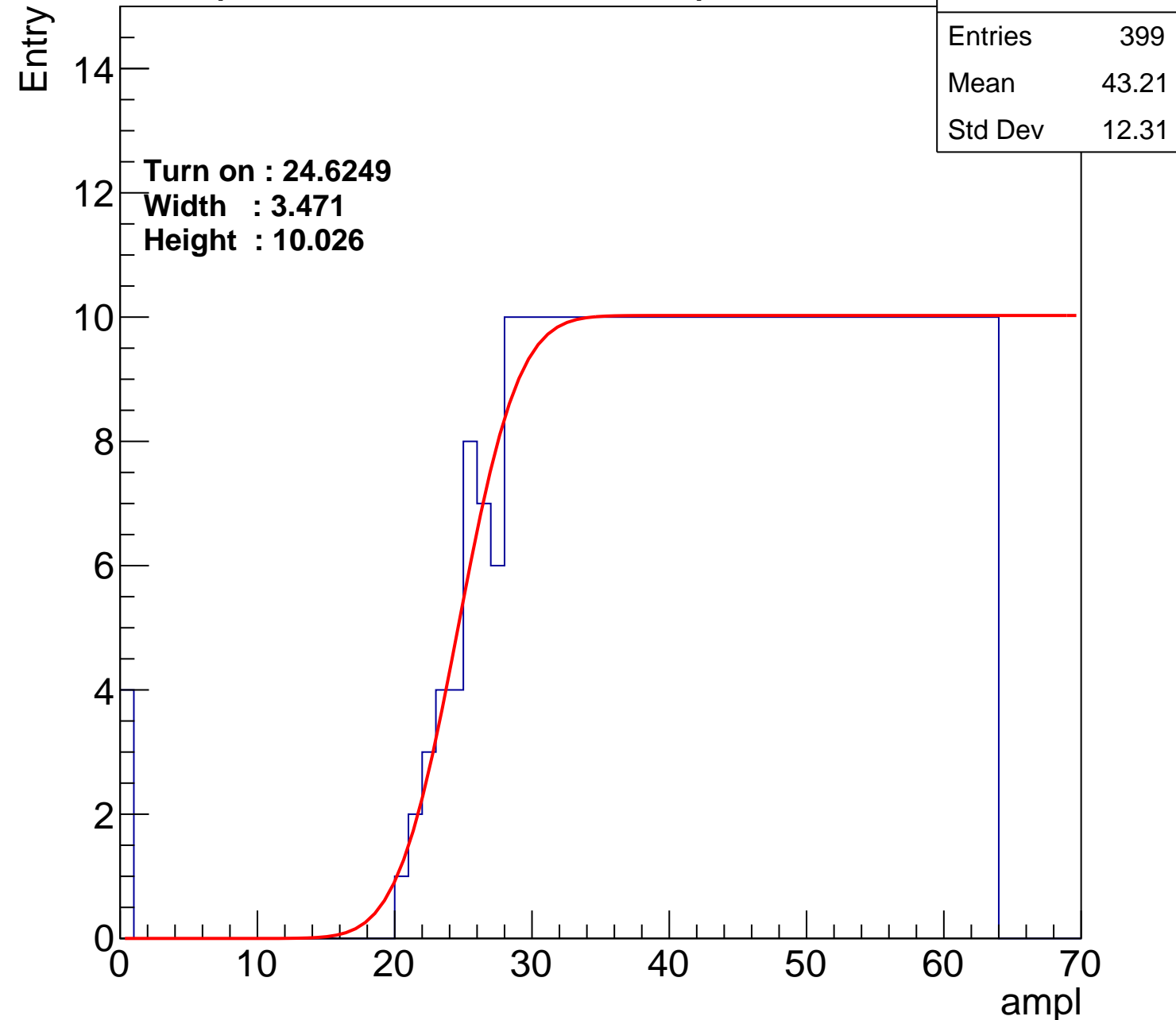
Width : 3.471

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch101

calib_packv5_042523_0143.root, FC#4, port A2

Entries	399
Mean	43.14
Std Dev	12.47

Turn on : 25.0066

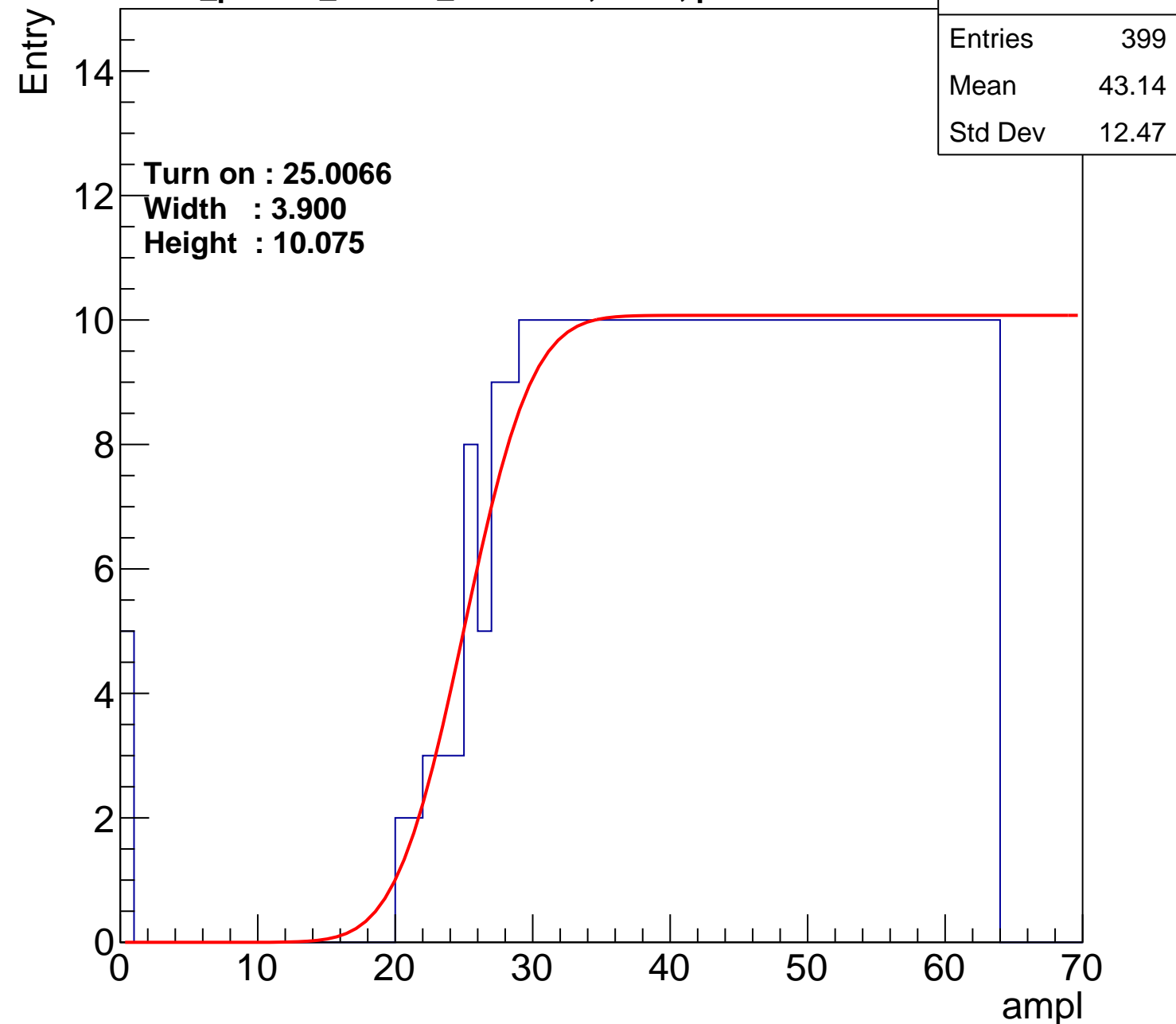
Width : 3.900

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.21
Std Dev	11.34

Turn on : 25.6487

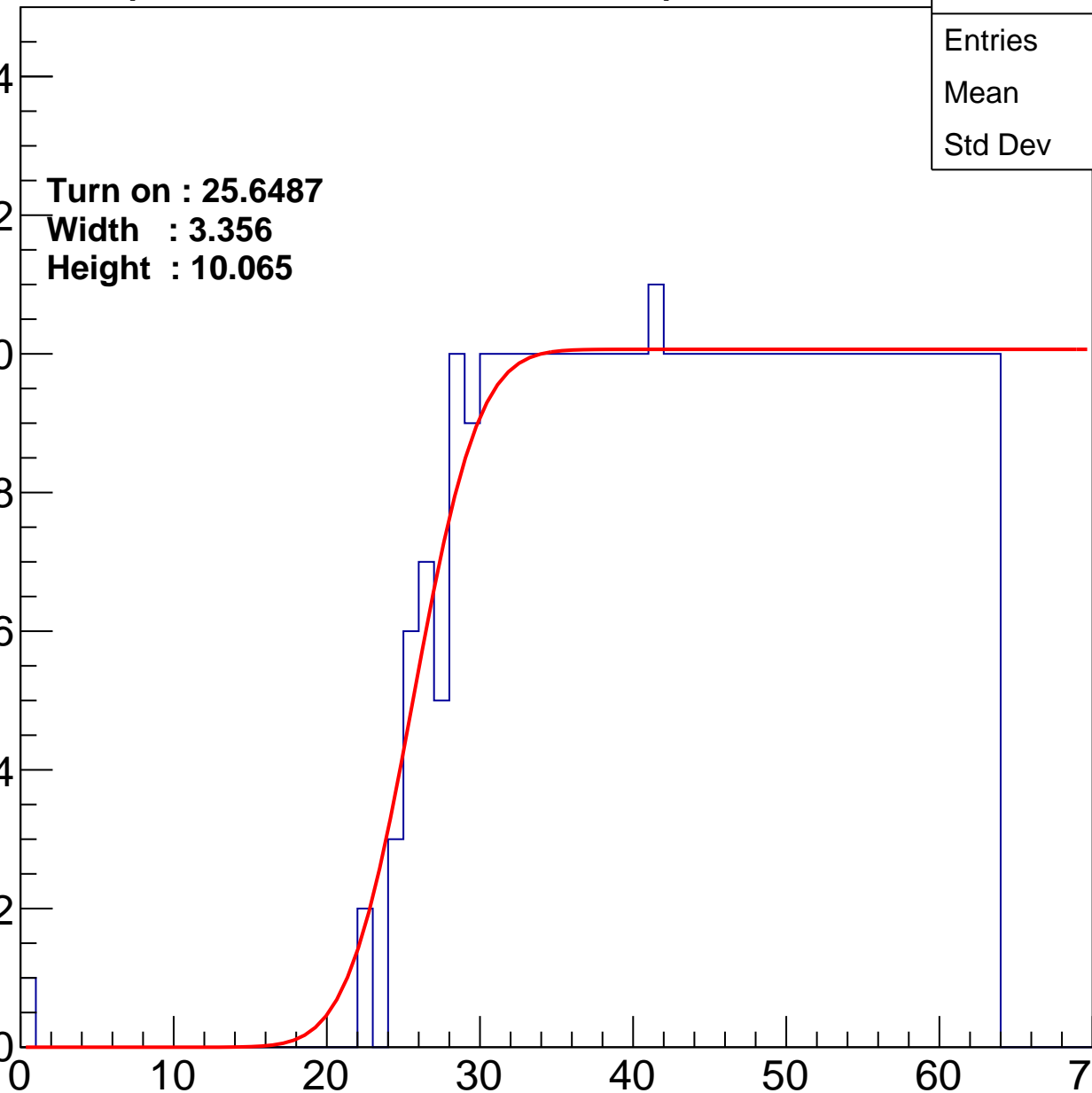
Width : 3.356

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.15
Std Dev	11.48

Turn on : 25.9797

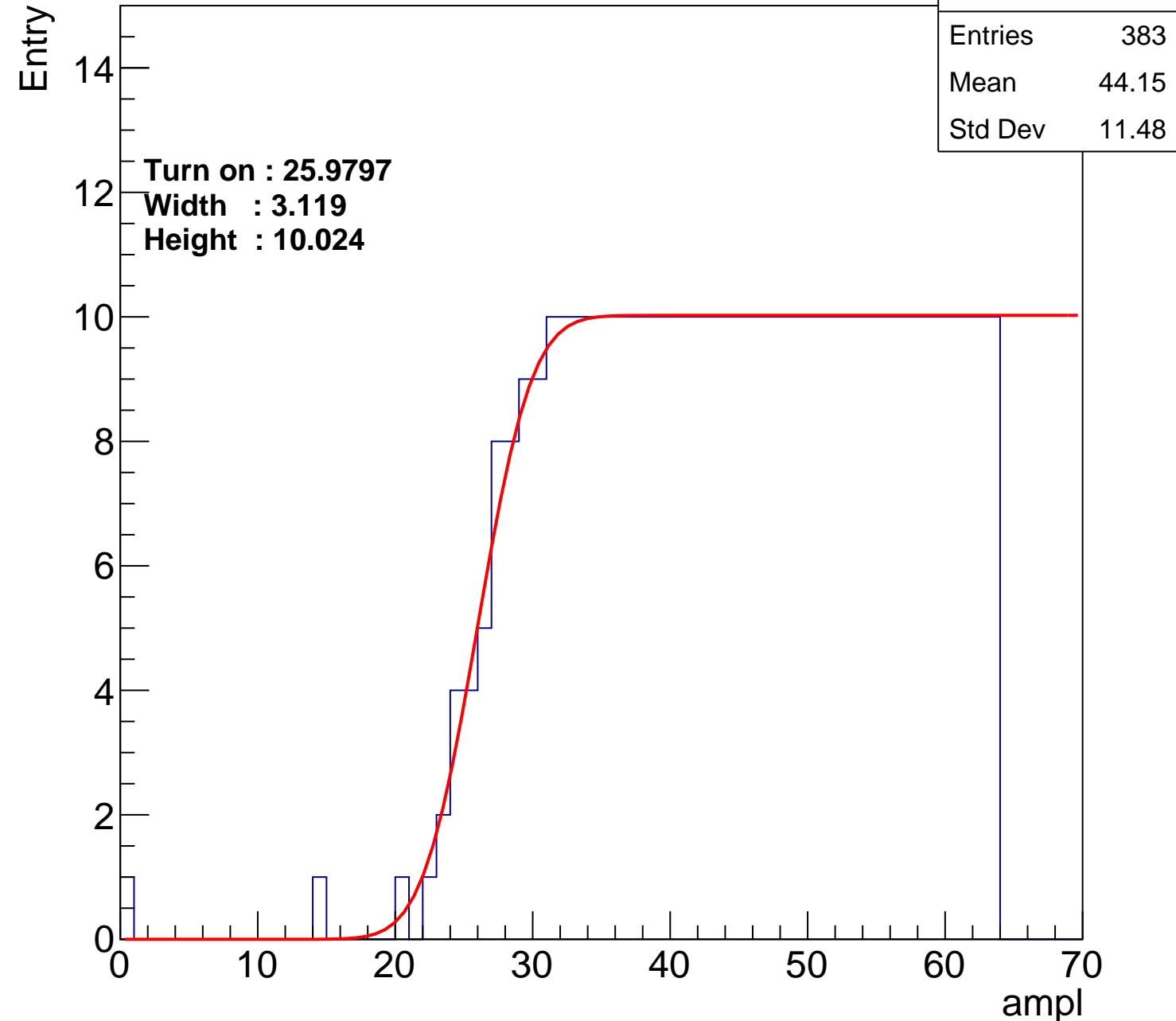
Width : 3.119

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch104

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.55
Std Dev	12.02

Turn on : 25.3445

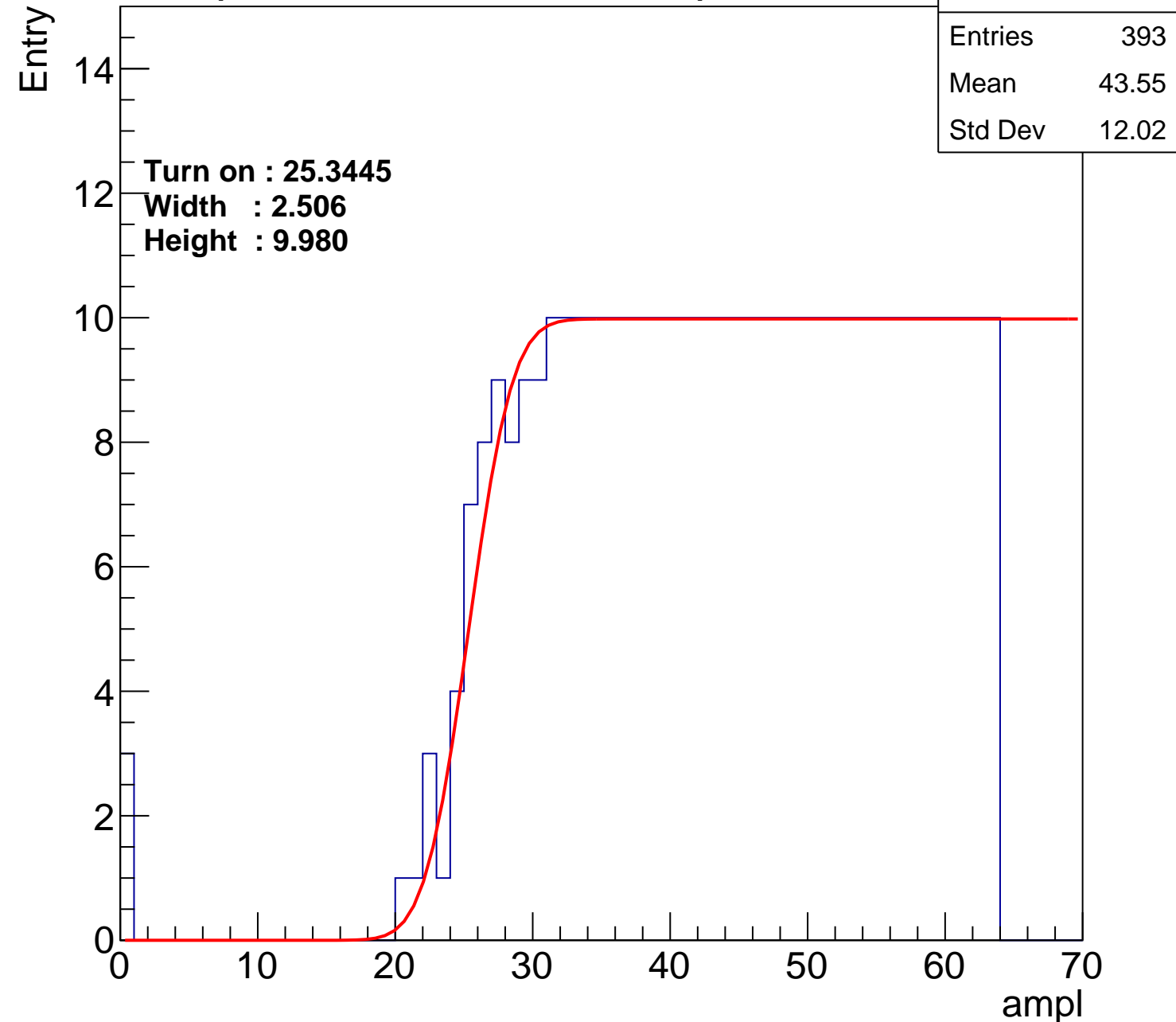
Width : 2.506

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch105

calib_packv5_042523_0143.root, FC#4, port A2

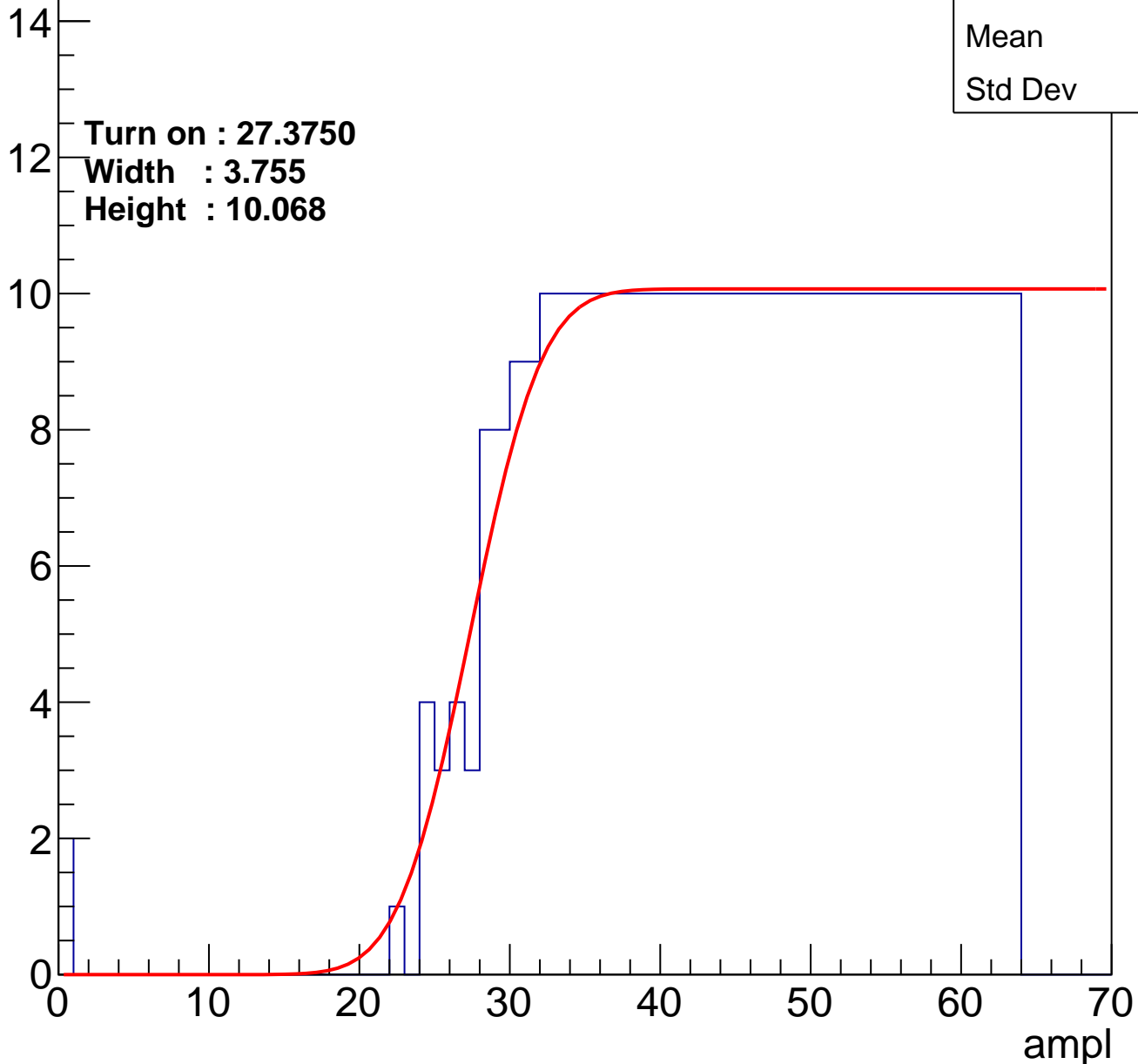
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.3750

Width : 3.755

Height : 10.068

Entry



B1L100S, U14-ch106

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.67
Std Dev	12.11

Turn on : 25.3445

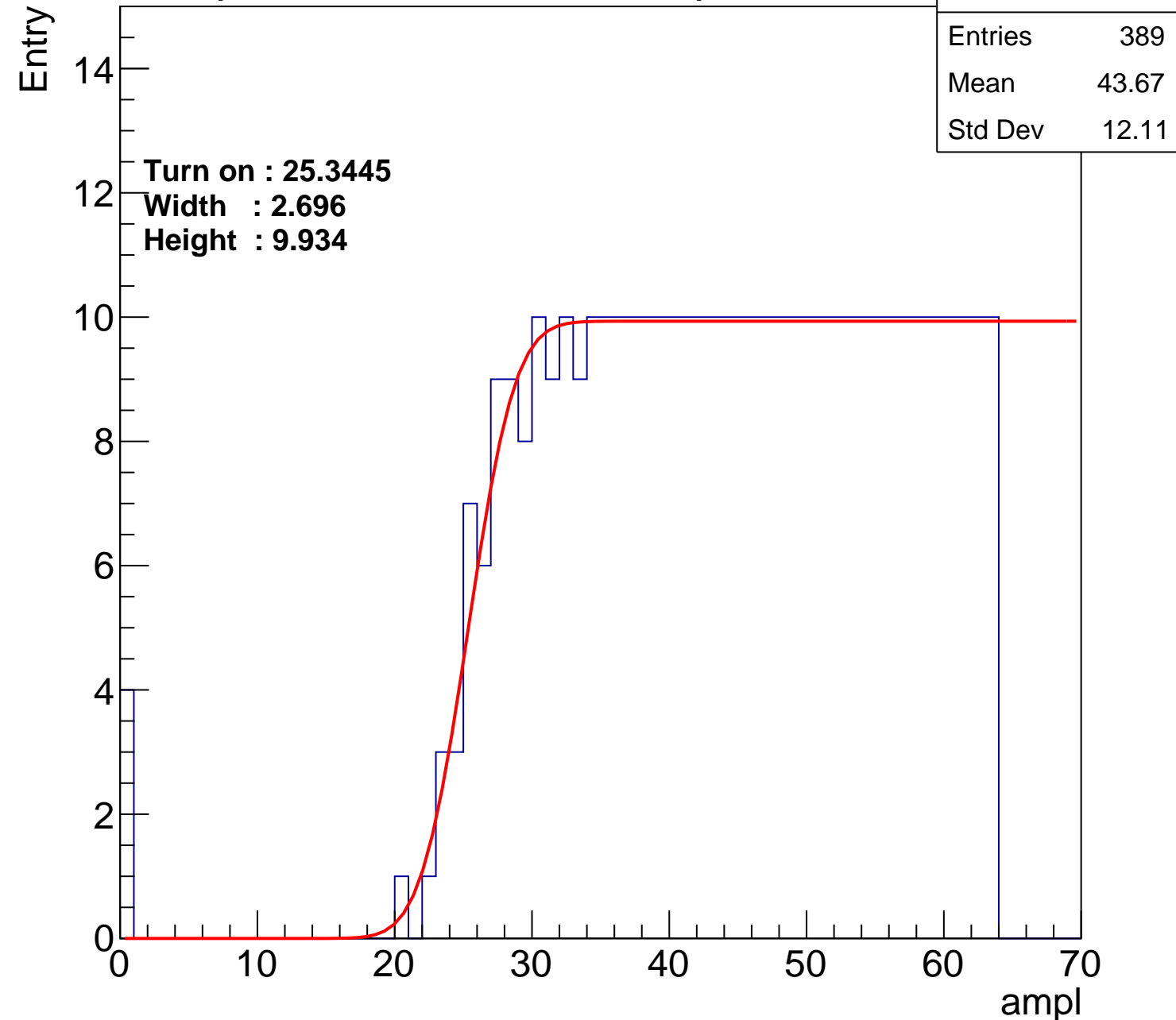
Width : 2.696

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.3070

Width : 3.095

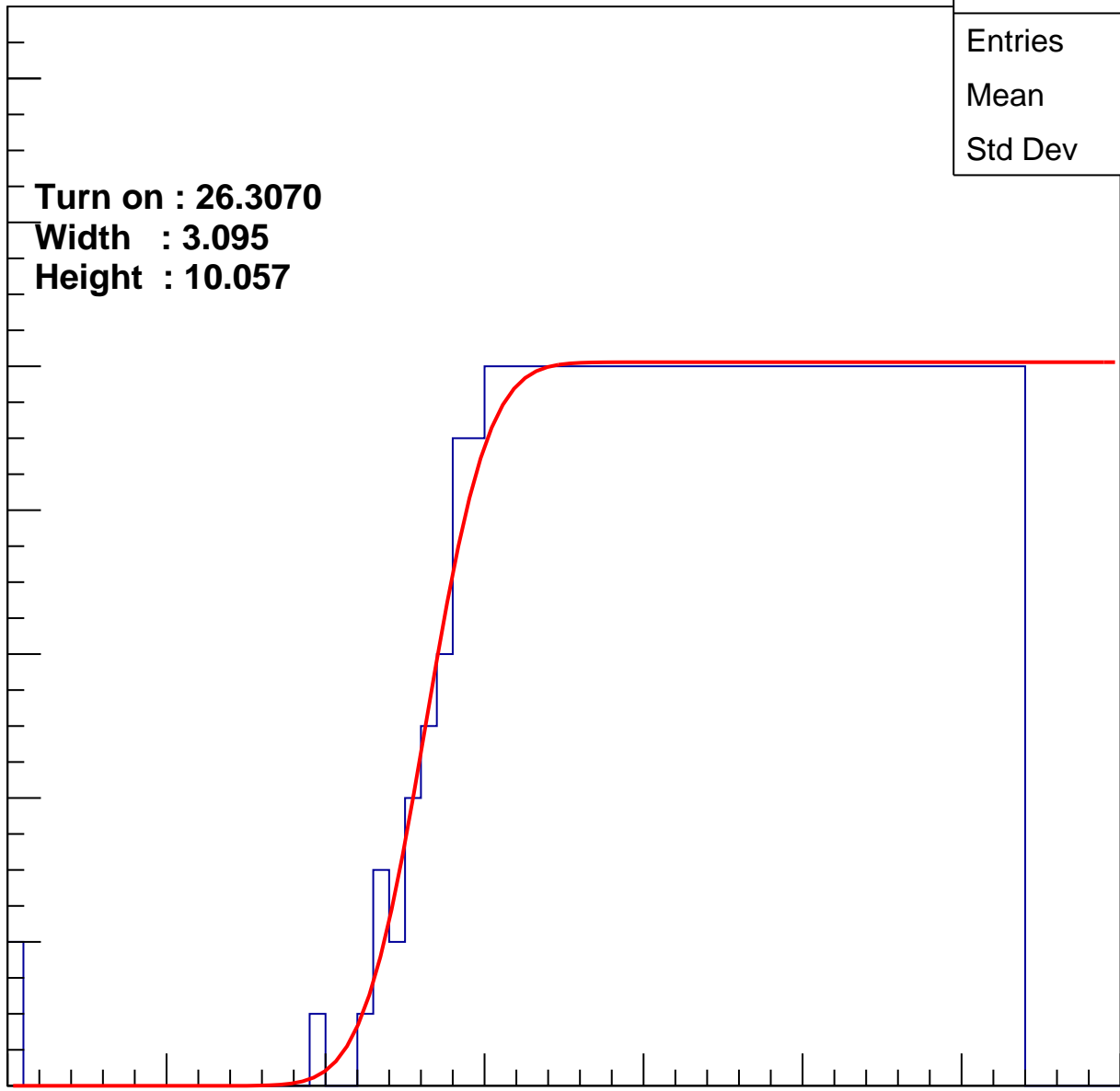
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U14-ch108

calib_packv5_042523_0143.root, FC#4, port A2

Entries	409
Mean	42.68
Std Dev	12.67

Turn on : 24.0864

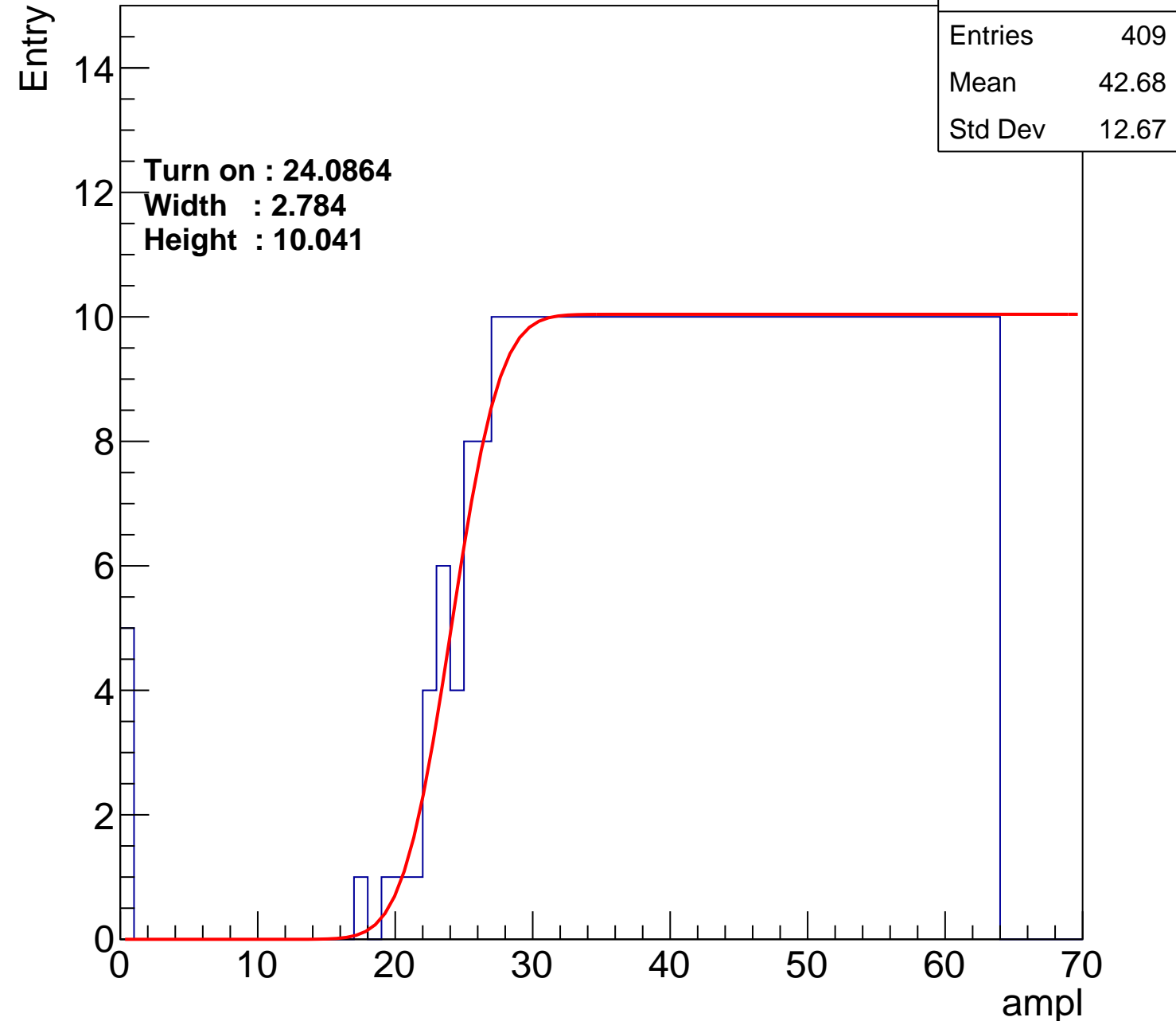
Width : 2.784

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch109

calib_packv5_042523_0143.root, FC#4, port A2

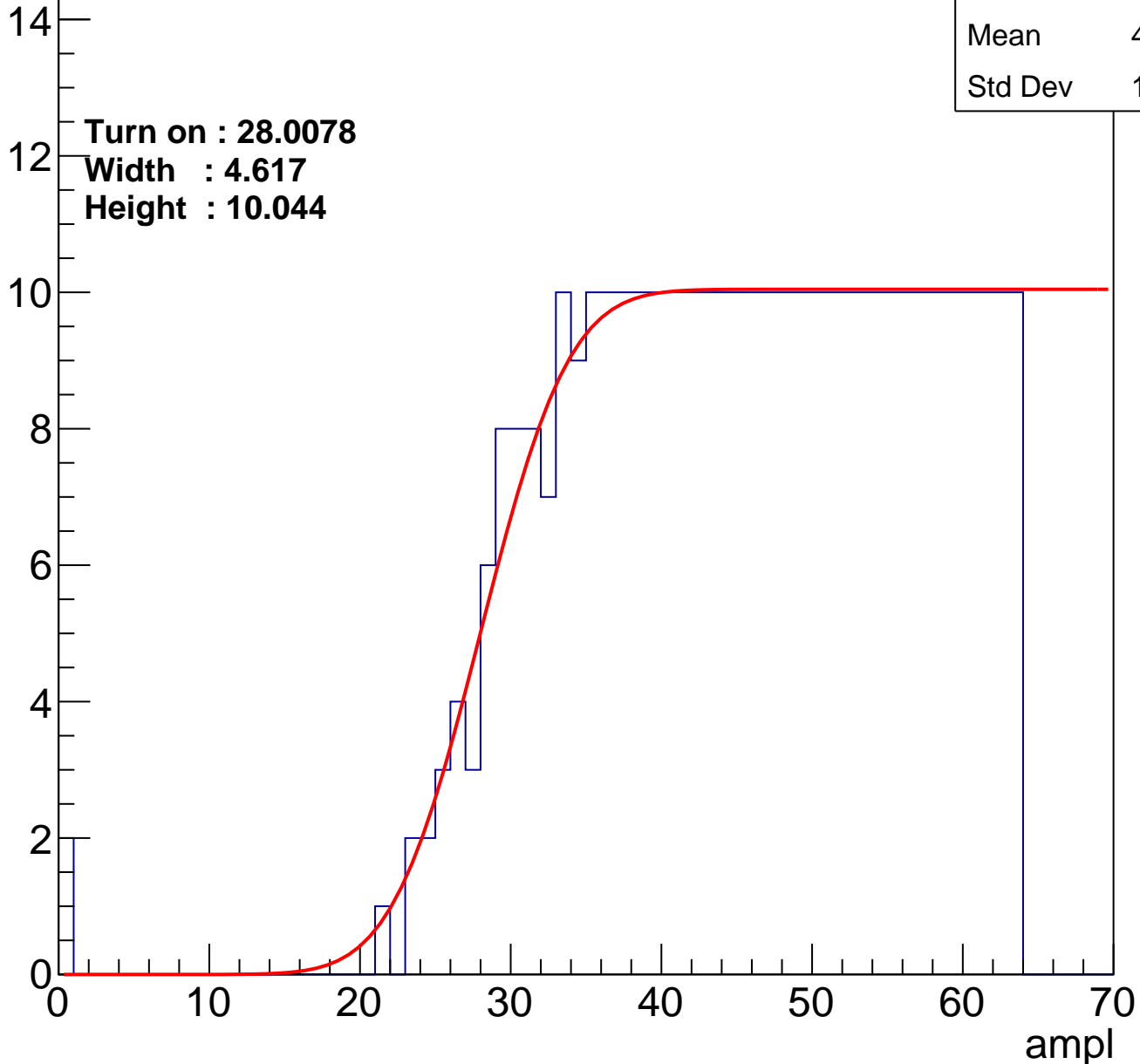
Entries	363
Mean	44.99
Std Dev	11.24

Turn on : 28.0078

Width : 4.617

Height : 10.044

Entry



B1L100S, U14-ch110

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
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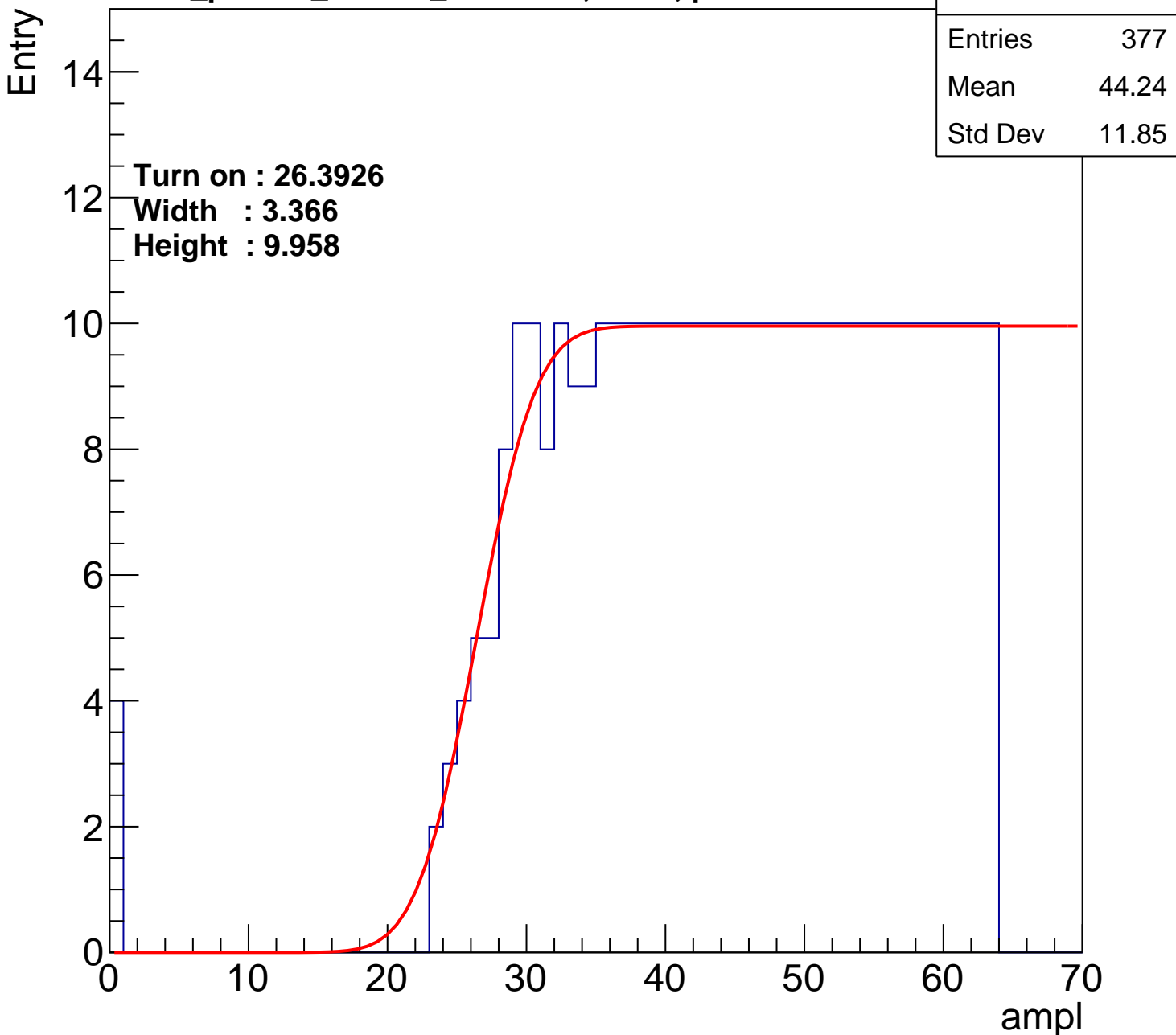
Mean	44.24
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Std Dev	11.85
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Turn on : 26.3926

Width : 3.366

Height : 9.958



B1L100S, U14-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.2
Std Dev	11.55

Turn on : 26.0874

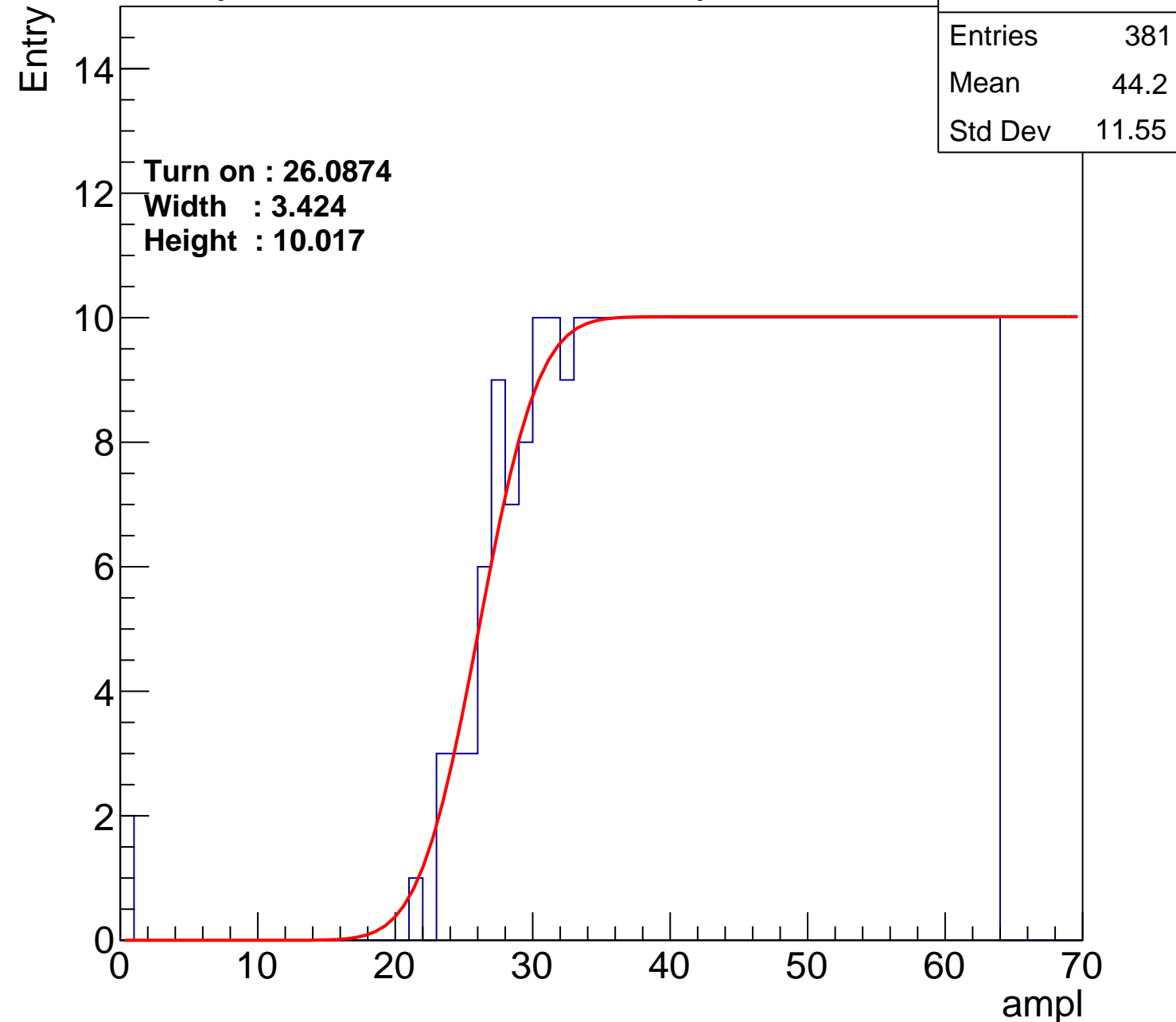
Width : 3.424

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch112

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.87
Std Dev	11.71

Turn on : 25.2974

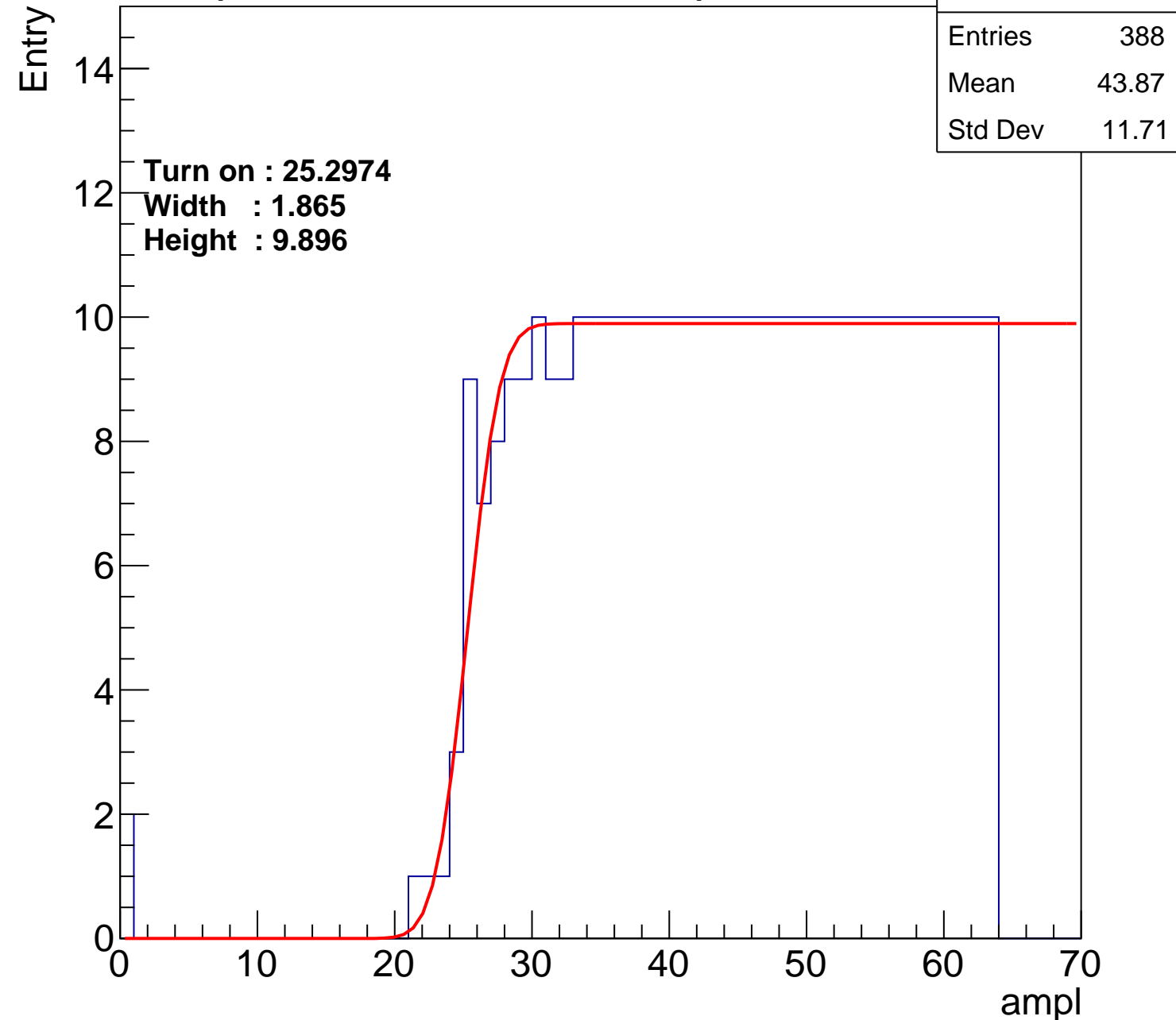
Width : 1.865

Height : 9.896

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.37
Std Dev	11.57

Turn on : 26.9186

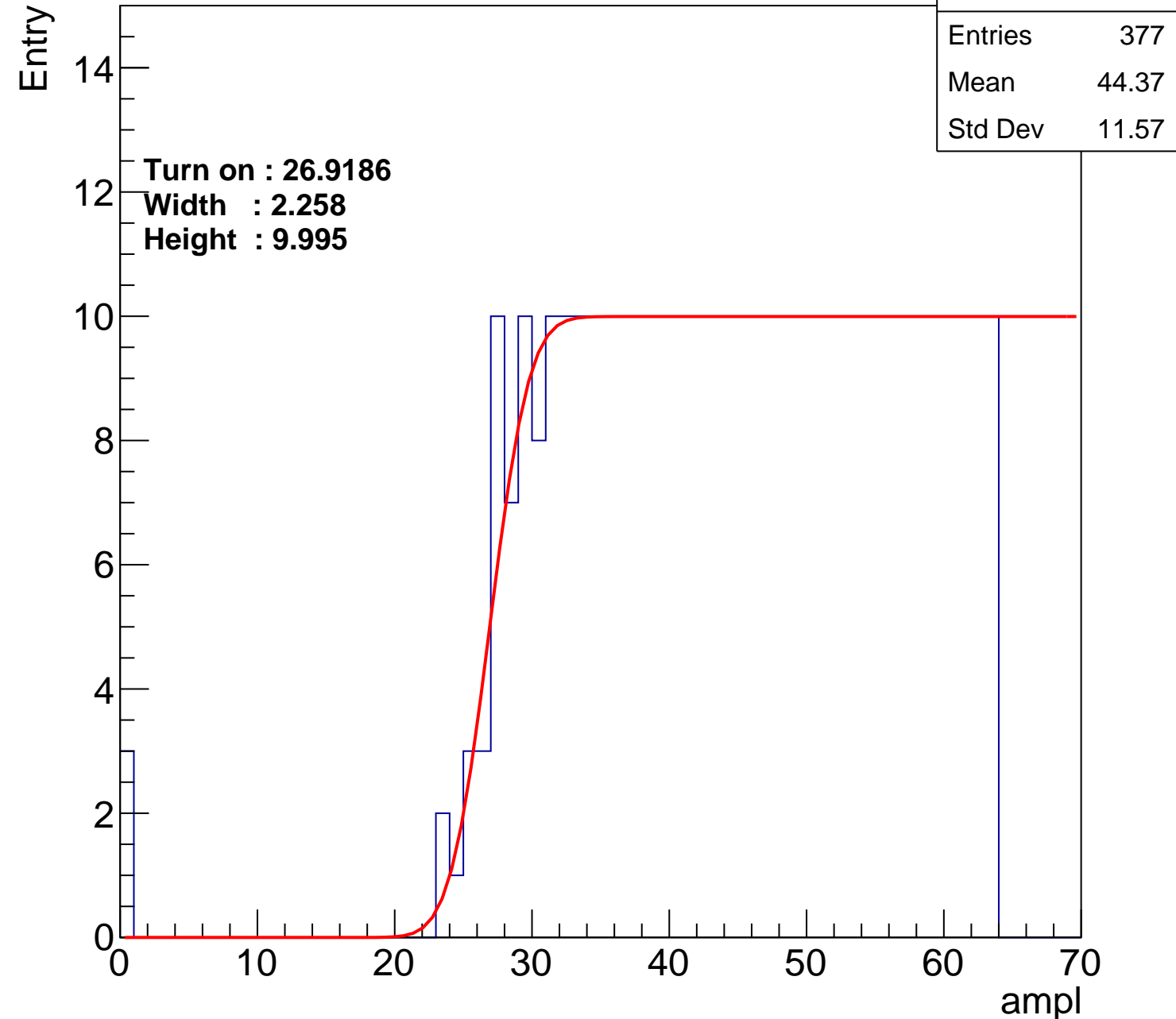
Width : 2.258

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.99
Std Dev	11.57

Turn on : 25.4746

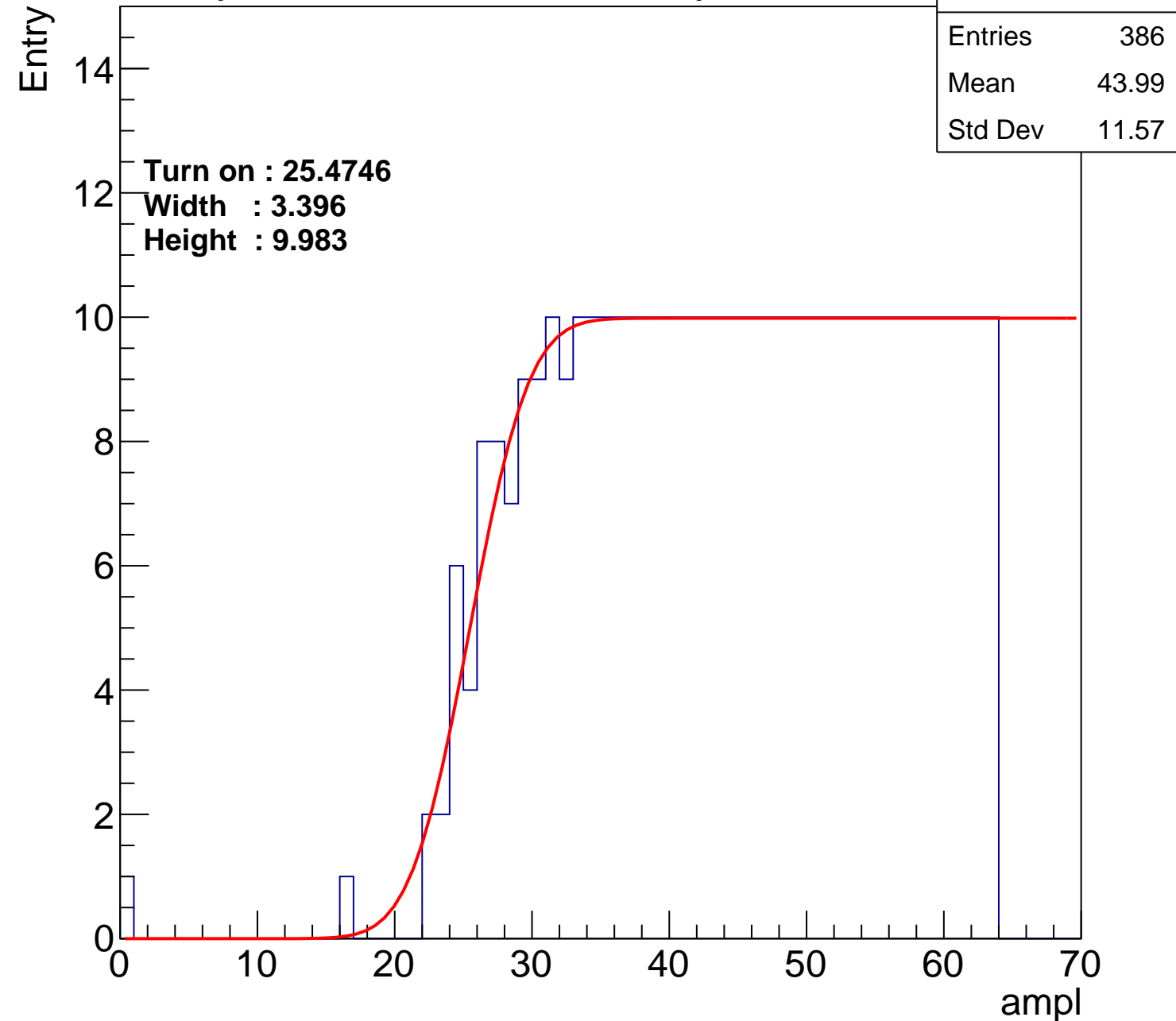
Width : 3.396

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch115

calib_packv5_042523_0143.root, FC#4, port A2

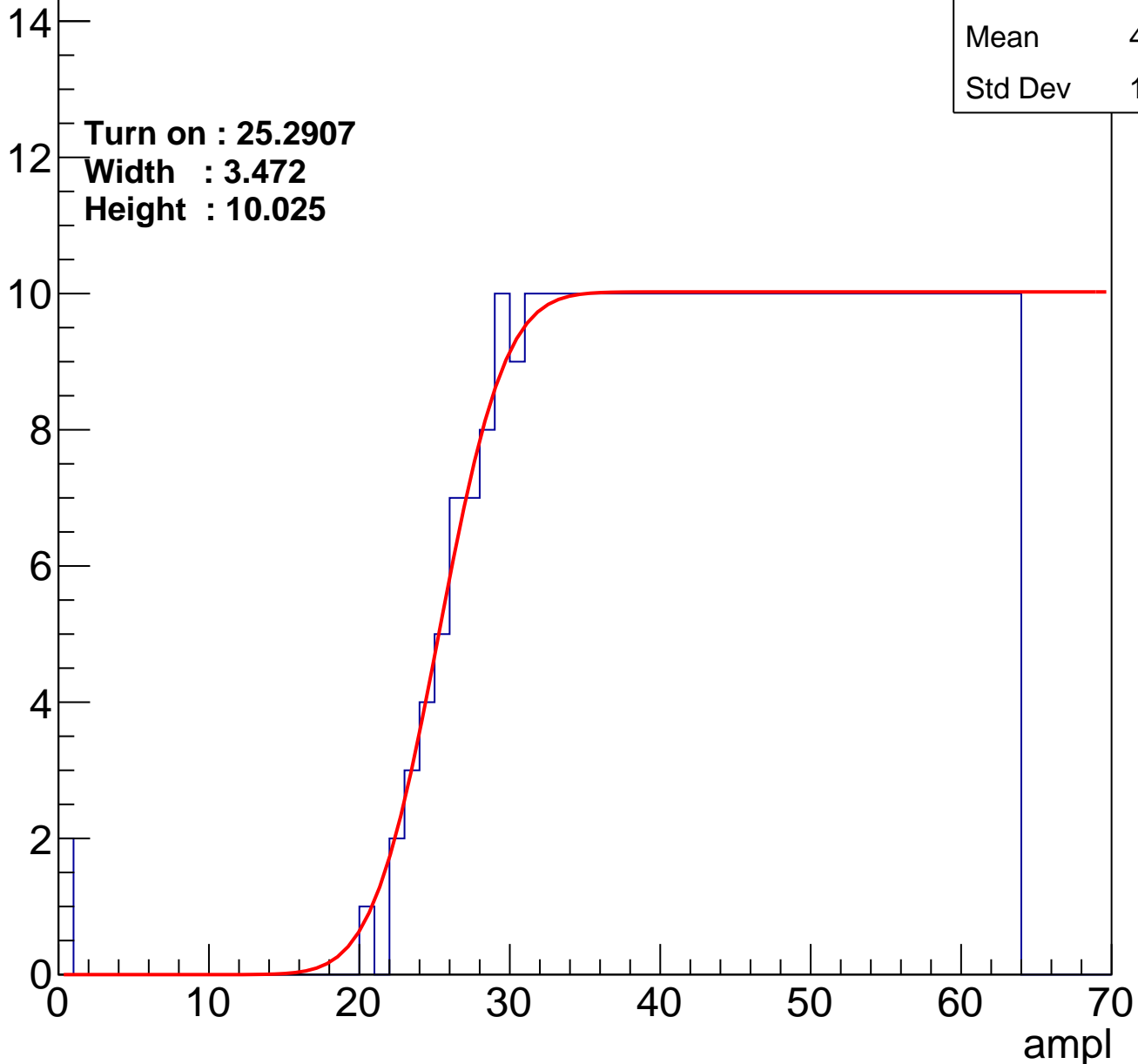
Entries	388
Mean	43.86
Std Dev	11.73

Turn on : 25.2907

Width : 3.472

Height : 10.025

Entry



B1L100S, U14-ch116

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.83
Std Dev	11.44

Turn on : 27.7682

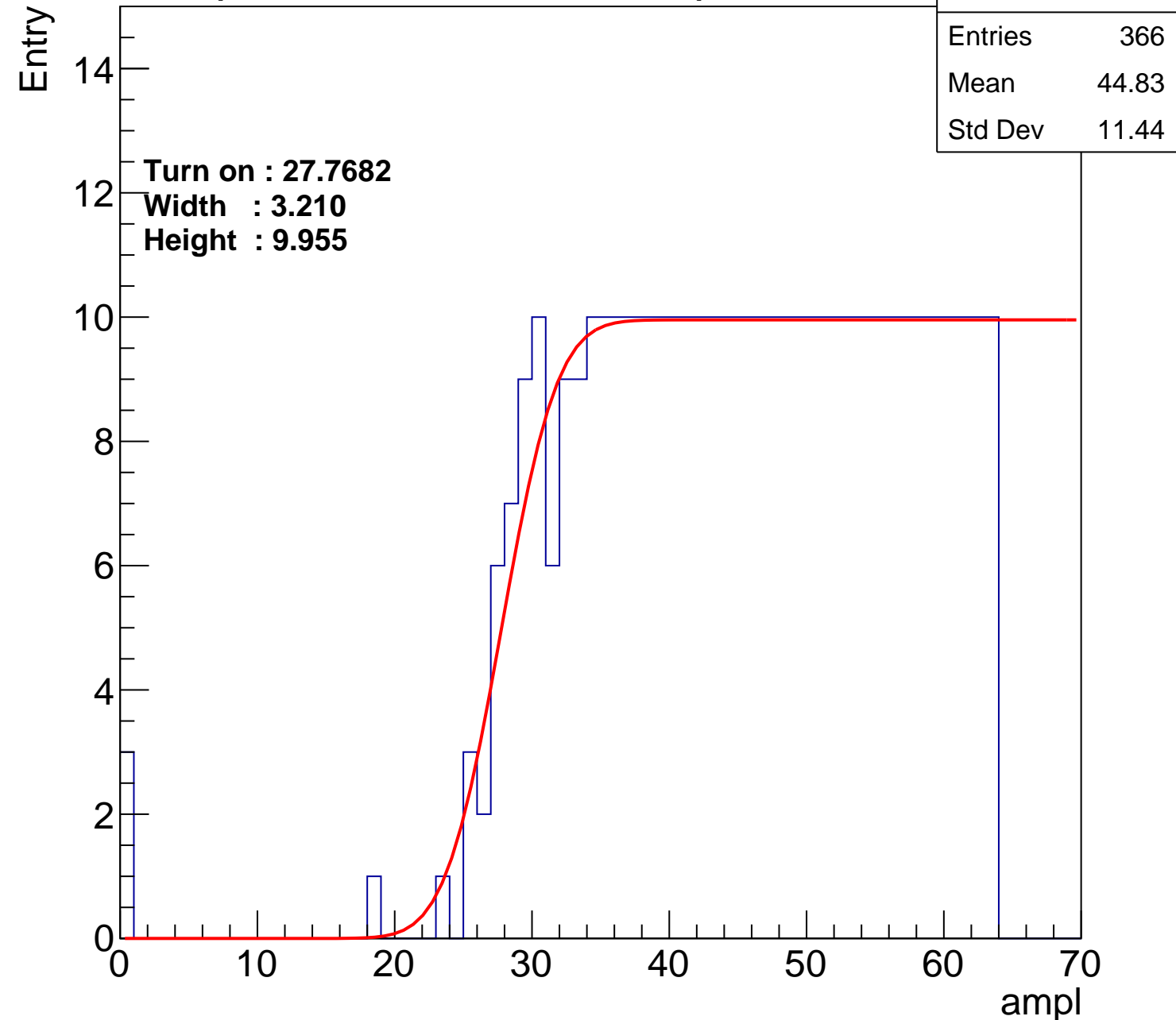
Width : 3.210

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch117

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 24.6152

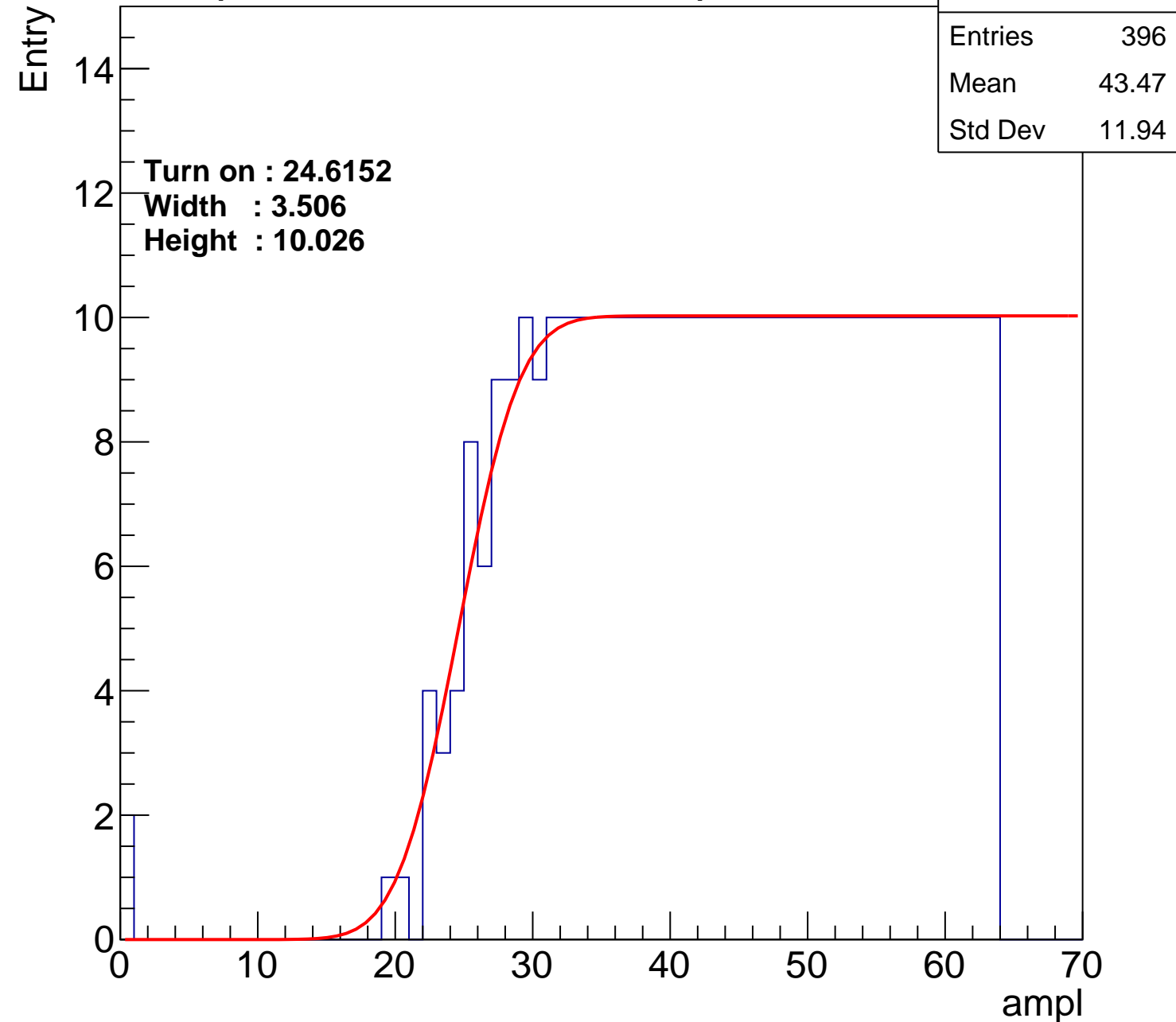
Width : 3.506

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch118

calib_packv5_042523_0143.root, FC#4, port A2

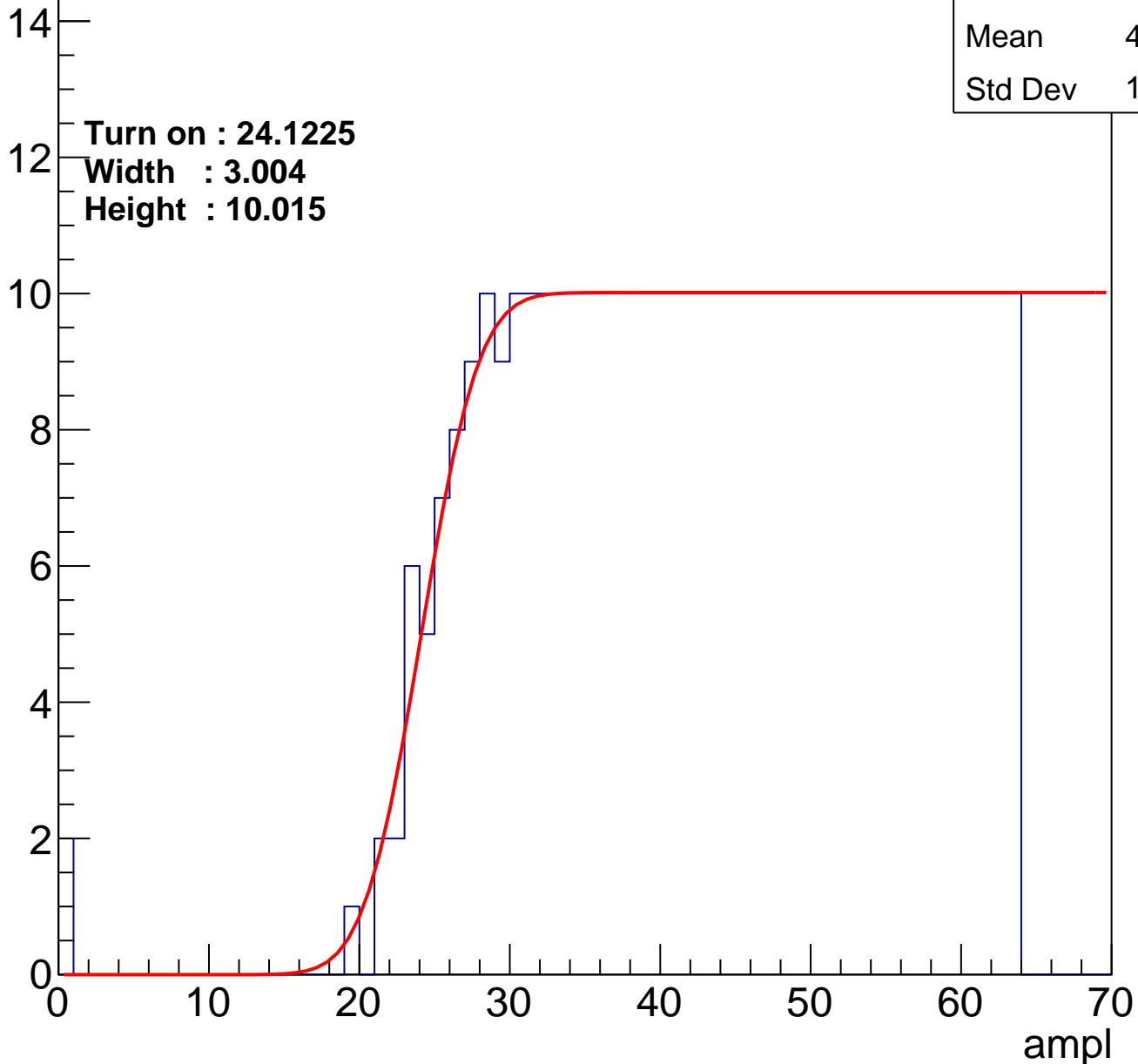
Entries	401
Mean	43.24
Std Dev	12.04

Turn on : 24.1225

Width : 3.004

Height : 10.015

Entry



B1L100S, U14-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.07
Std Dev	11.86

Turn on : 26.4490

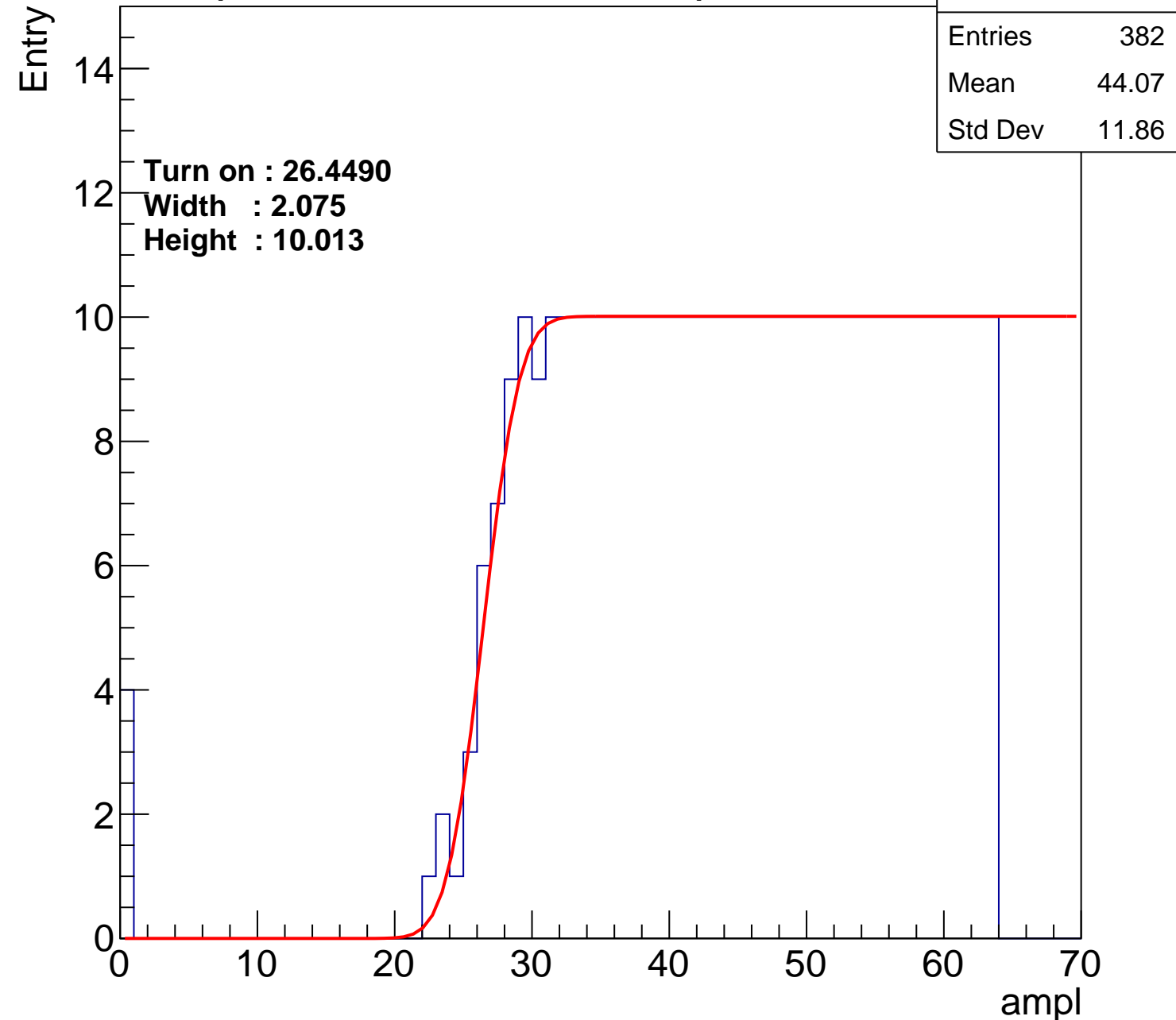
Width : 2.075

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch120

calib_packv5_042523_0143.root, FC#4, port A2

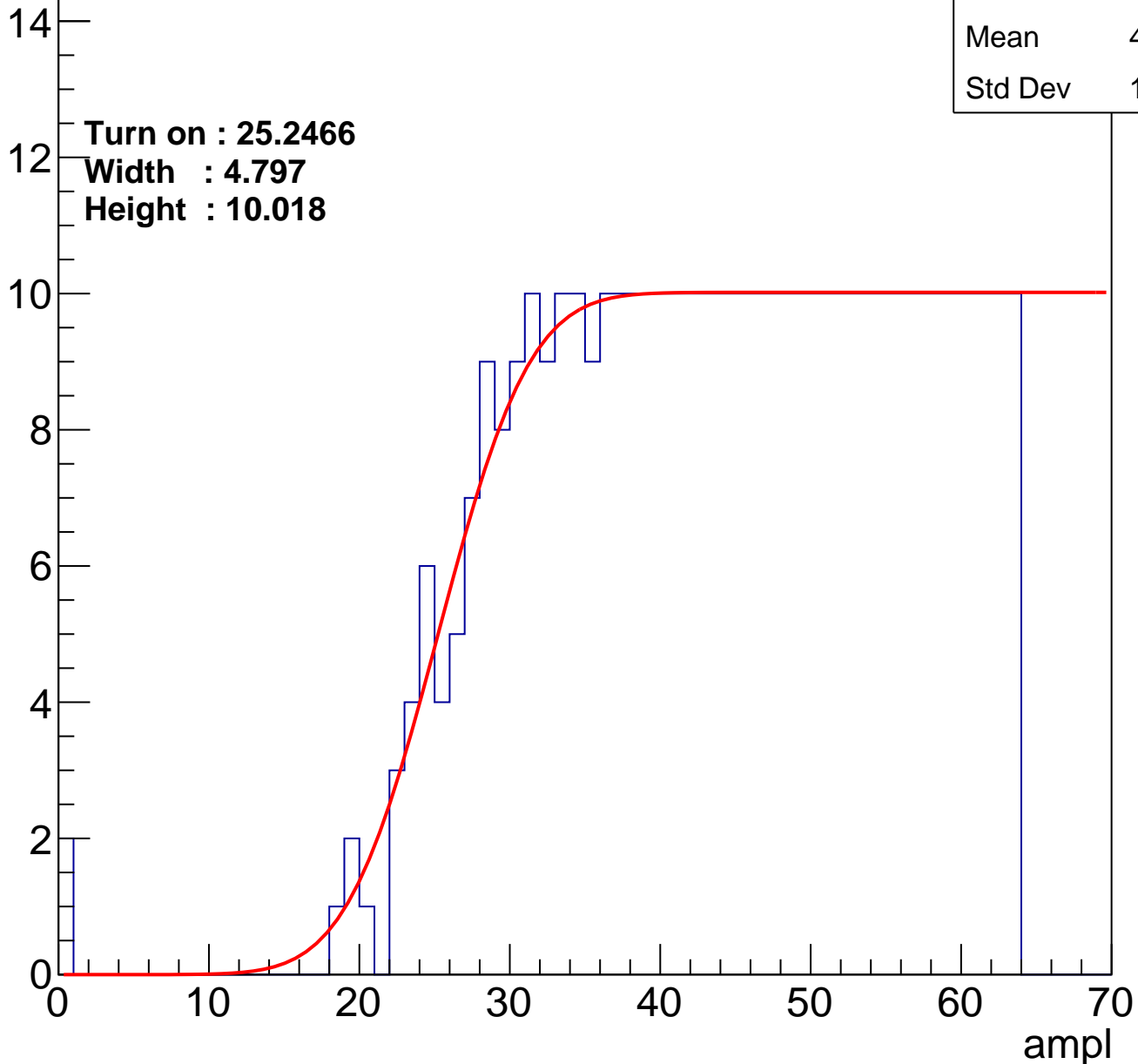
Entries	389
Mean	43.69
Std Dev	11.95

Turn on : 25.2466

Width : 4.797

Height : 10.018

Entry



B1L100S, U14-ch121

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 26.9156

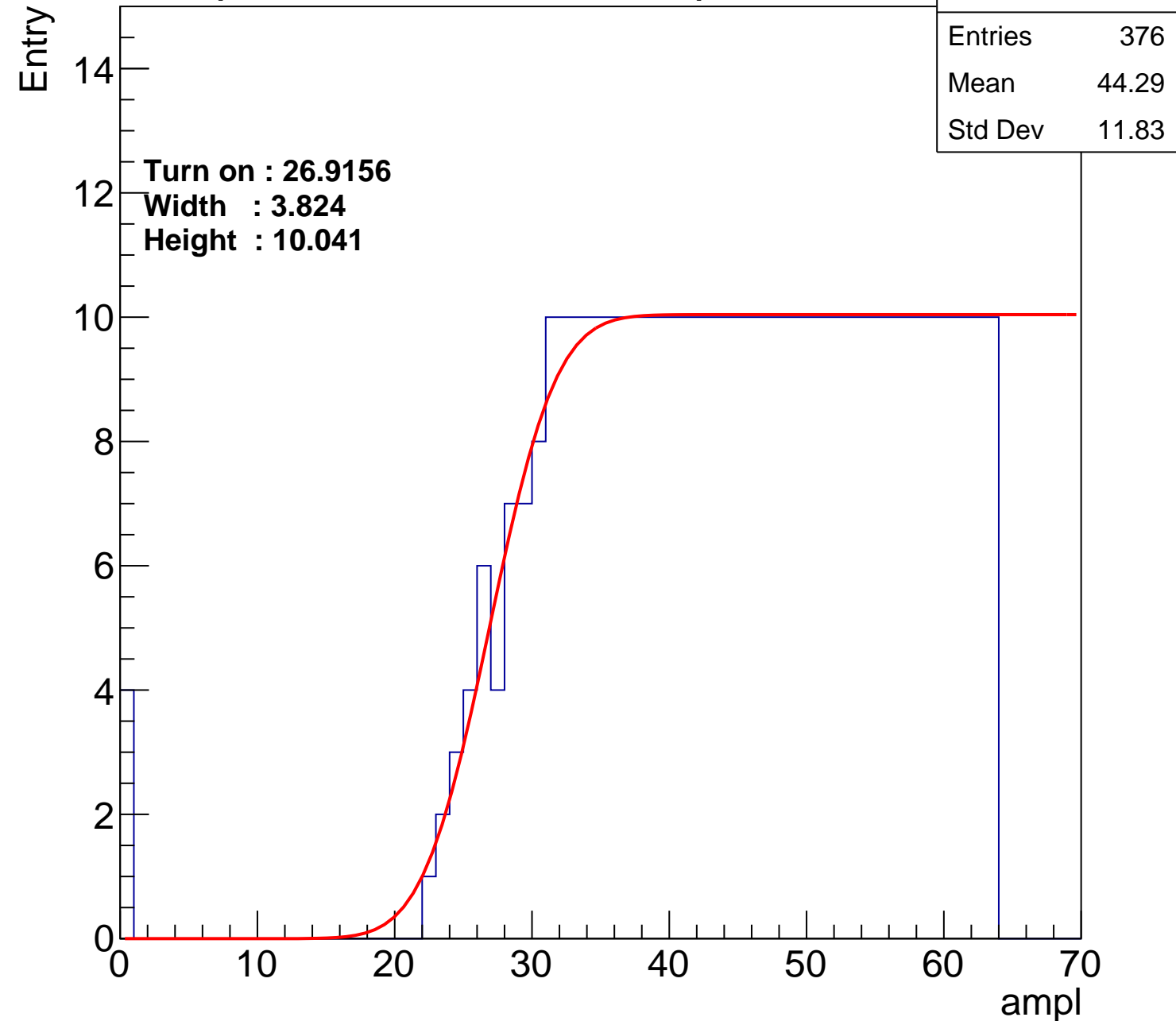
Width : 3.824

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch122

calib_packv5_042523_0143.root, FC#4, port A2

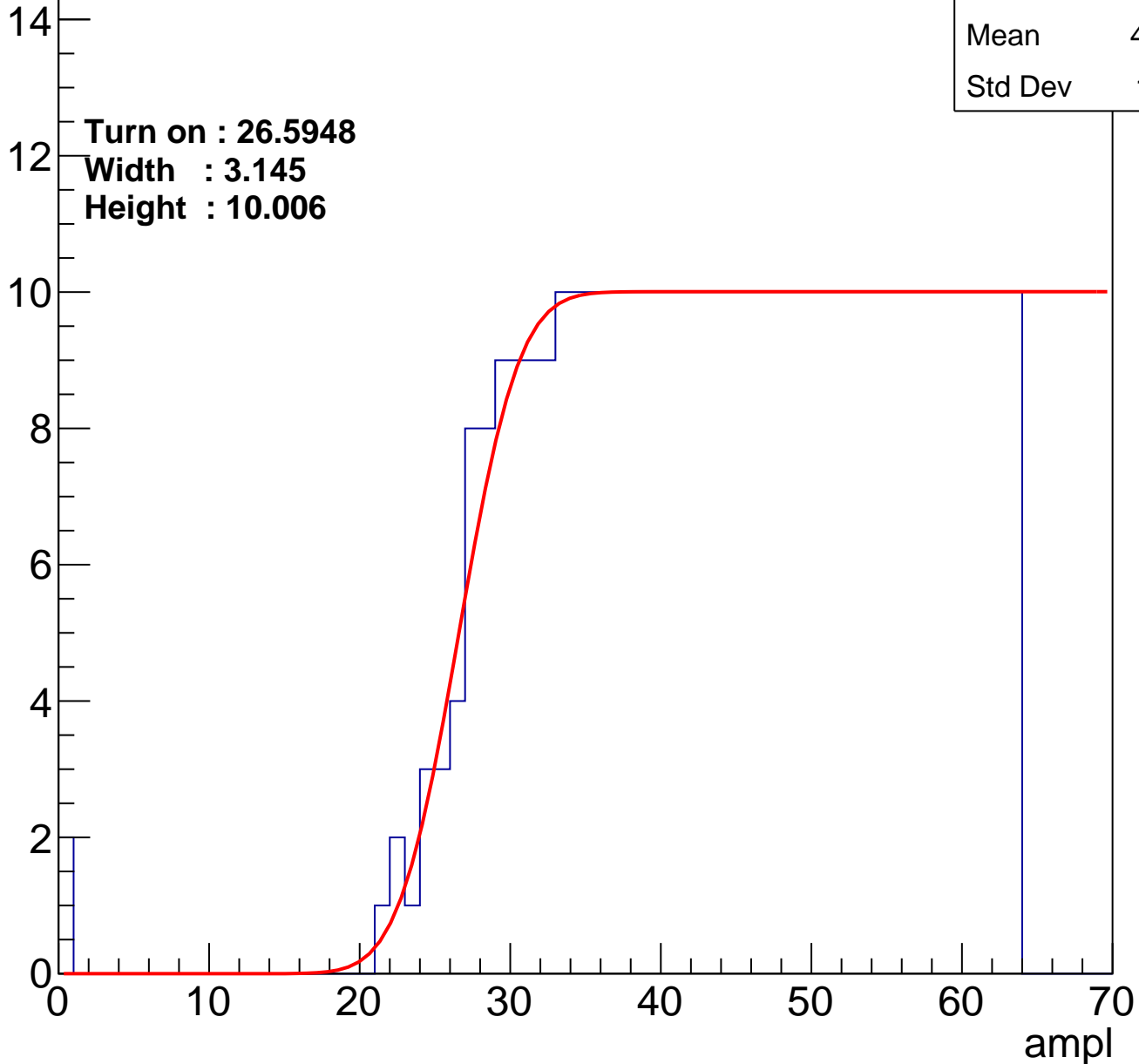
Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.5948

Width : 3.145

Height : 10.006

Entry



B1L100S, U14-ch123

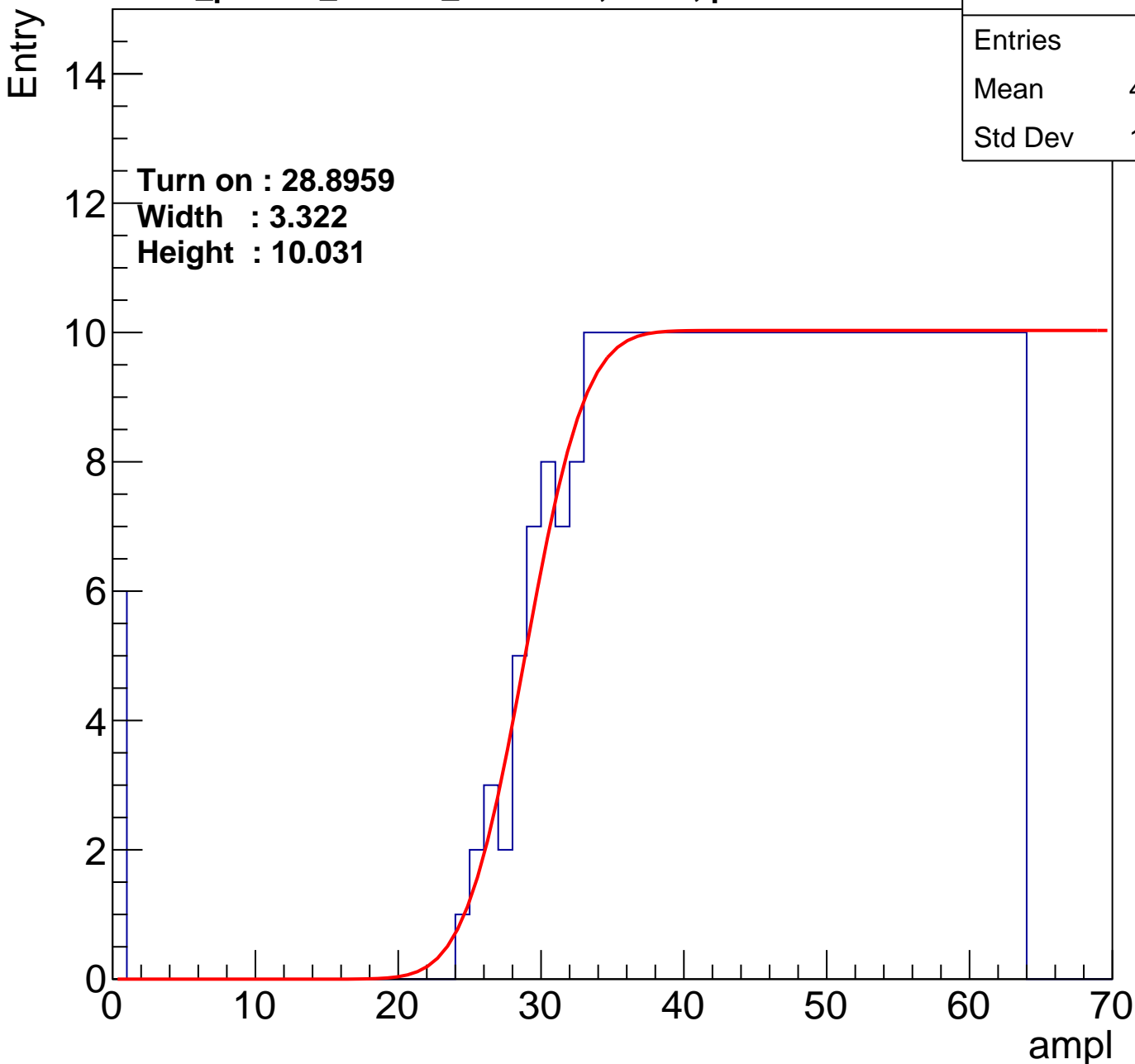
calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	44.96
Std Dev	11.86

Turn on : 28.8959

Width : 3.322

Height : 10.031



B1L100S, U14-ch124

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 26.0339

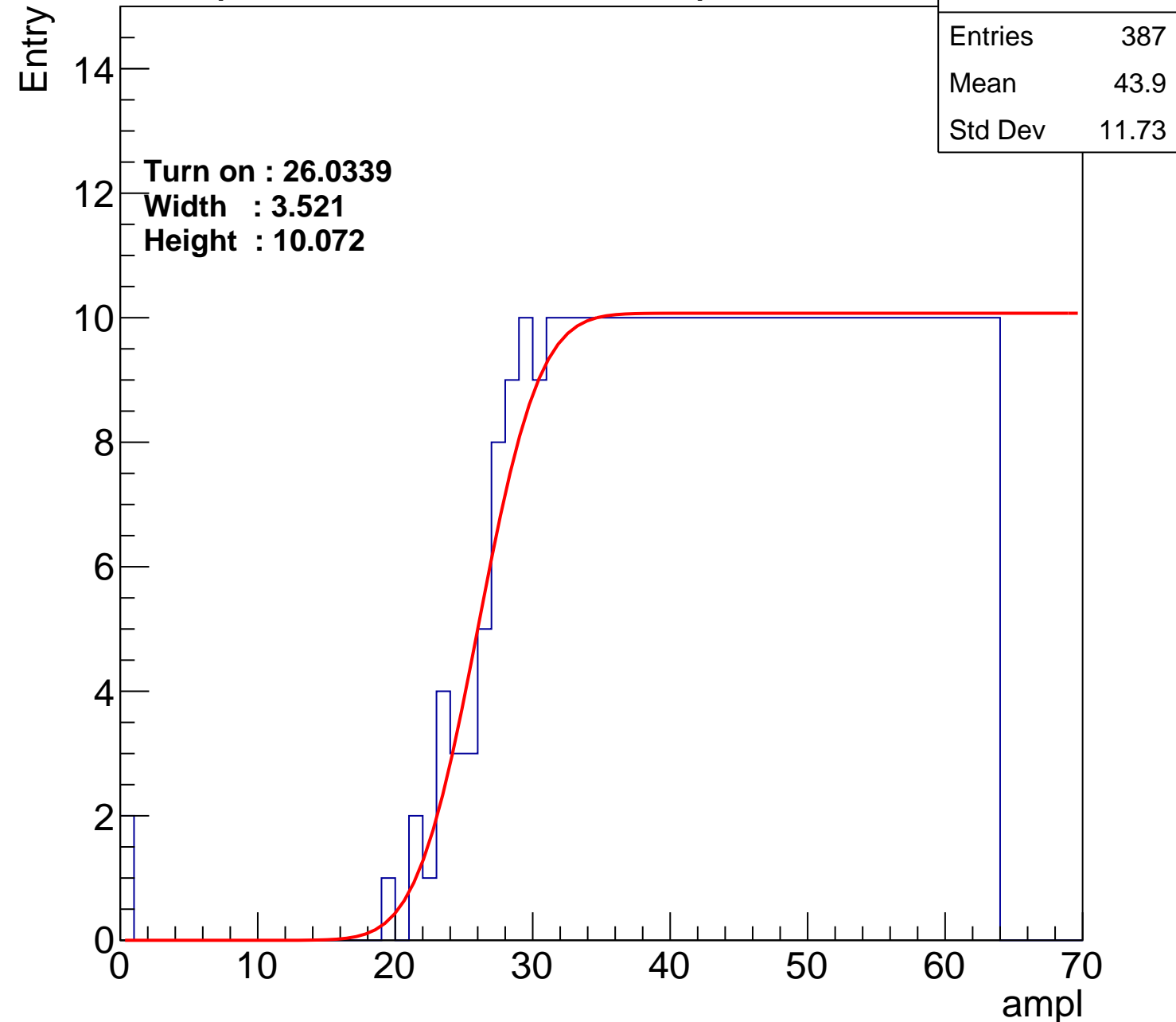
Width : 3.521

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch125

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.89
Std Dev	11.98

Turn on : 26.0091

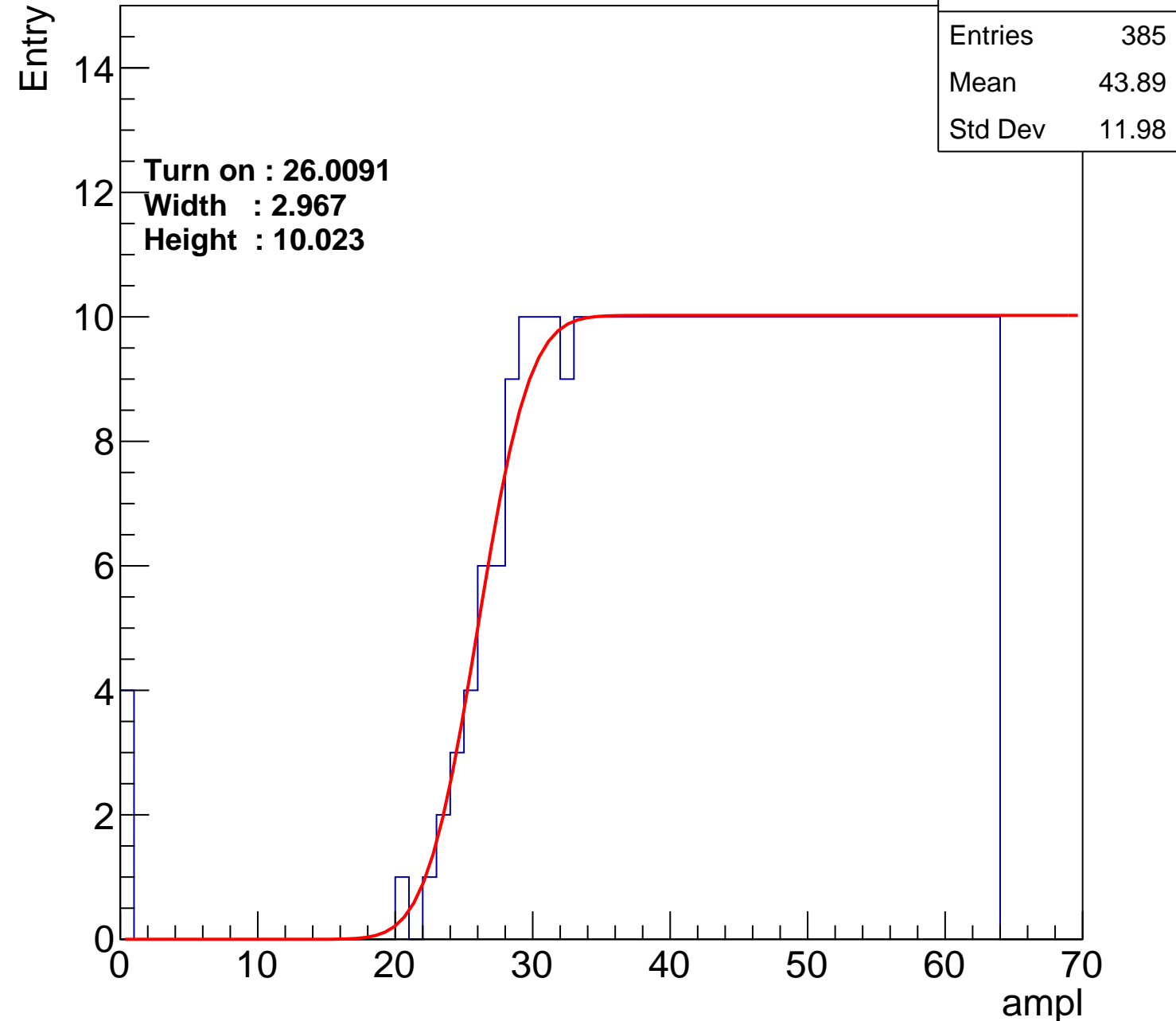
Width : 2.967

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch126

calib_packv5_042523_0143.root, FC#4, port A2

Entries	400
Mean	43.28
Std Dev	12.04

Turn on : 24.3993

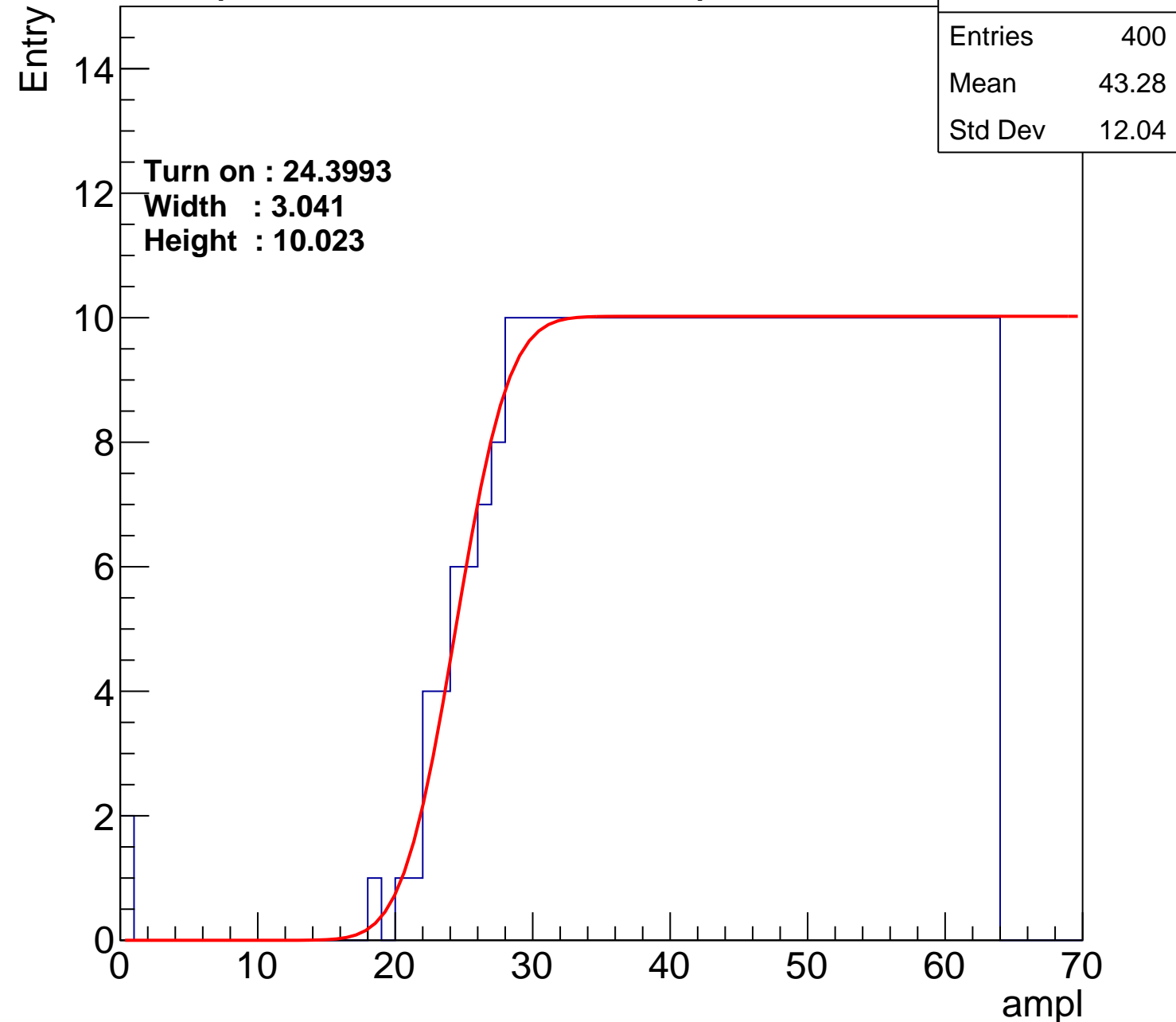
Width : 3.041

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U14-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
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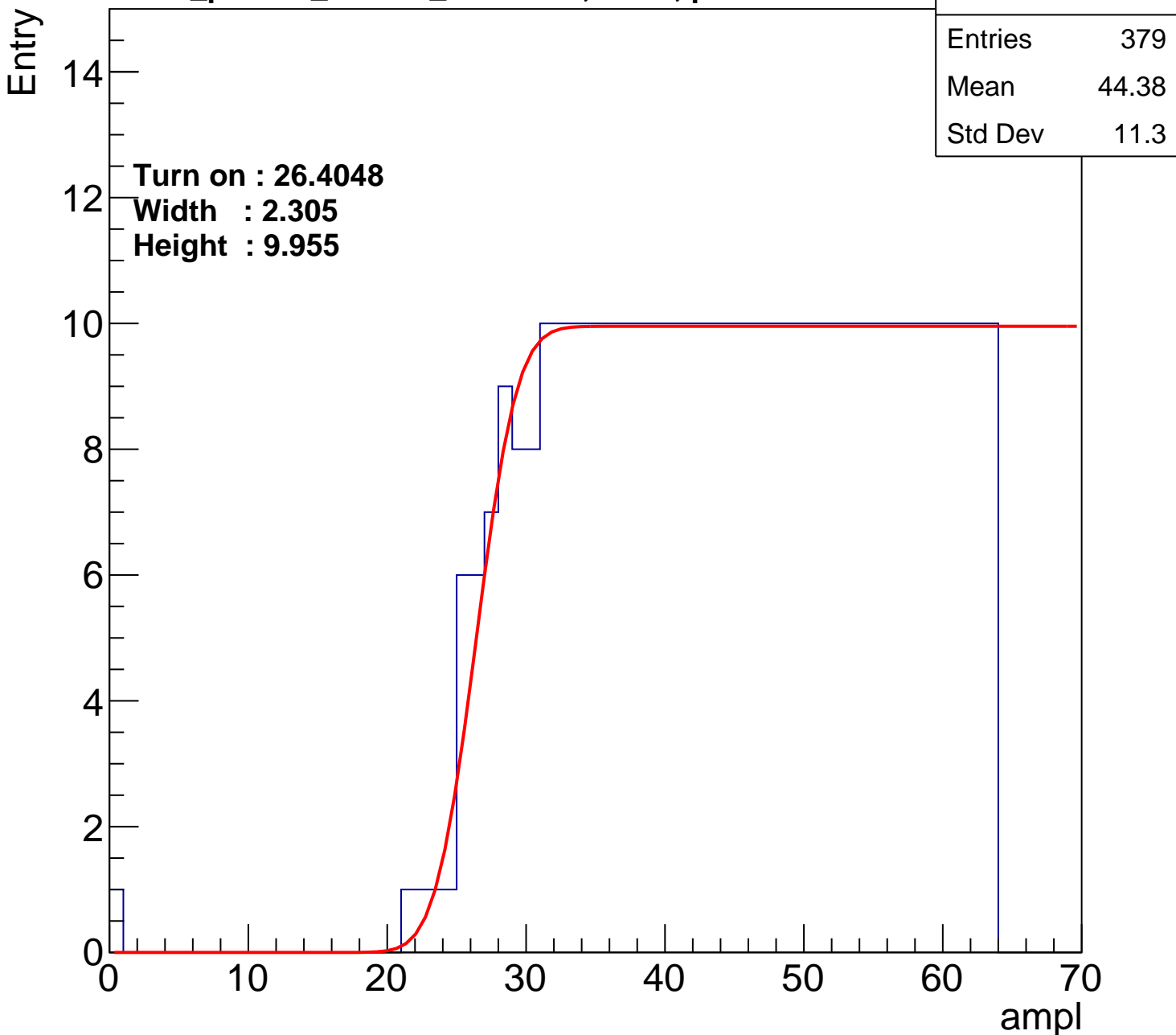
Mean	44.38
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Std Dev	11.3
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Turn on : 26.4048

Width : 2.305

Height : 9.955



B1L100S, U14-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 26.4048

Width : 2.305

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl

