

B1L102S, U24-ch0

calib_packv5_042523_0143.root, FC#11, port A2

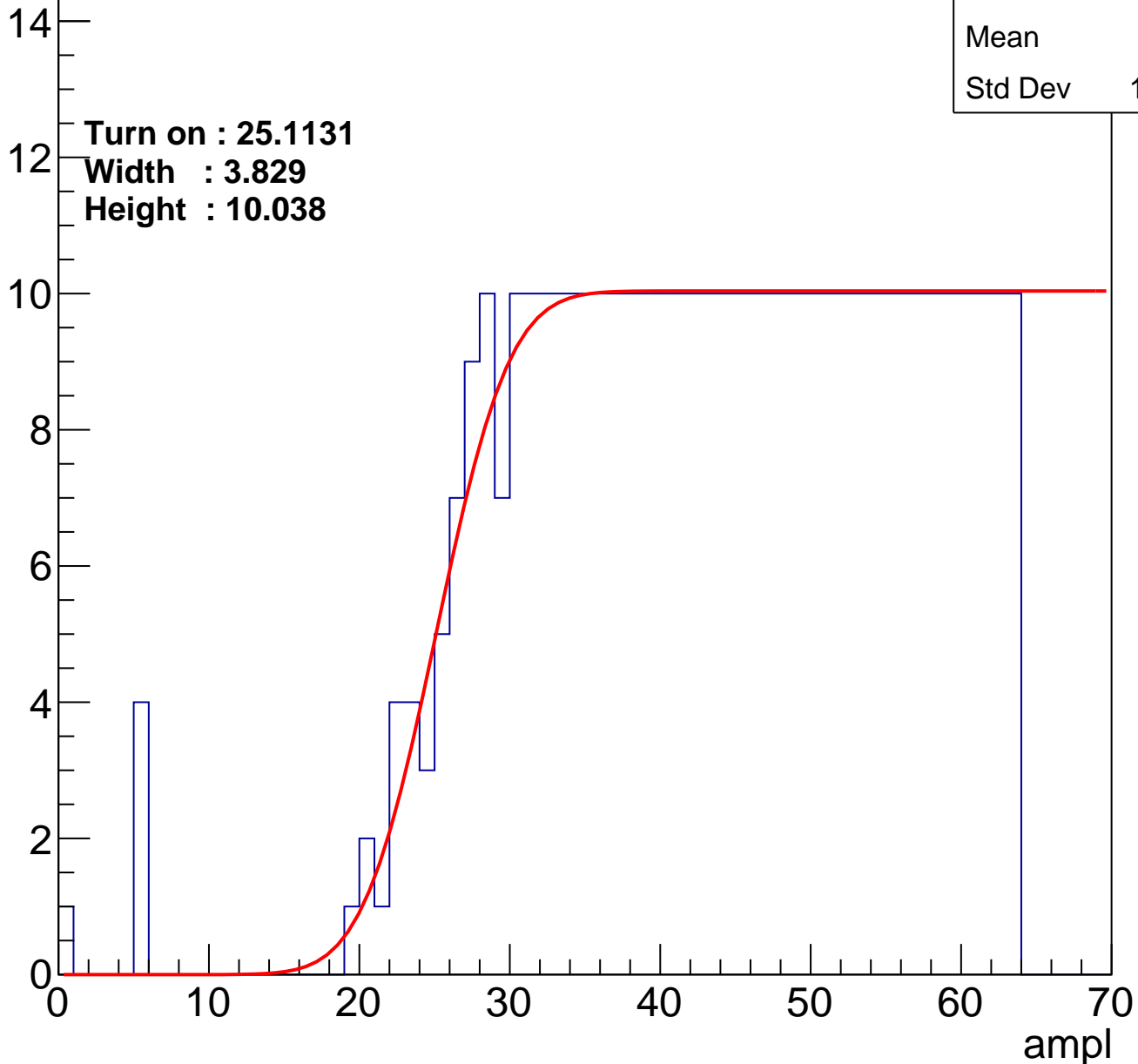
Entries	398
Mean	43.2
Std Dev	12.34

Turn on : 25.1131

Width : 3.829

Height : 10.038

Entry



B1L102S, U24-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.95
Std Dev	11.03

Turn on : 27.9056

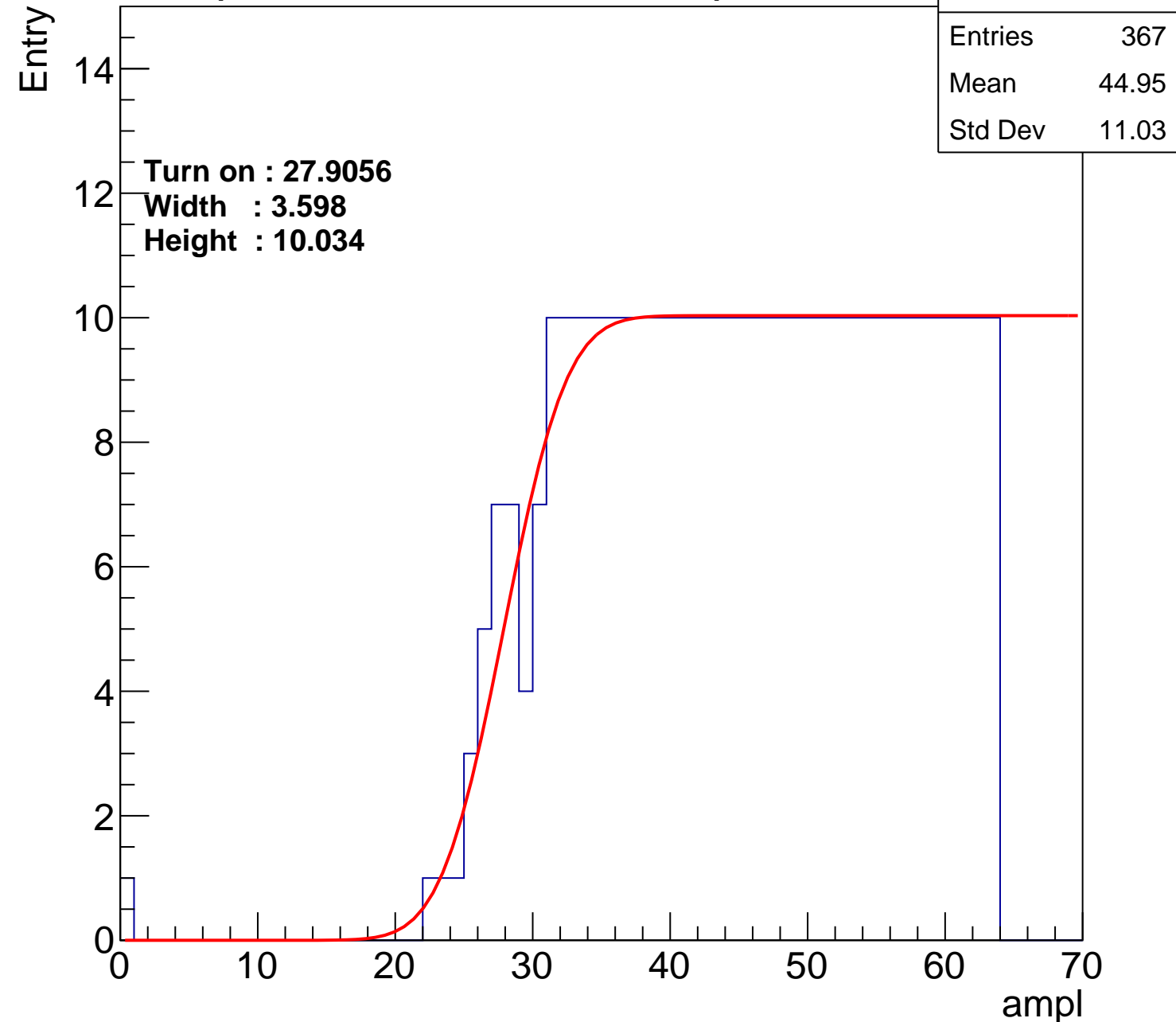
Width : 3.598

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.68
Std Dev	11.99

Turn on : 25.4066

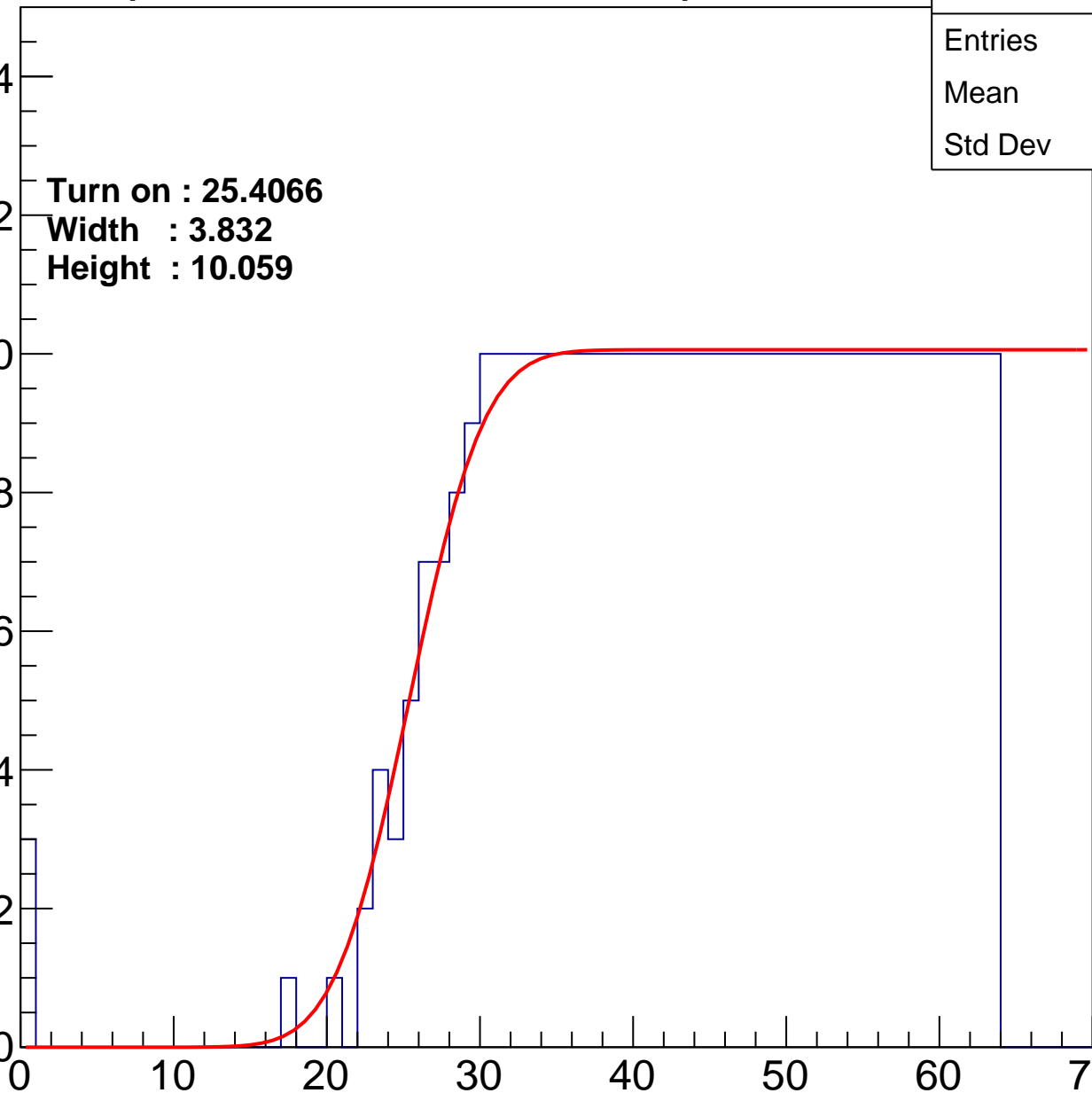
Width : 3.832

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	357
Mean	45.47
Std Dev	10.71

Turn on : 28.6472

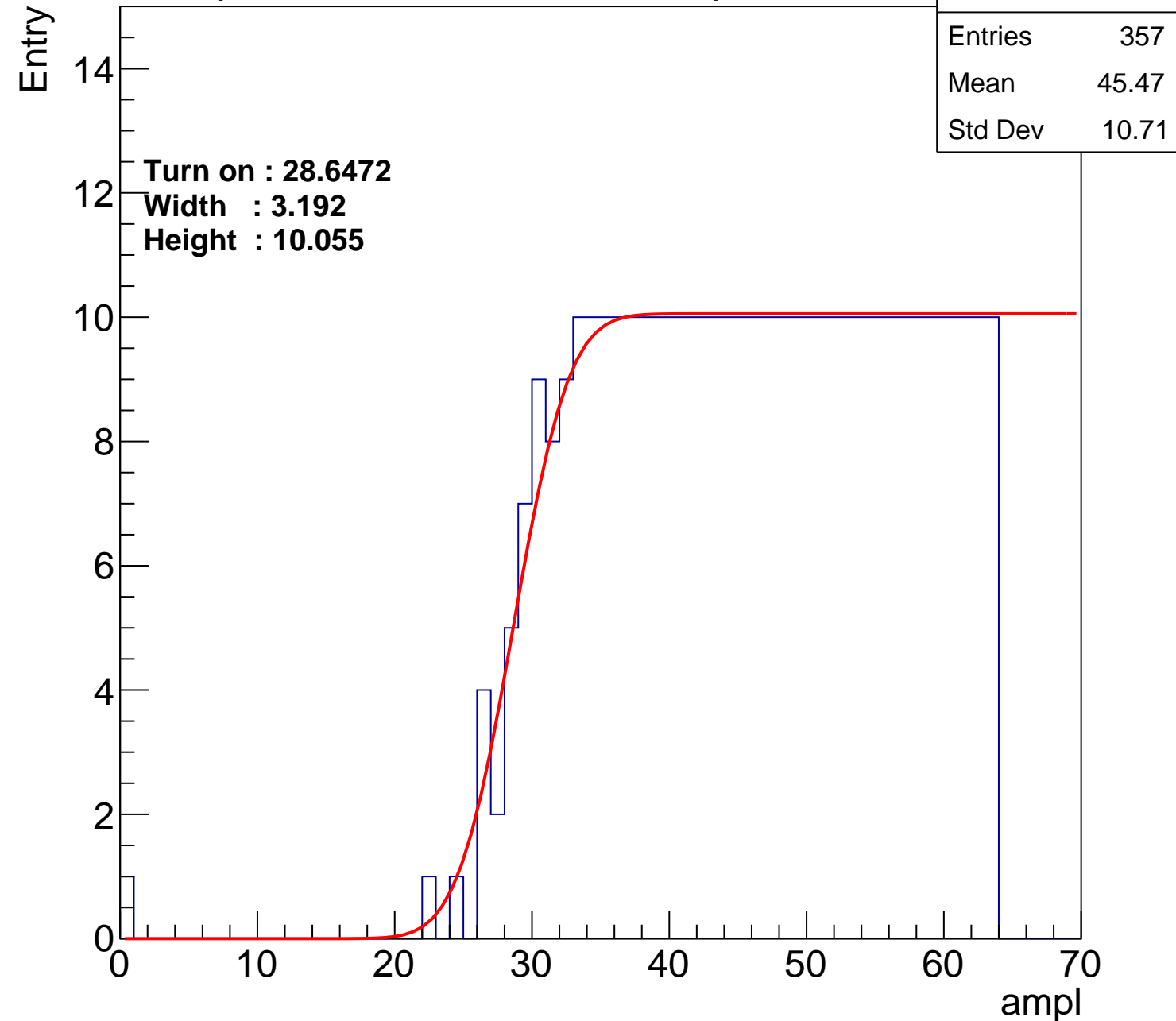
Width : 3.192

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.94
Std Dev	11.03

Turn on : 27.5388

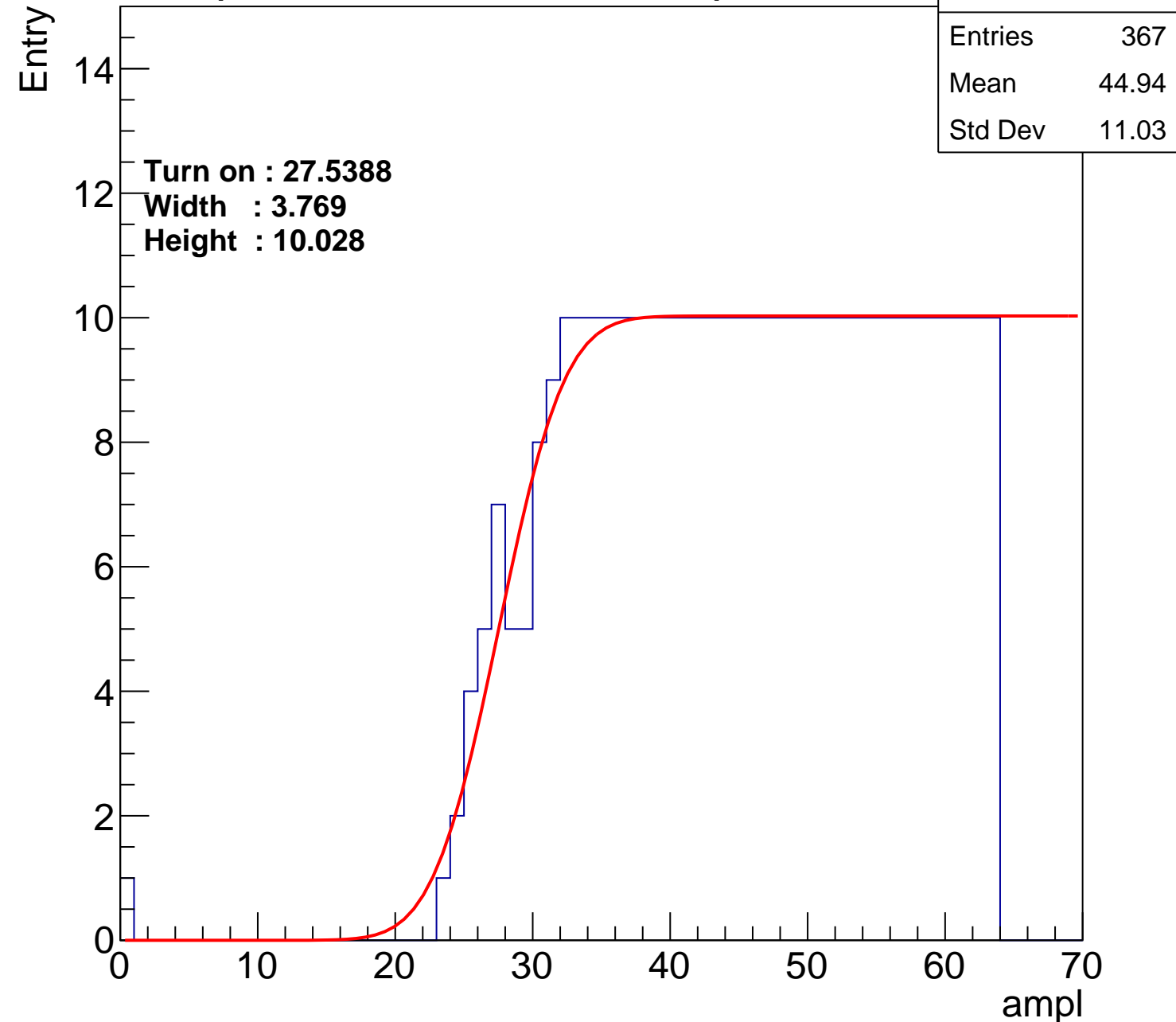
Width : 3.769

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 28.2015

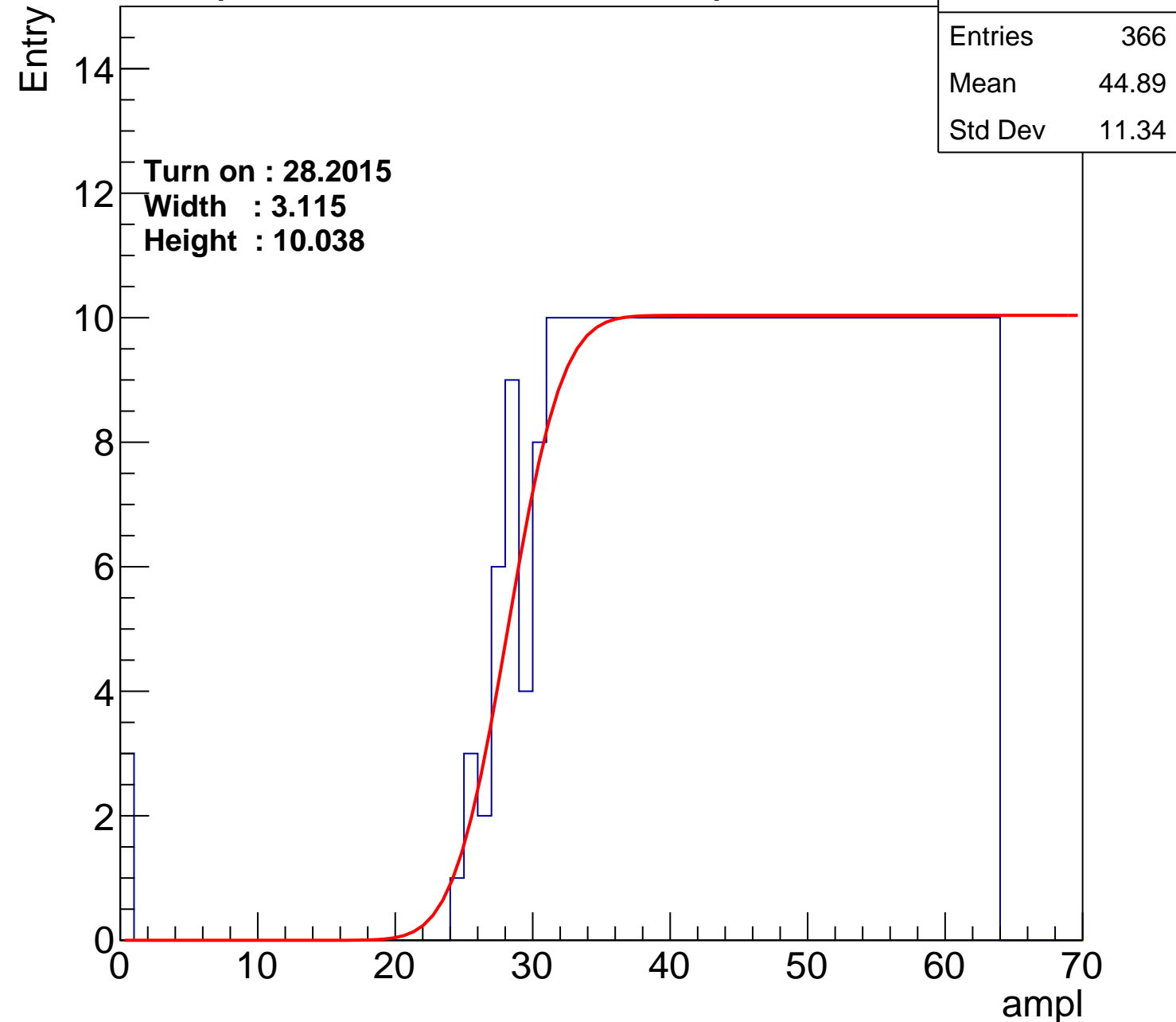
Width : 3.115

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.36
Std Dev	11.54

Turn on : 27.1292

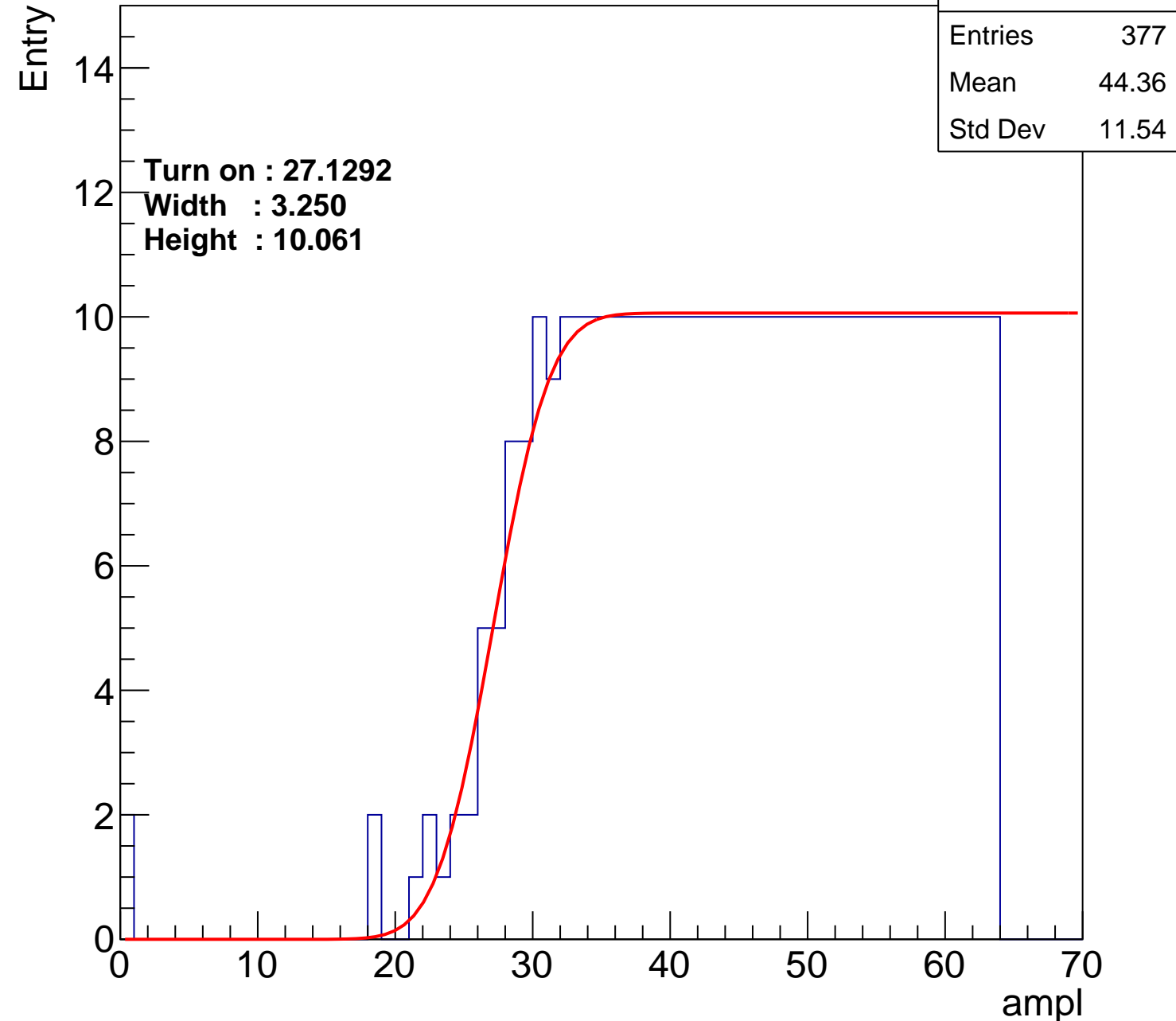
Width : 3.250

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.5293

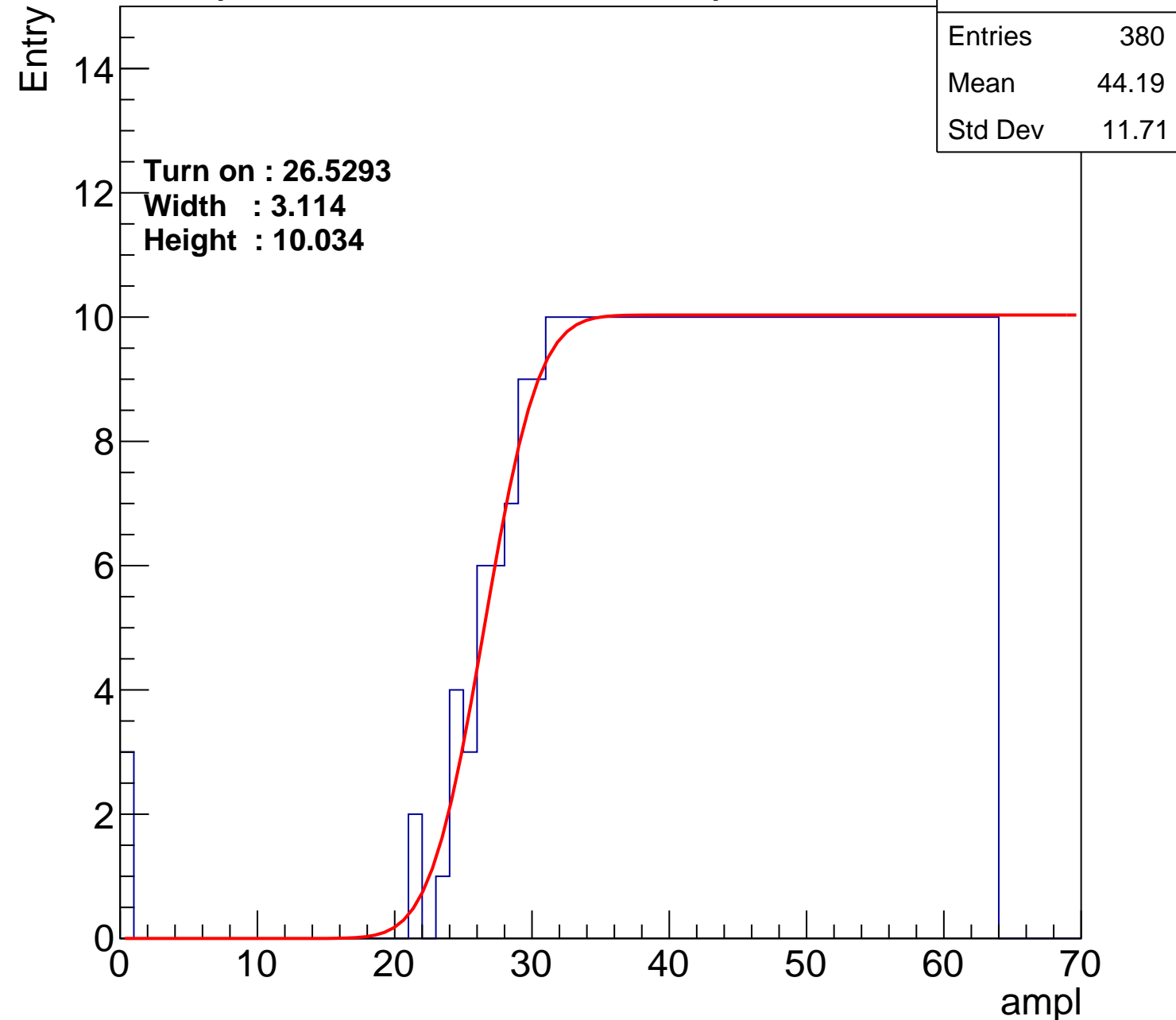
Width : 3.114

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.16
Std Dev	11.45

Turn on : 26.6678

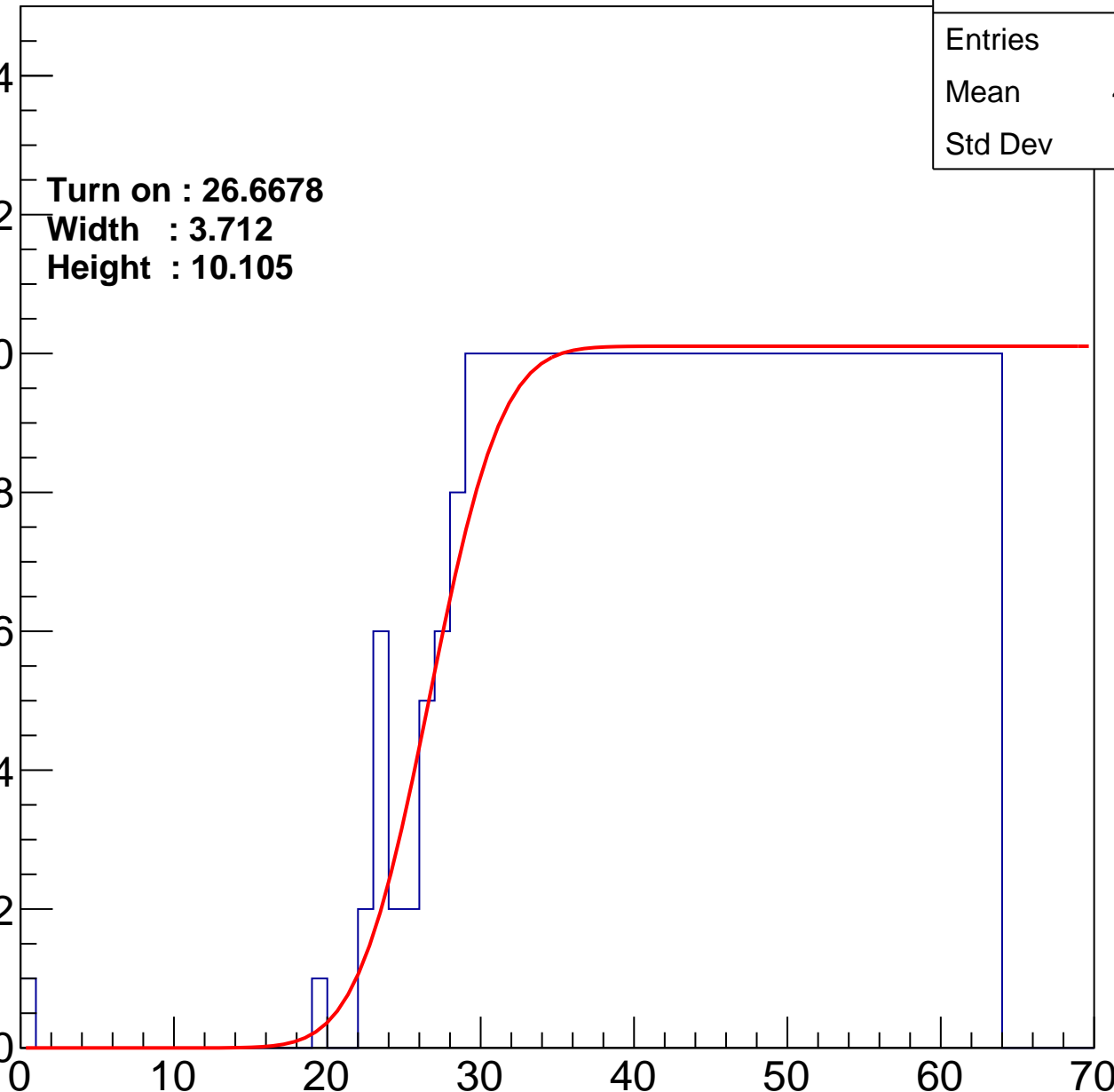
Width : 3.712

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	409
Mean	42.77
Std Dev	12.43

Turn on : 23.6955

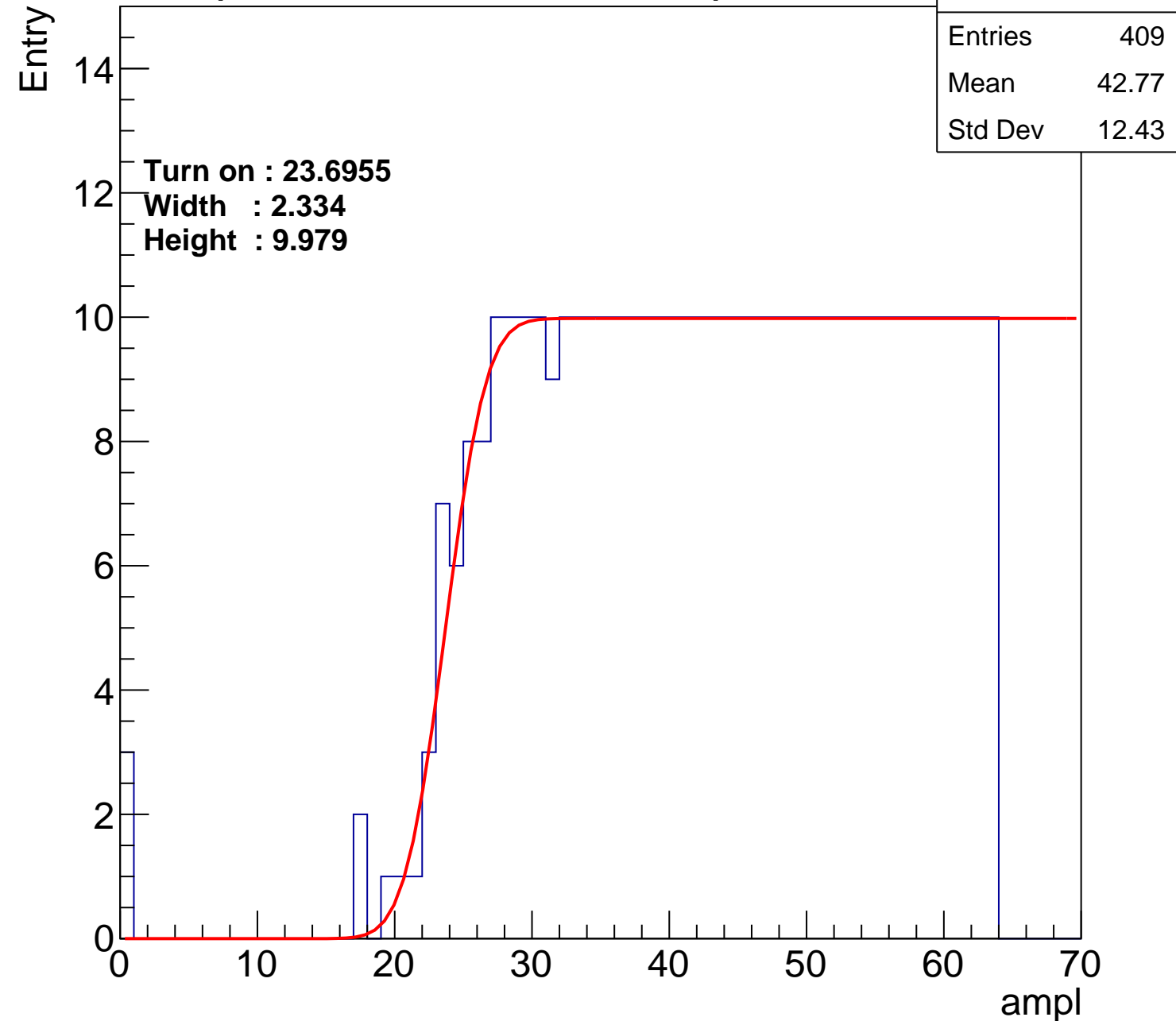
Width : 2.334

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch10

calib_packv5_042523_0143.root, FC#11, port A2

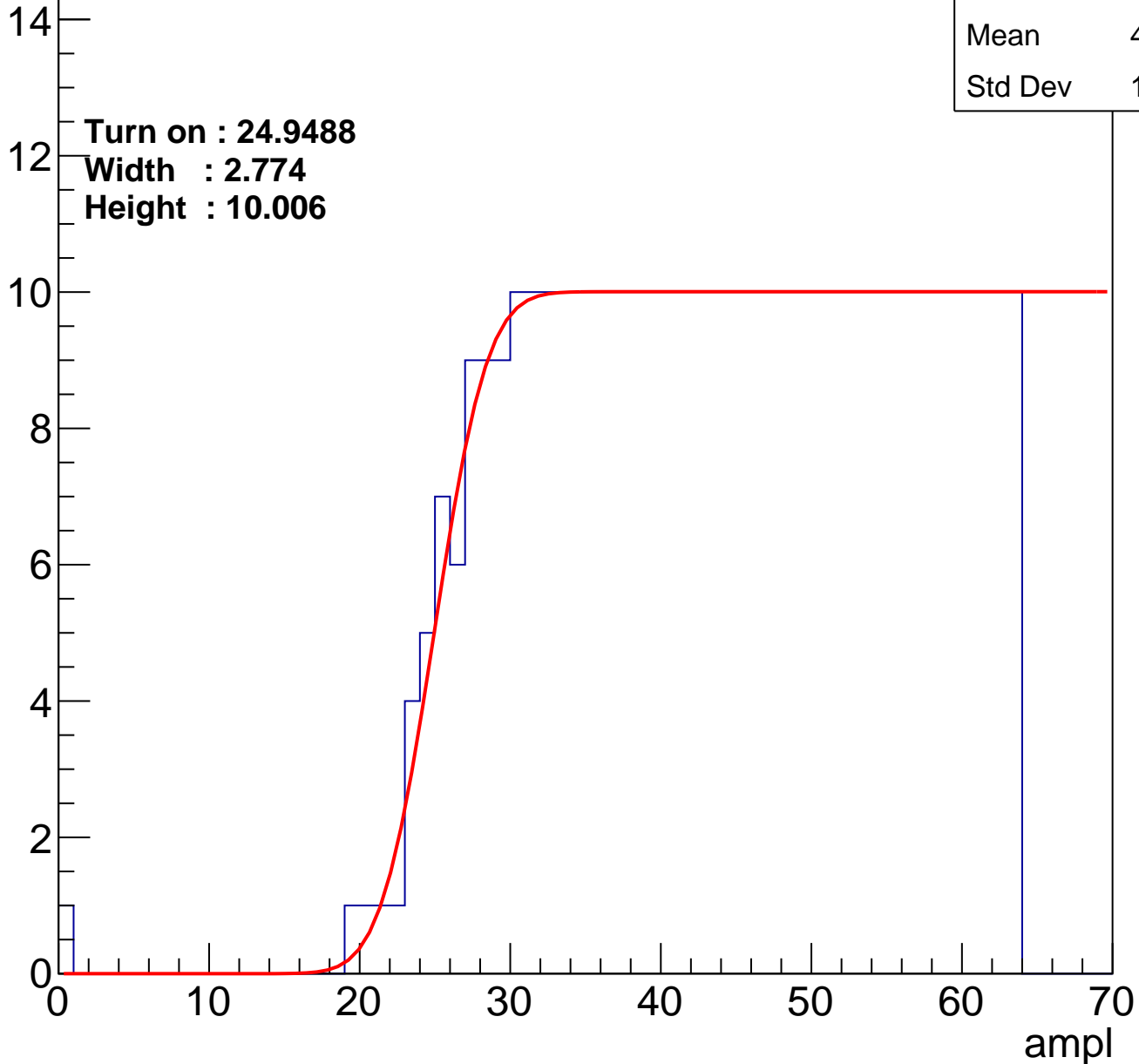
Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 24.9488

Width : 2.774

Height : 10.006

Entry



B1L102S, U24-ch11

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.63
Std Dev	11.22

Turn on : 27.2850

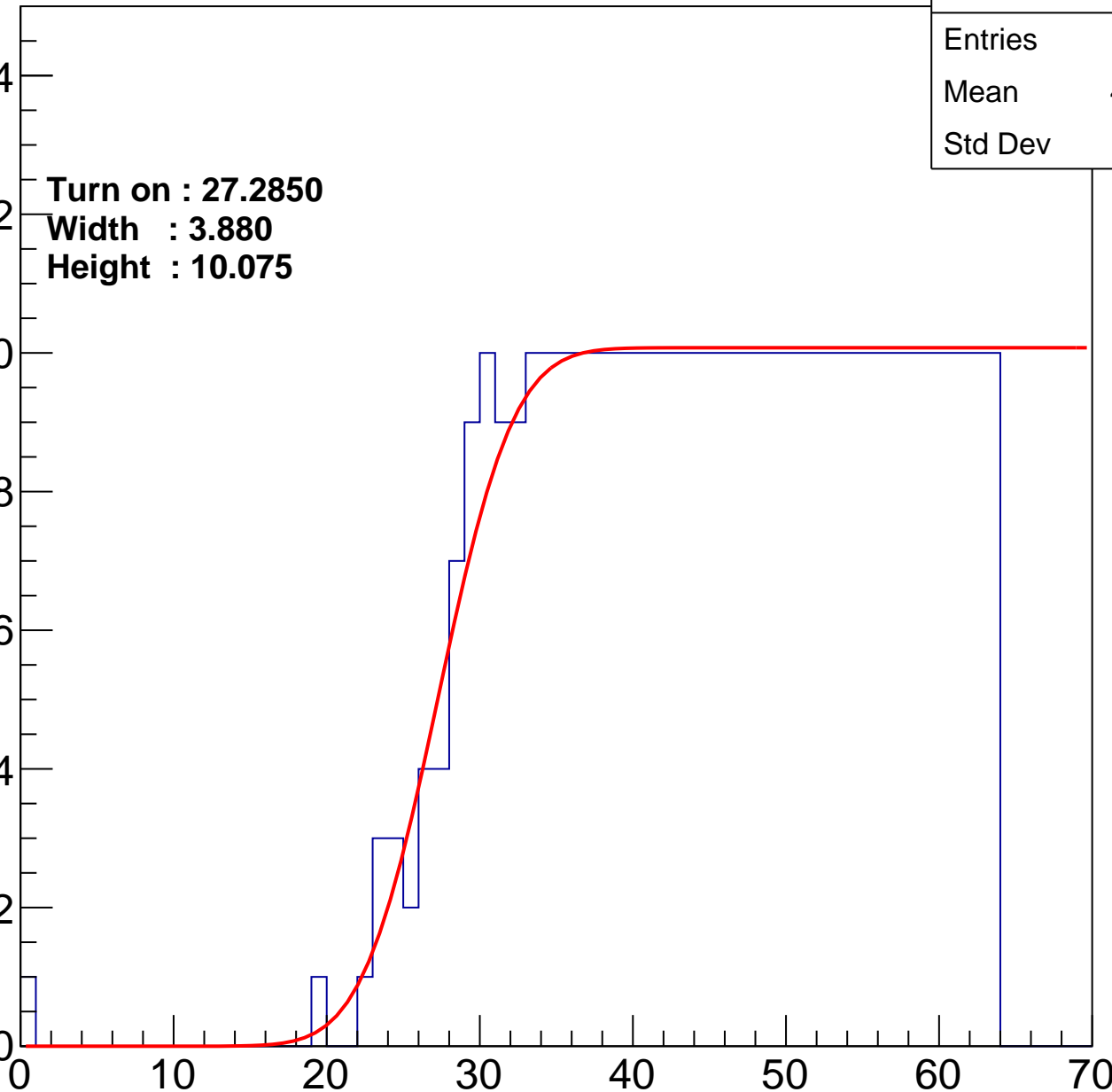
Width : 3.880

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 25.1711

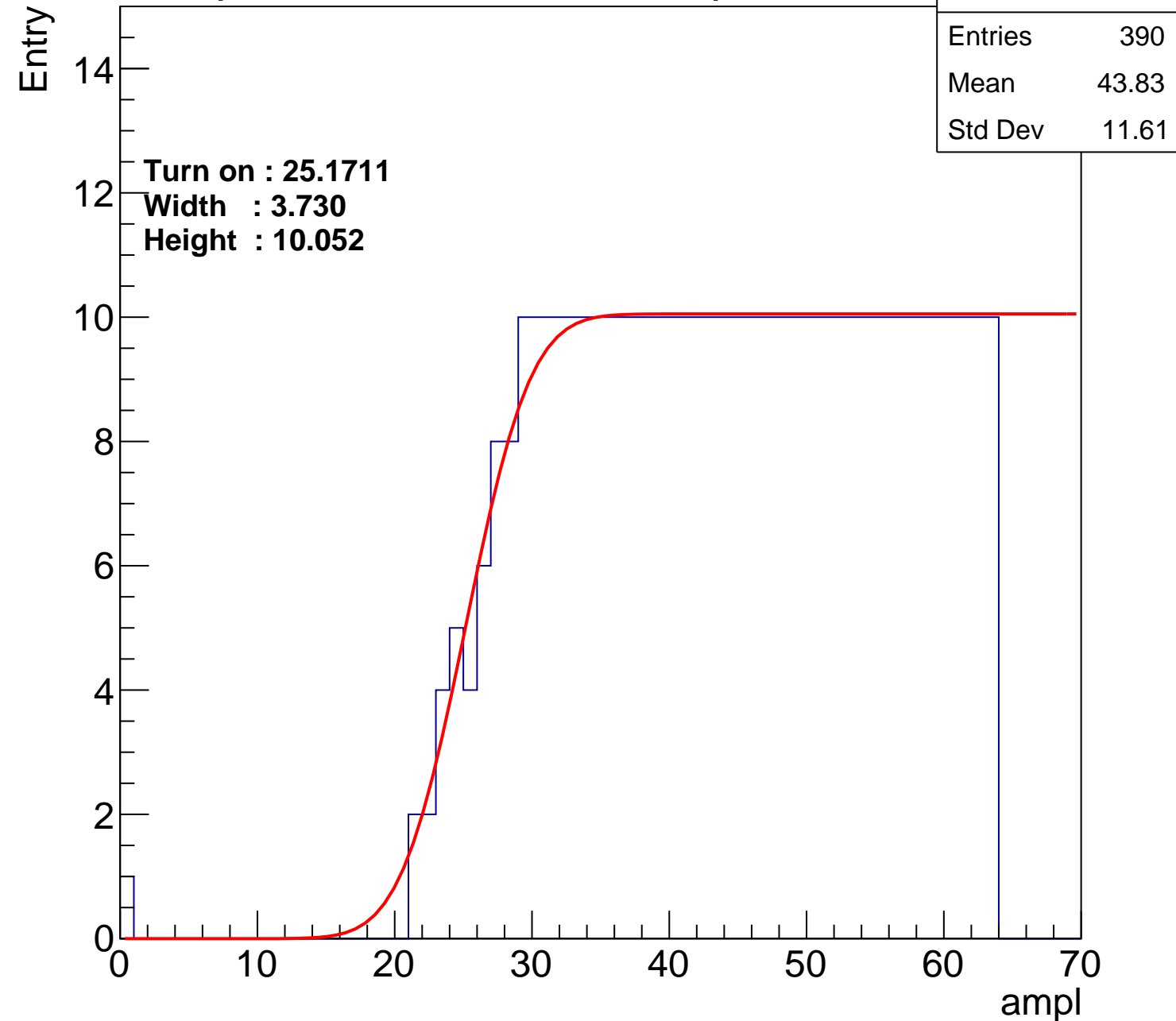
Width : 3.730

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch13

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 27.0401

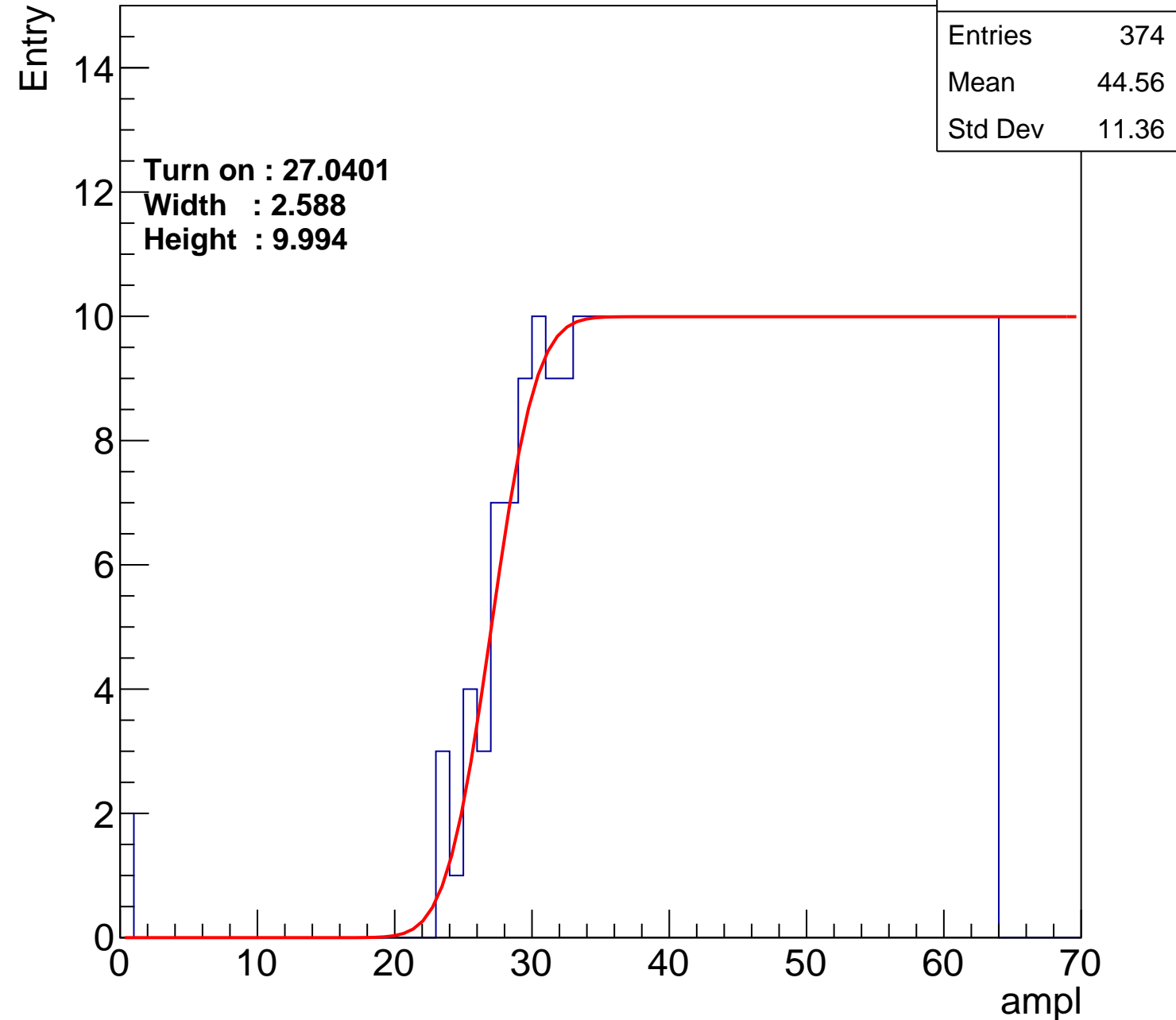
Width : 2.588

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch14

calib_packv5_042523_0143.root, FC#11, port A2

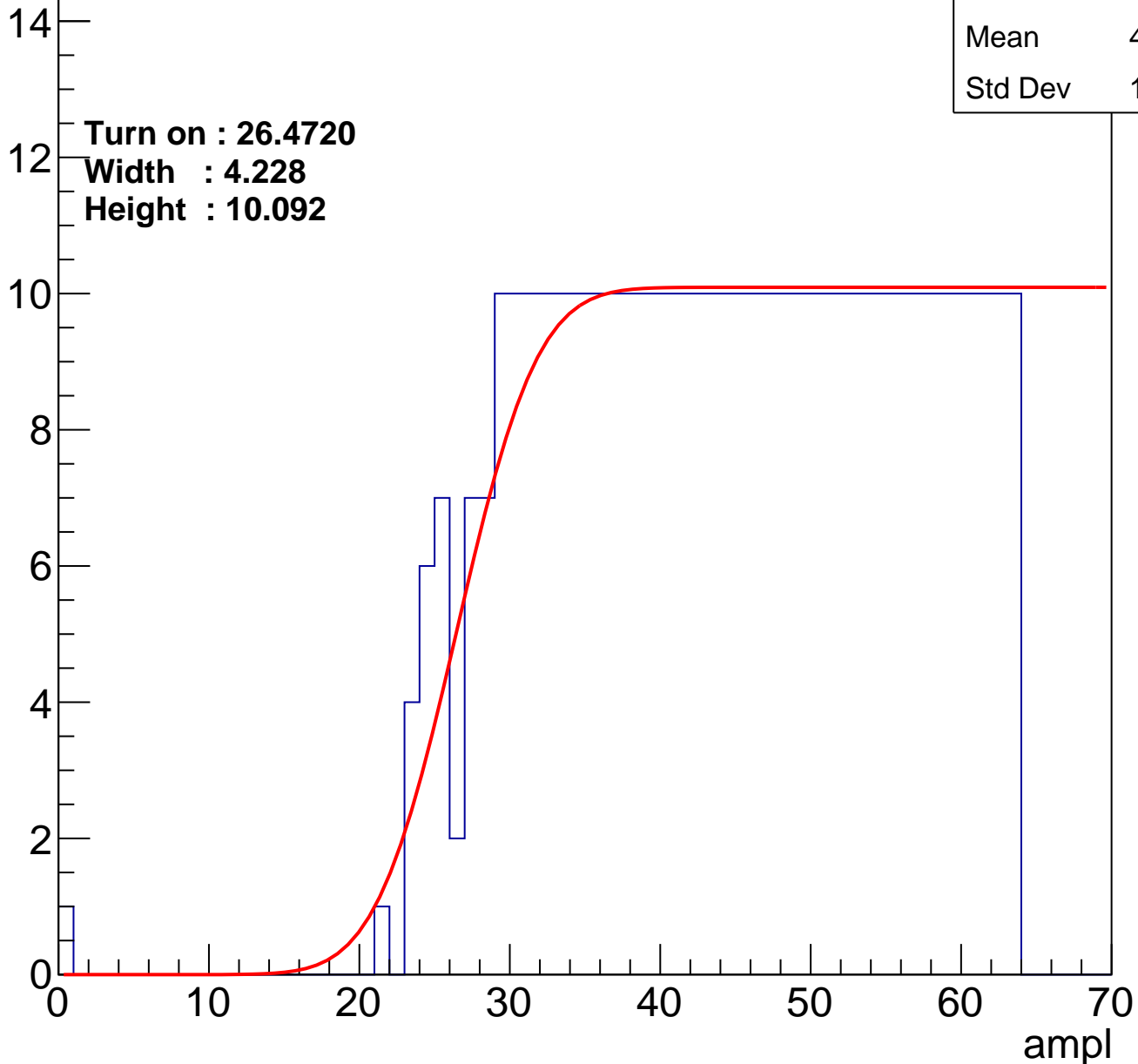
Entries	385
Mean	44.08
Std Dev	11.48

Turn on : 26.4720

Width : 4.228

Height : 10.092

Entry



B1L102S, U24-ch15

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 27.9292

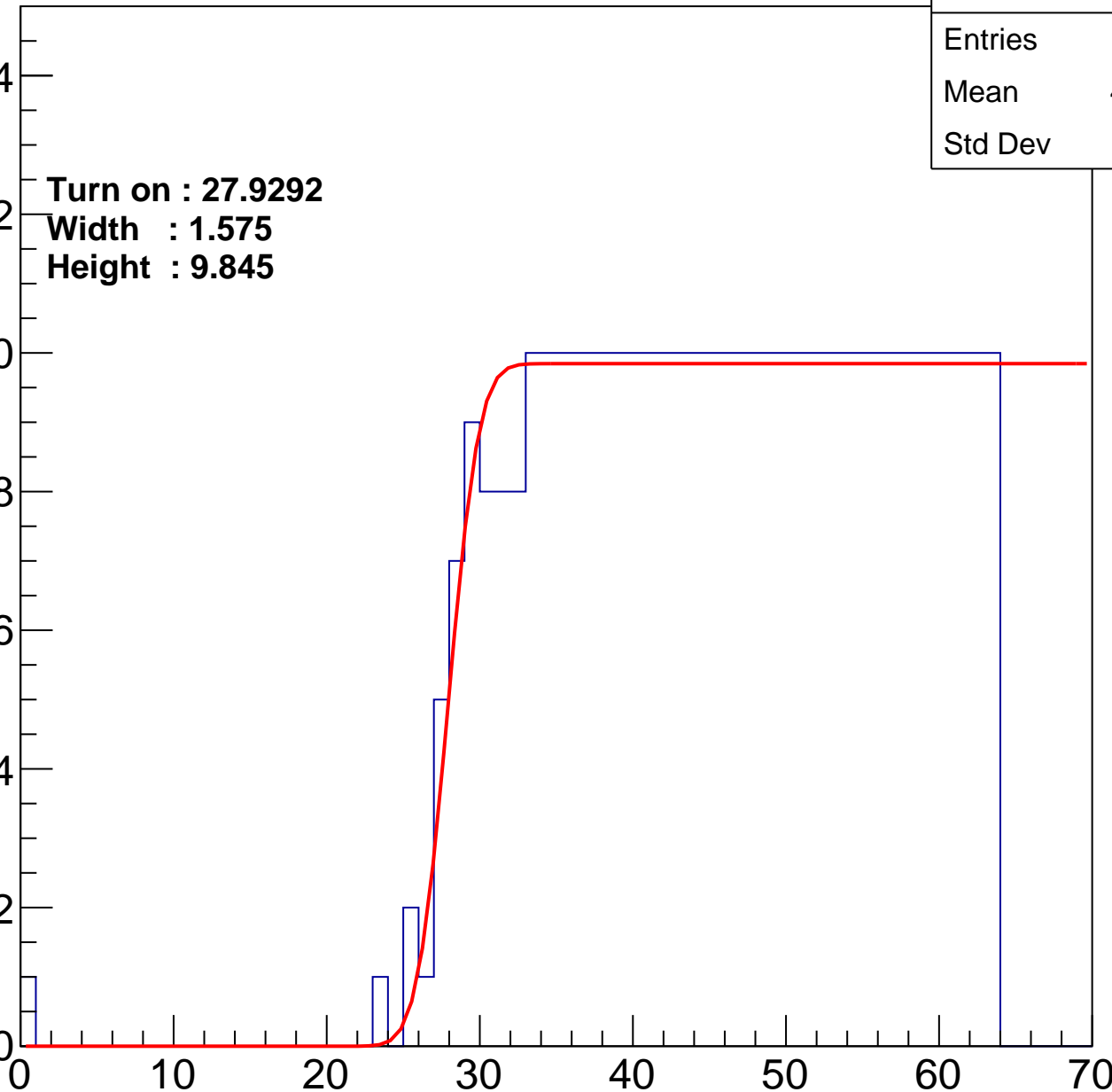
Width : 1.575

Height : 9.845

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
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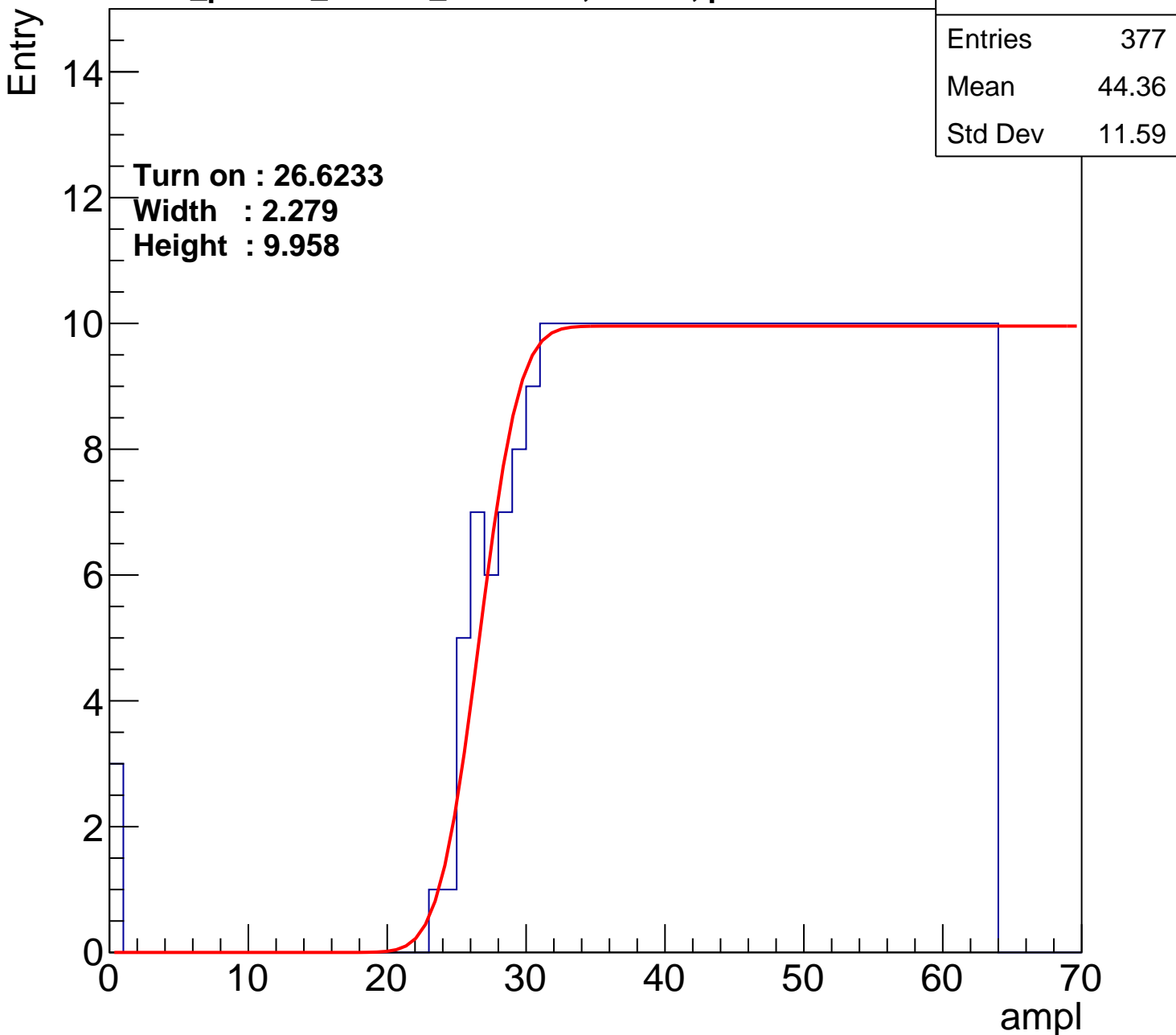
Mean	44.36
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Std Dev	11.59
---------	-------

Turn on : 26.6233

Width : 2.279

Height : 9.958



B1L102S, U24-ch17

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.84
Std Dev	11.52

Turn on : 28.6148

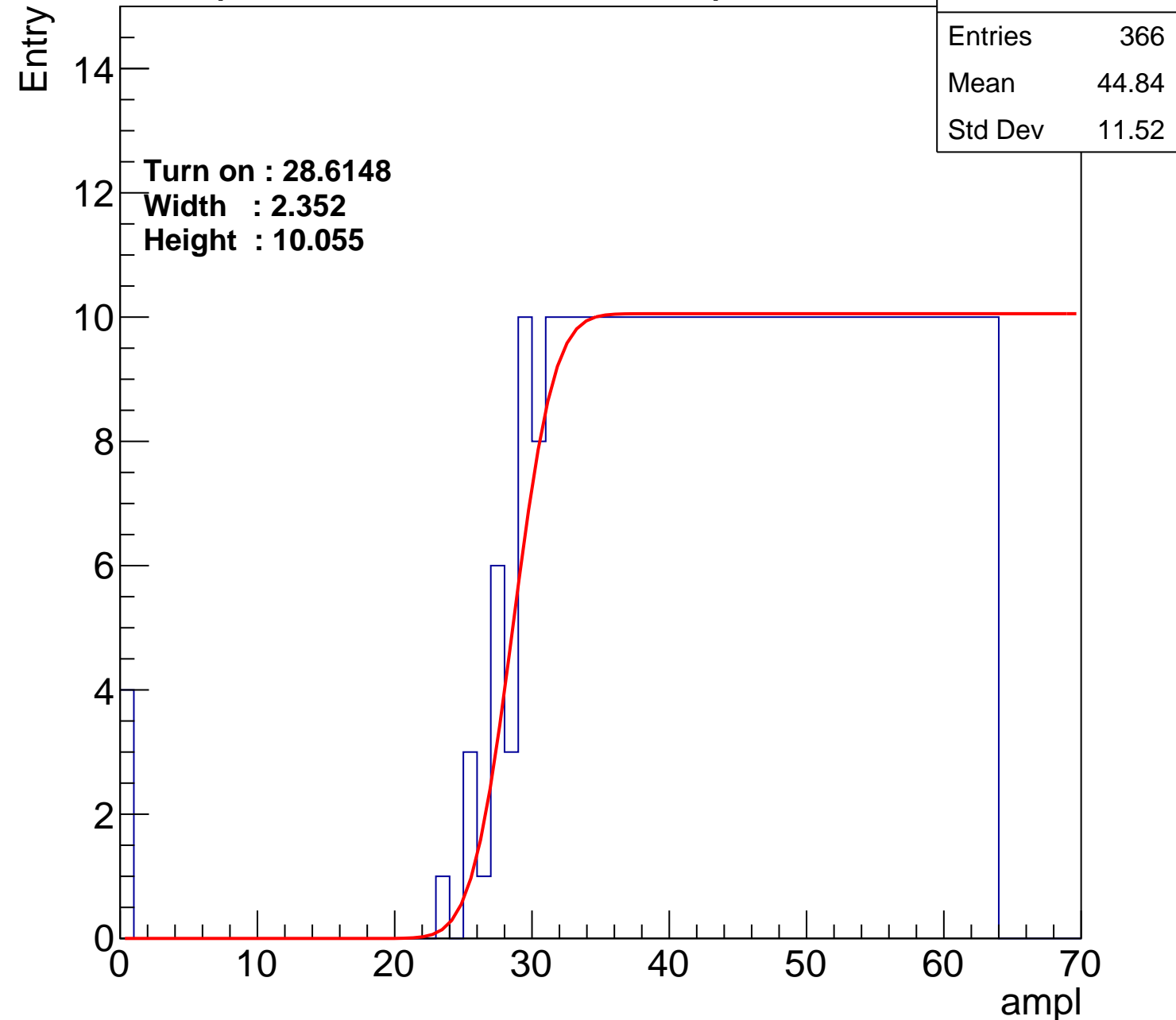
Width : 2.352

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.0642

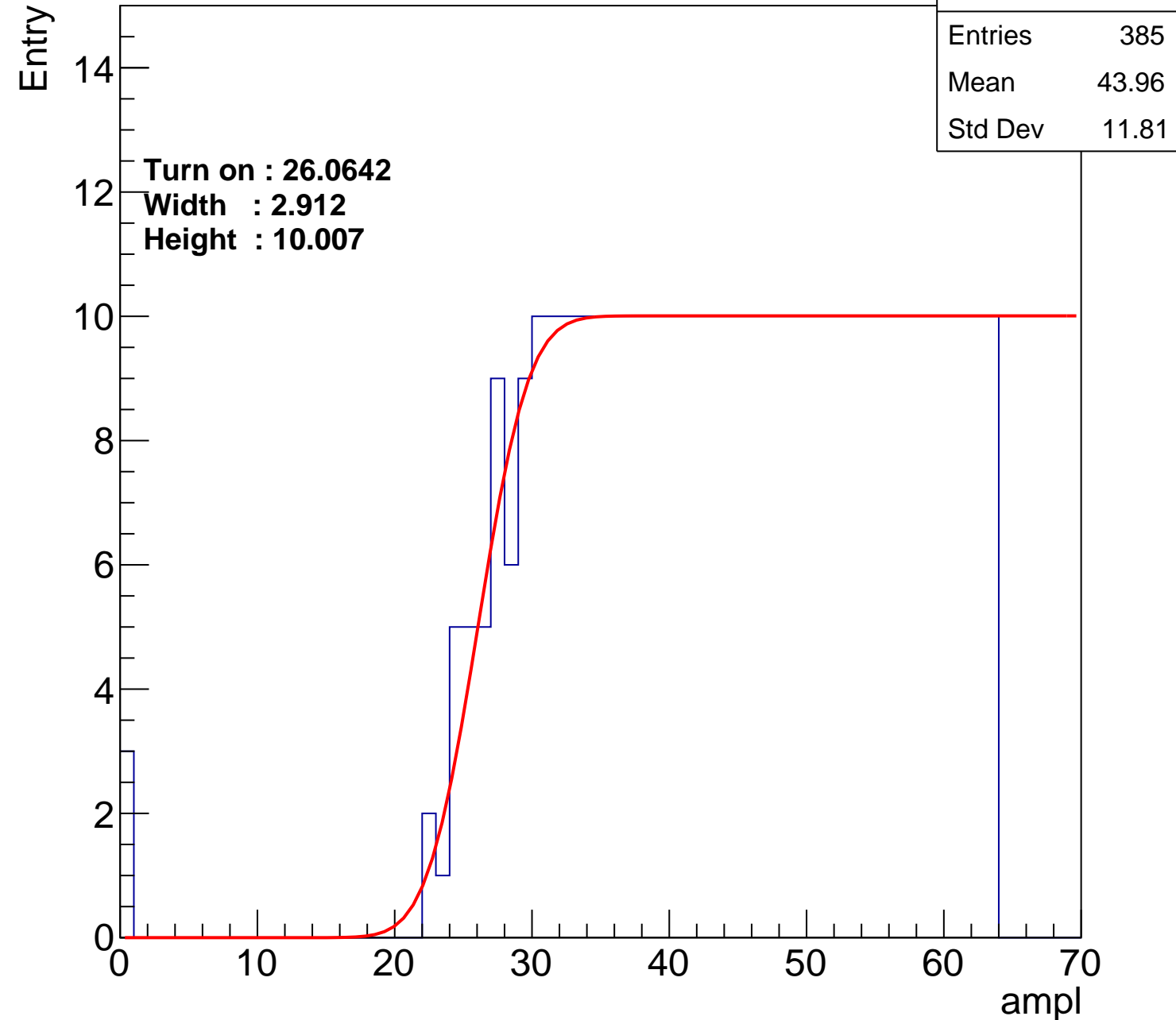
Width : 2.912

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch19

calib_packv5_042523_0143.root, FC#11, port A2

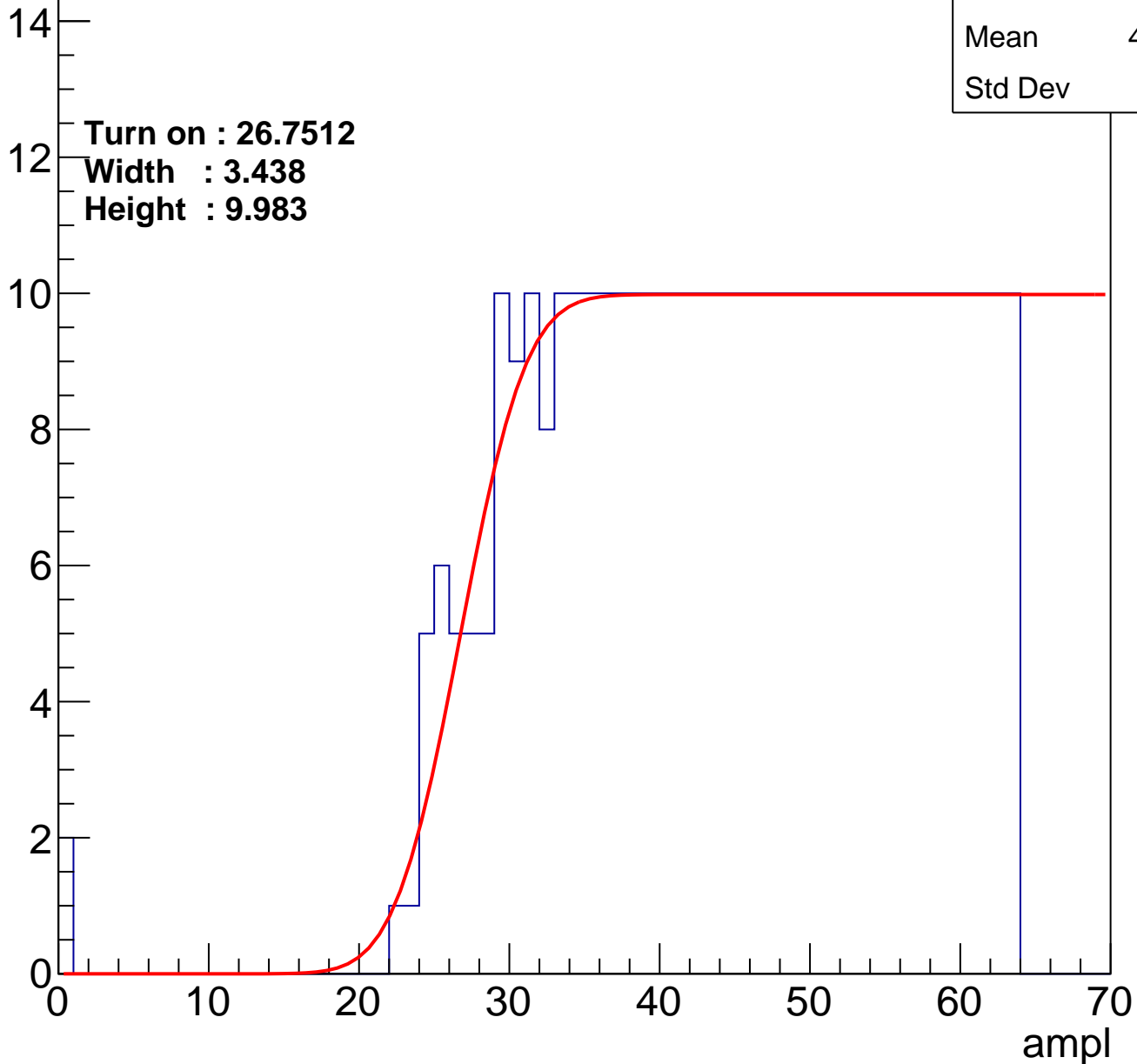
Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 26.7512

Width : 3.438

Height : 9.983

Entry



B1L102S, U24-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.14
Std Dev	11.79

Turn on : 27.1096

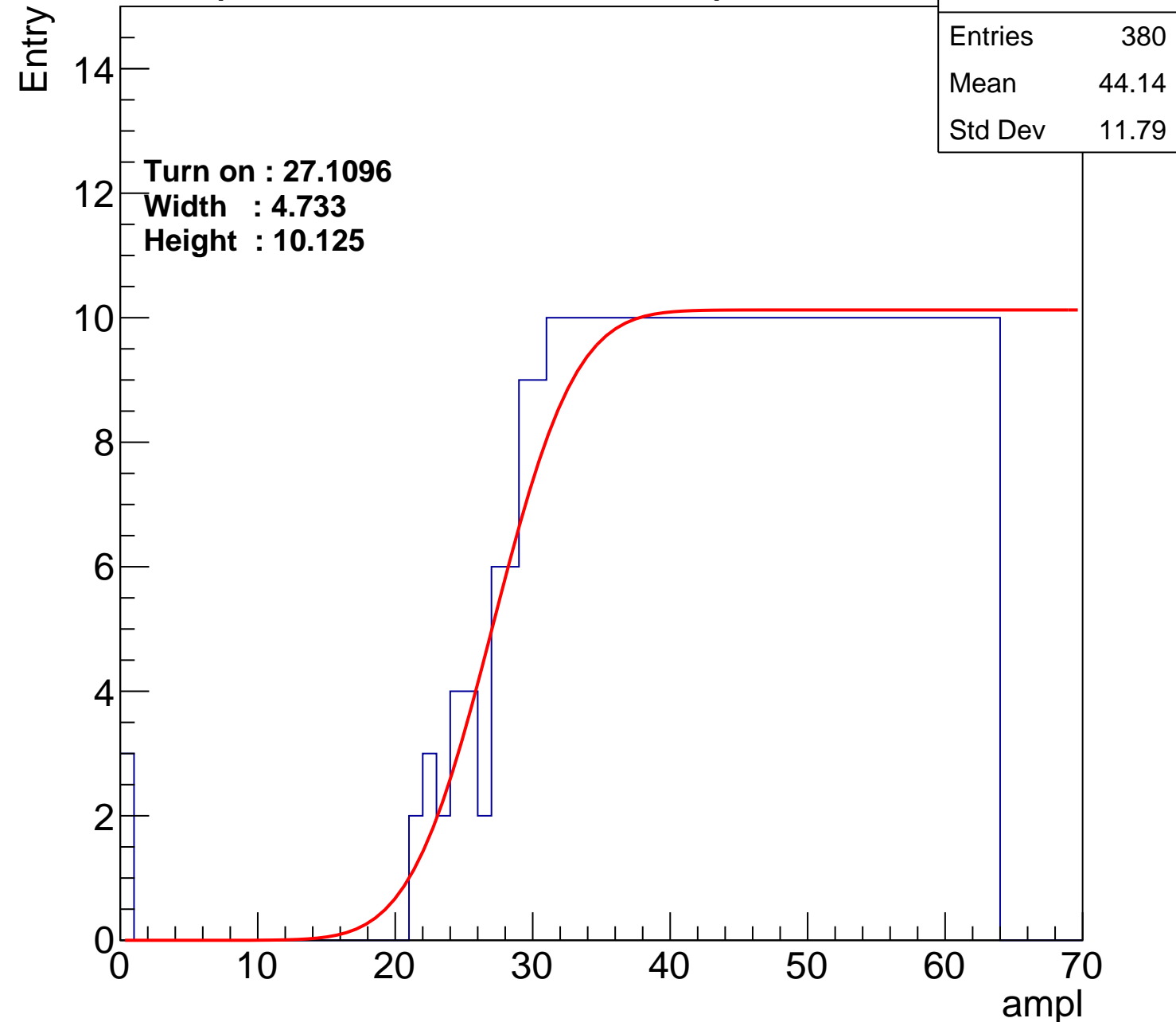
Width : 4.733

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch21

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.65
Std Dev	11.12

Turn on : 26.5166

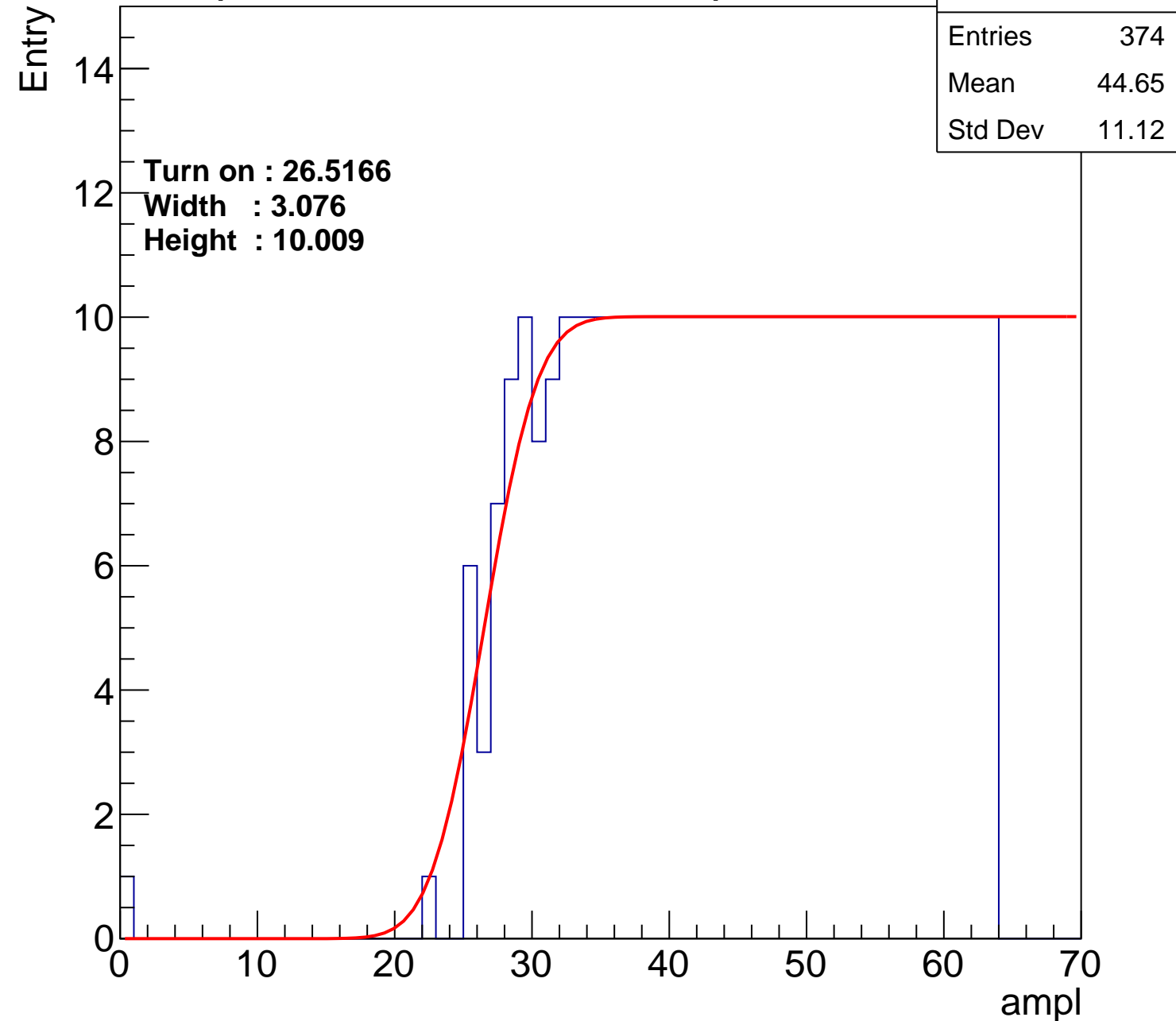
Width : 3.076

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch22

calib_packv5_042523_0143.root, FC#11, port A2

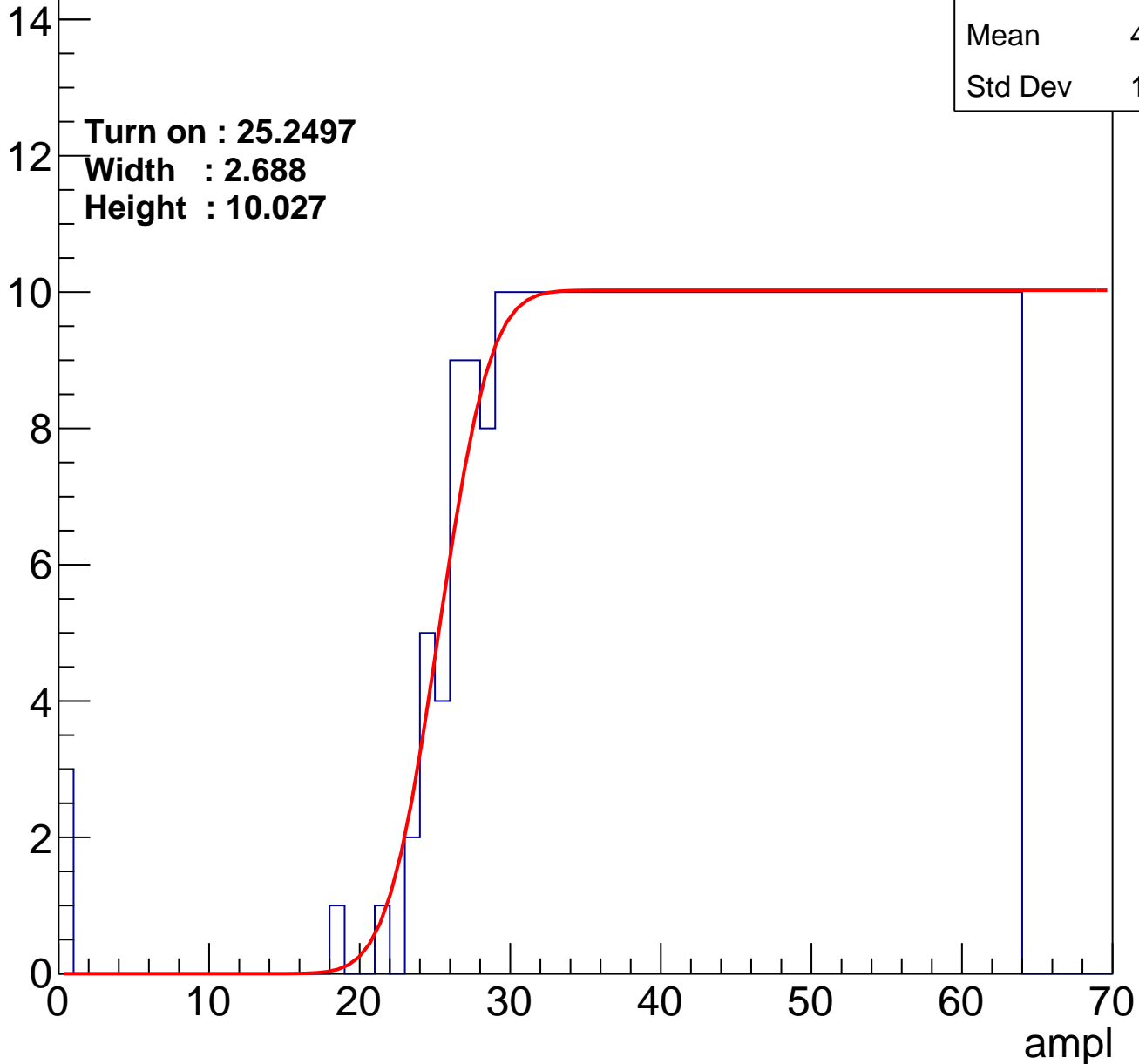
Entries	392
Mean	43.64
Std Dev	11.95

Turn on : 25.2497

Width : 2.688

Height : 10.027

Entry



B1L102S, U24-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.4498

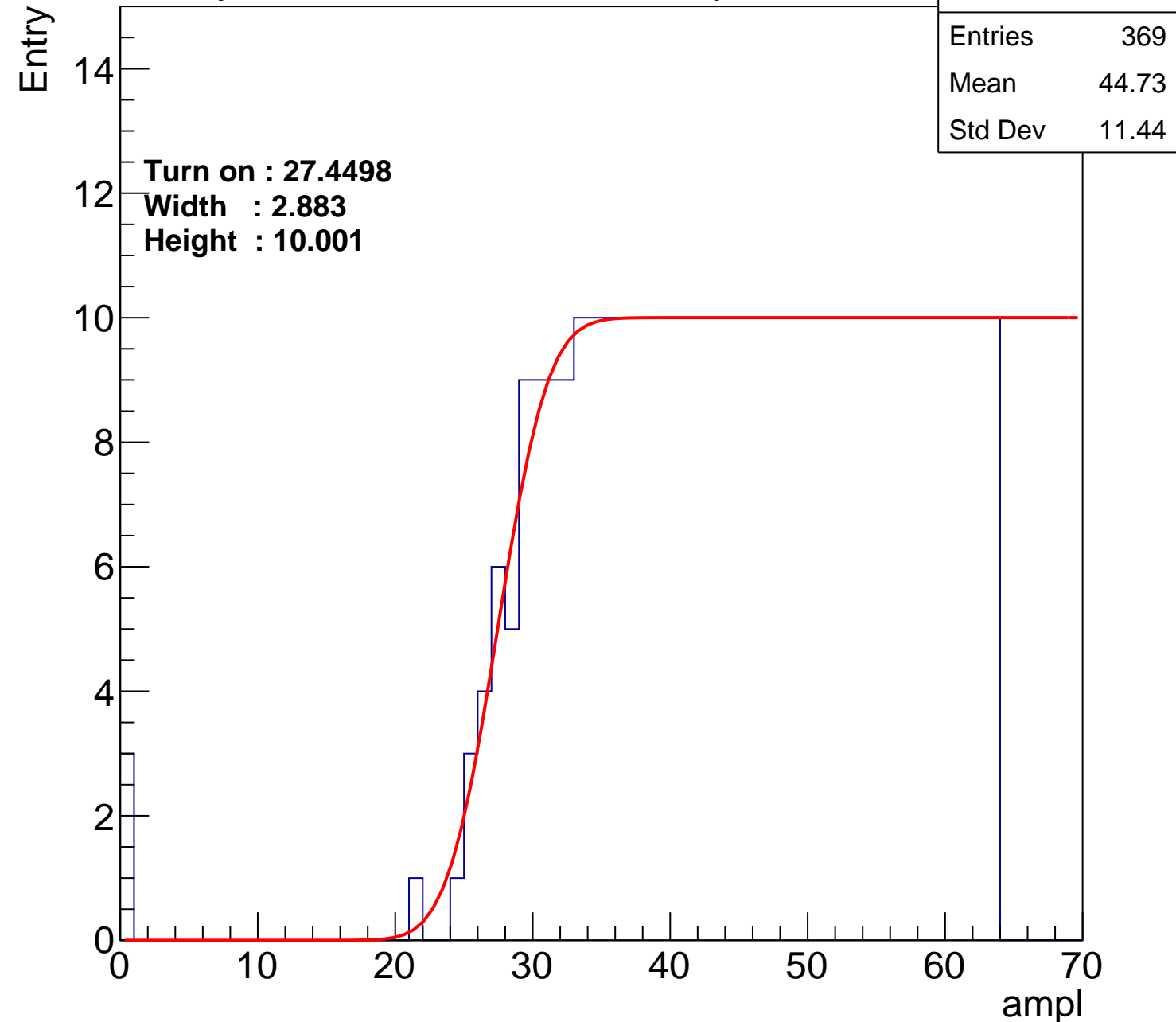
Width : 2.883

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch24

calib_packv5_042523_0143.root, FC#11, port A2

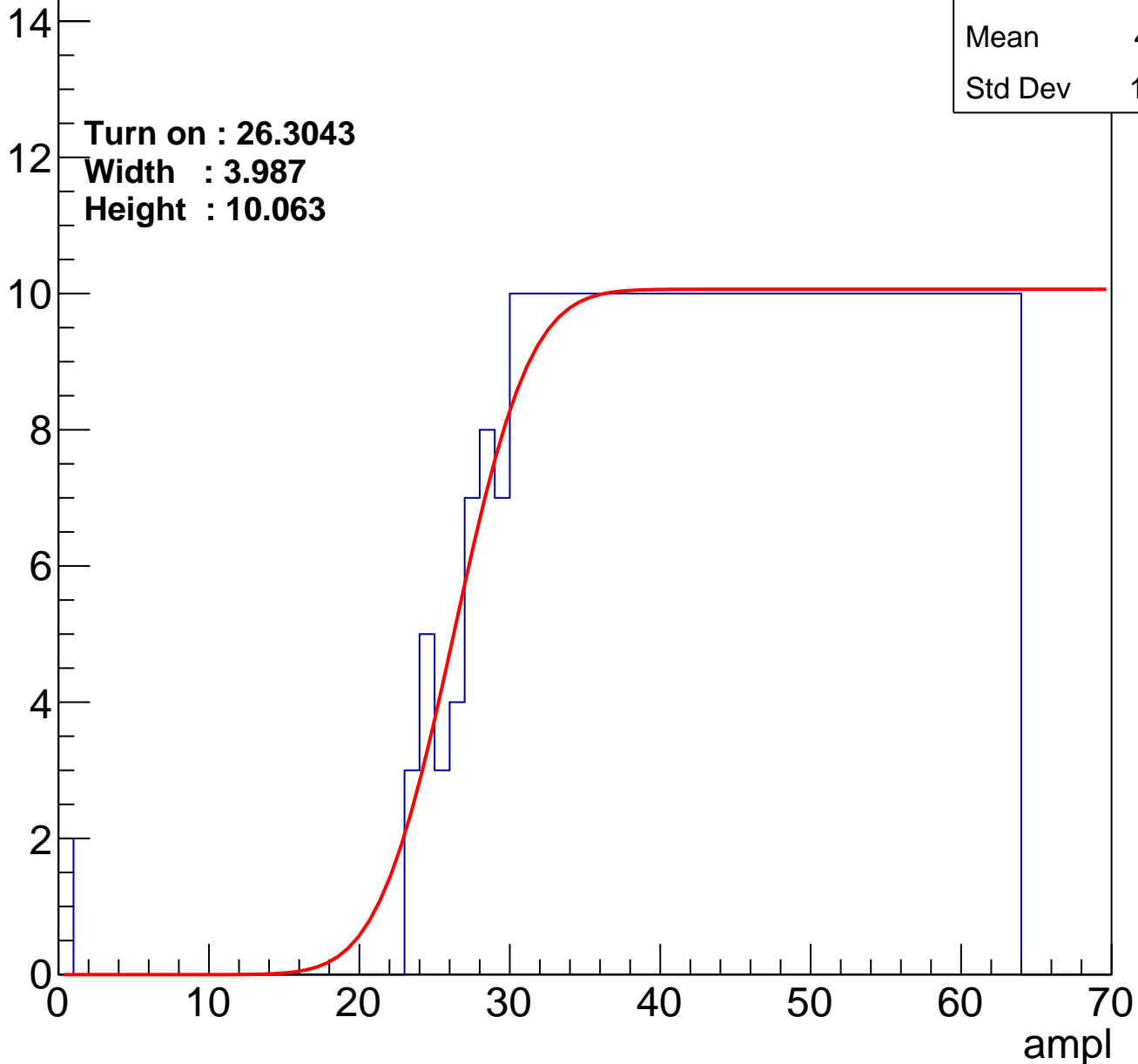
Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.3043

Width : 3.987

Height : 10.063

Entry



B1L102S, U24-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.15
Std Dev	11.84

Turn on : 26.4202

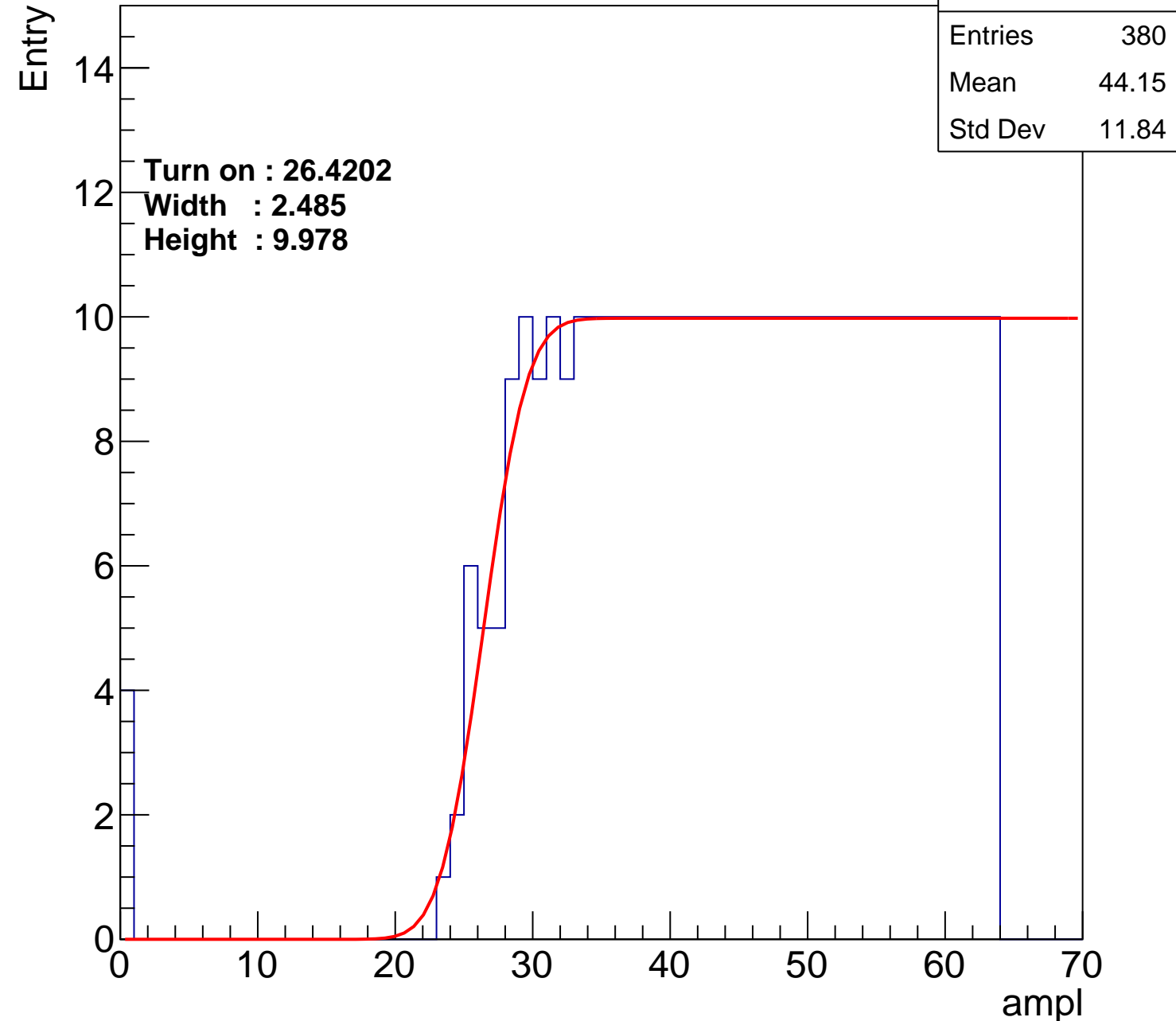
Width : 2.485

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.3
Std Dev	11.56

Turn on : 26.8756

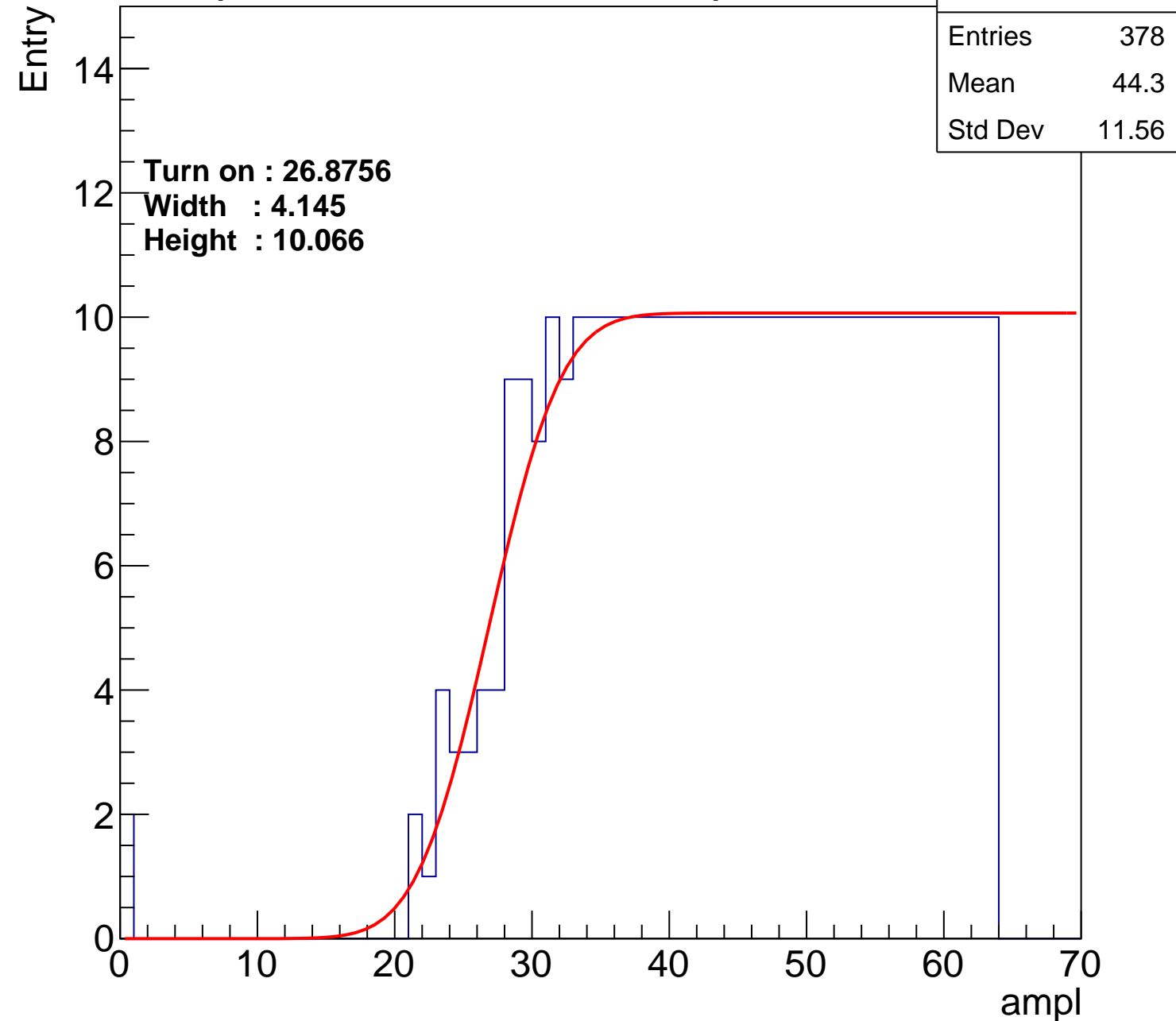
Width : 4.145

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	43.87
Std Dev	12.18

Turn on : 26.4786

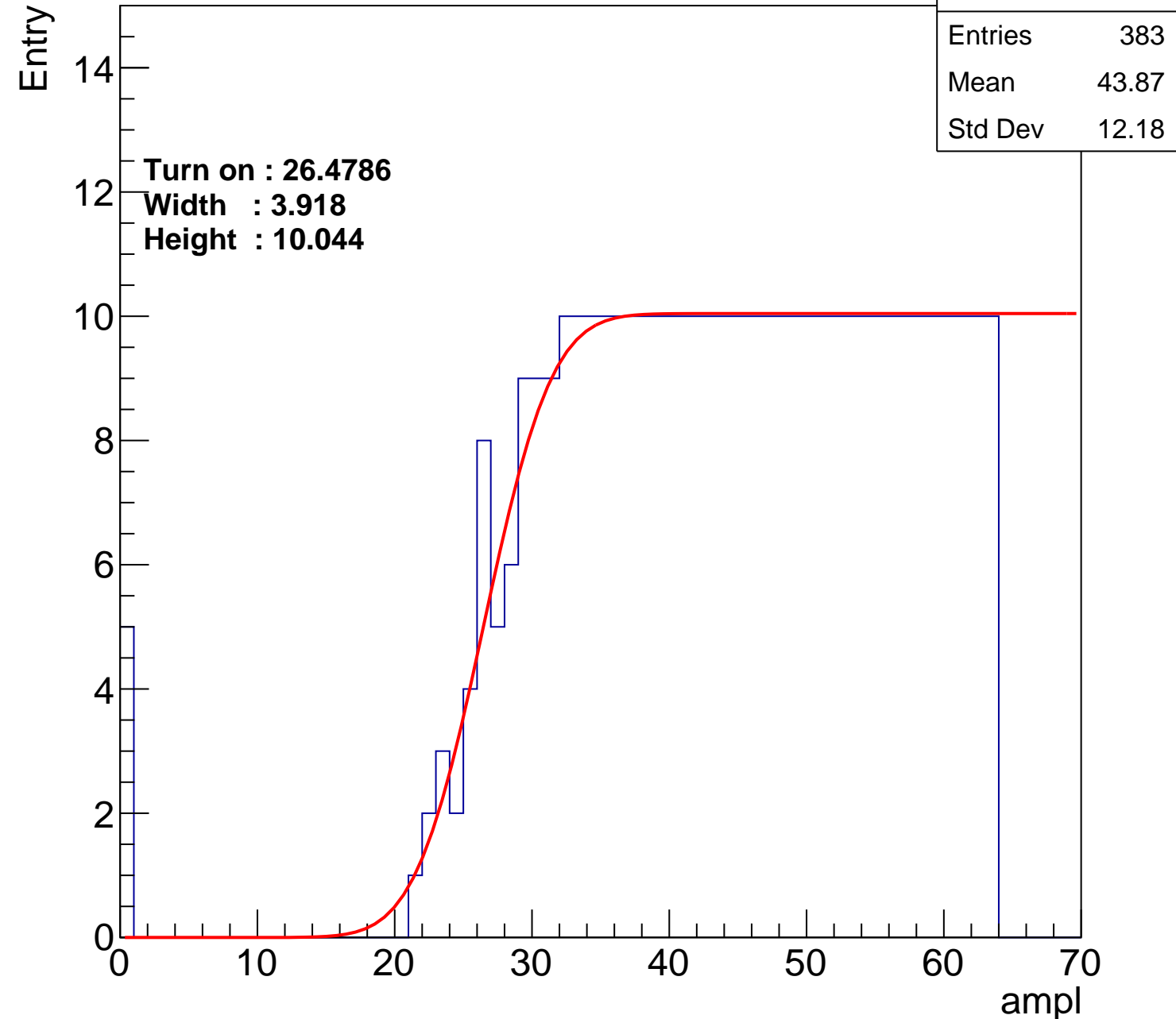
Width : 3.918

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.57
Std Dev	12

Turn on : 25.5238

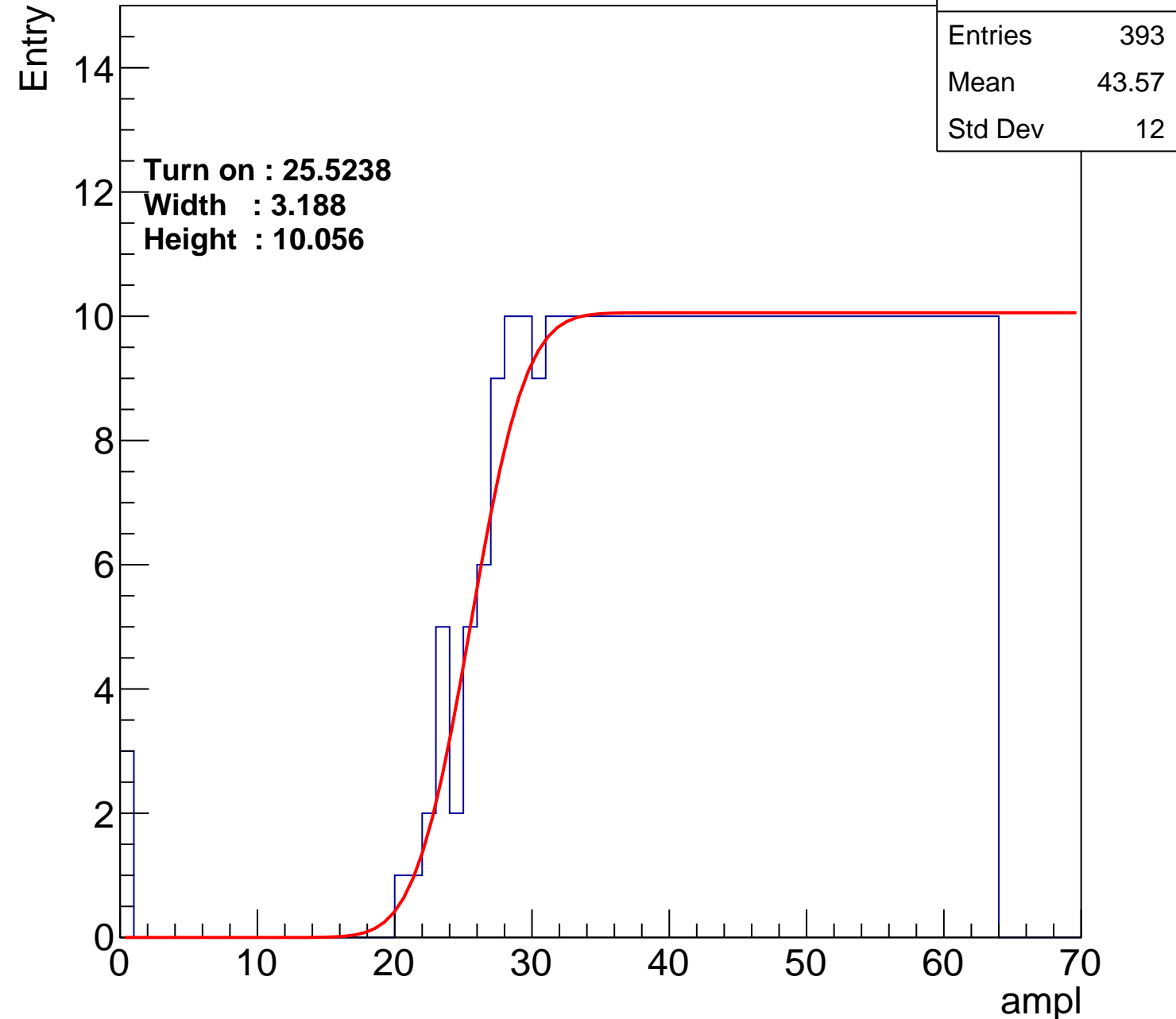
Width : 3.188

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch29

calib_packv5_042523_0143.root, FC#11, port A2

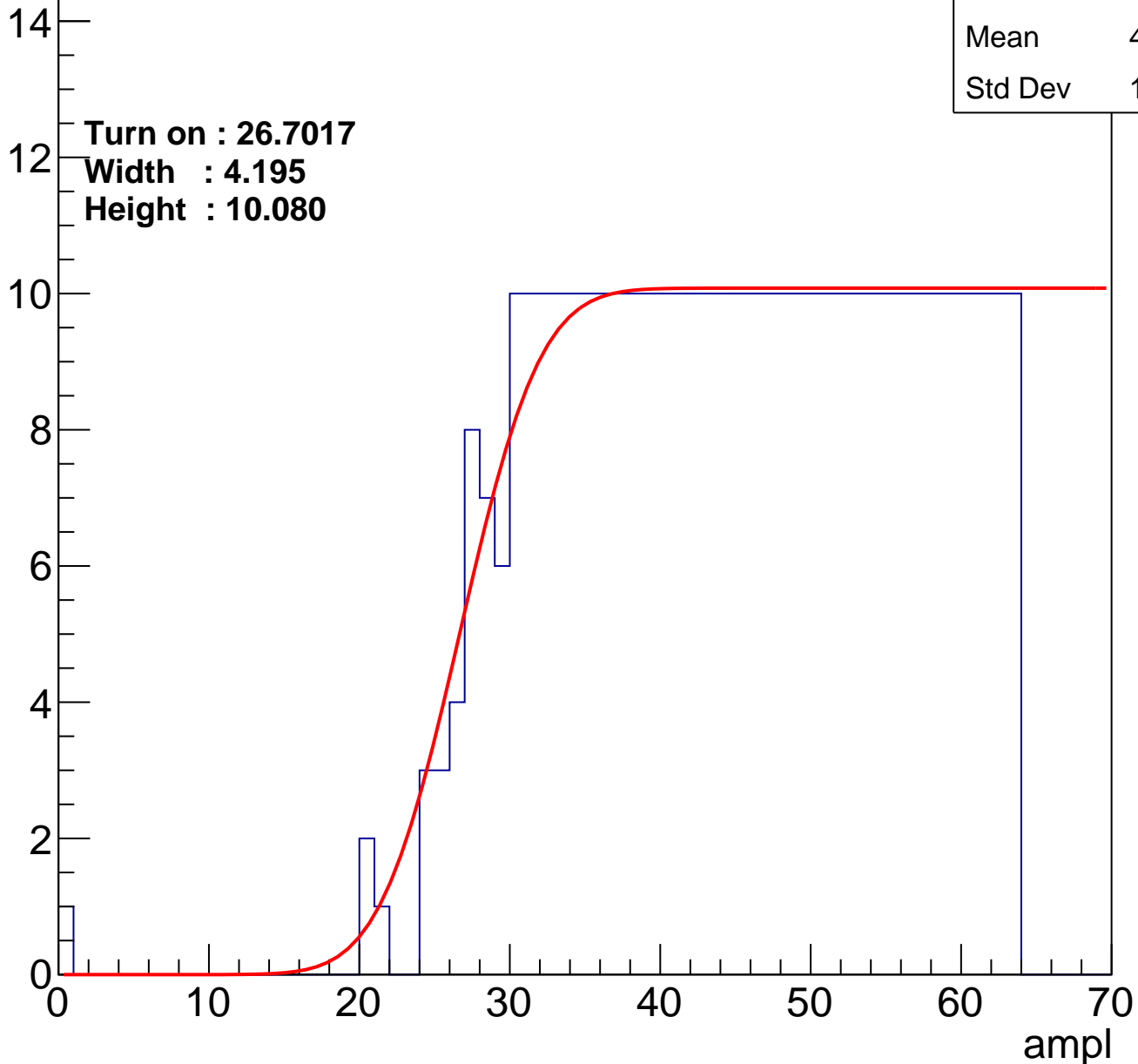
Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 26.7017

Width : 4.195

Height : 10.080

Entry



B1L102S, U24-ch30

calib_packv5_042523_0143.root, FC#11, port A2

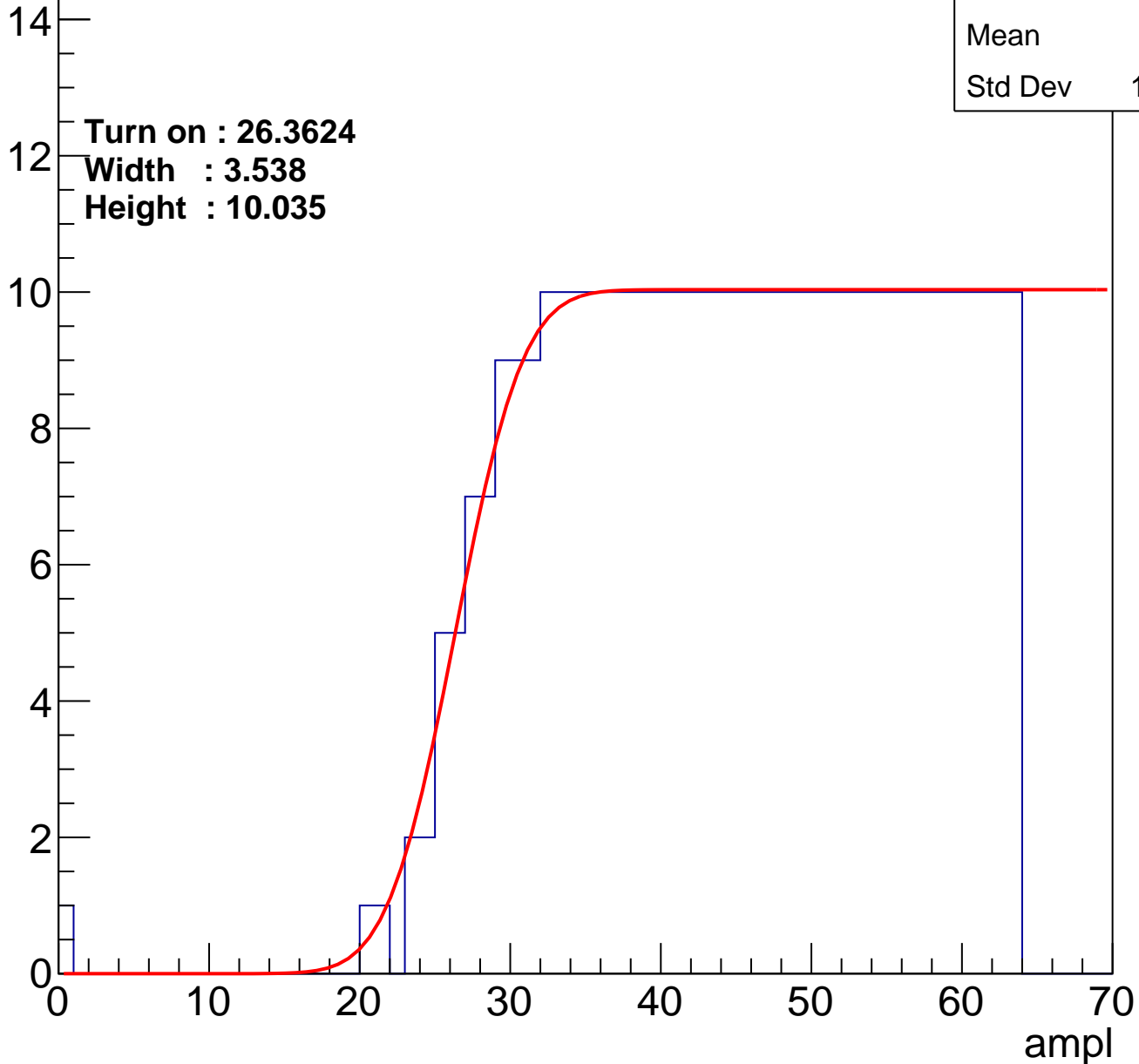
Entries	378
Mean	44.4
Std Dev	11.32

Turn on : 26.3624

Width : 3.538

Height : 10.035

Entry



B1L102S, U24-ch31

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.72
Std Dev	11.86

Turn on : 26.2071

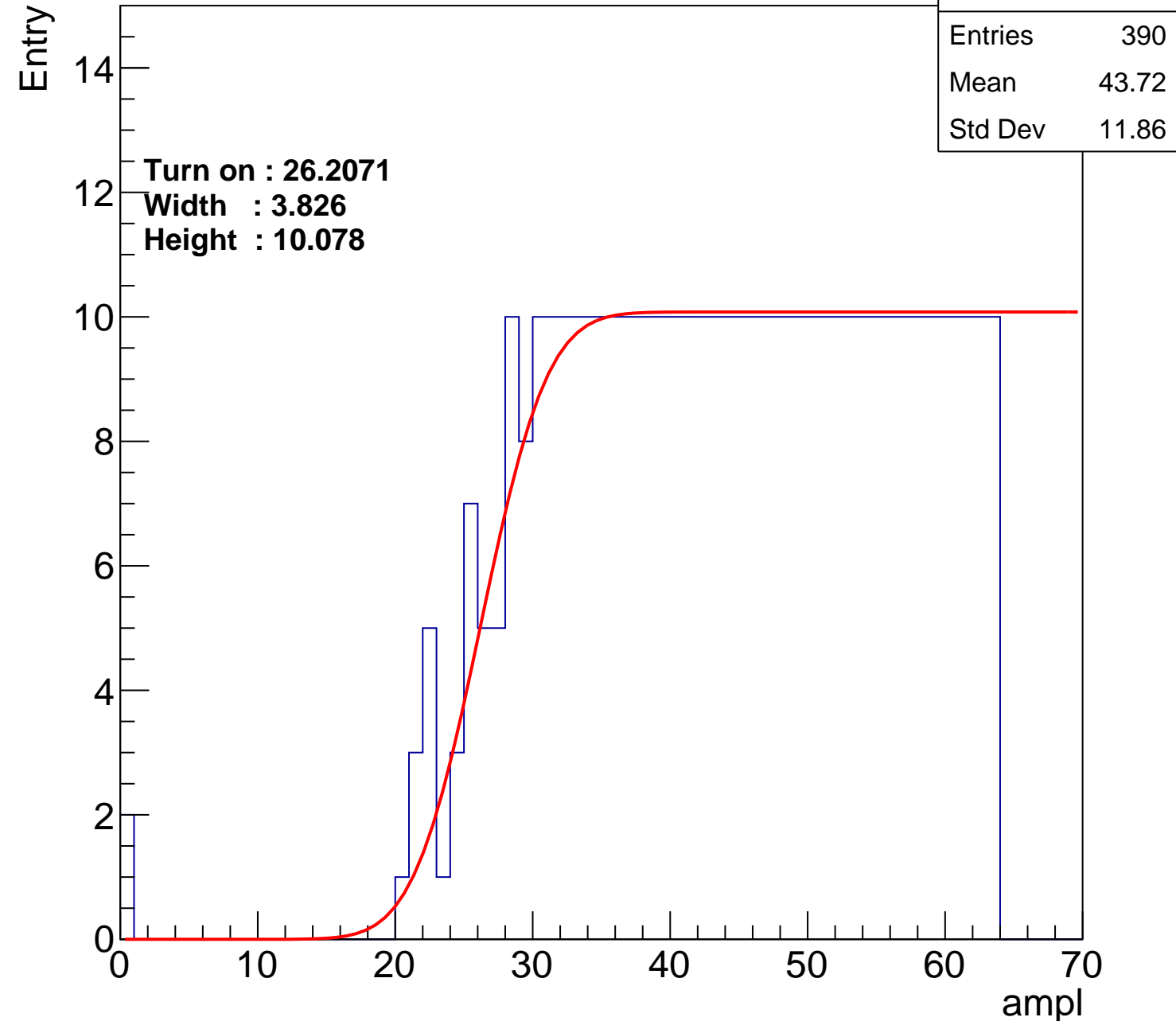
Width : 3.826

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.26
Std Dev	12.01

Turn on : 27.3434

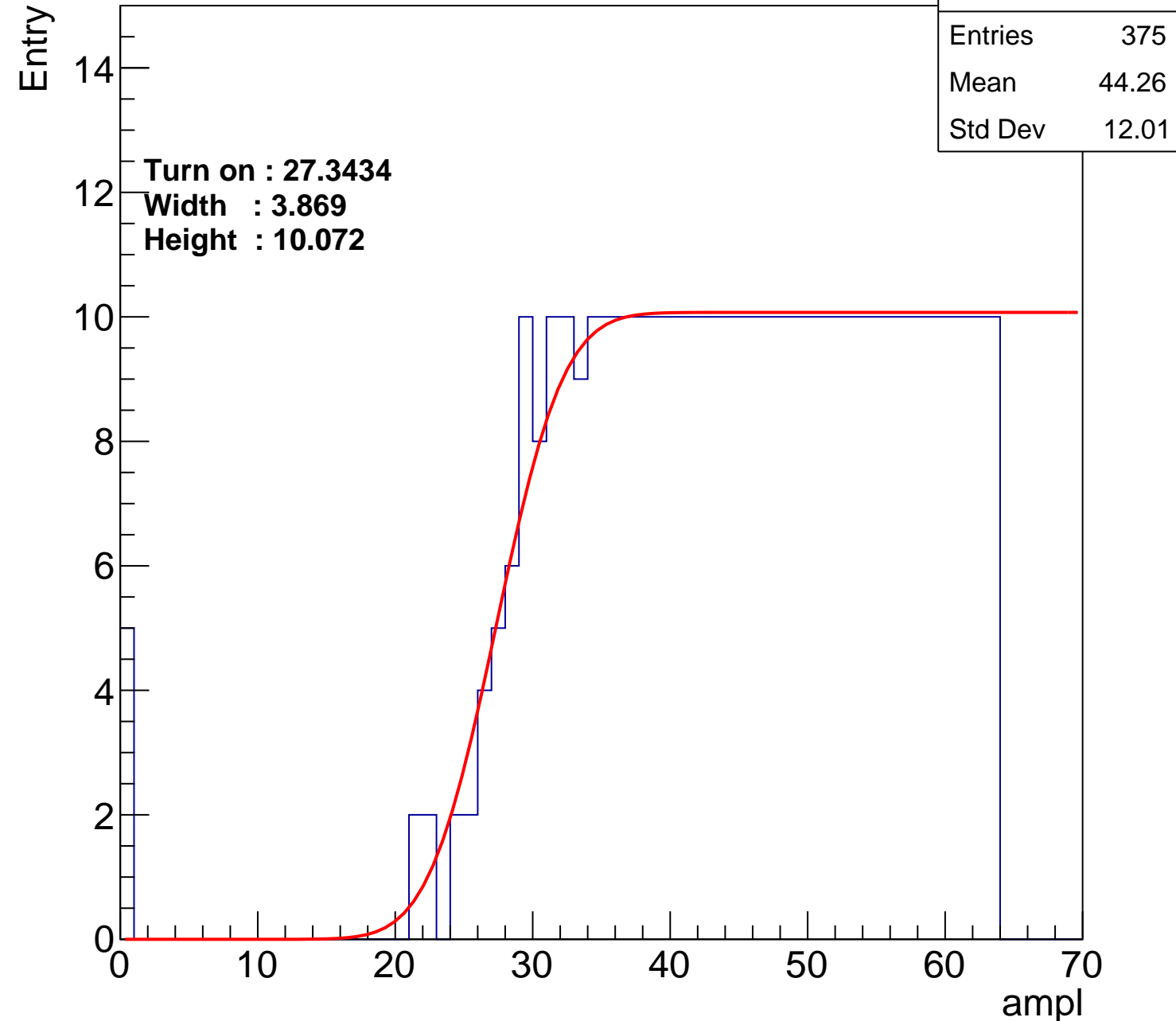
Width : 3.869

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.27
Std Dev	11.56

Turn on : 26.1188

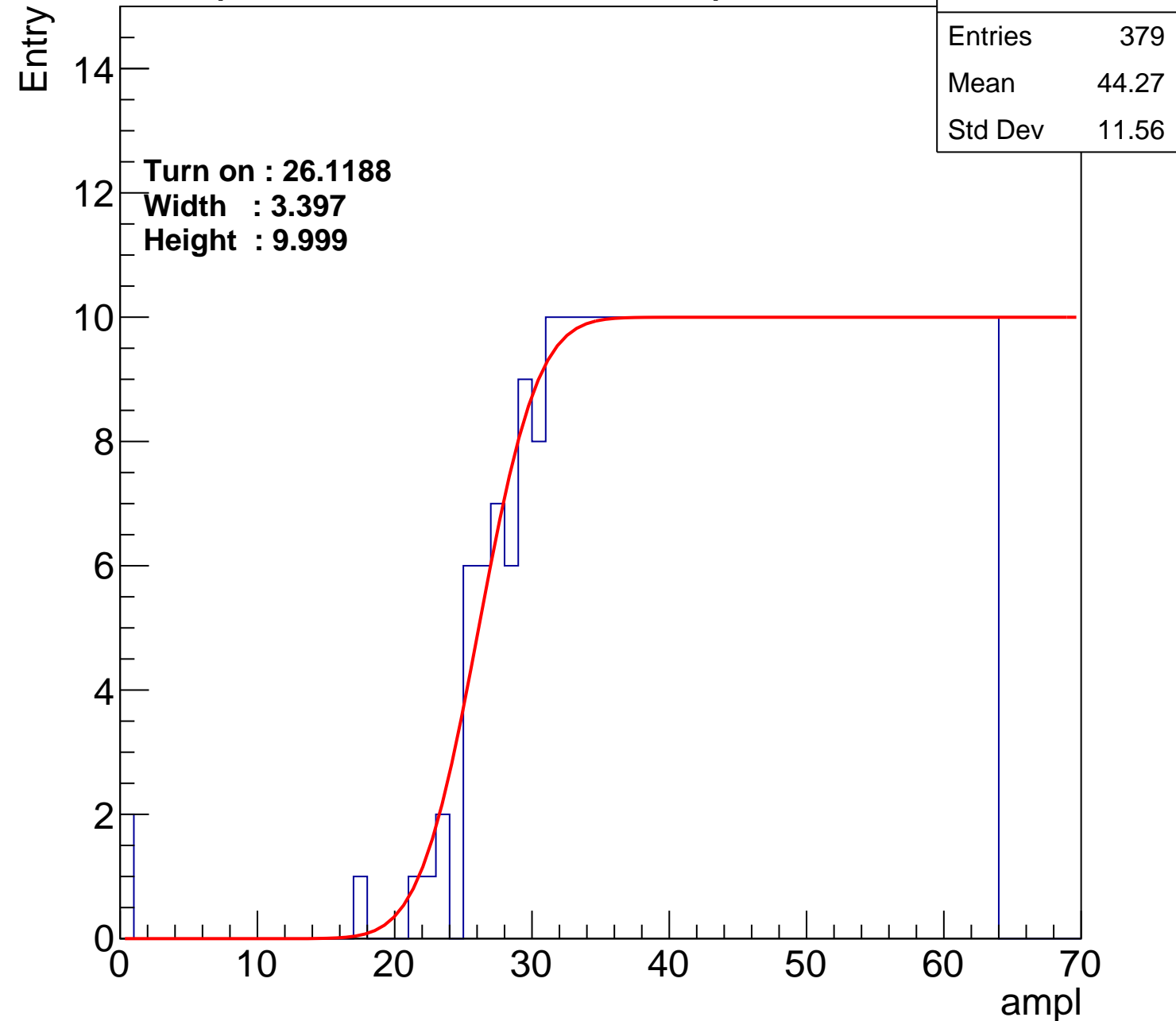
Width : 3.397

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch34

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.94
Std Dev	11.95

Turn on : 25.4195

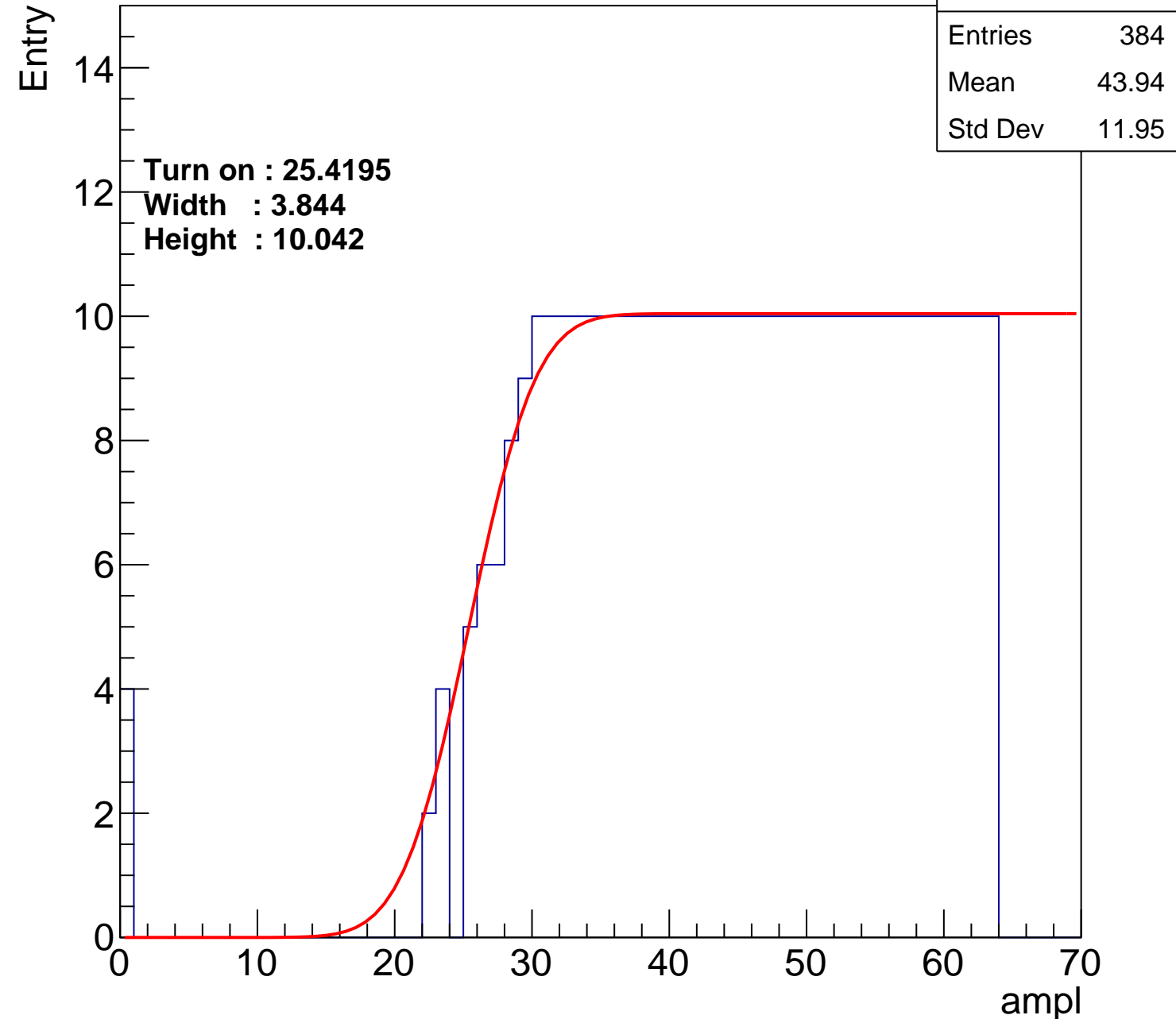
Width : 3.844

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch35

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.79
Std Dev	11.68

Turn on : 25.6658

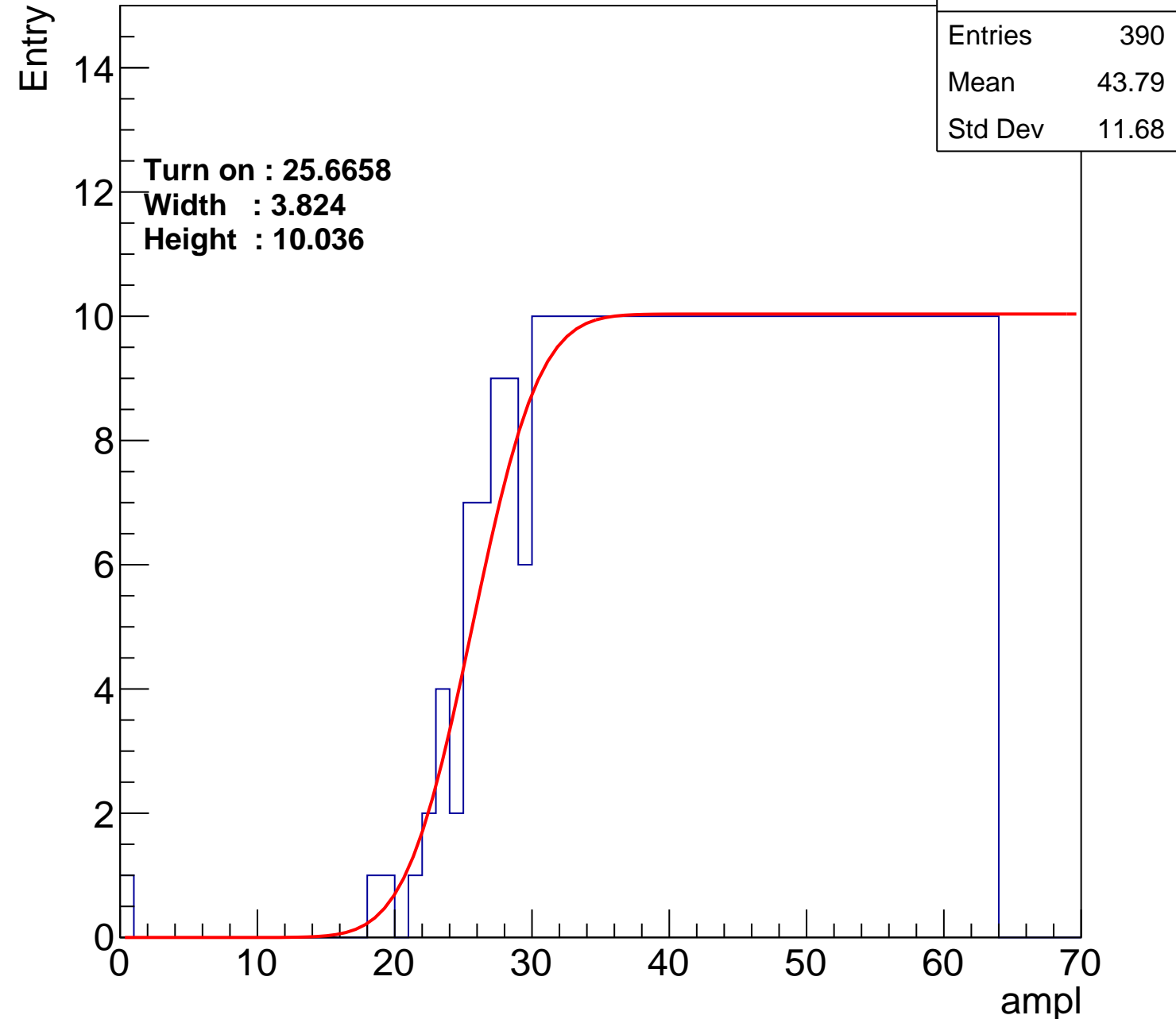
Width : 3.824

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch36

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 27.4591

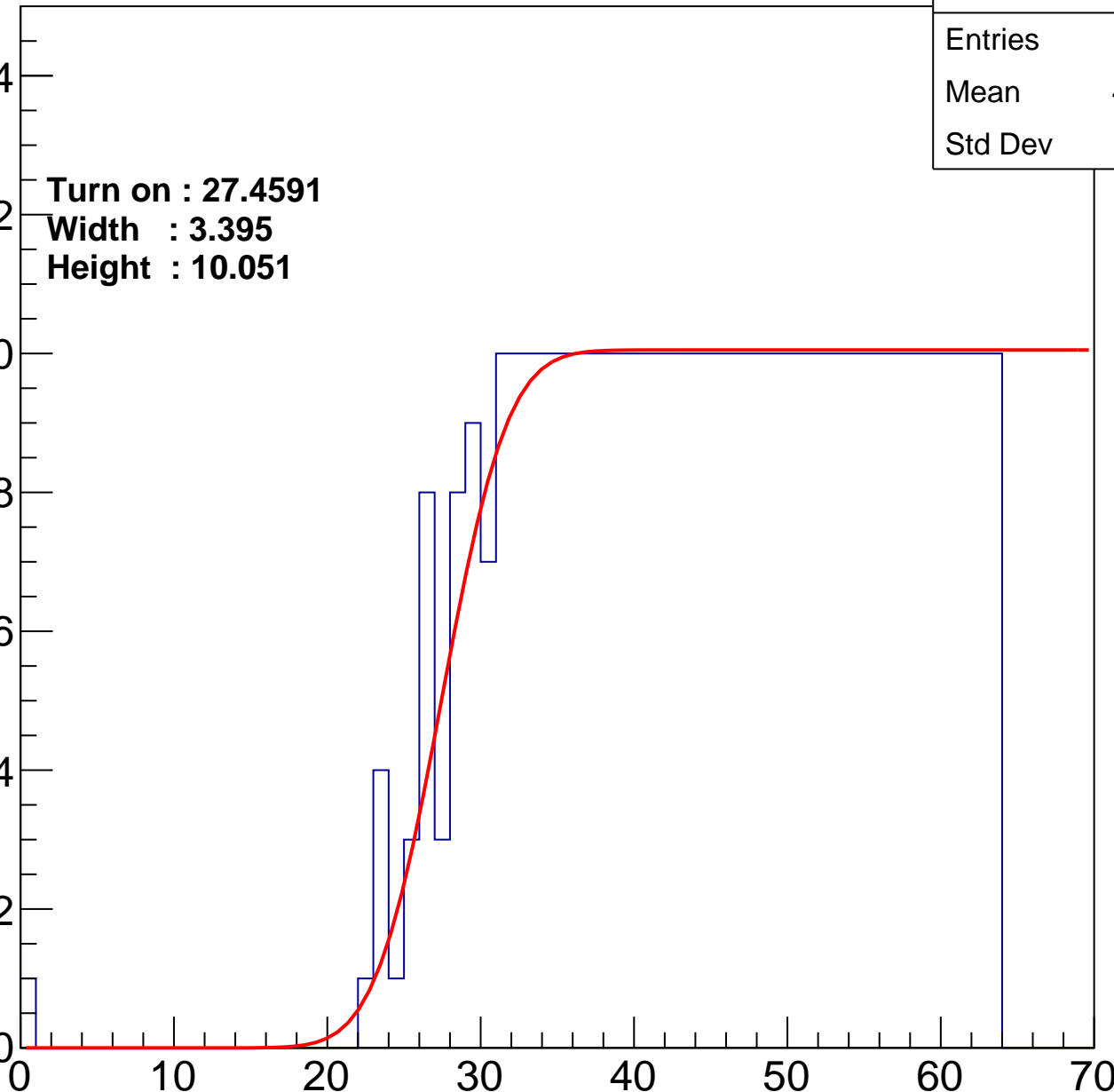
Width : 3.395

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.0473

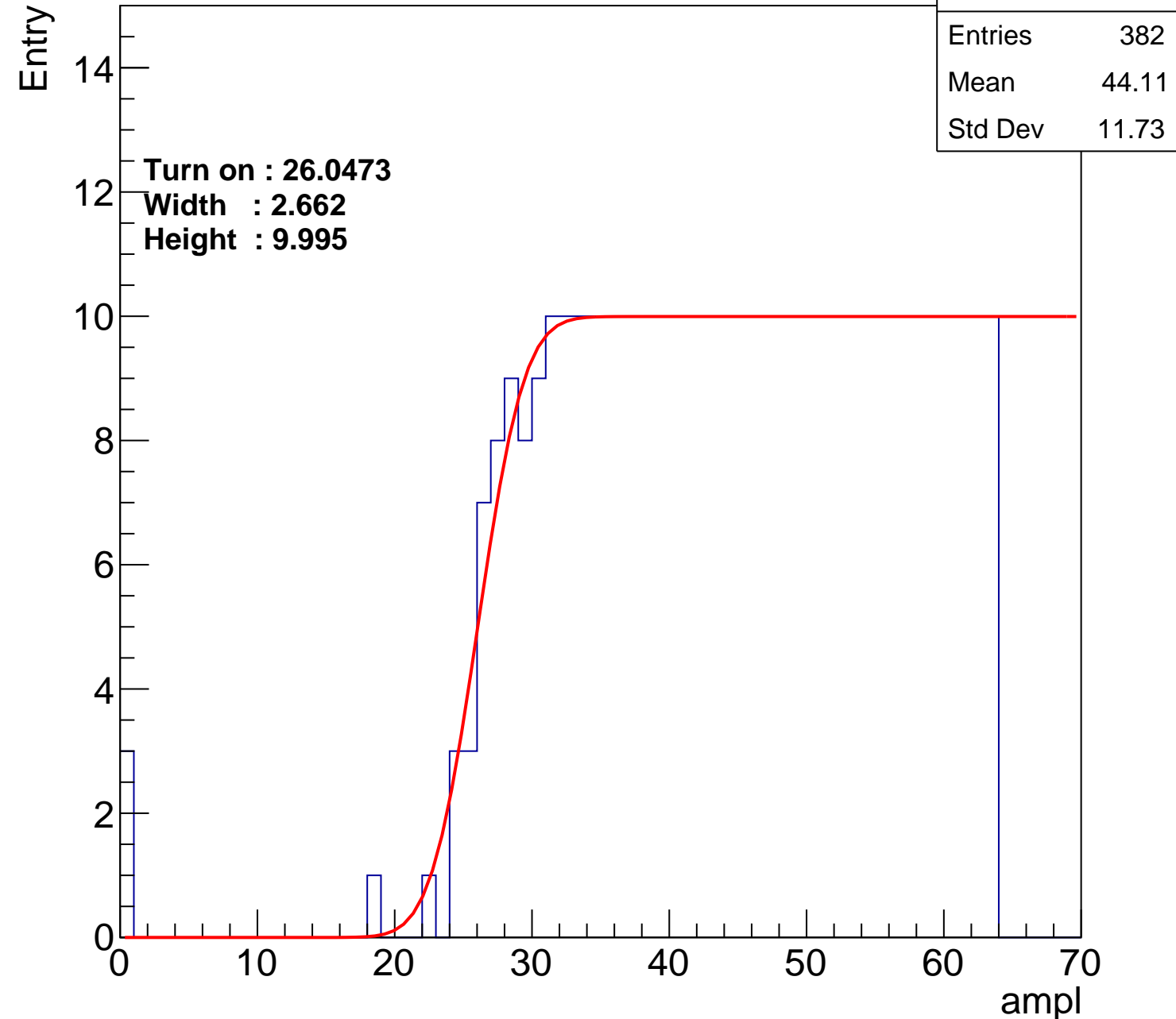
Width : 2.662

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.9
Std Dev	11.6

Turn on : 25.5252

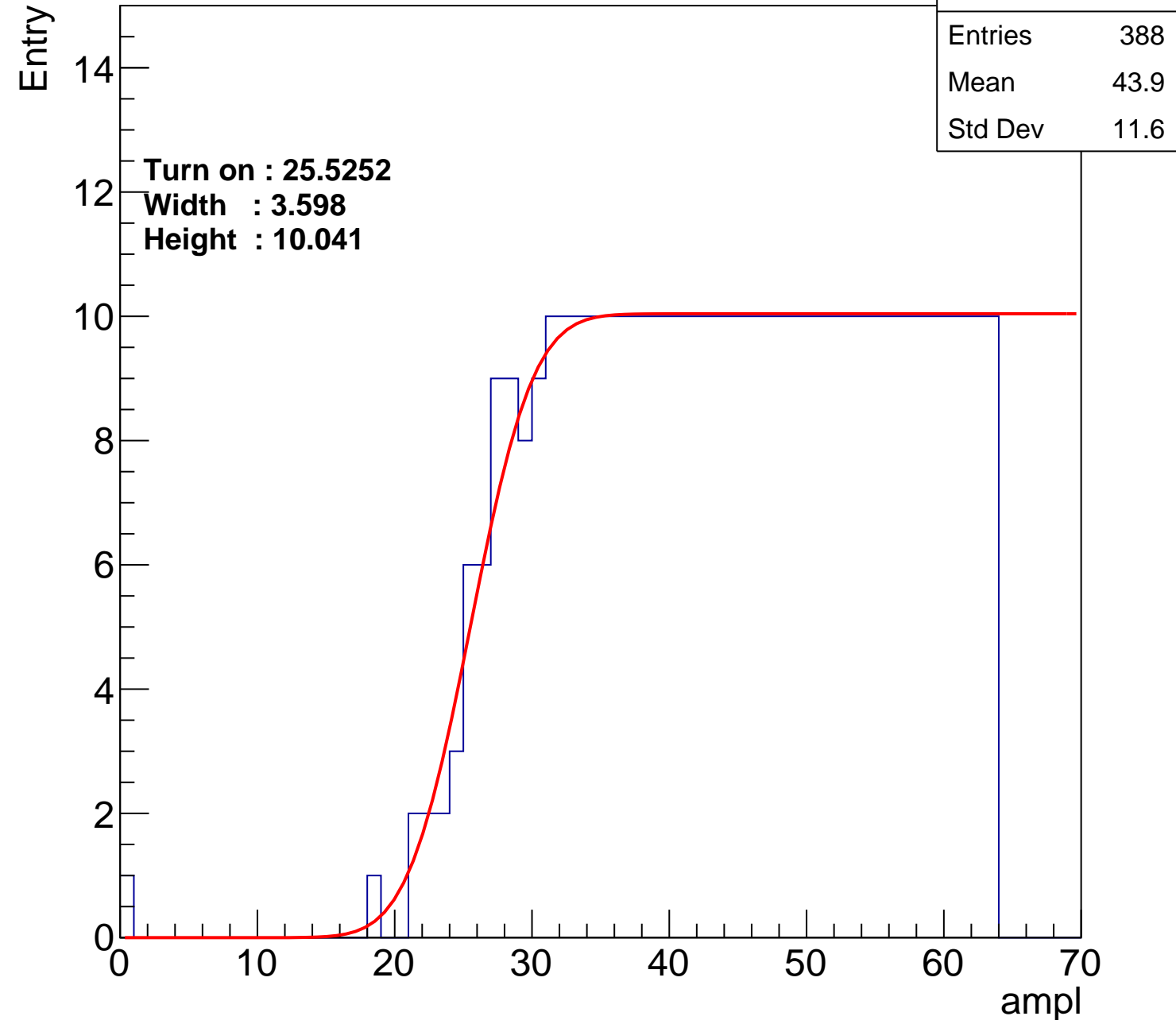
Width : 3.598

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	43.05
Std Dev	12.13

Turn on : 23.5733

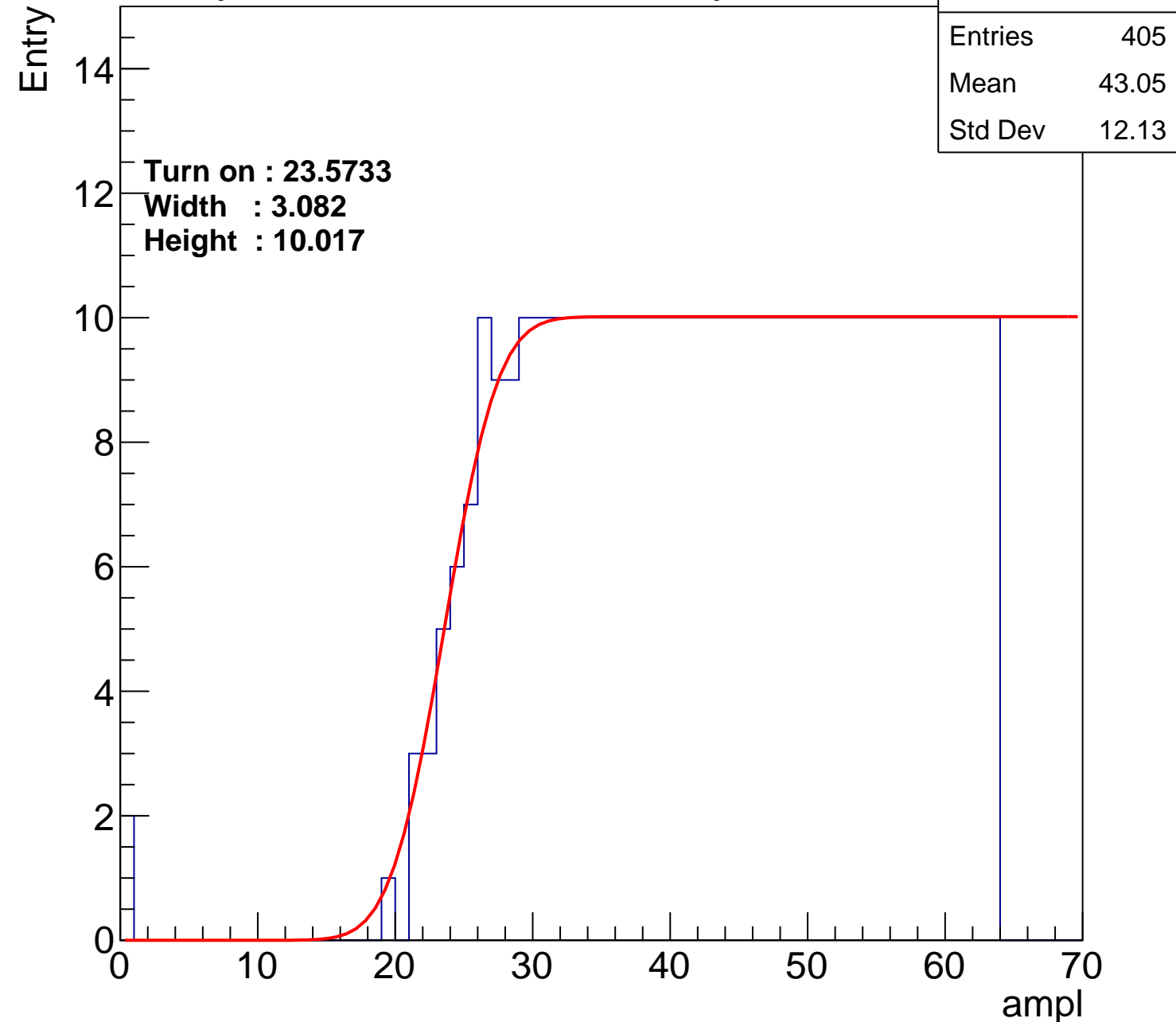
Width : 3.082

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.36
Std Dev	11.52

Turn on : 26.4311

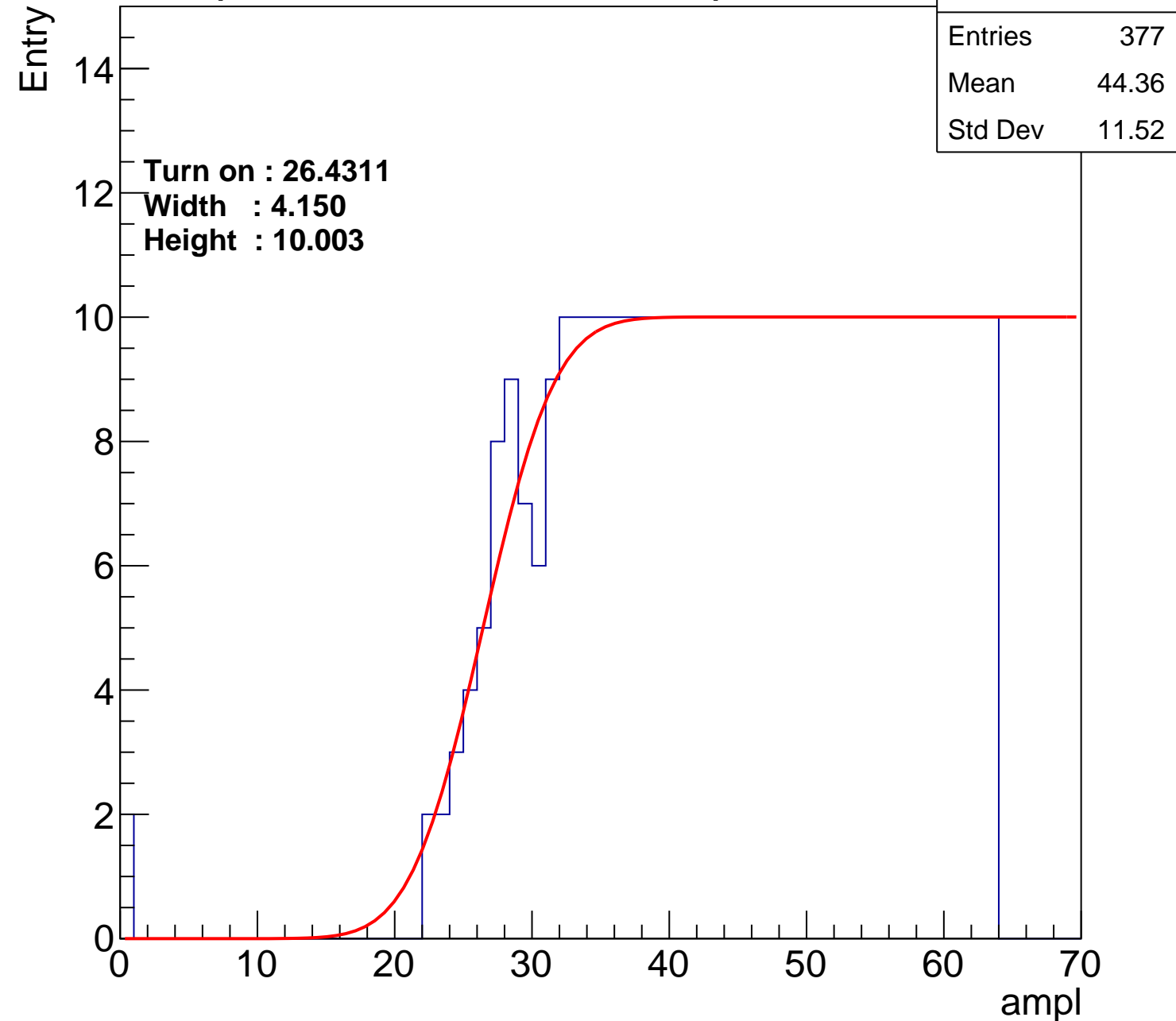
Width : 4.150

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.1910

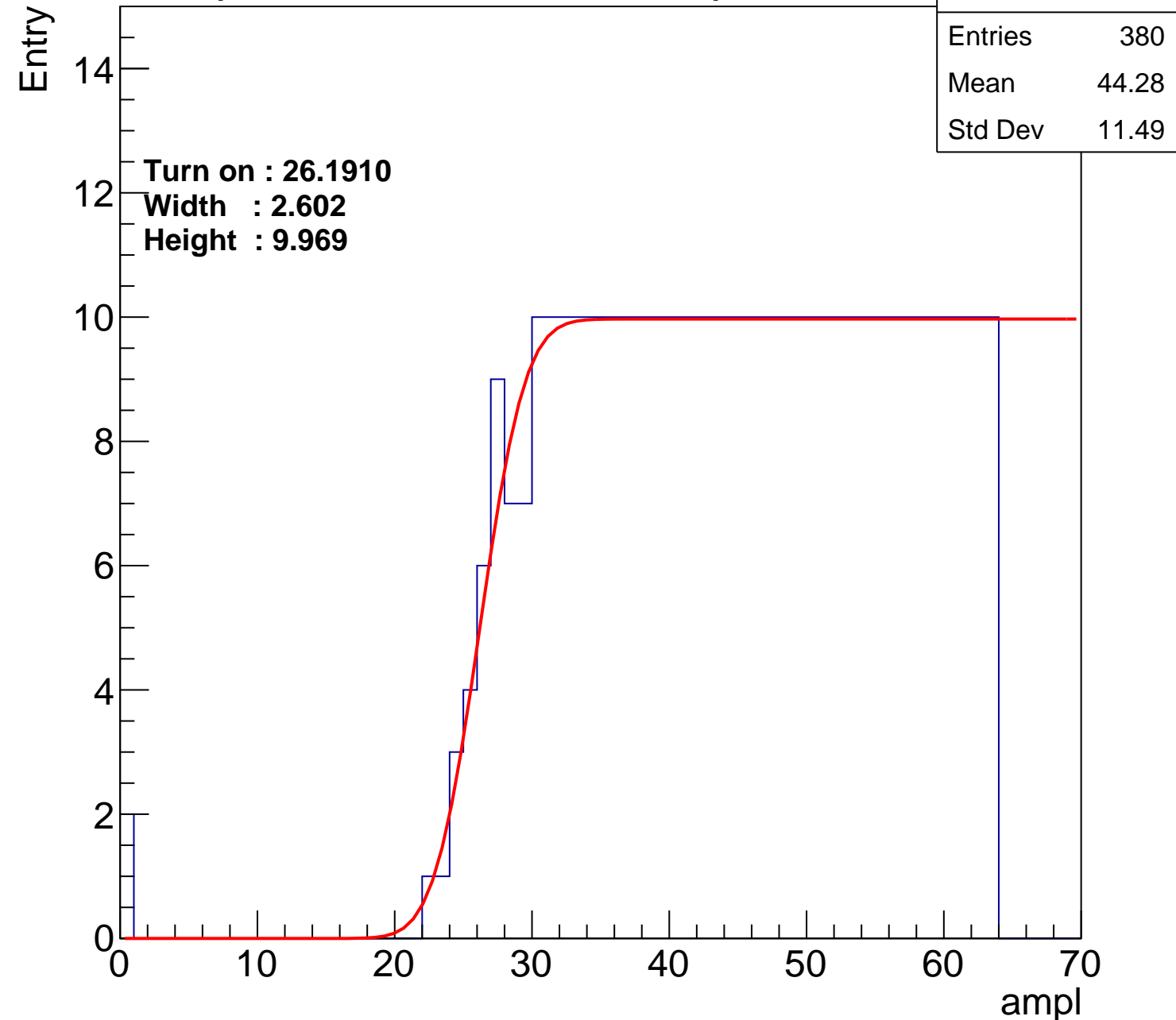
Width : 2.602

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch42

calib_packv5_042523_0143.root, FC#11, port A2

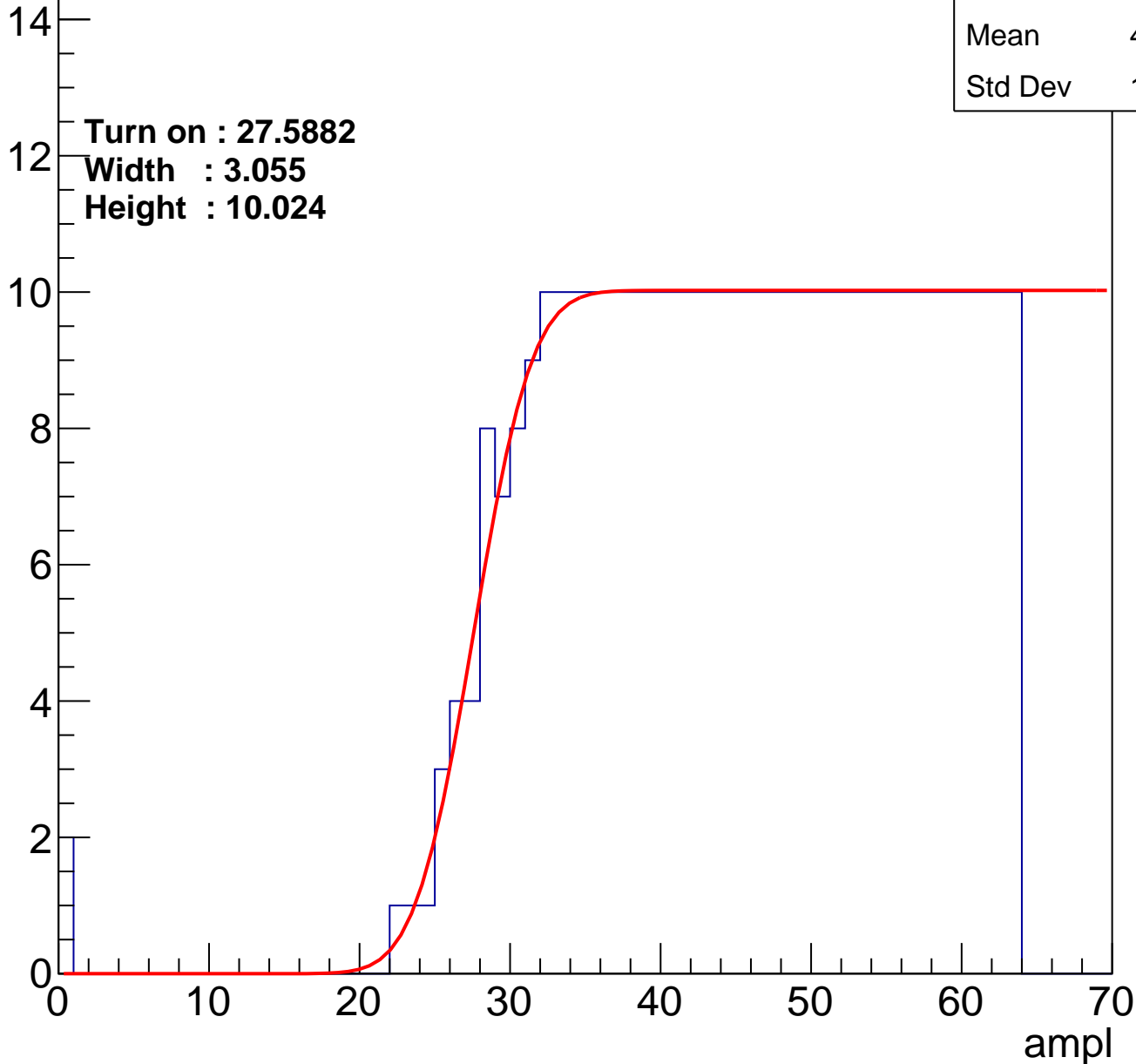
Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.5882

Width : 3.055

Height : 10.024

Entry



B1L102S, U24-ch43

calib_packv5_042523_0143.root, FC#11, port A2

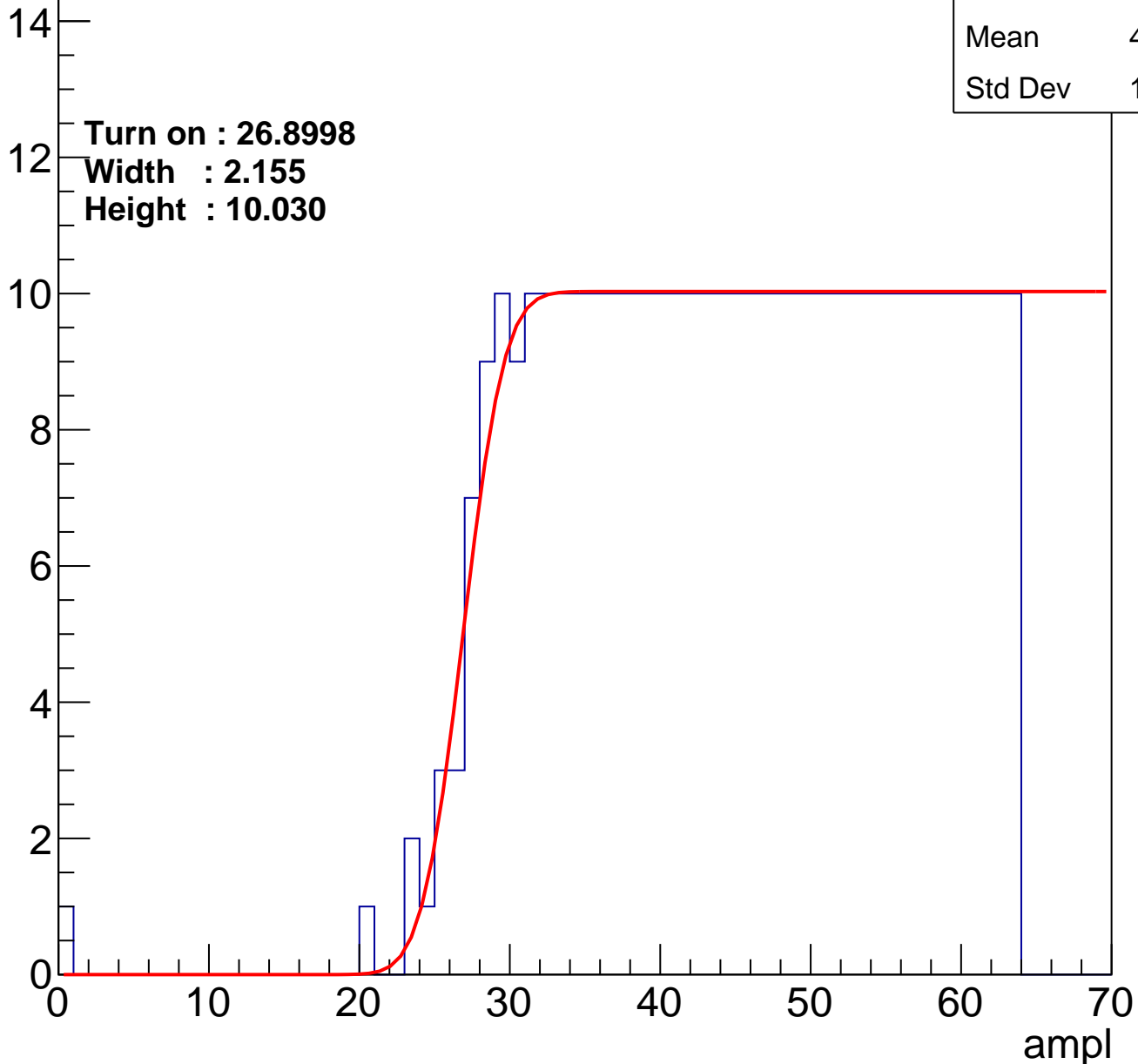
Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 26.8998

Width : 2.155

Height : 10.030

Entry



B1L102S, U24-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.4
Std Dev	12.22

Turn on : 24.2821

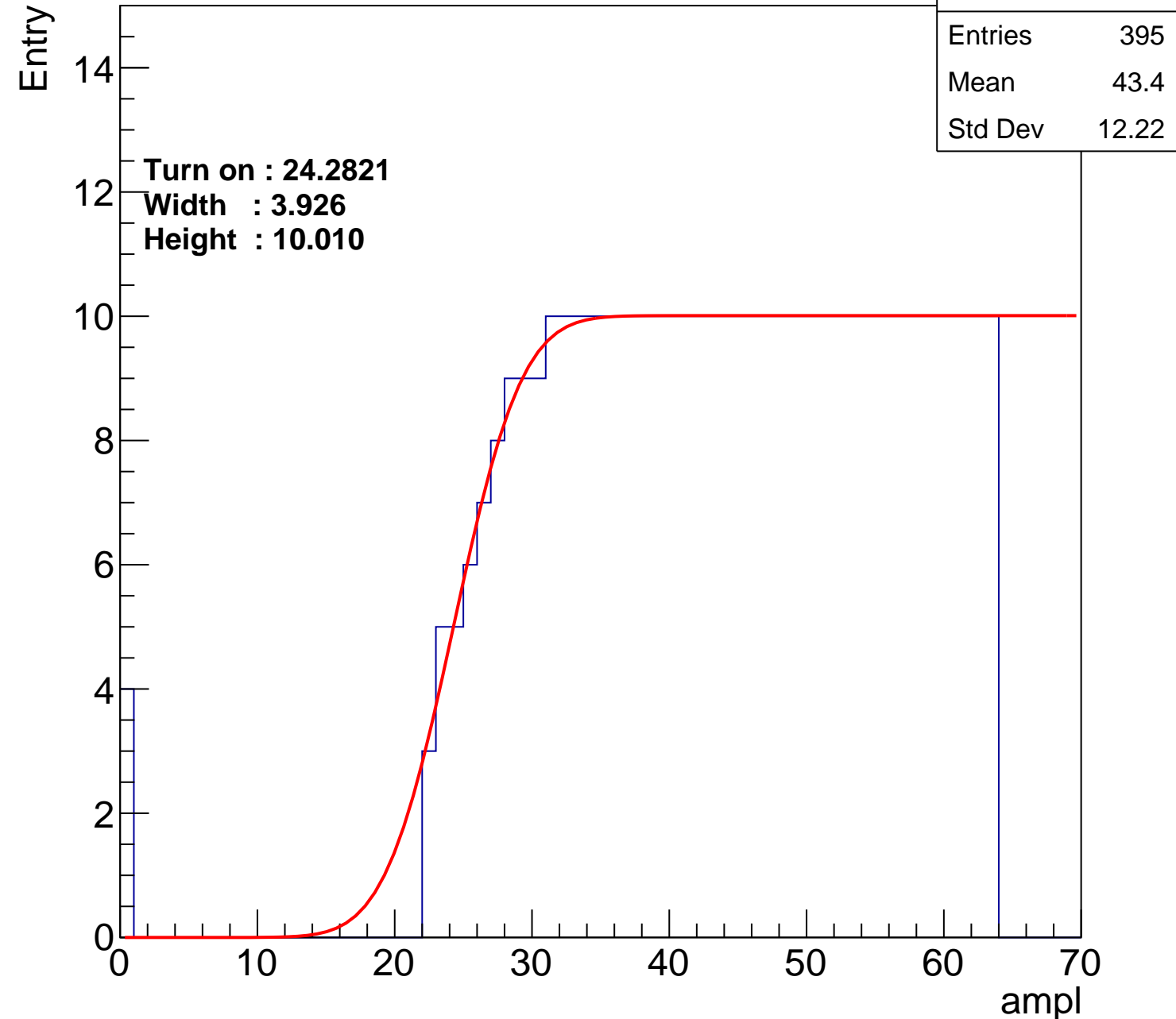
Width : 3.926

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.11
Std Dev	11.9

Turn on : 26.8051

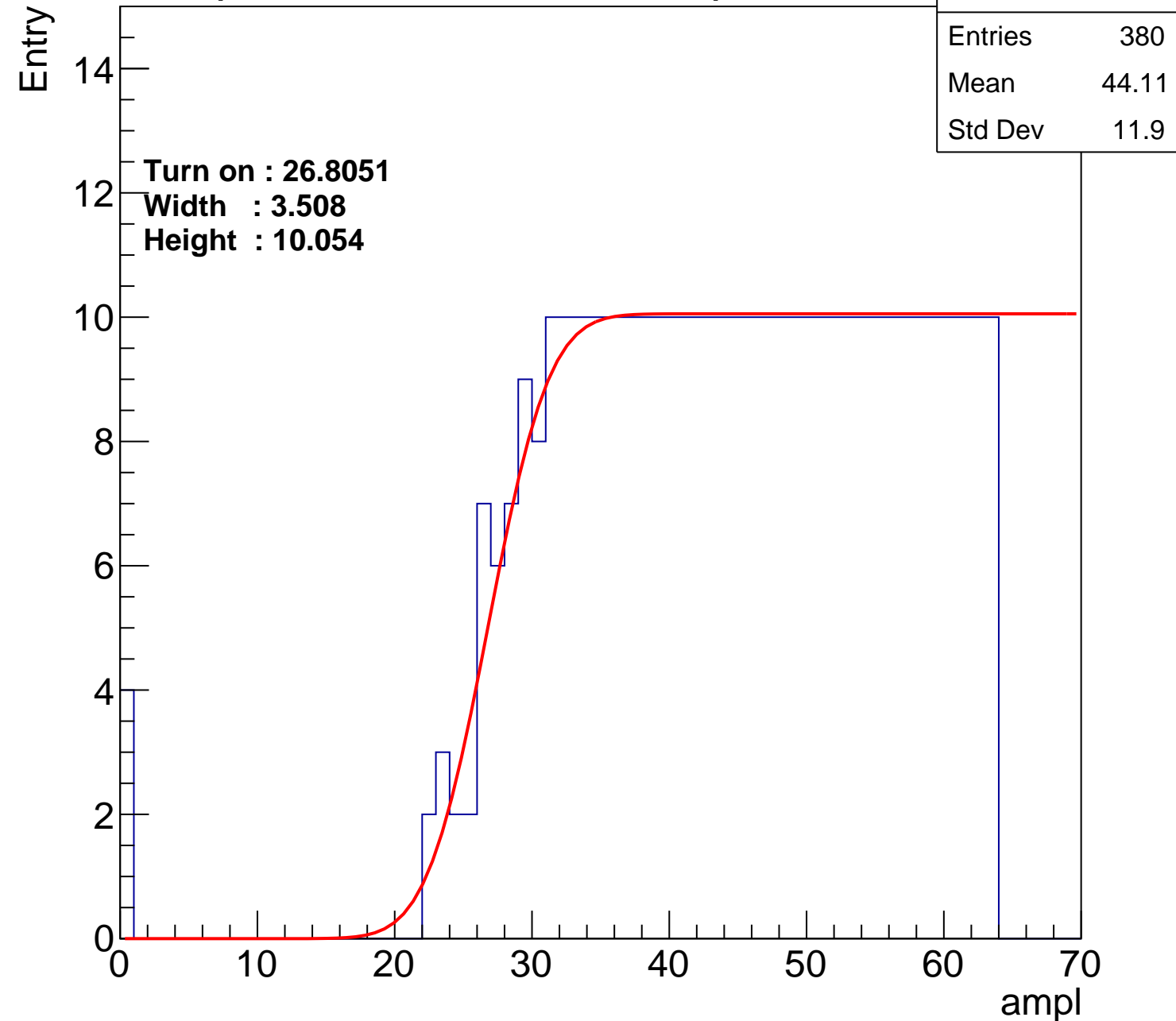
Width : 3.508

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.39
Std Dev	12.11

Turn on : 24.6927

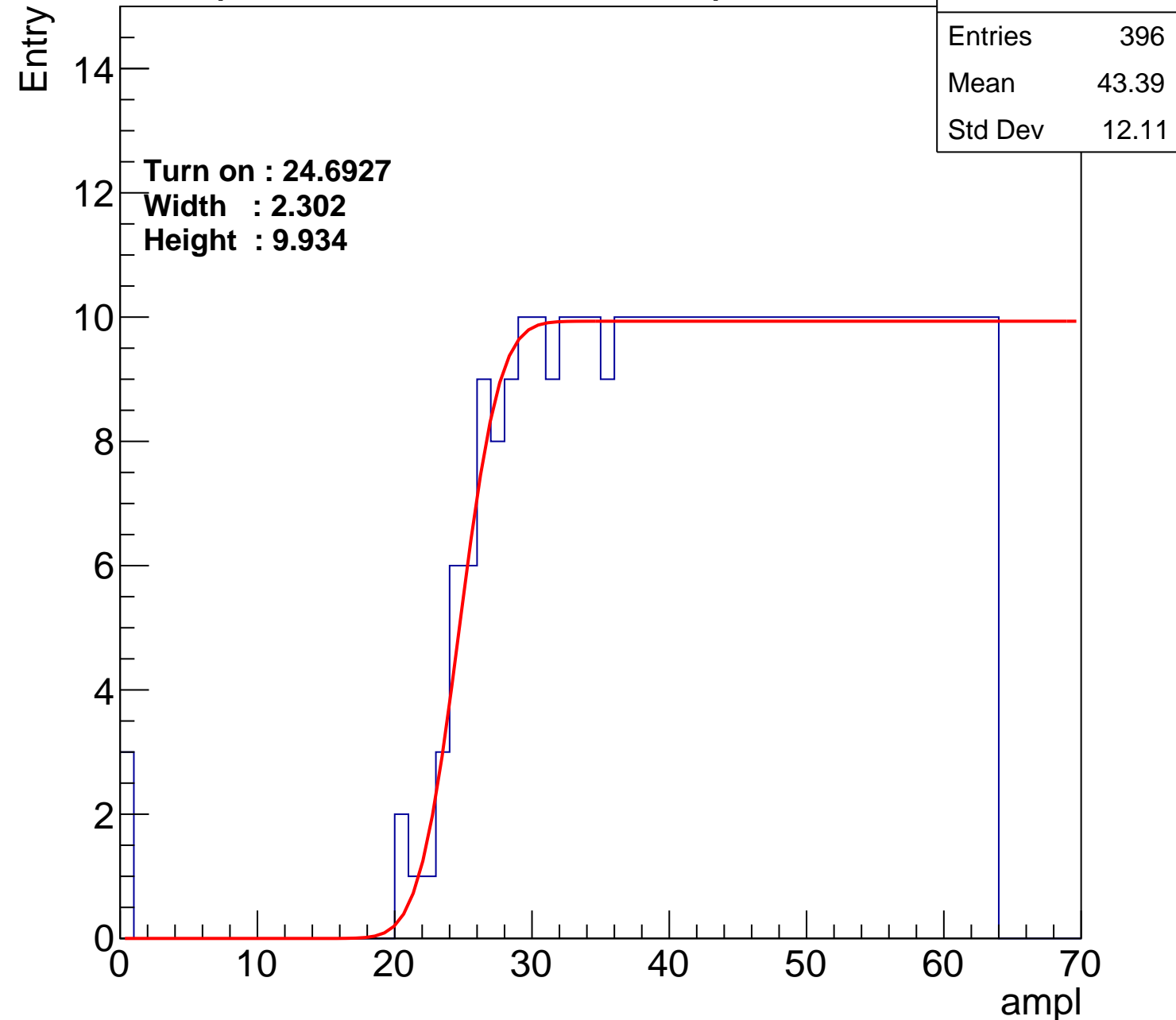
Width : 2.302

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch47

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.2345

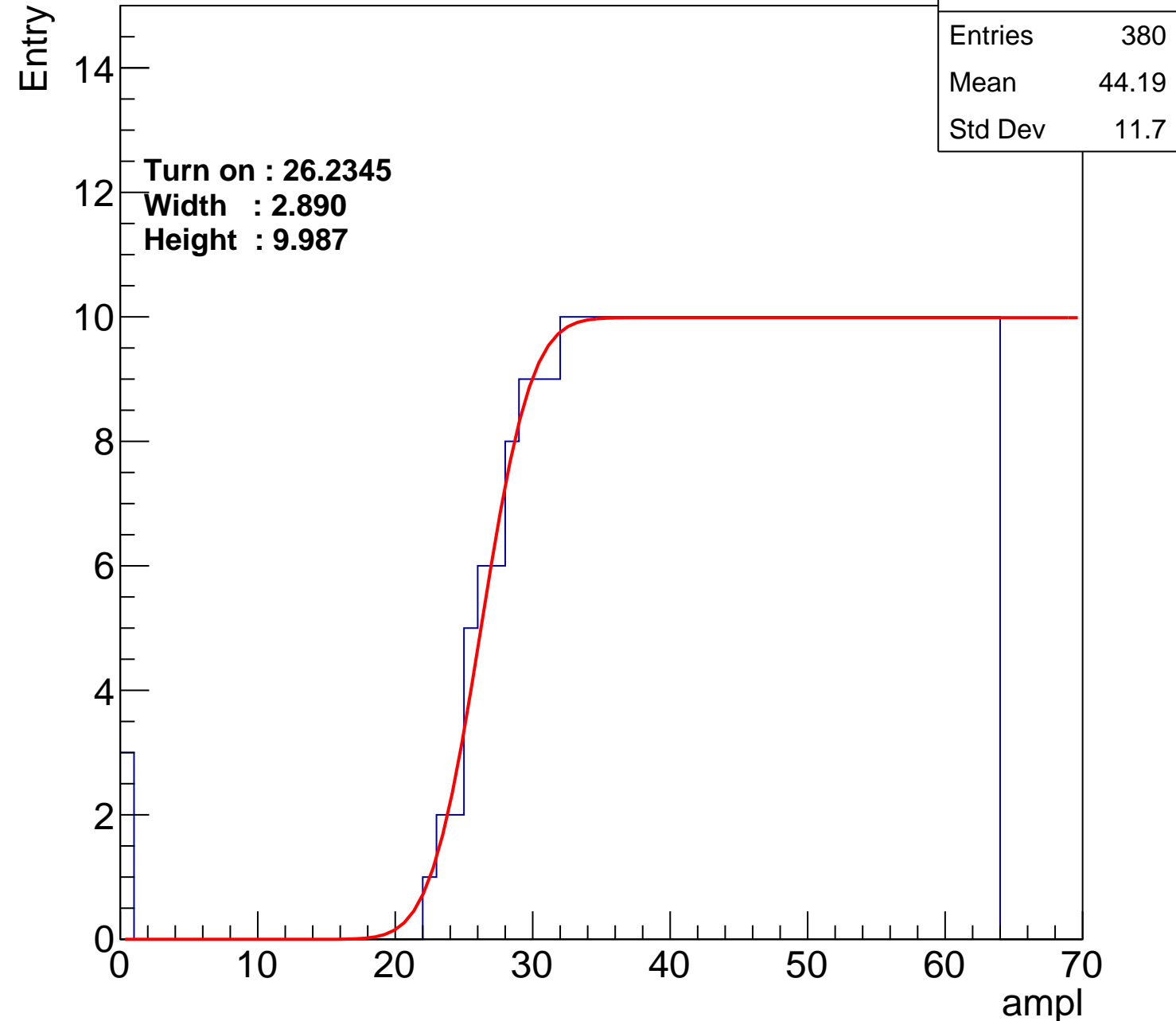
Width : 2.890

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 26.7762

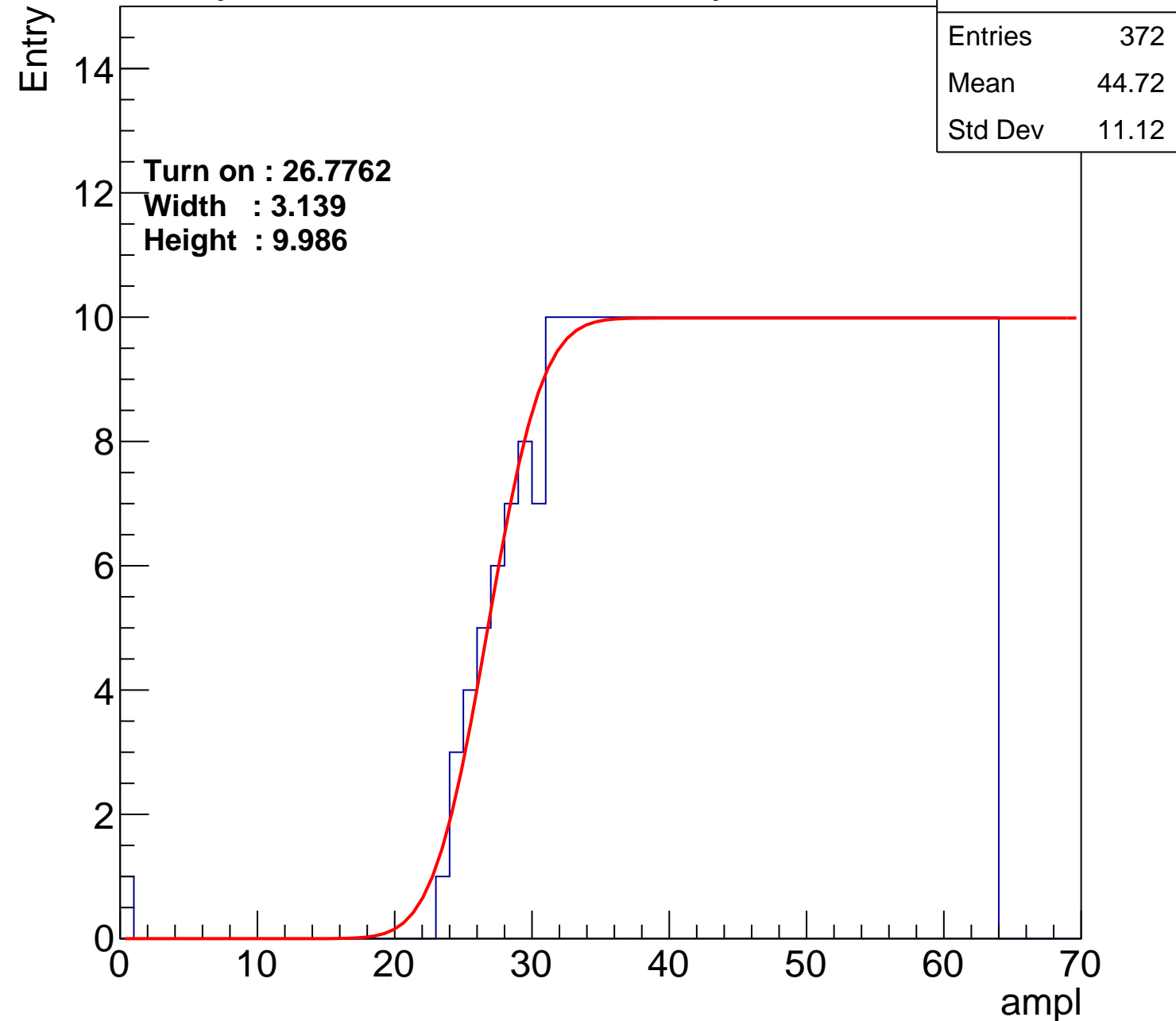
Width : 3.139

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch49

calib_packv5_042523_0143.root, FC#11, port A2

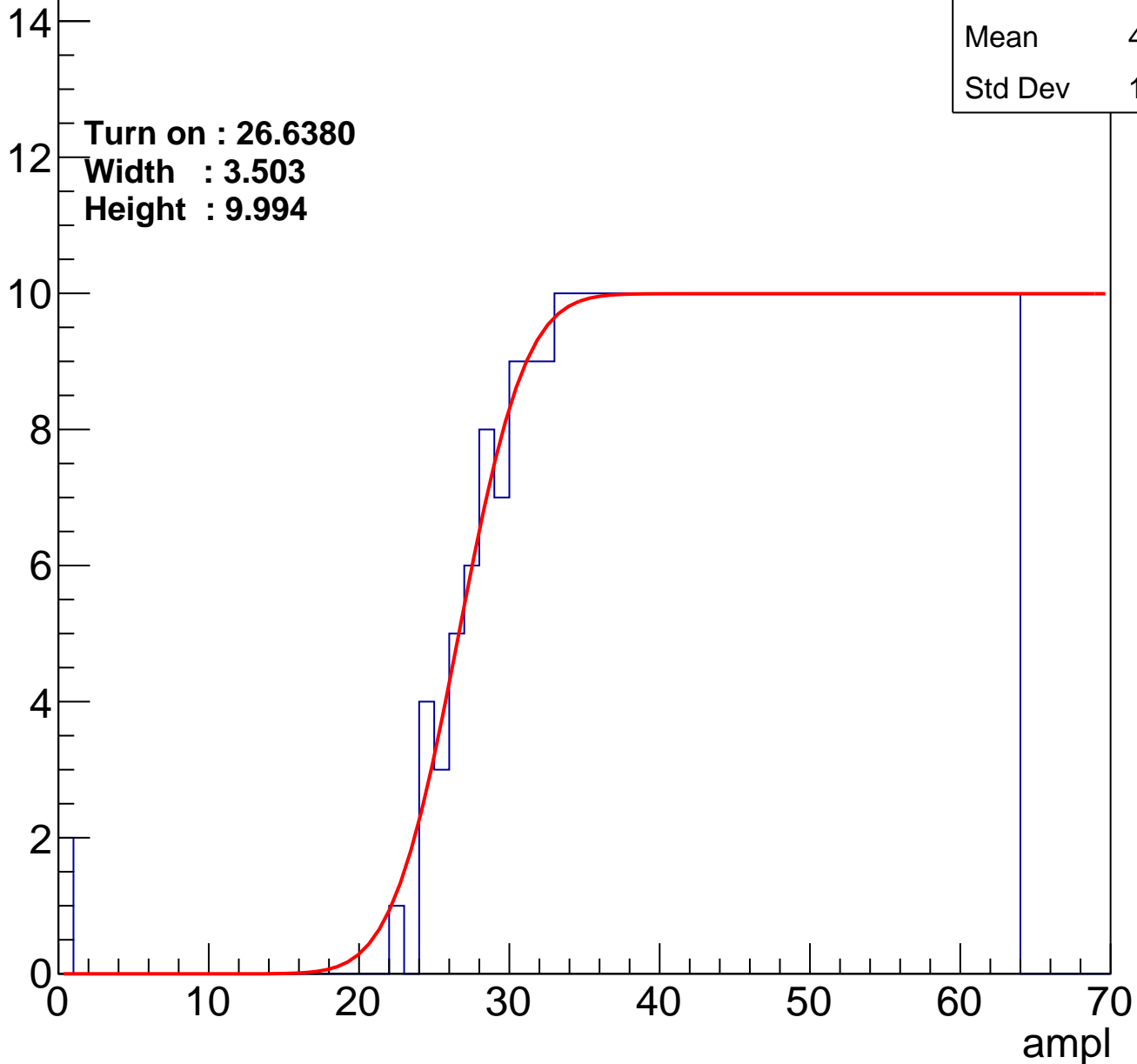
Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 26.6380

Width : 3.503

Height : 9.994

Entry



B1L102S, U24-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.5963

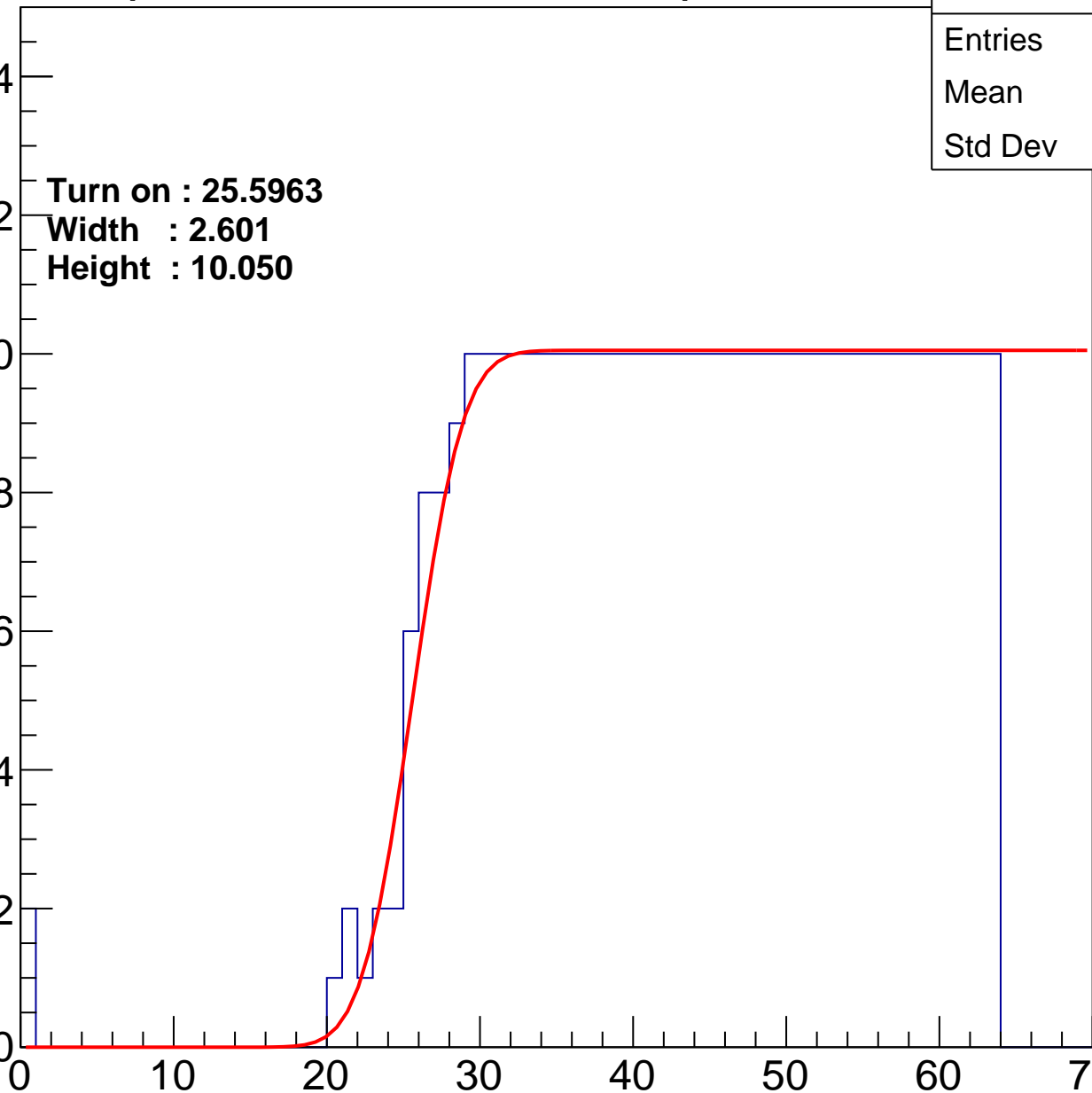
Width : 2.601

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	356
Mean	45.31
Std Dev	11.22

Turn on : 29.6578

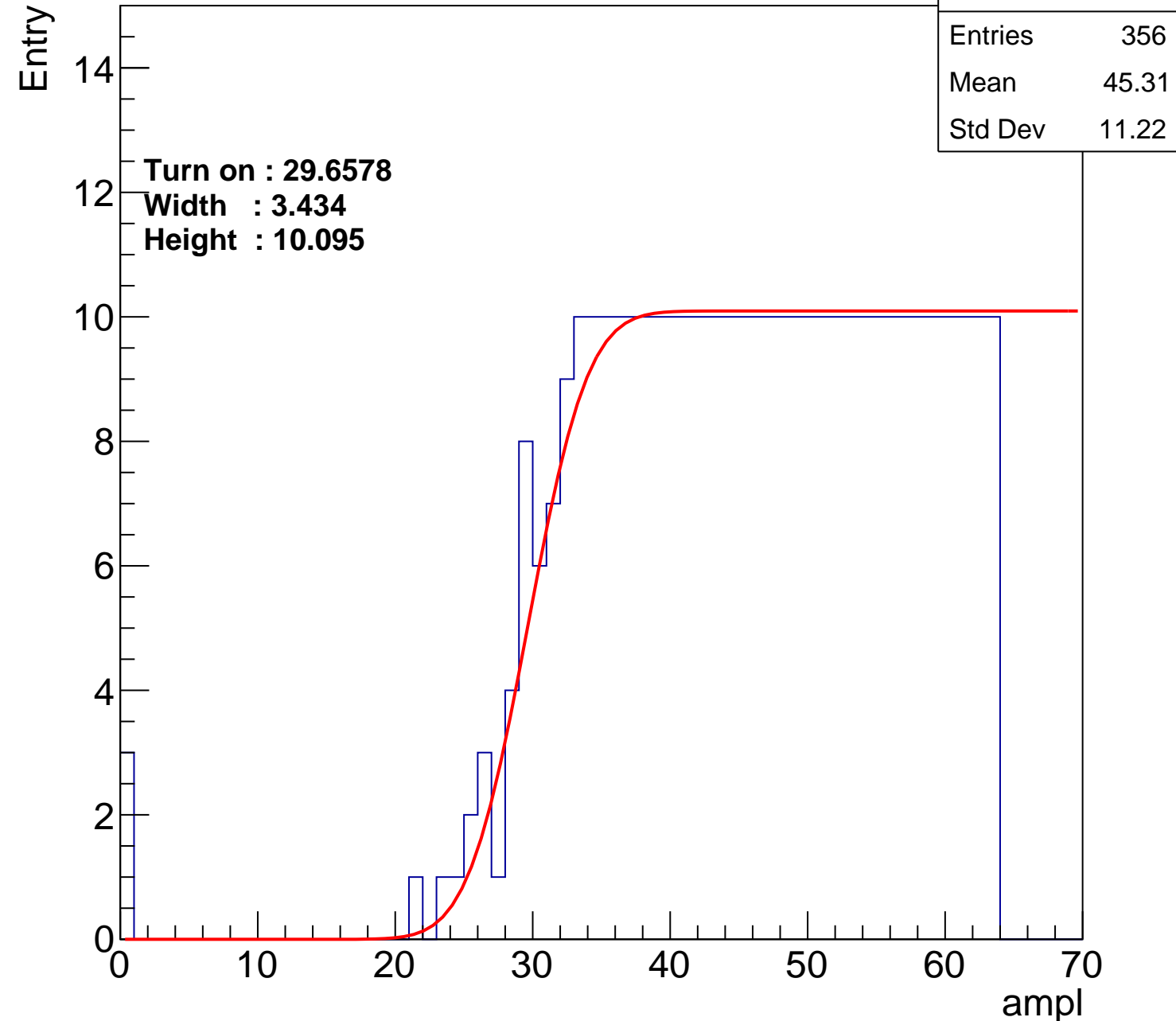
Width : 3.434

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch52

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.58
Std Dev	11.88

Turn on : 27.6263

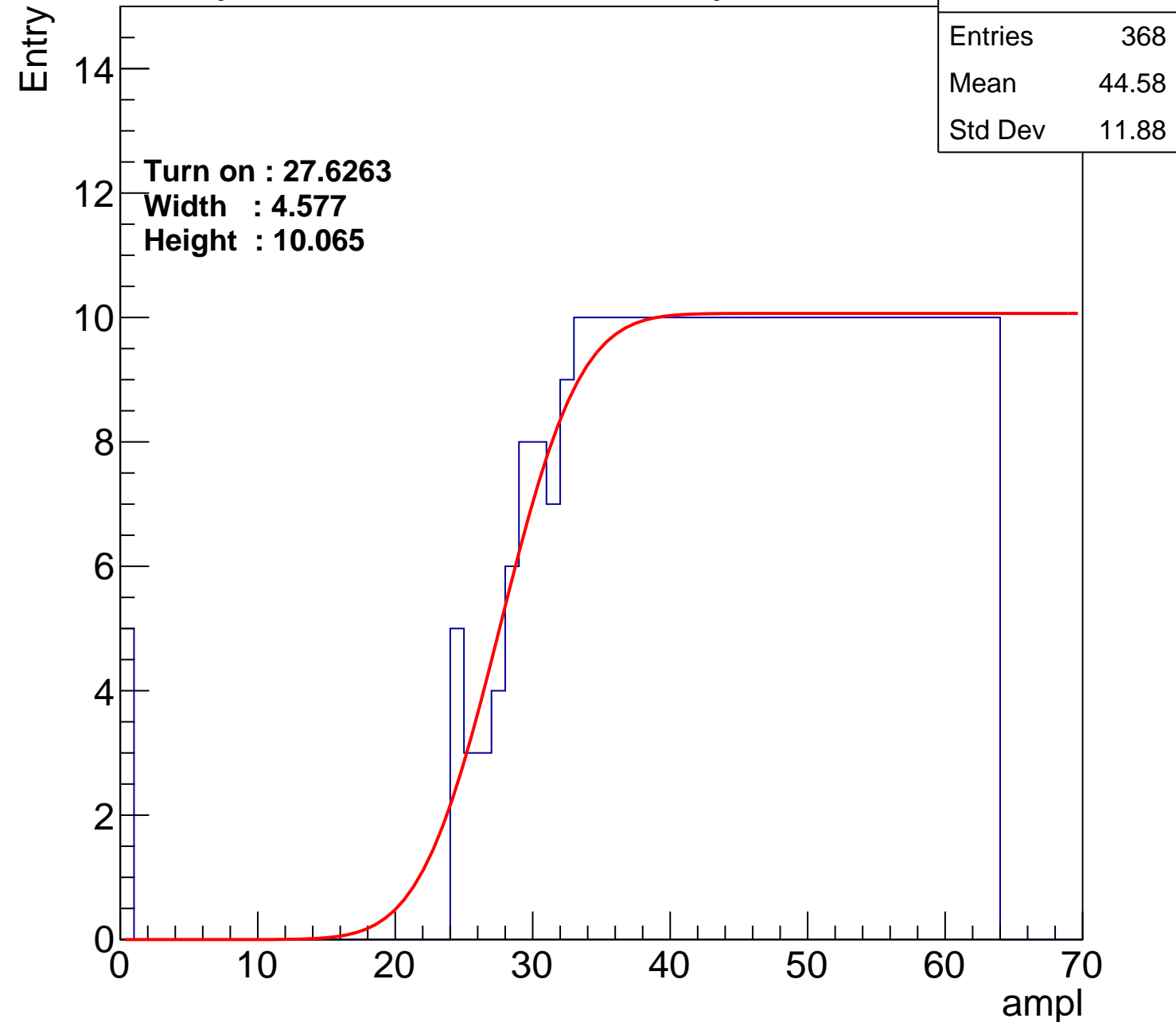
Width : 4.577

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch53

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.54
Std Dev	11.38

Turn on : 27.1993

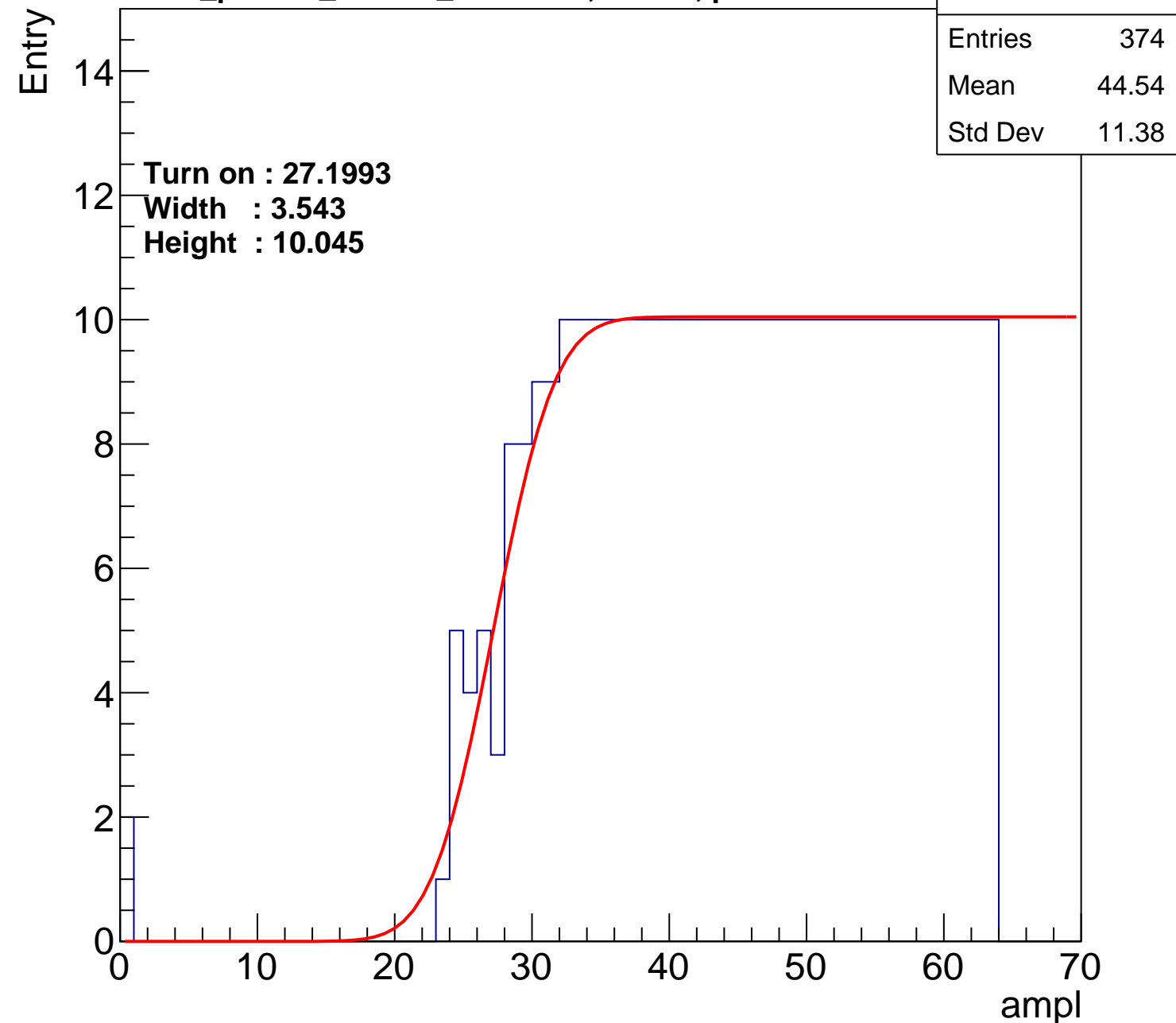
Width : 3.543

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.57
Std Dev	11.93

Turn on : 25.7452

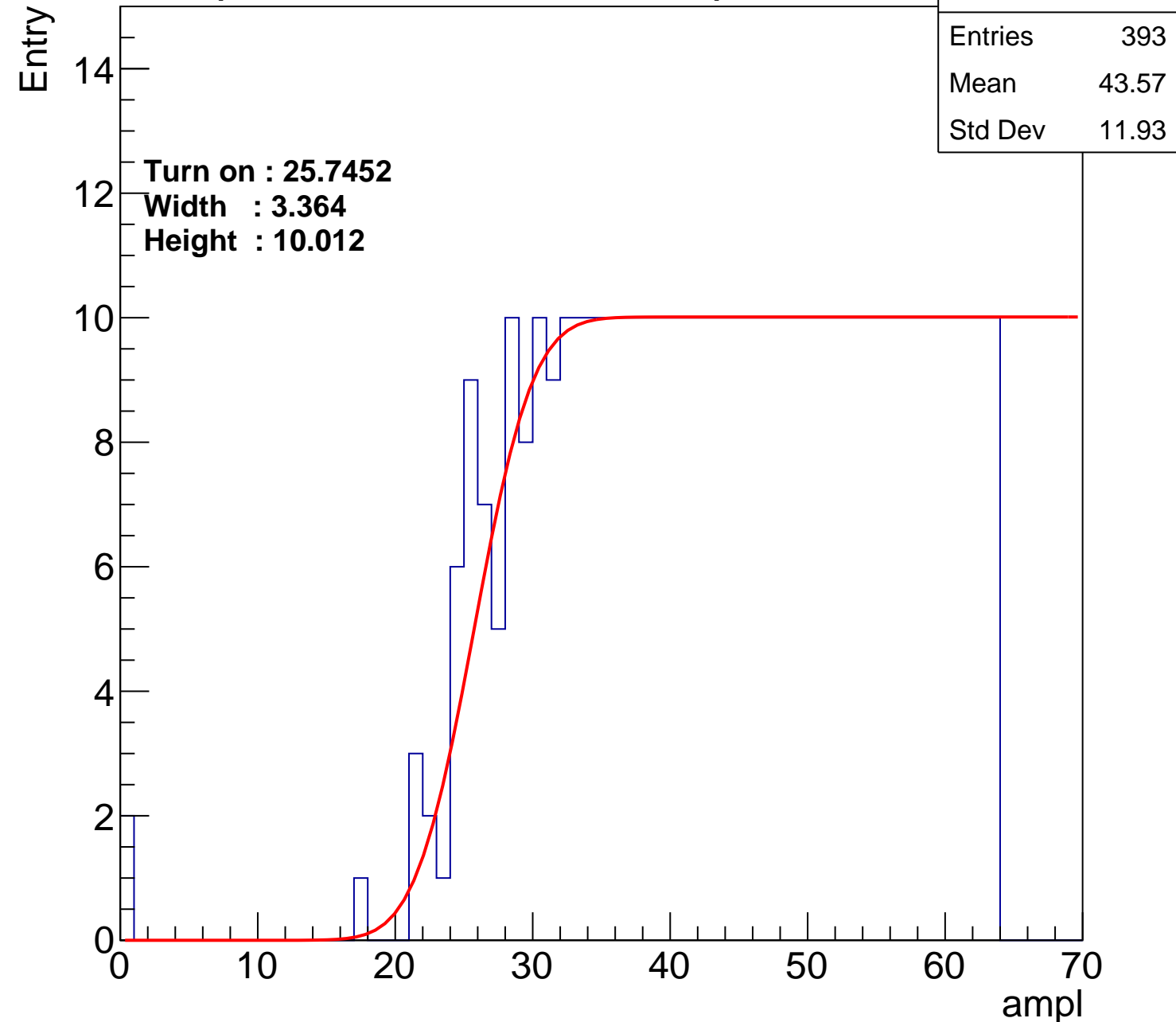
Width : 3.364

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.66
Std Dev	11.63

Turn on : 27.4095

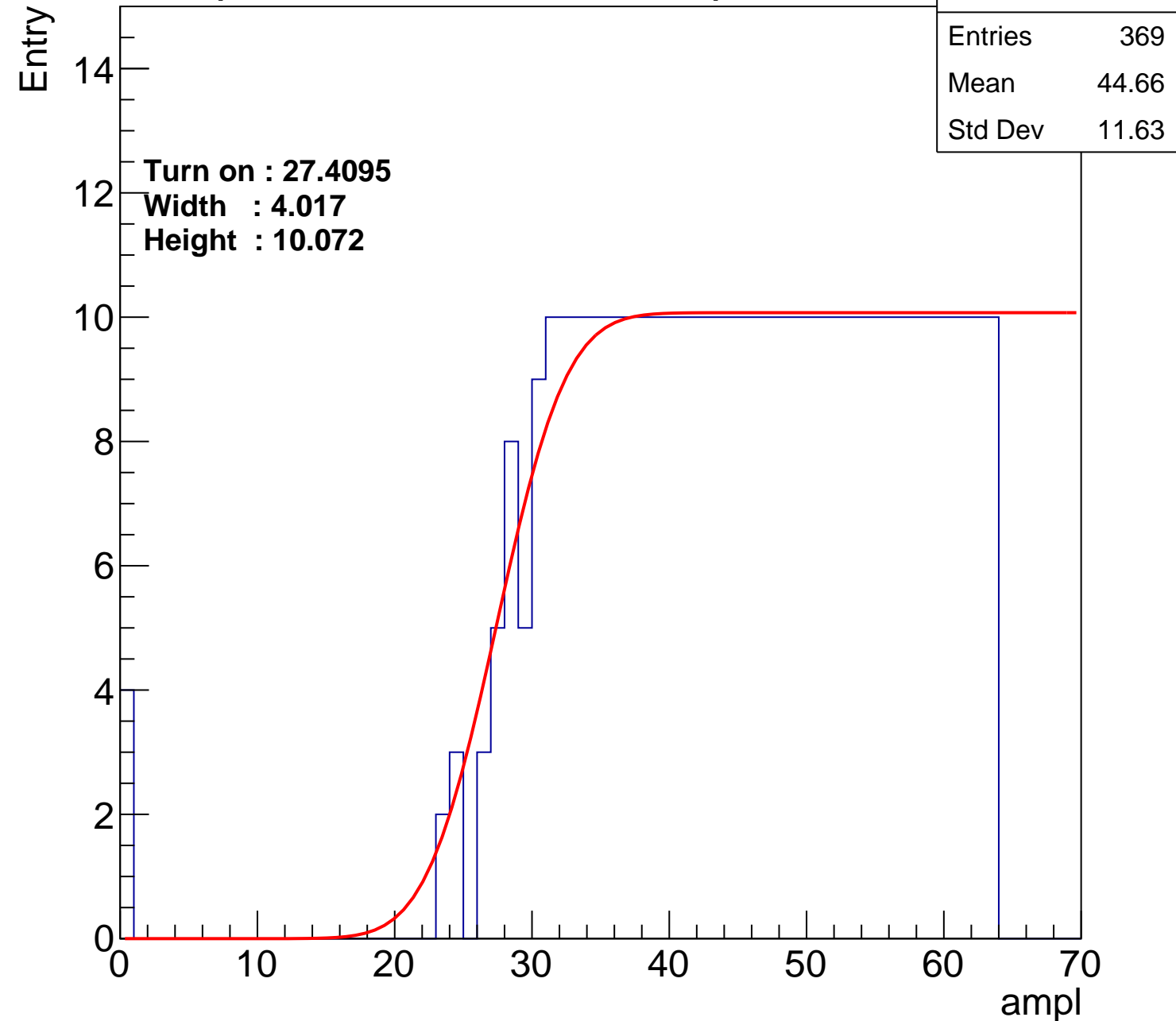
Width : 4.017

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch56

calib_packv5_042523_0143.root, FC#11, port A2

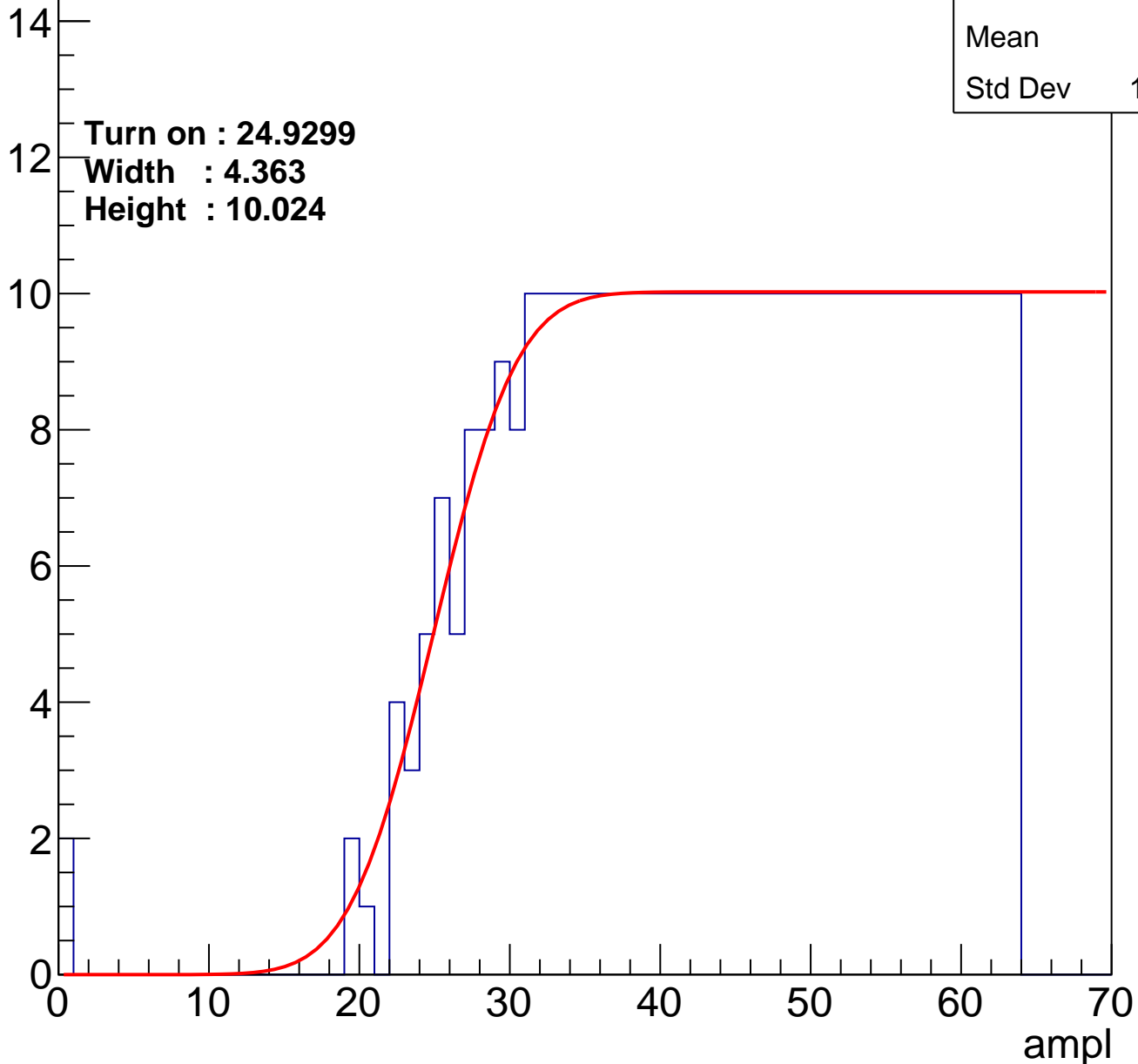
Entries	392
Mean	43.6
Std Dev	11.94

Turn on : 24.9299

Width : 4.363

Height : 10.024

Entry



B1L102S, U24-ch57

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.87
Std Dev	11.71

Turn on : 25.4912

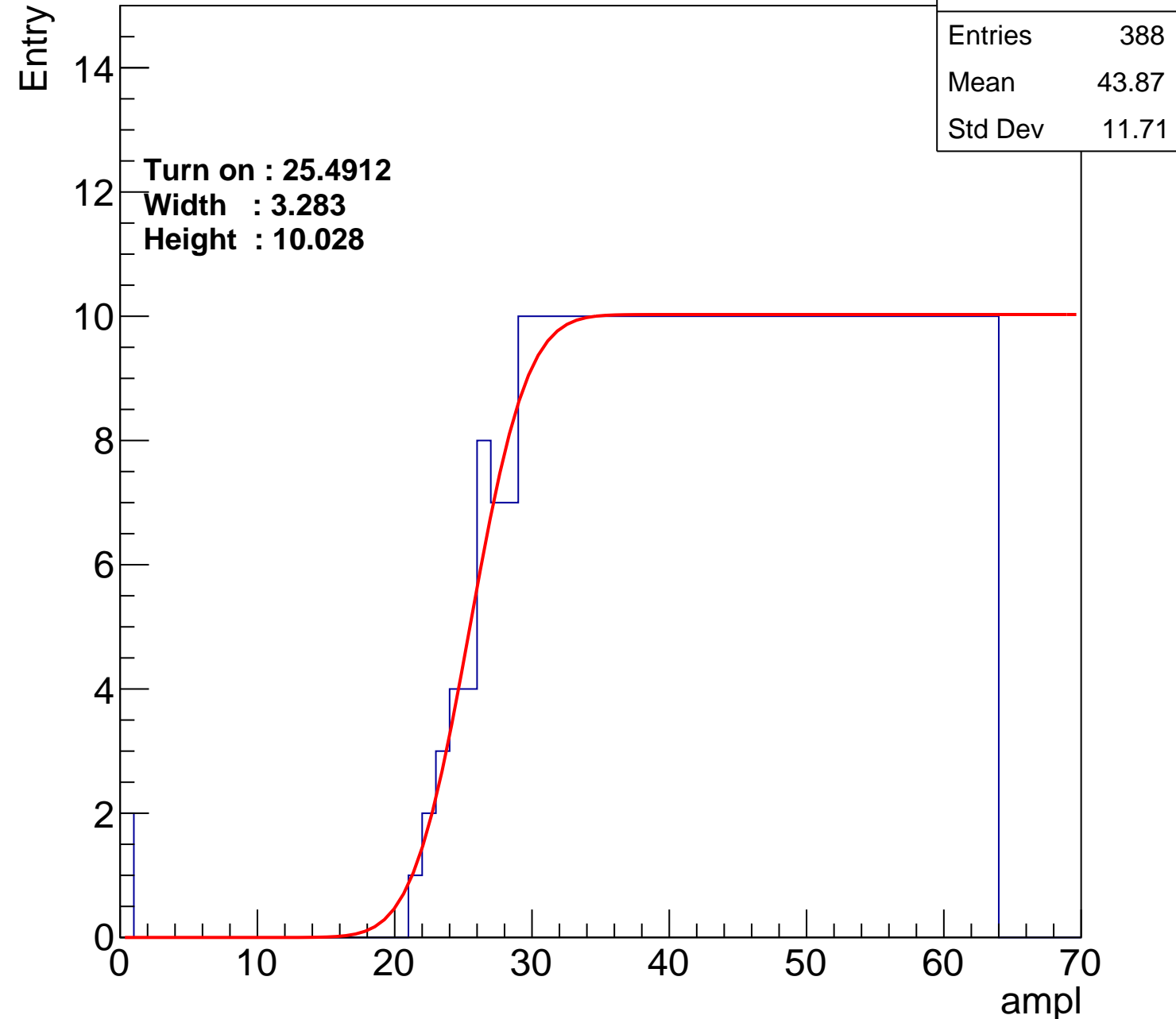
Width : 3.283

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.78
Std Dev	11.64

Turn on : 25.4317

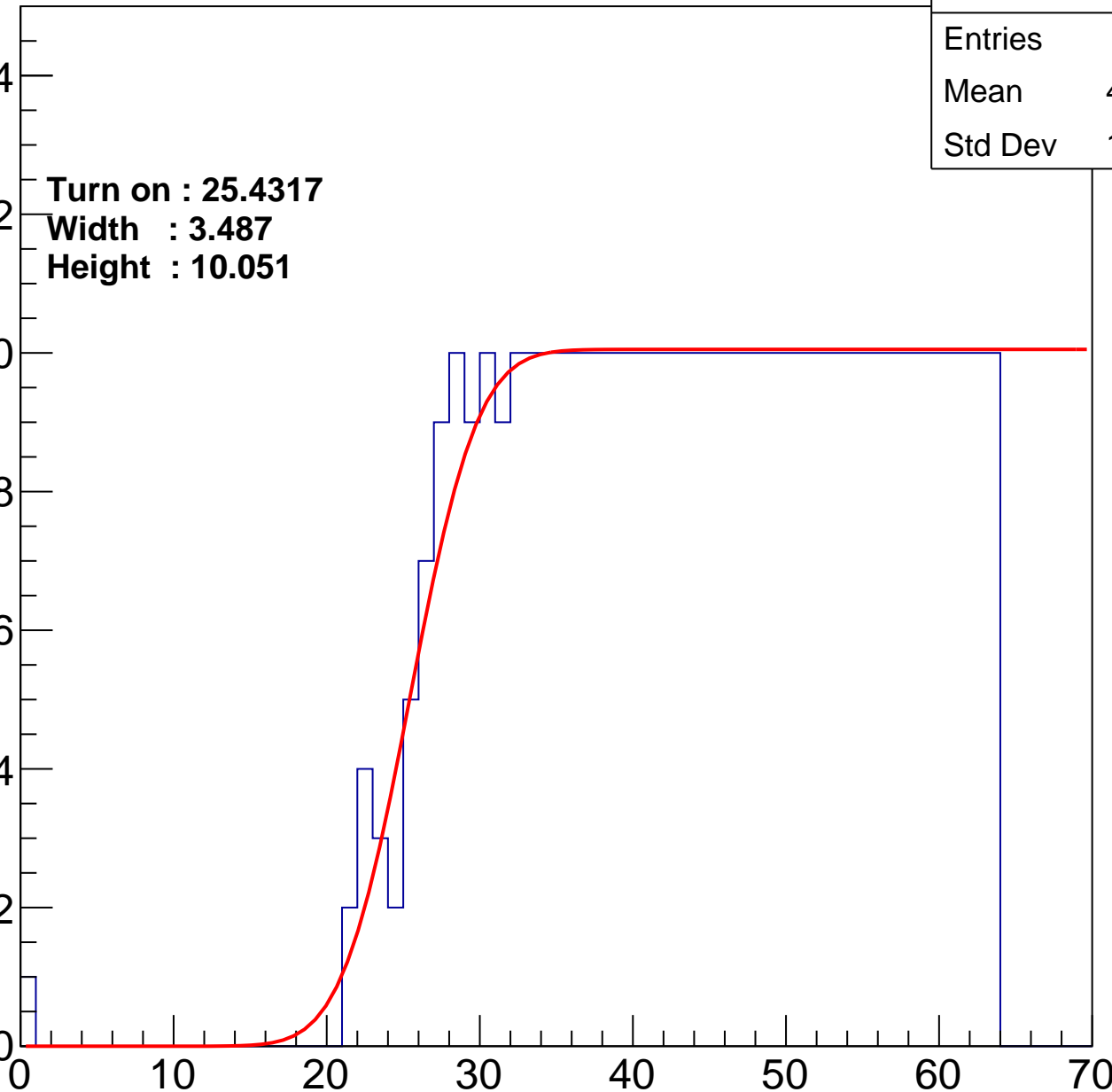
Width : 3.487

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.23
Std Dev	11.38

Turn on : 26.7630

Width : 3.011

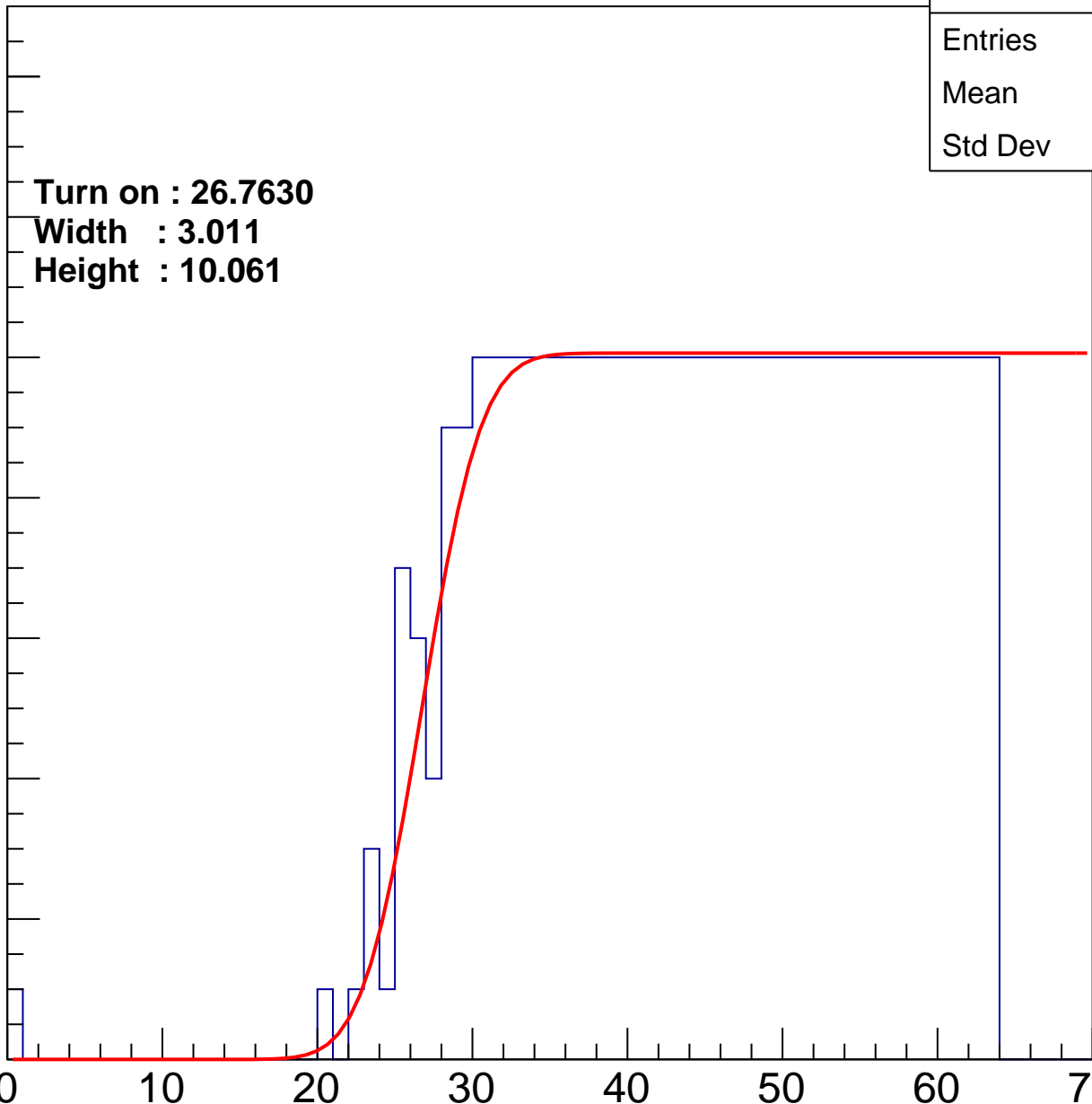
Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U24-ch60

calib_packv5_042523_0143.root, FC#11, port A2

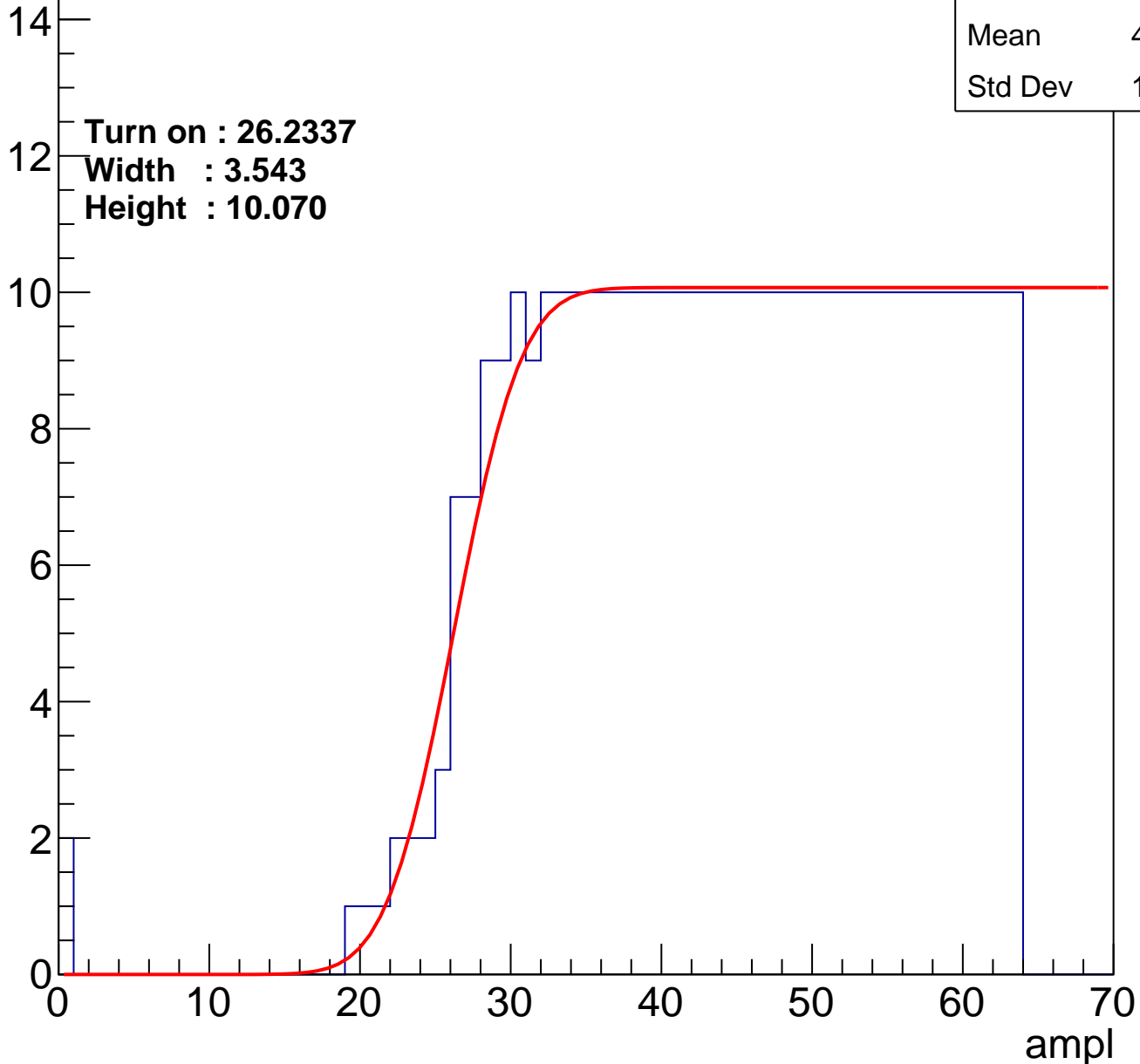
Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 26.2337

Width : 3.543

Height : 10.070

Entry



B1L102S, U24-ch61

calib_packv5_042523_0143.root, FC#11, port A2

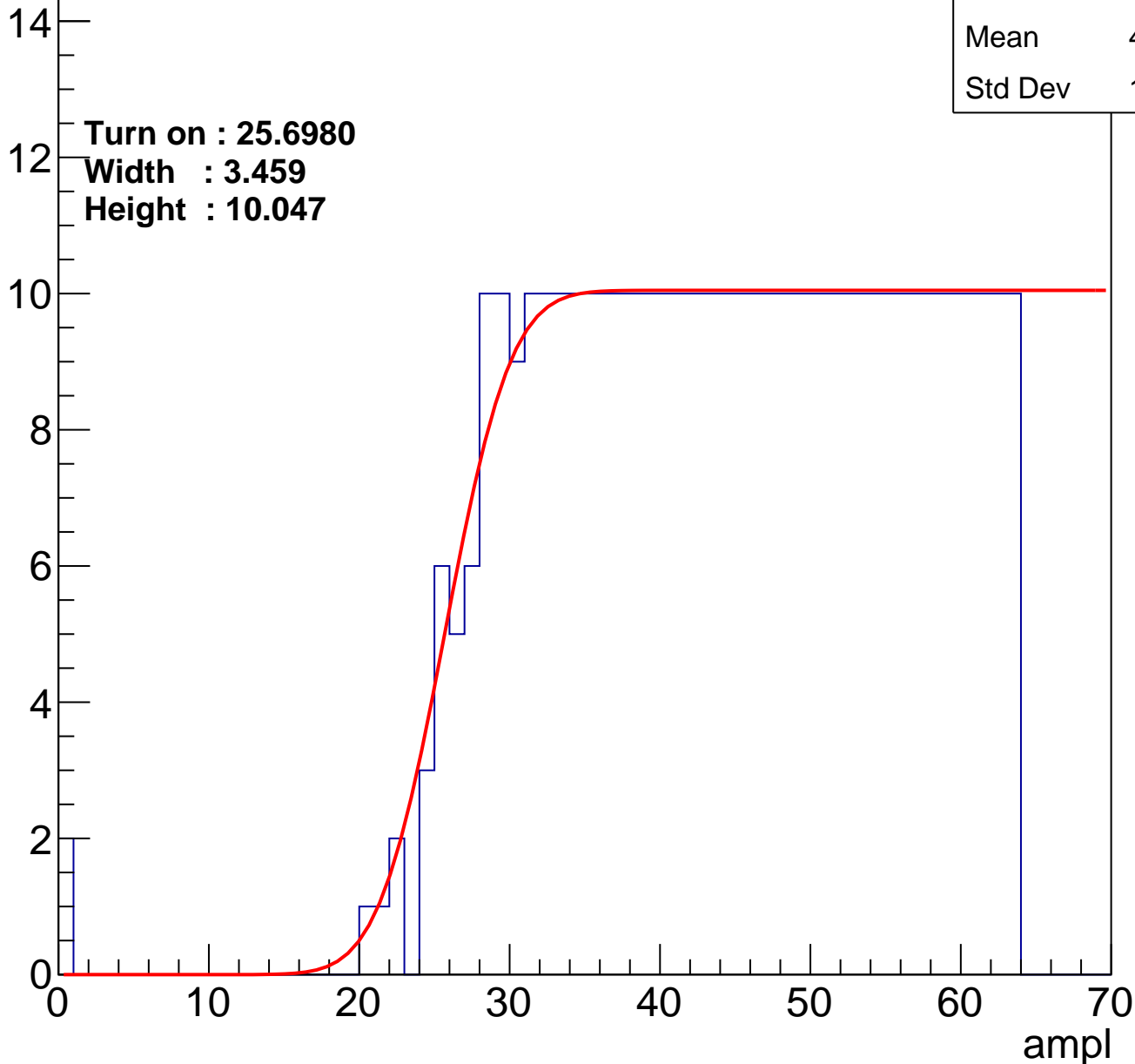
Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.6980

Width : 3.459

Height : 10.047

Entry



B1L102S, U24-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.83
Std Dev	11.68

Turn on : 25.4402

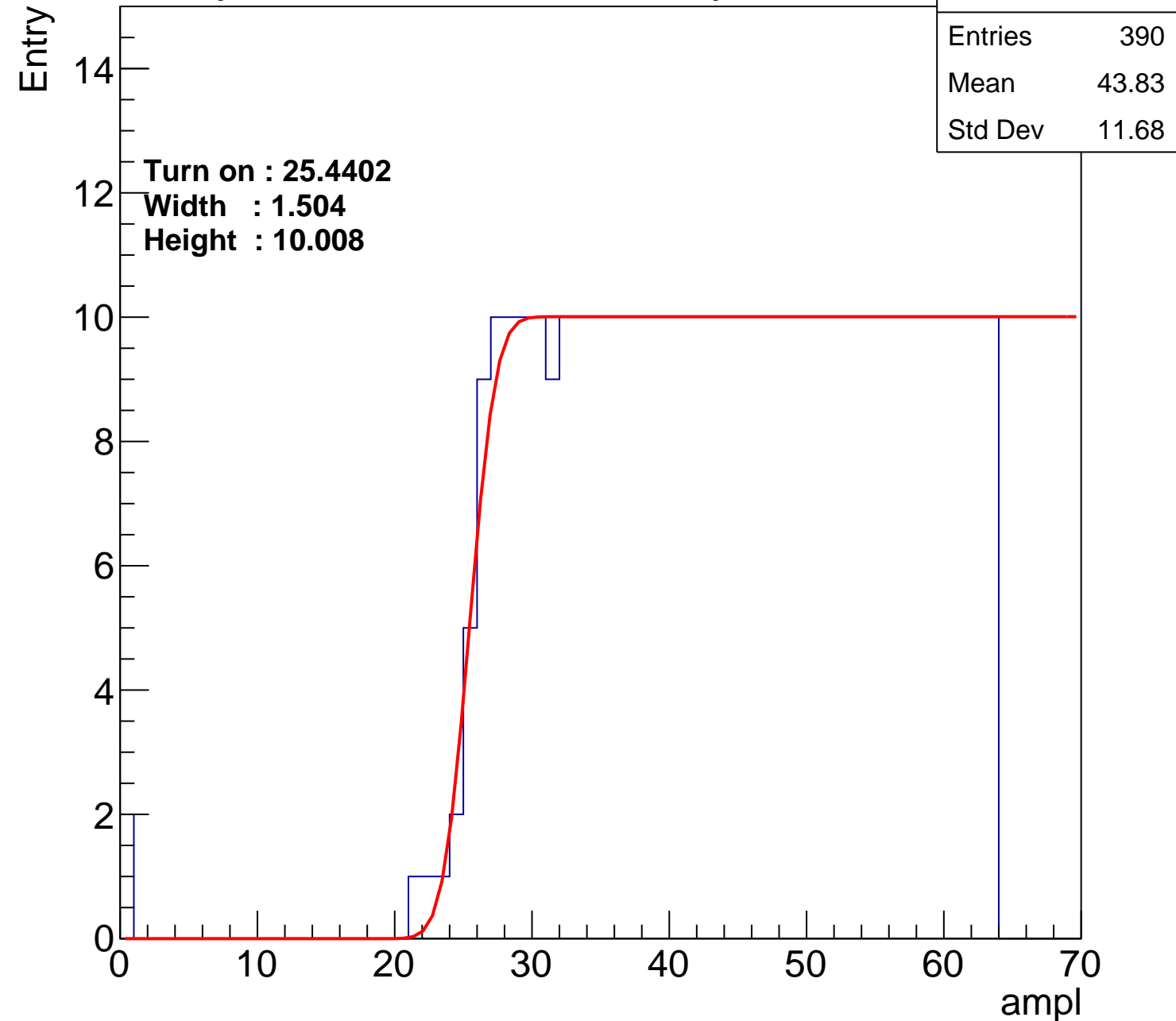
Width : 1.504

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.5723

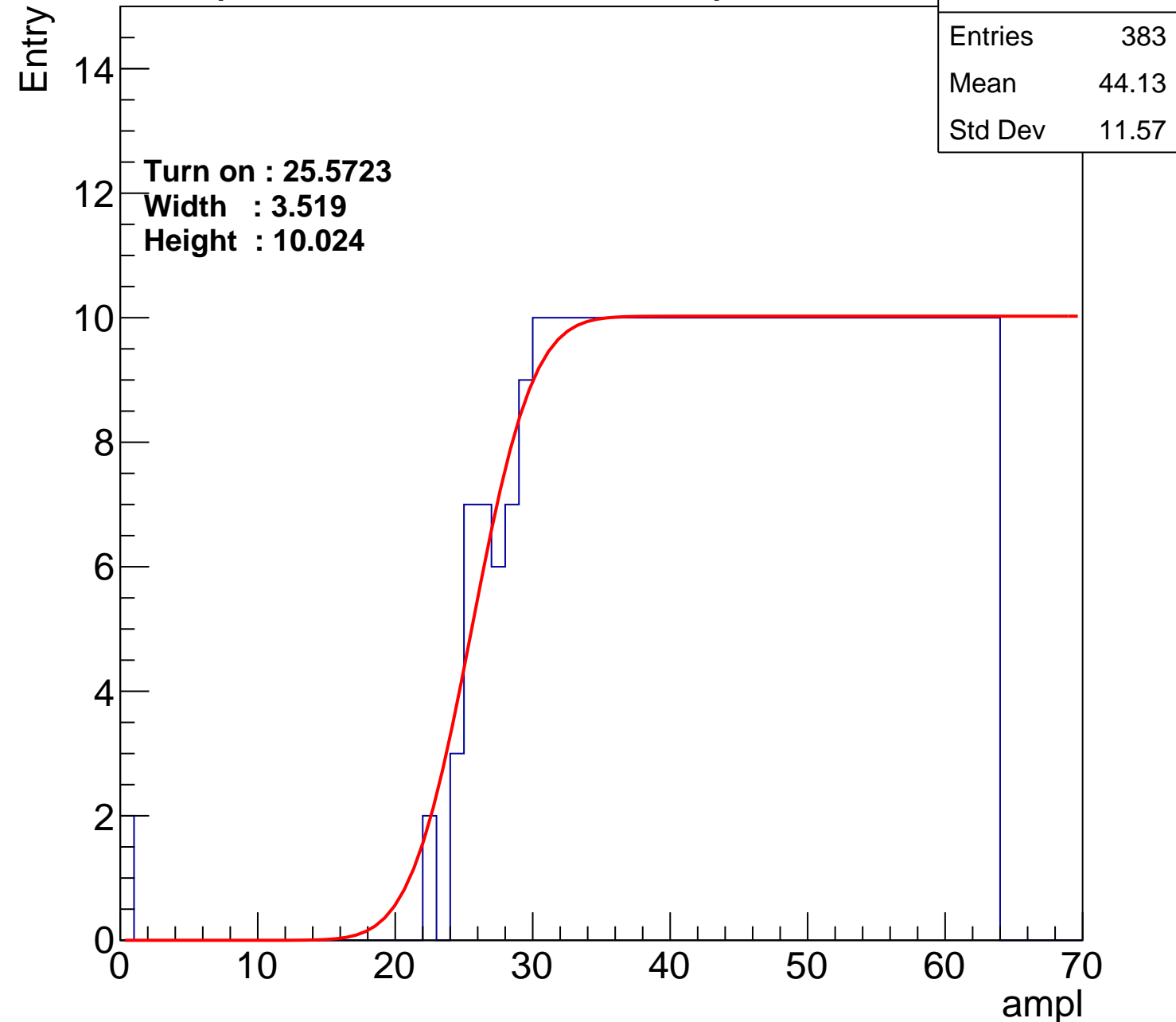
Width : 3.519

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch64

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.4068

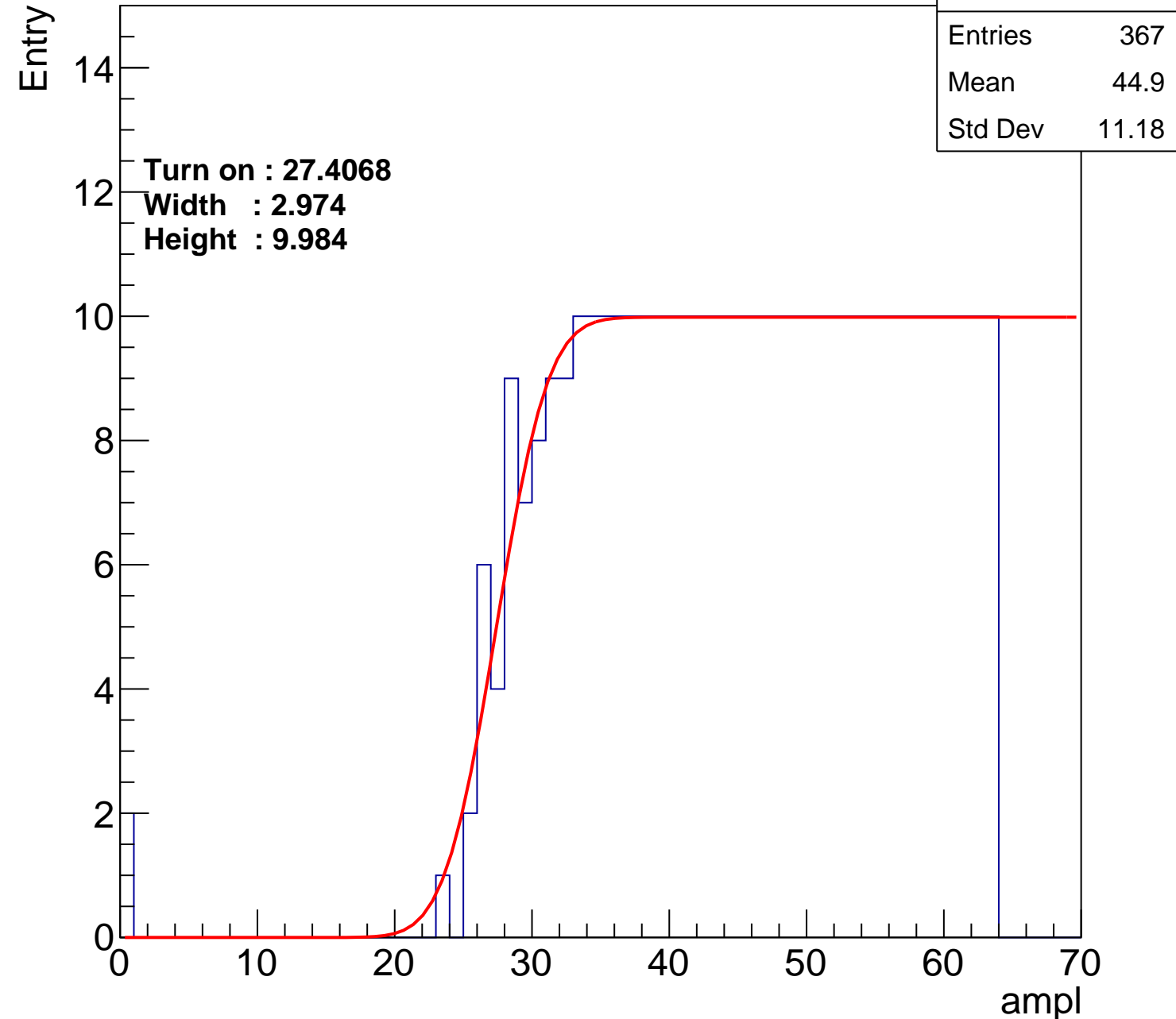
Width : 2.974

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.62
Std Dev	11.64

Turn on : 27.6344

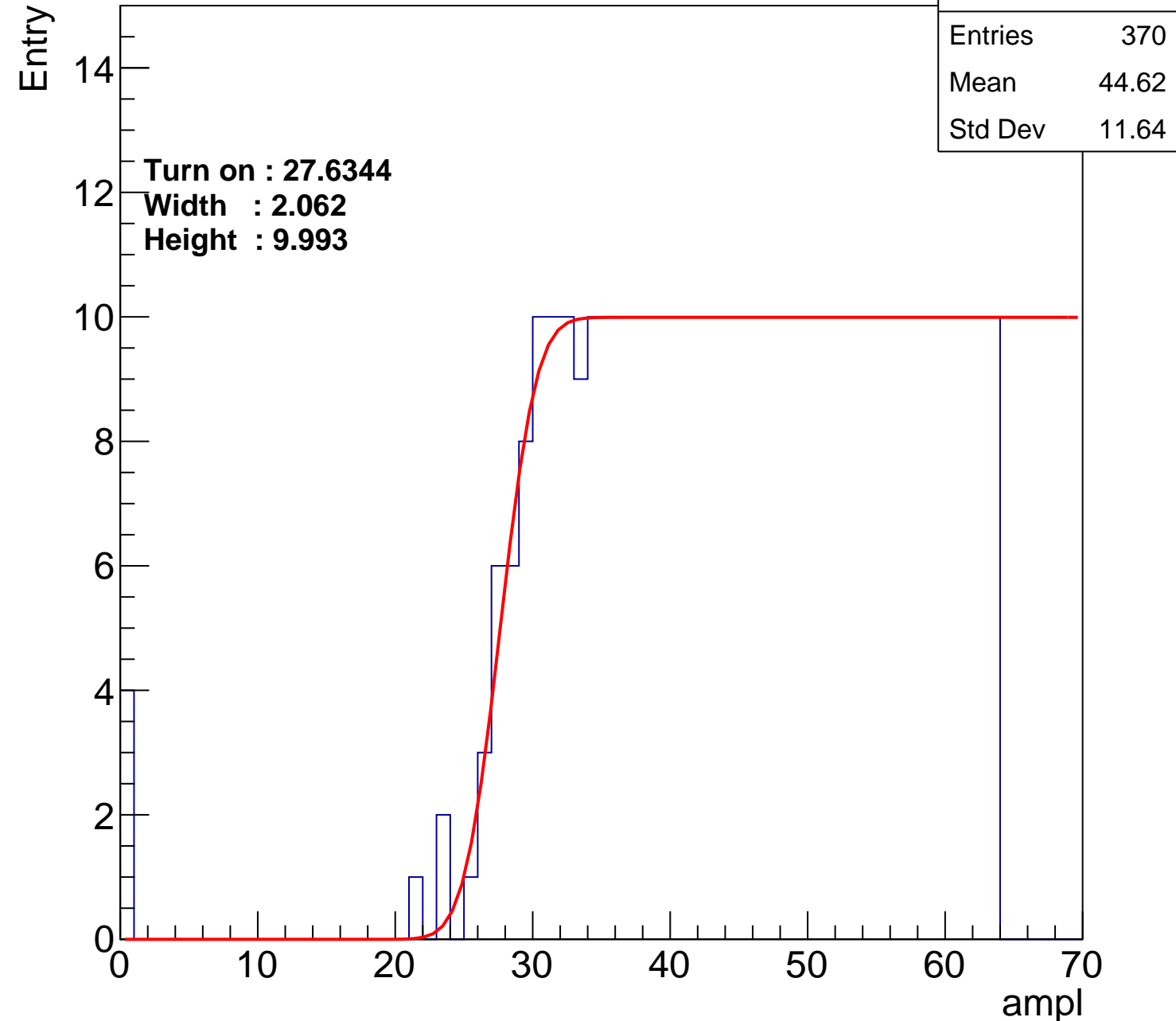
Width : 2.062

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch66

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.3
Std Dev	11.51

Turn on : 26.8731

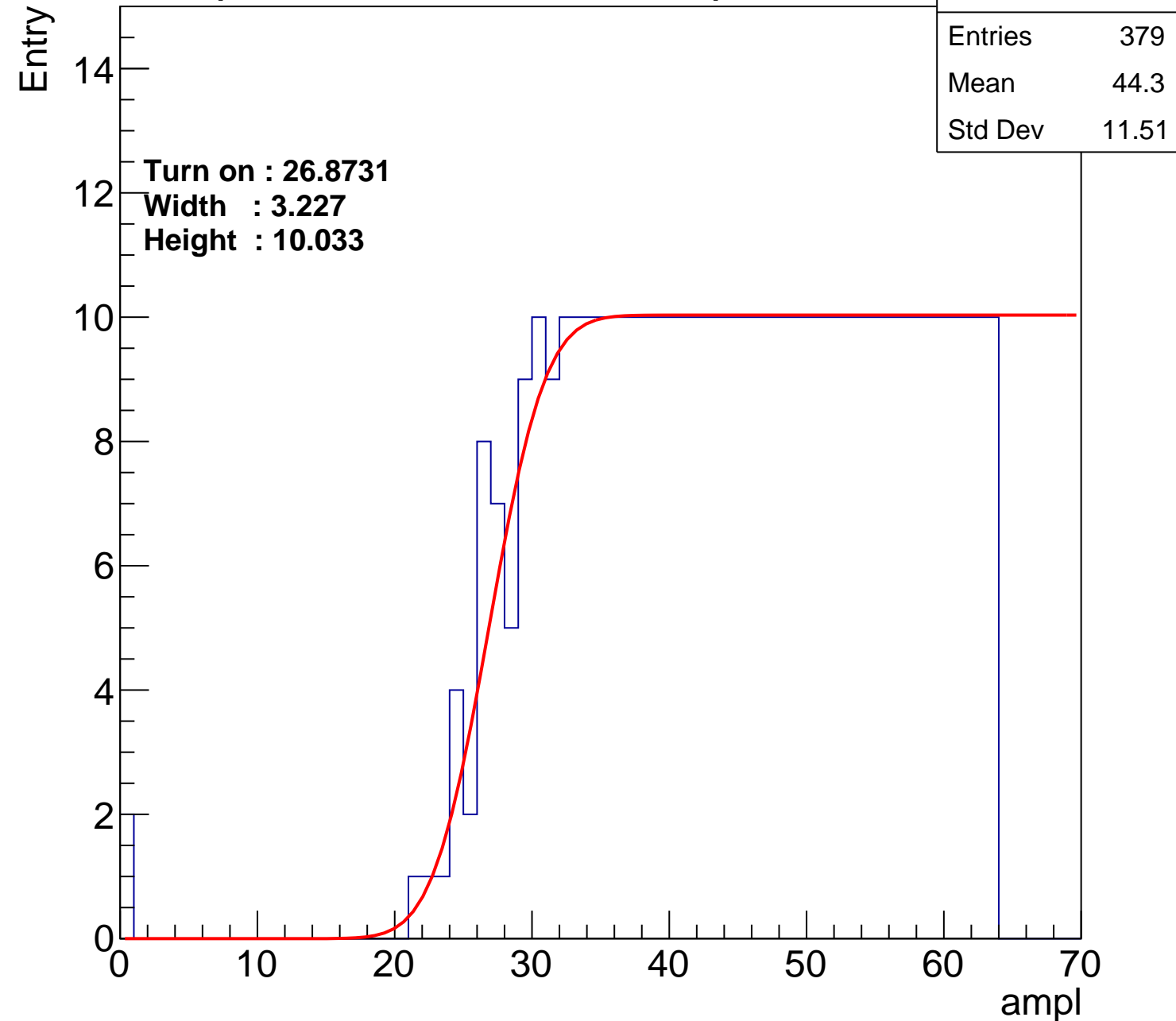
Width : 3.227

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch67

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.2
Std Dev	12.21

Turn on : 24.3137

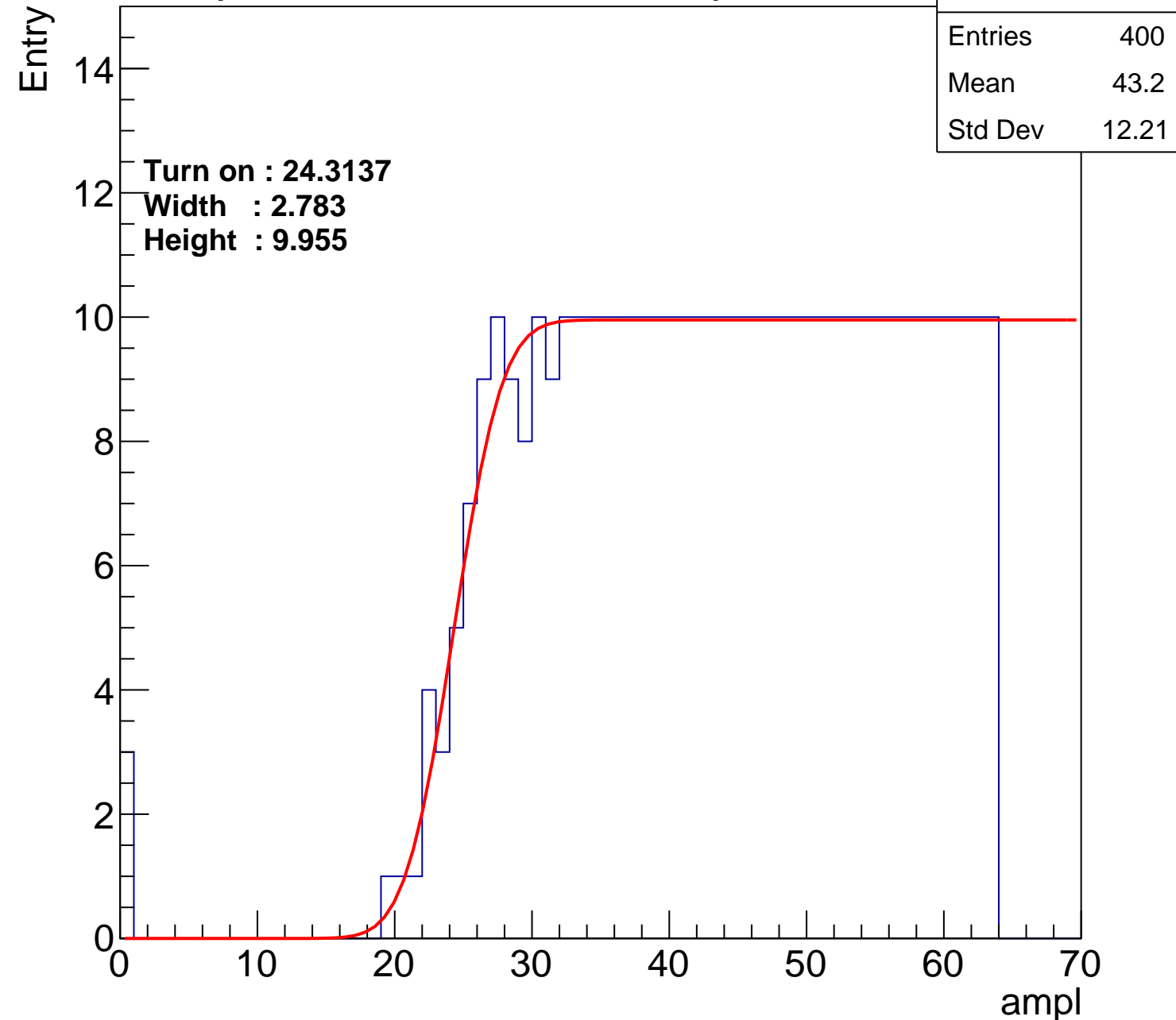
Width : 2.783

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.03
Std Dev	11.92

Turn on : 27.0030

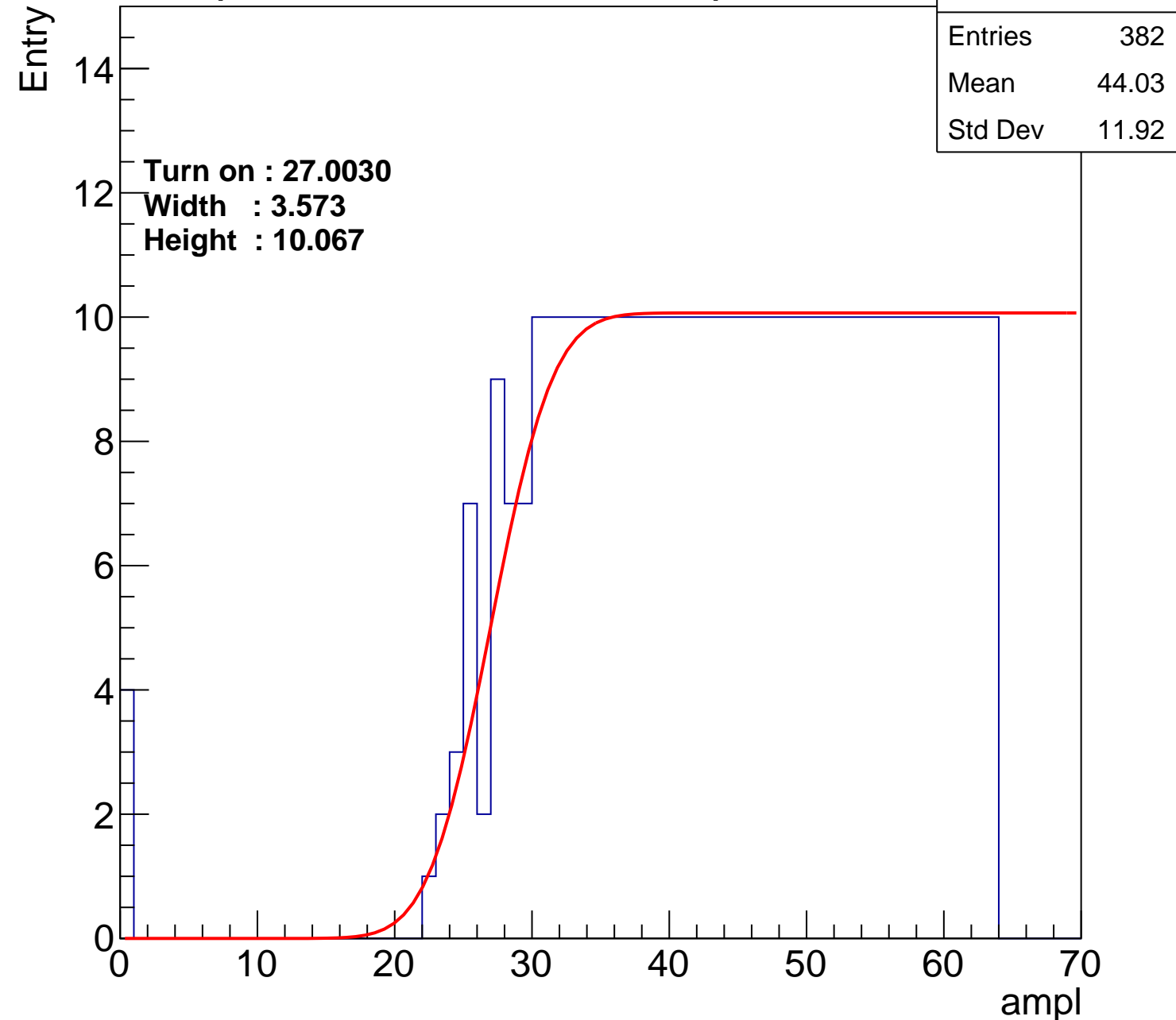
Width : 3.573

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch69

calib_packv5_042523_0143.root, FC#11, port A2

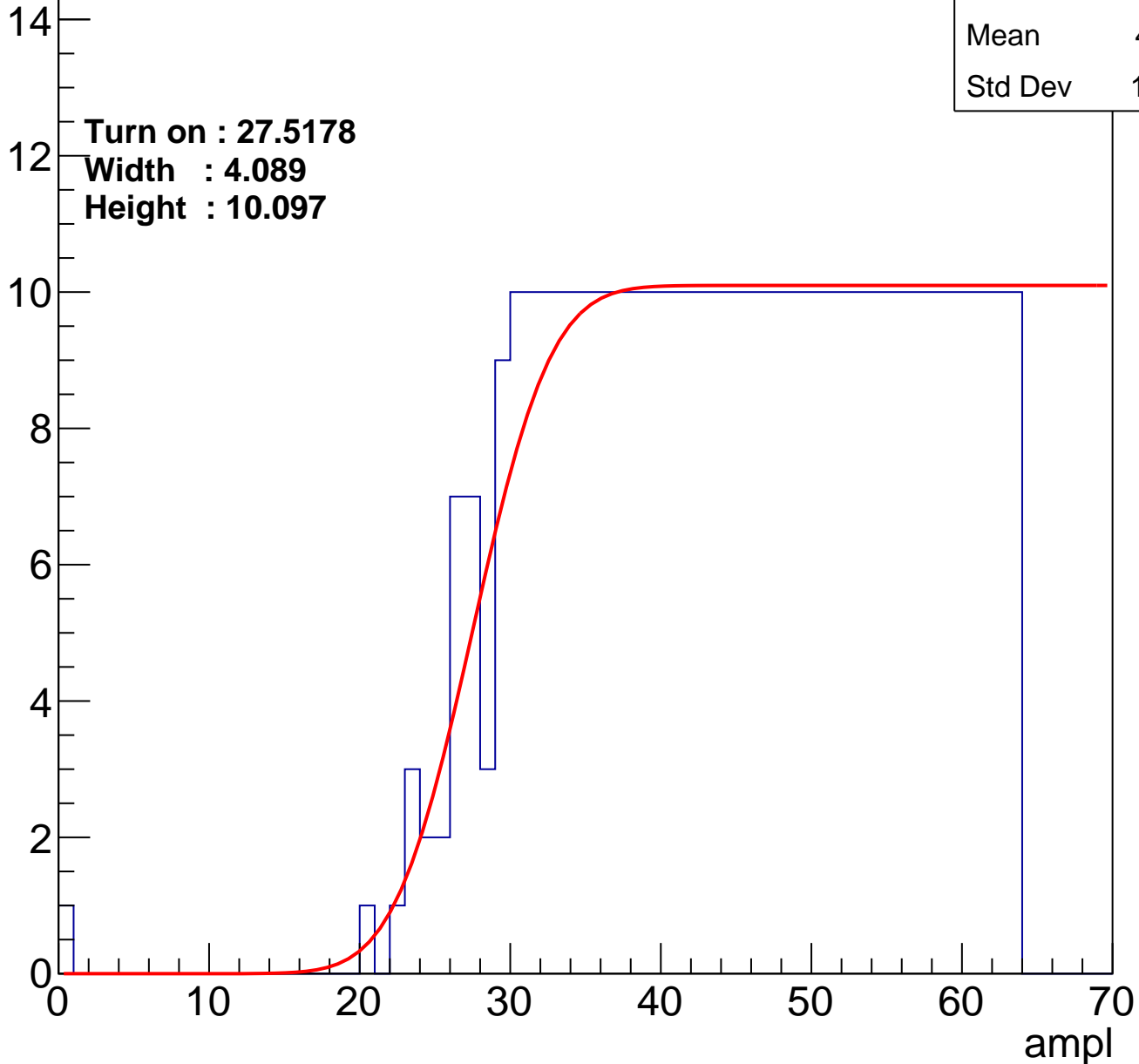
Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 27.5178

Width : 4.089

Height : 10.097

Entry



B1L102S, U24-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.0681

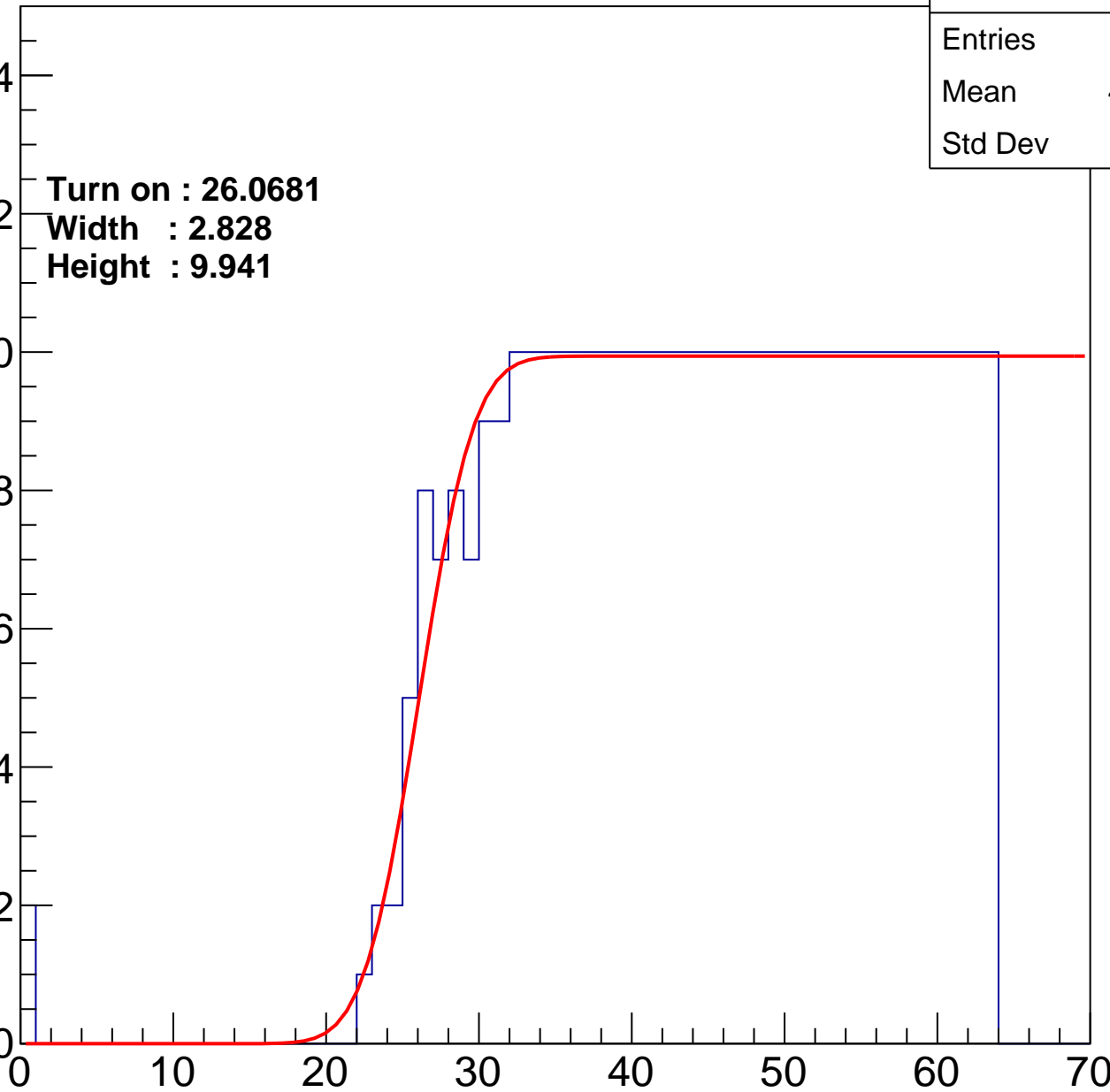
Width : 2.828

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch71

calib_packv5_042523_0143.root, FC#11, port A2

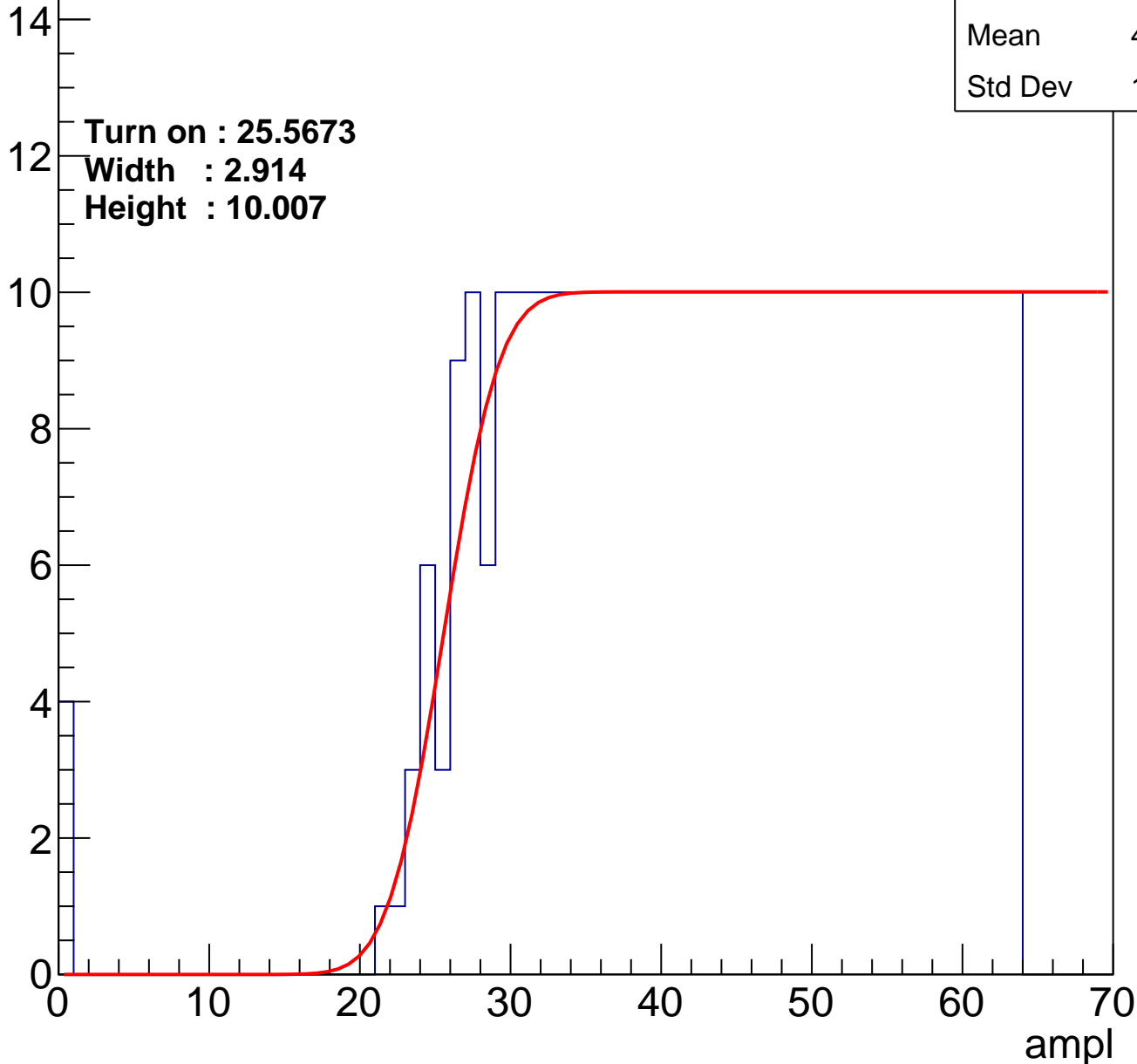
Entries	393
Mean	43.52
Std Dev	12.14

Turn on : 25.5673

Width : 2.914

Height : 10.007

Entry



B1L102S, U24-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 25.2833

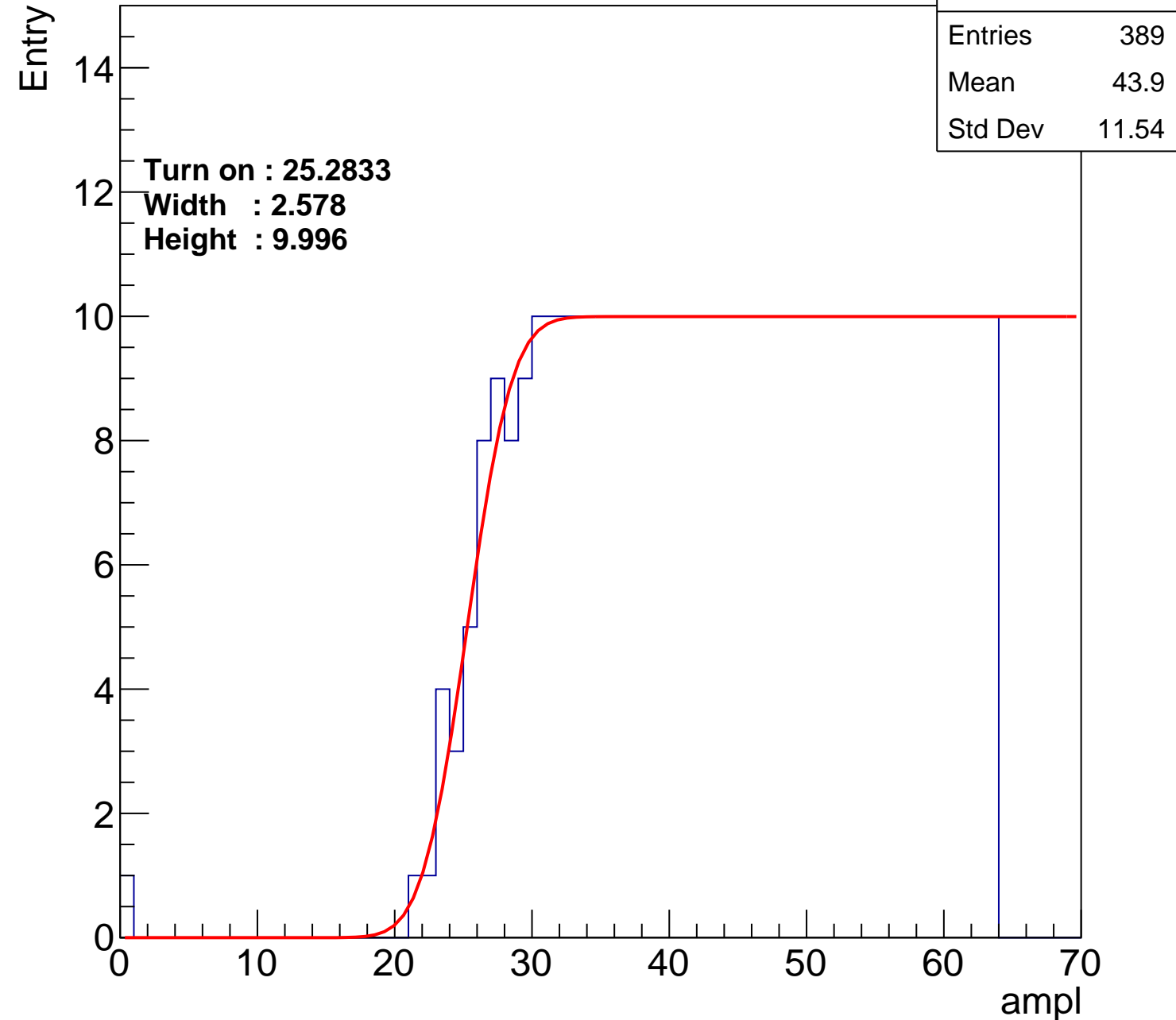
Width : 2.578

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.74
Std Dev	11.64

Turn on : 25.0844

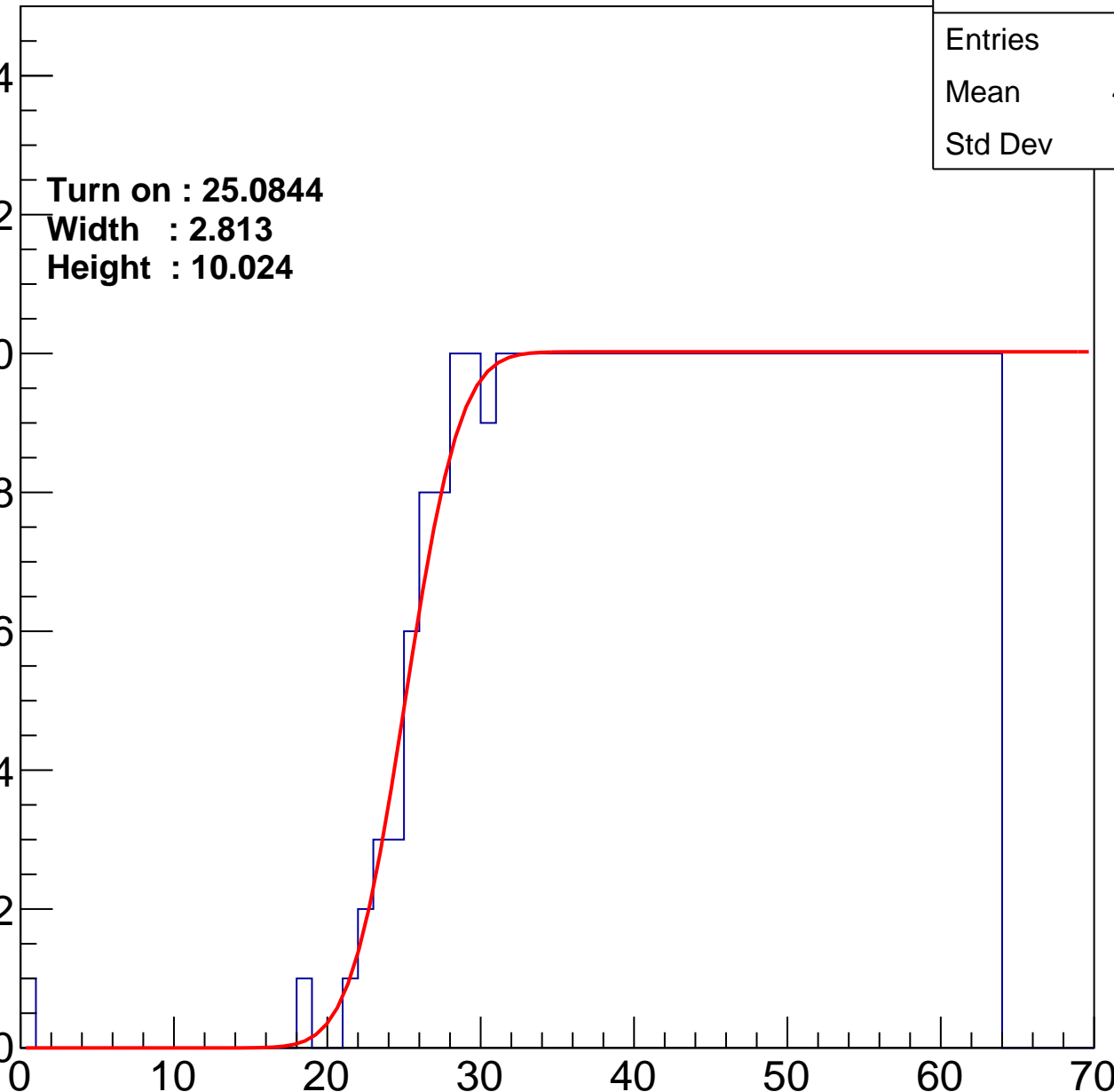
Width : 2.813

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch74

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.7695

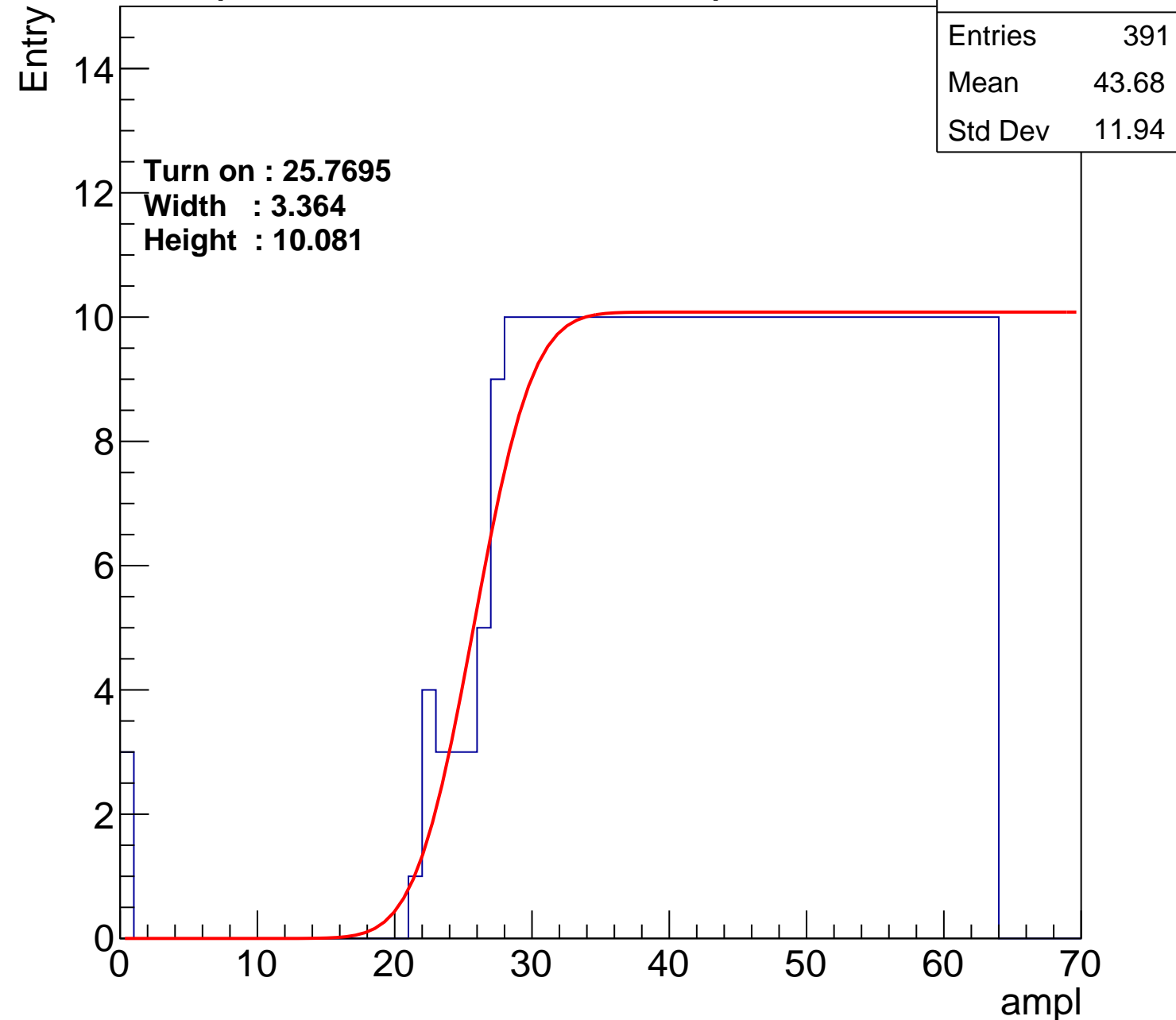
Width : 3.364

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.34
Std Dev	12.14

Turn on : 24.6704

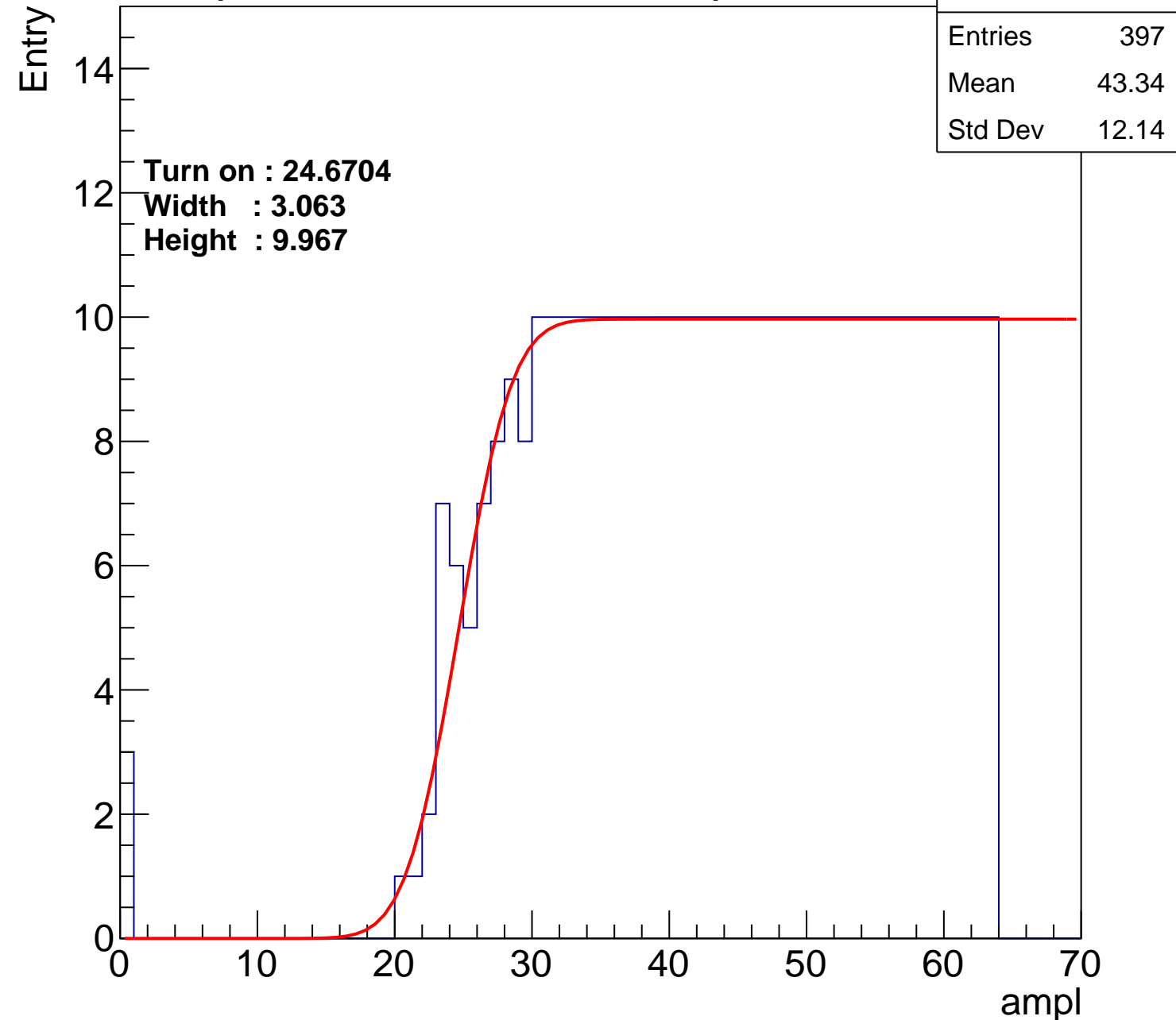
Width : 3.063

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch76

calib_packv5_042523_0143.root, FC#11, port A2

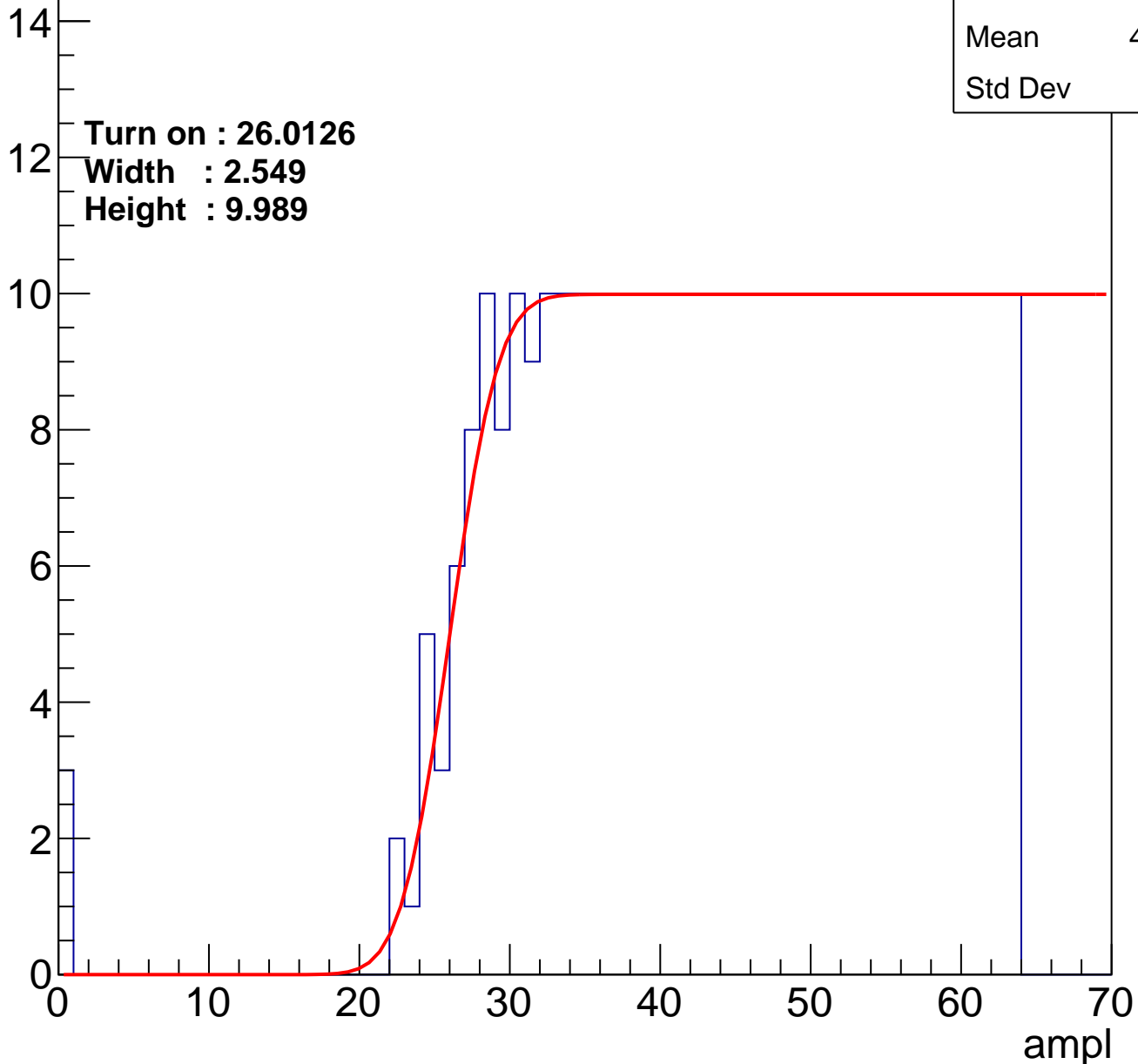
Entries	385
Mean	43.96
Std Dev	11.8

Turn on : 26.0126

Width : 2.549

Height : 9.989

Entry



B1L102S, U24-ch77

calib_packv5_042523_0143.root, FC#11, port A2

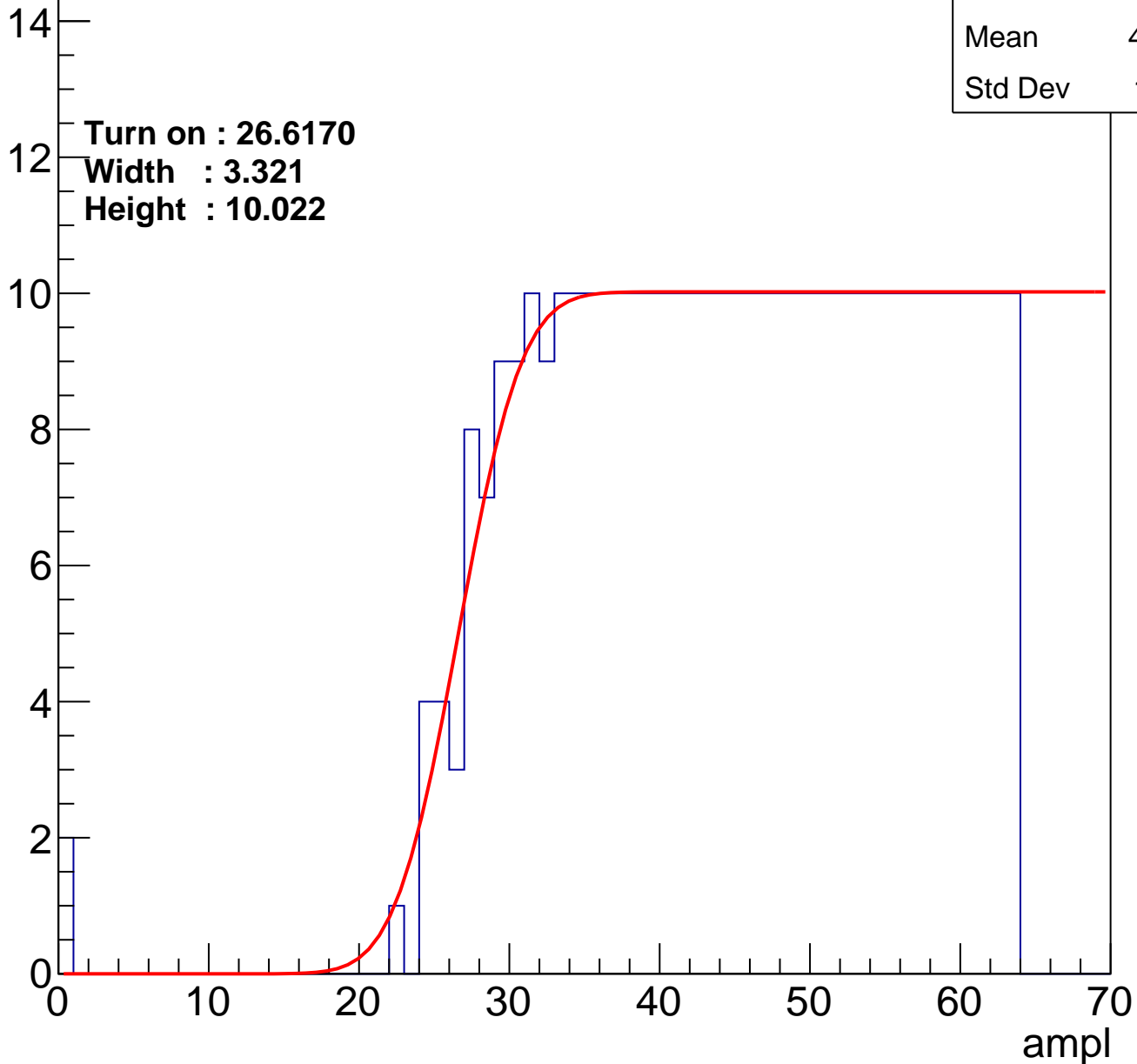
Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.6170

Width : 3.321

Height : 10.022

Entry



B1L102S, U24-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.35
Std Dev	11.67

Turn on : 26.9370

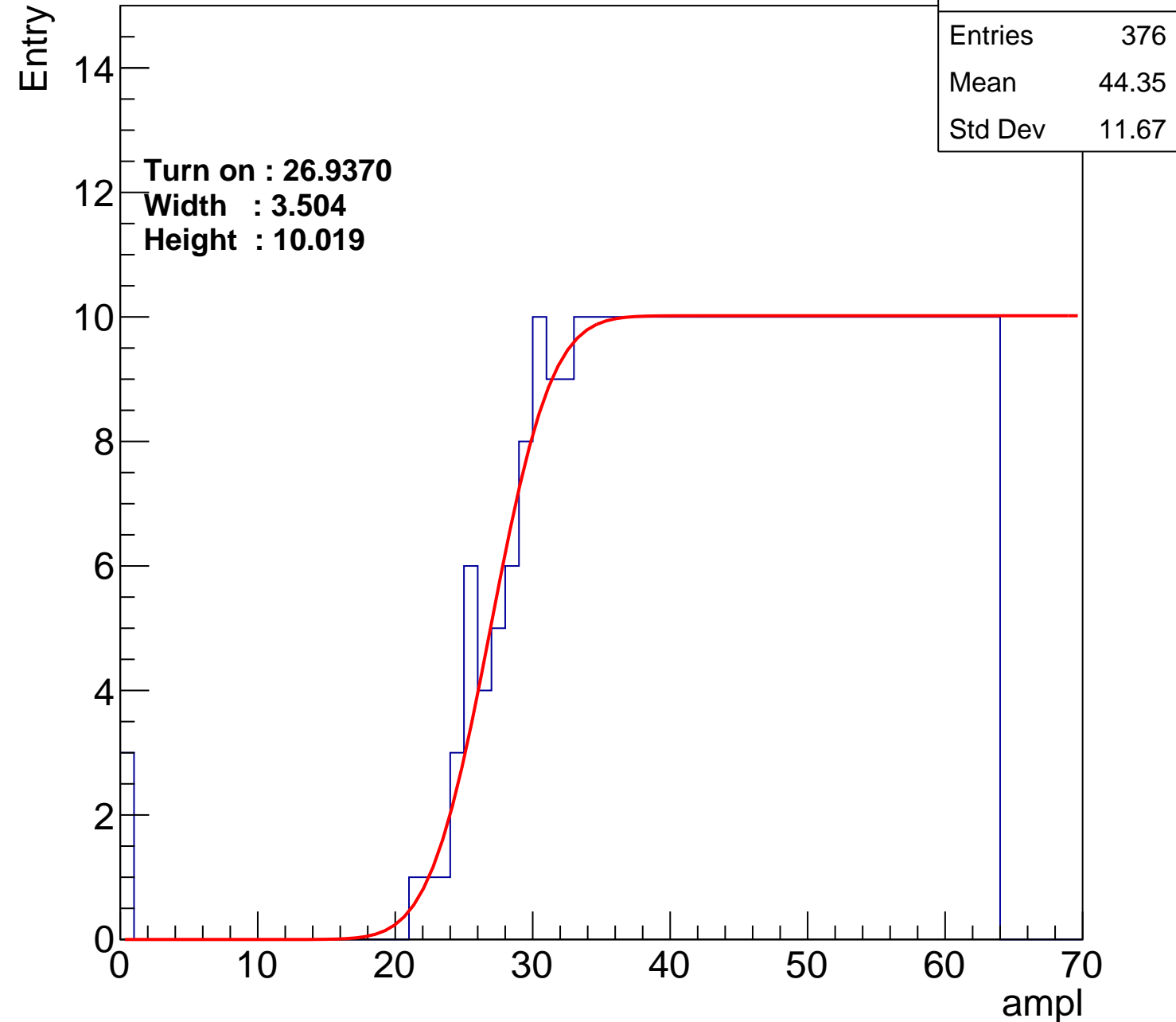
Width : 3.504

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.72
Std Dev	11.97

Turn on : 25.7111

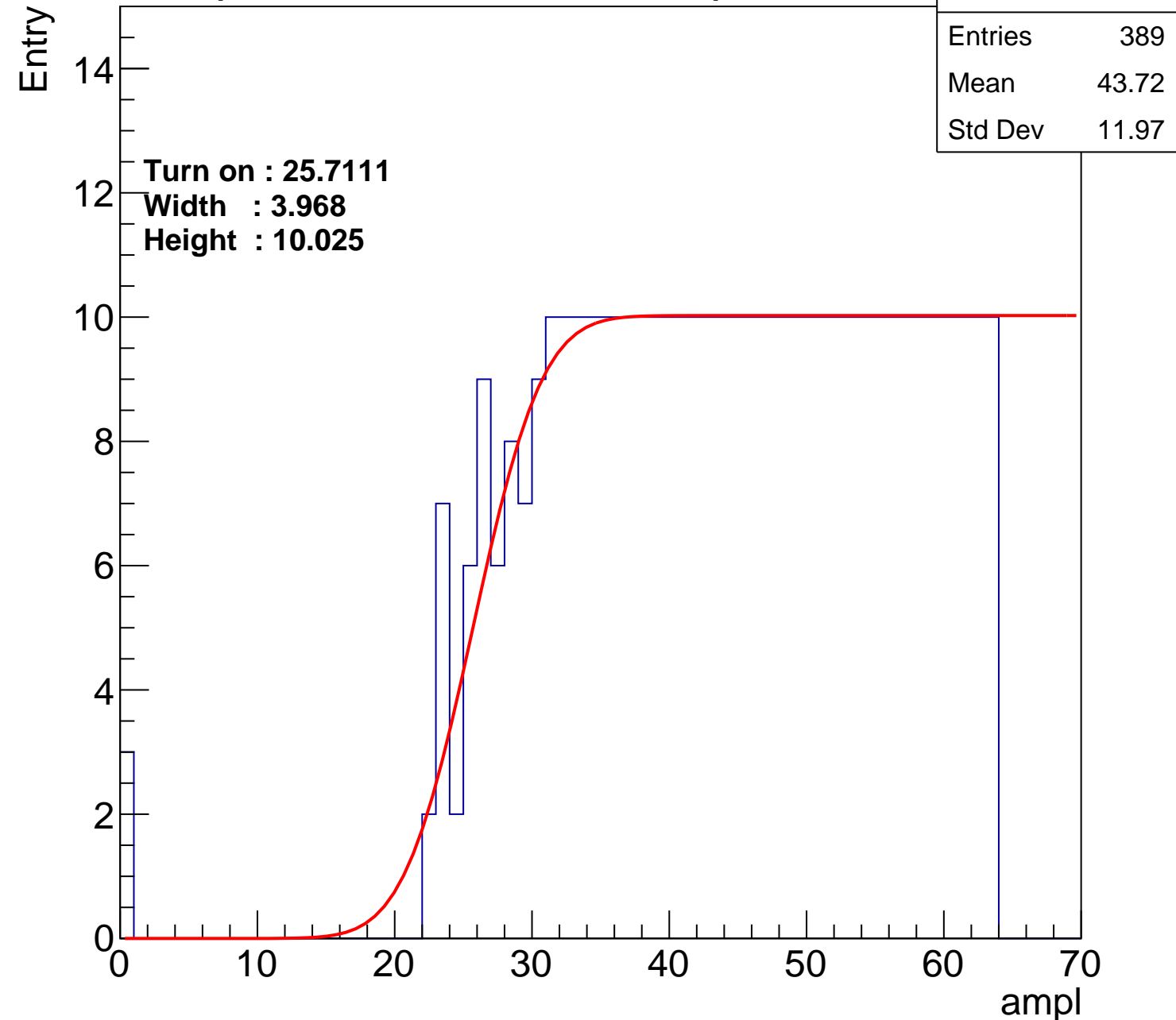
Width : 3.968

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch80

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.15
Std Dev	11.9

Turn on : 27.2078

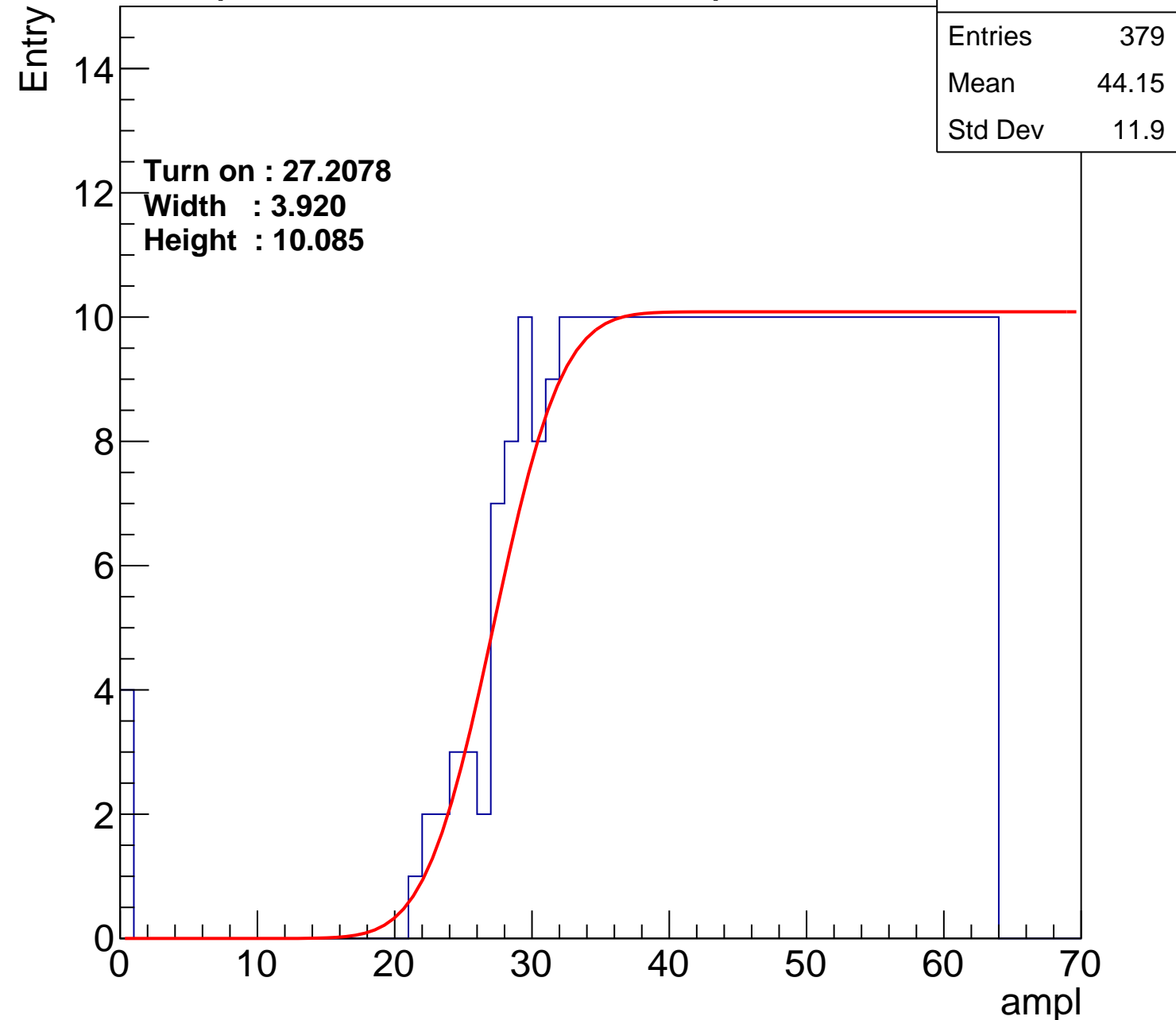
Width : 3.920

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.7644

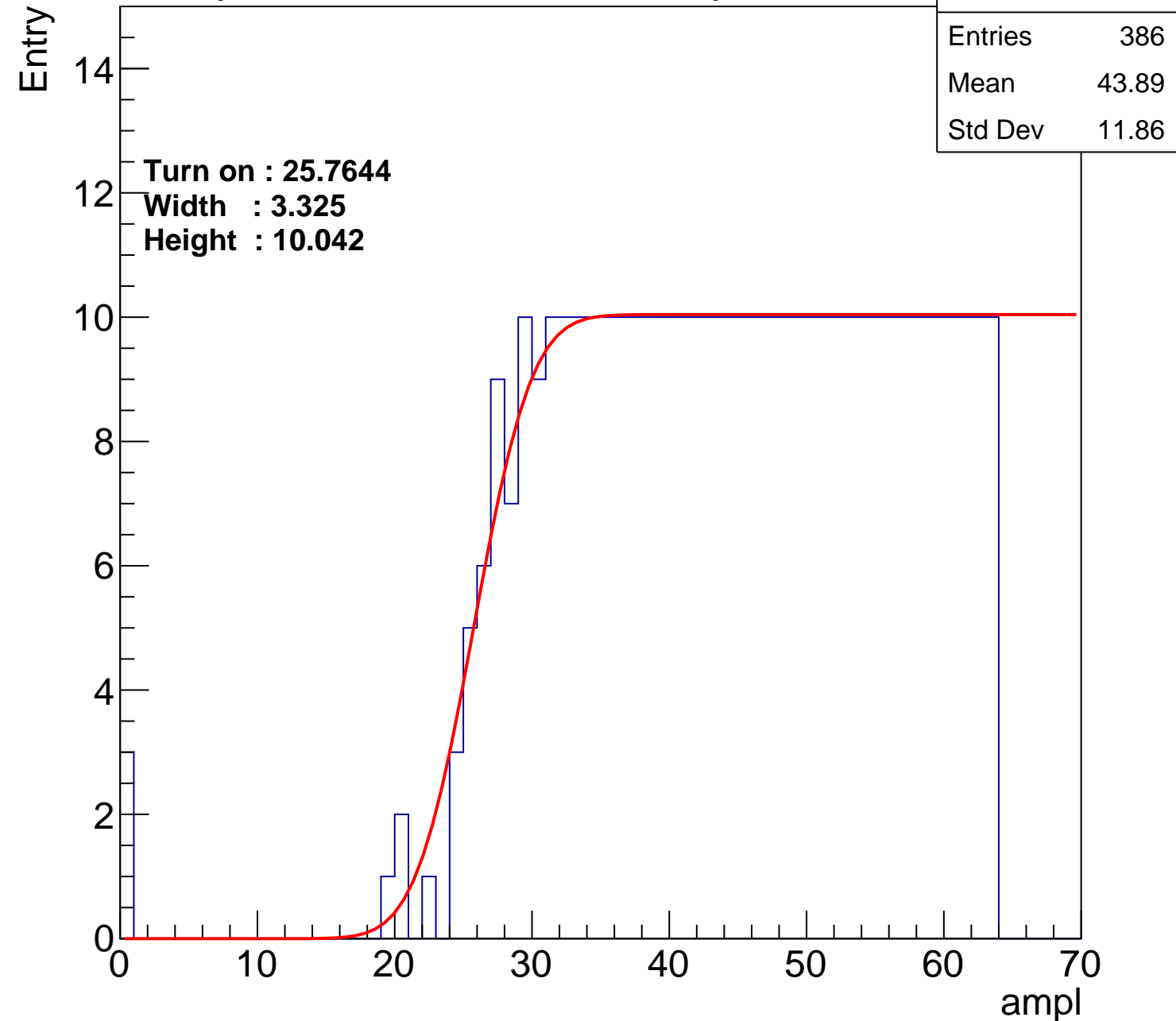
Width : 3.325

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.49
Std Dev	12.08

Turn on : 25.1021

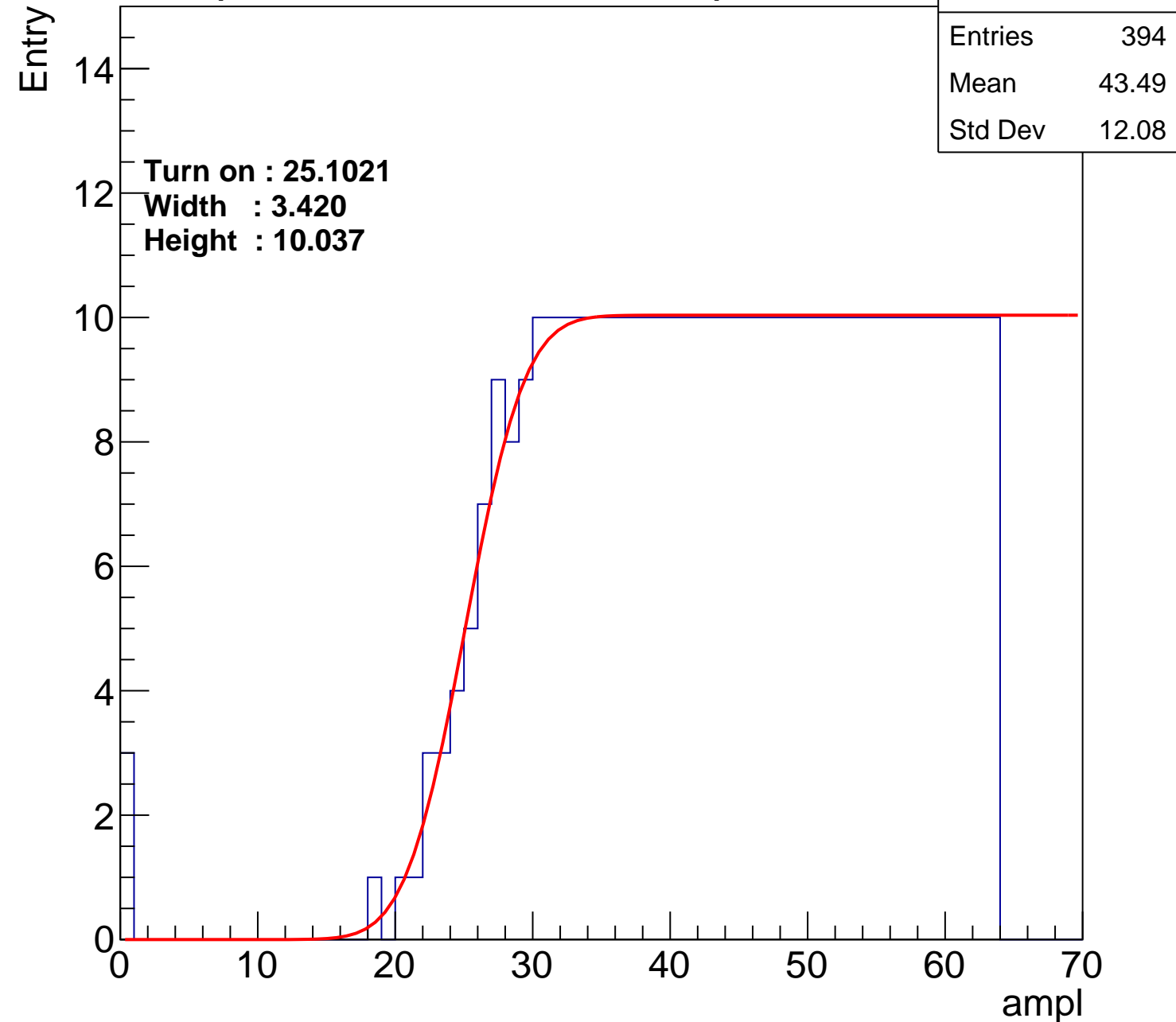
Width : 3.420

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch83

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.3
Std Dev	11.52

Turn on : 27.1059

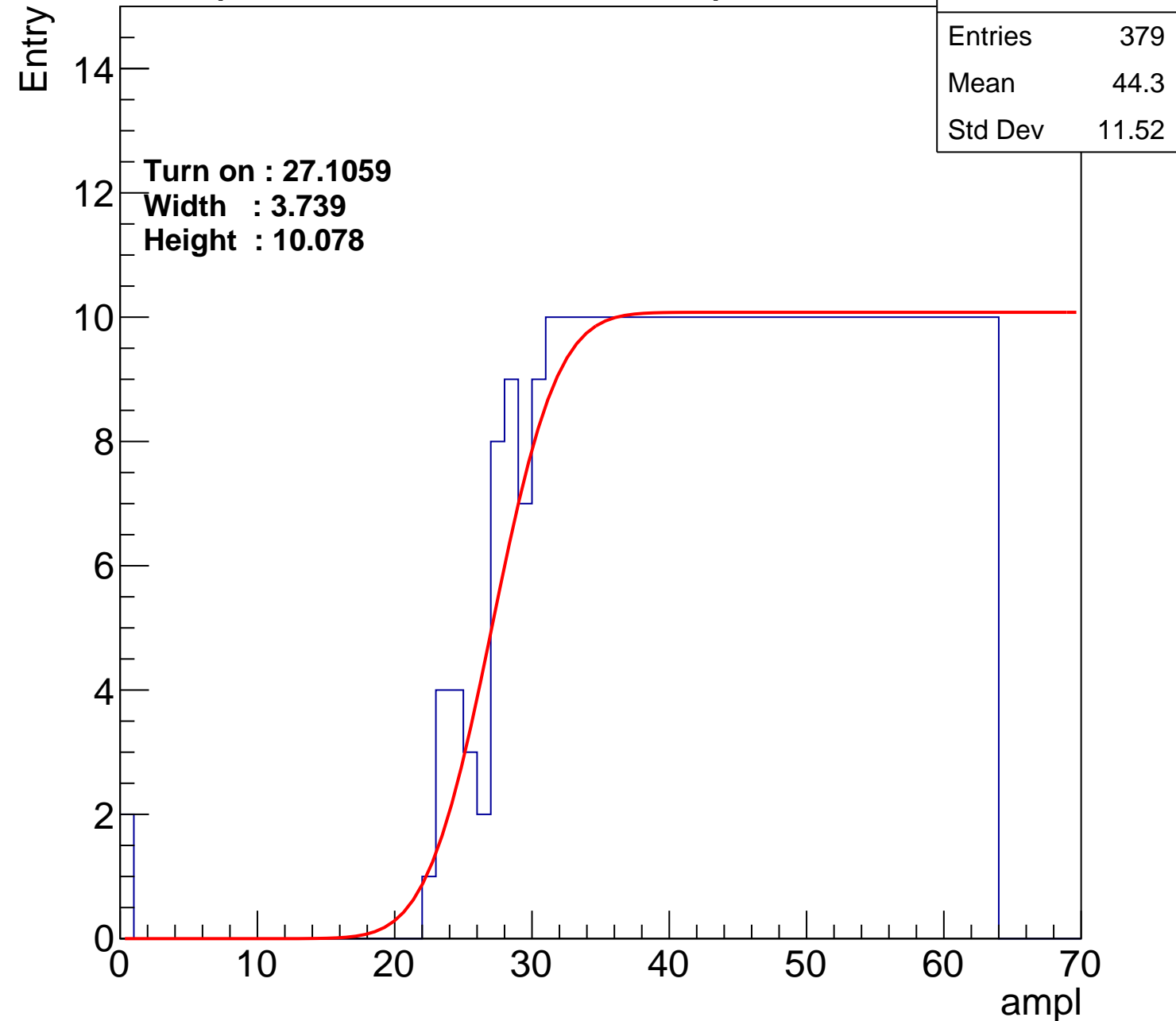
Width : 3.739

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch84

calib_packv5_042523_0143.root, FC#11, port A2

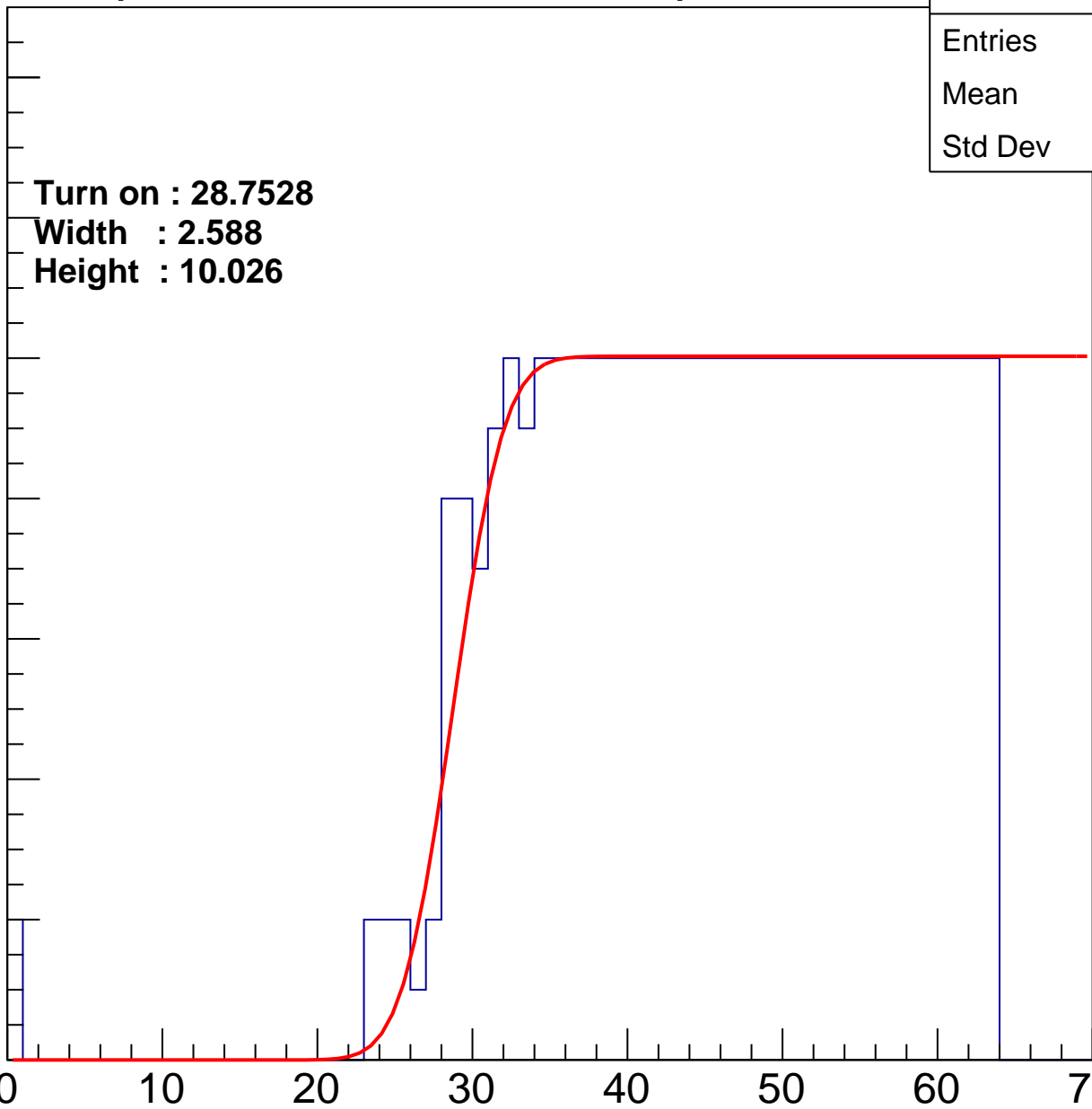
Entry

14
12
10
8
6
4
2
0

Turn on : 28.7528
Width : 2.588
Height : 10.026

Entries	362
Mean	45.13
Std Dev	11.09

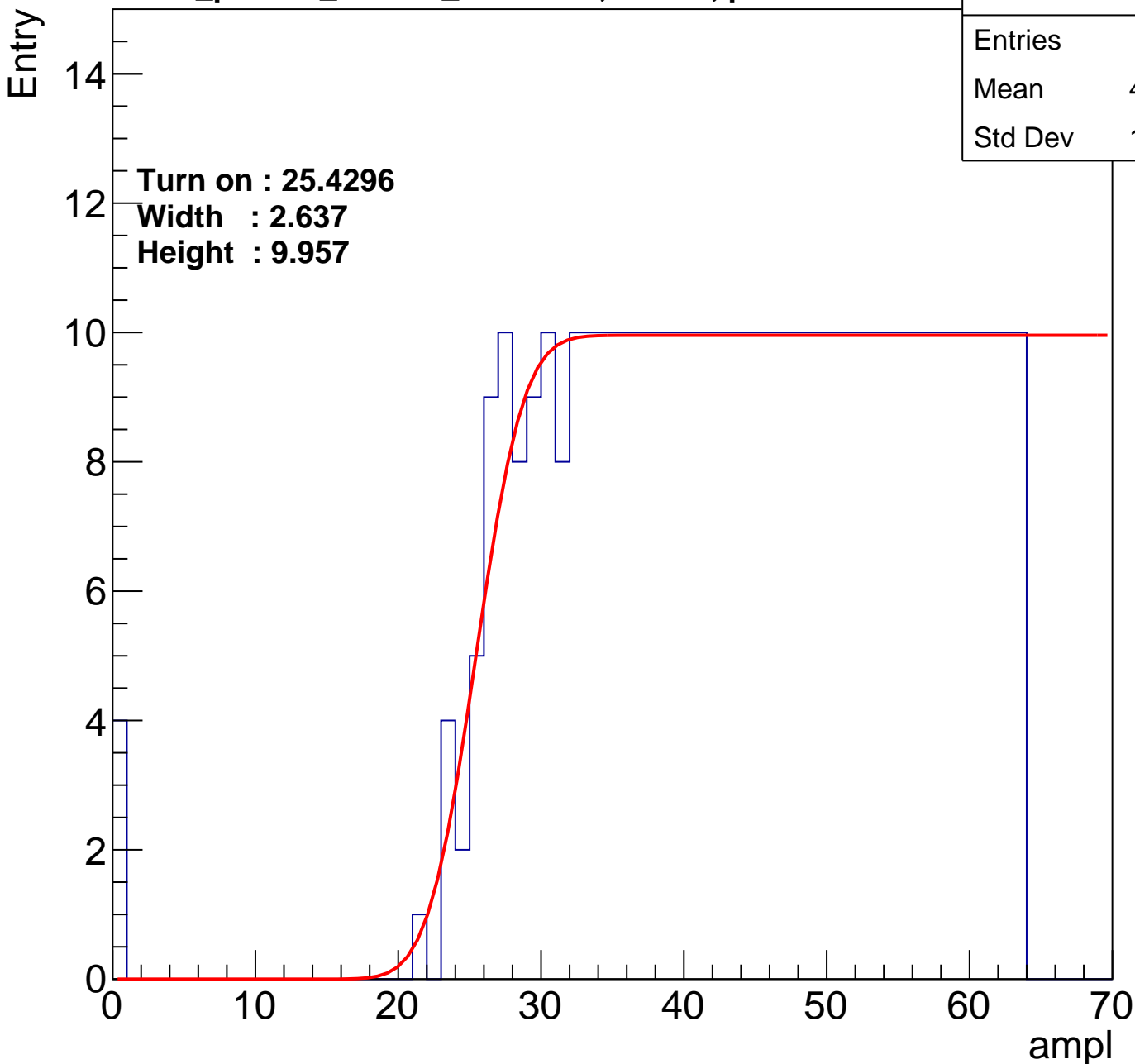
ampl



calib_packv5_042523_0143.root, FC#11, port A2

calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 25.4296
Width : 2.637
Height : 9.957



B1L102S, U24-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.48
Std Dev	12.15

Turn on : 25.9525

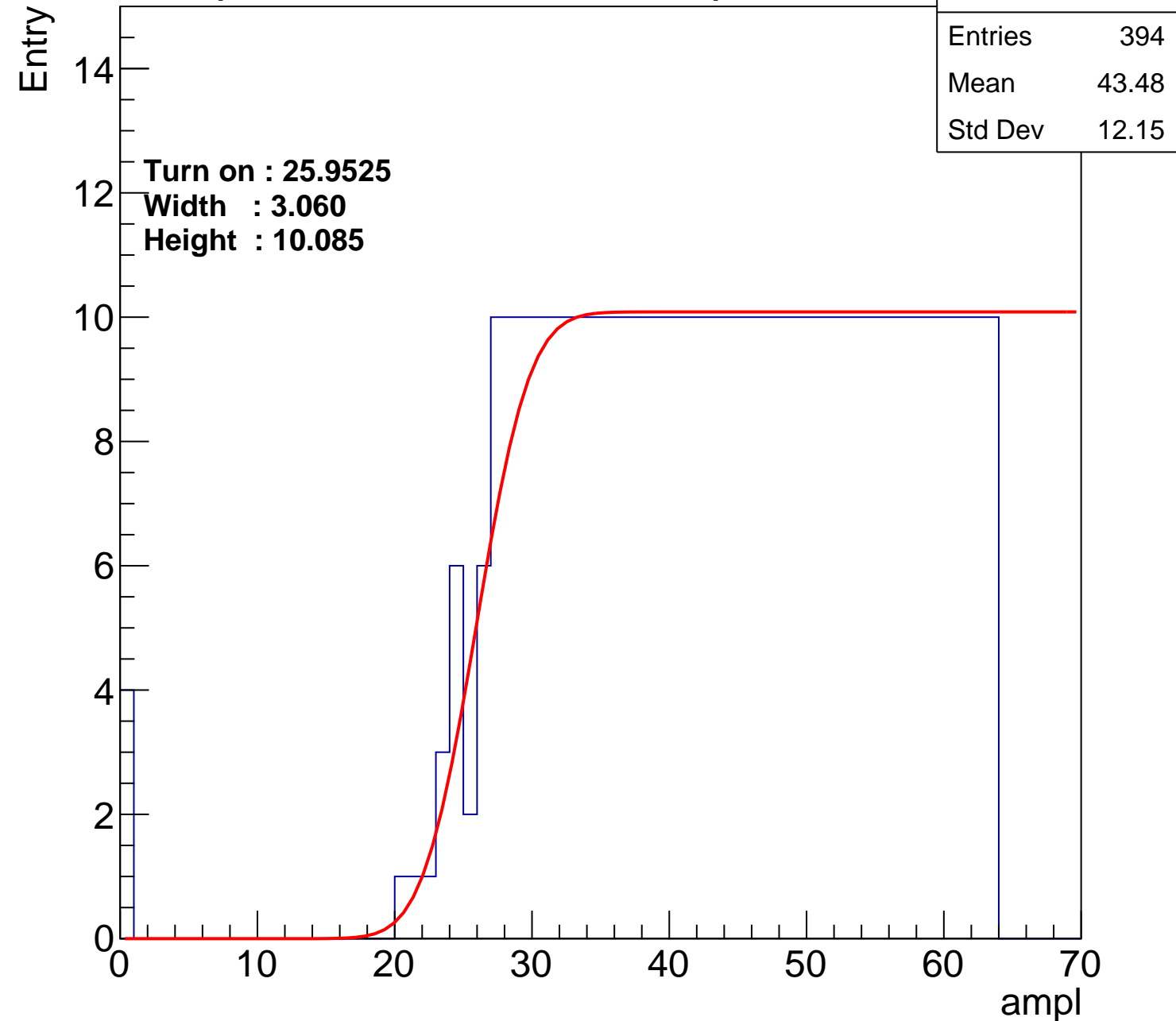
Width : 3.060

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch87

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.64
Std Dev	11.83

Turn on : 25.2402

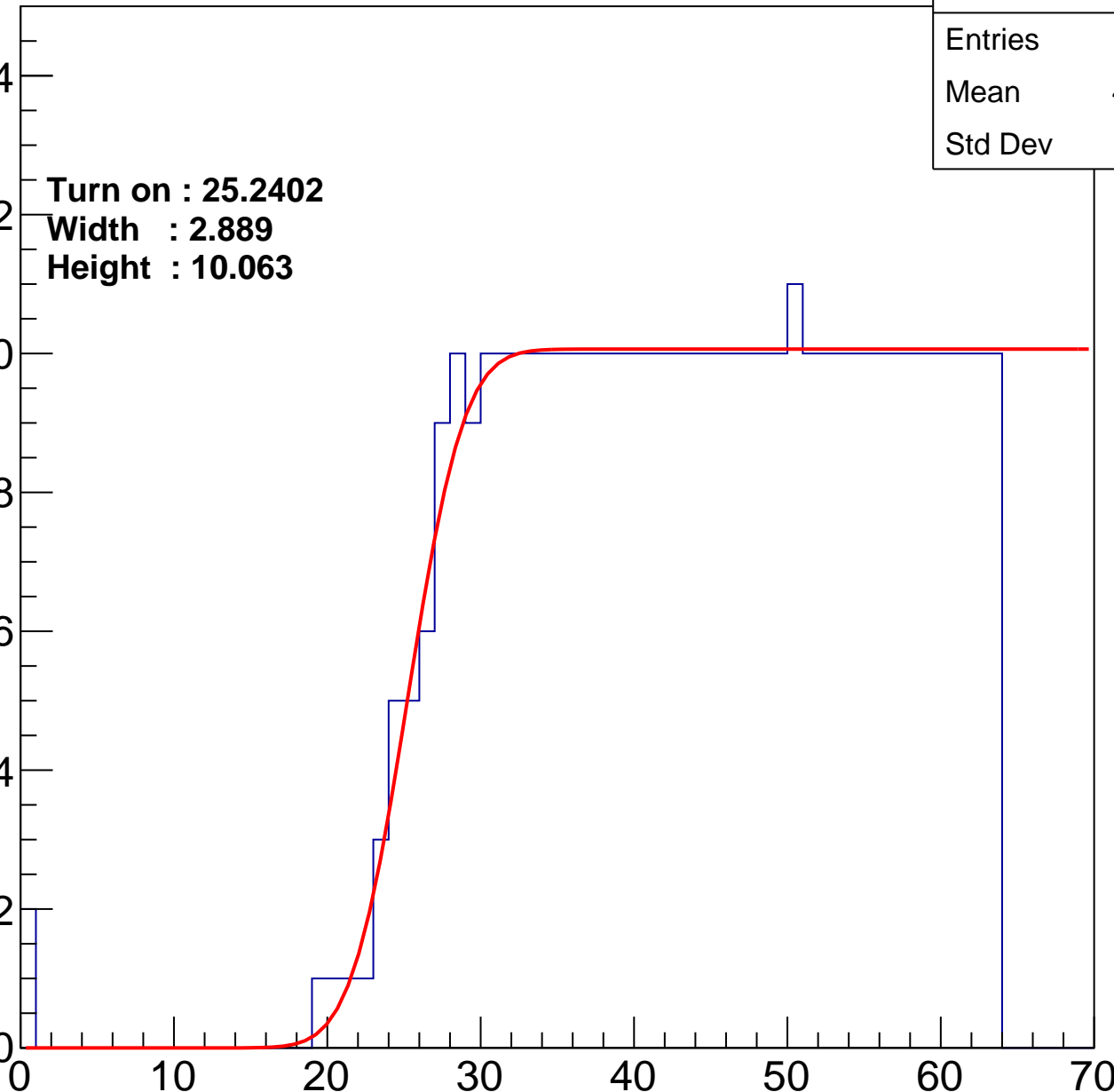
Width : 2.889

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.93
Std Dev	11.98

Turn on : 26.3491

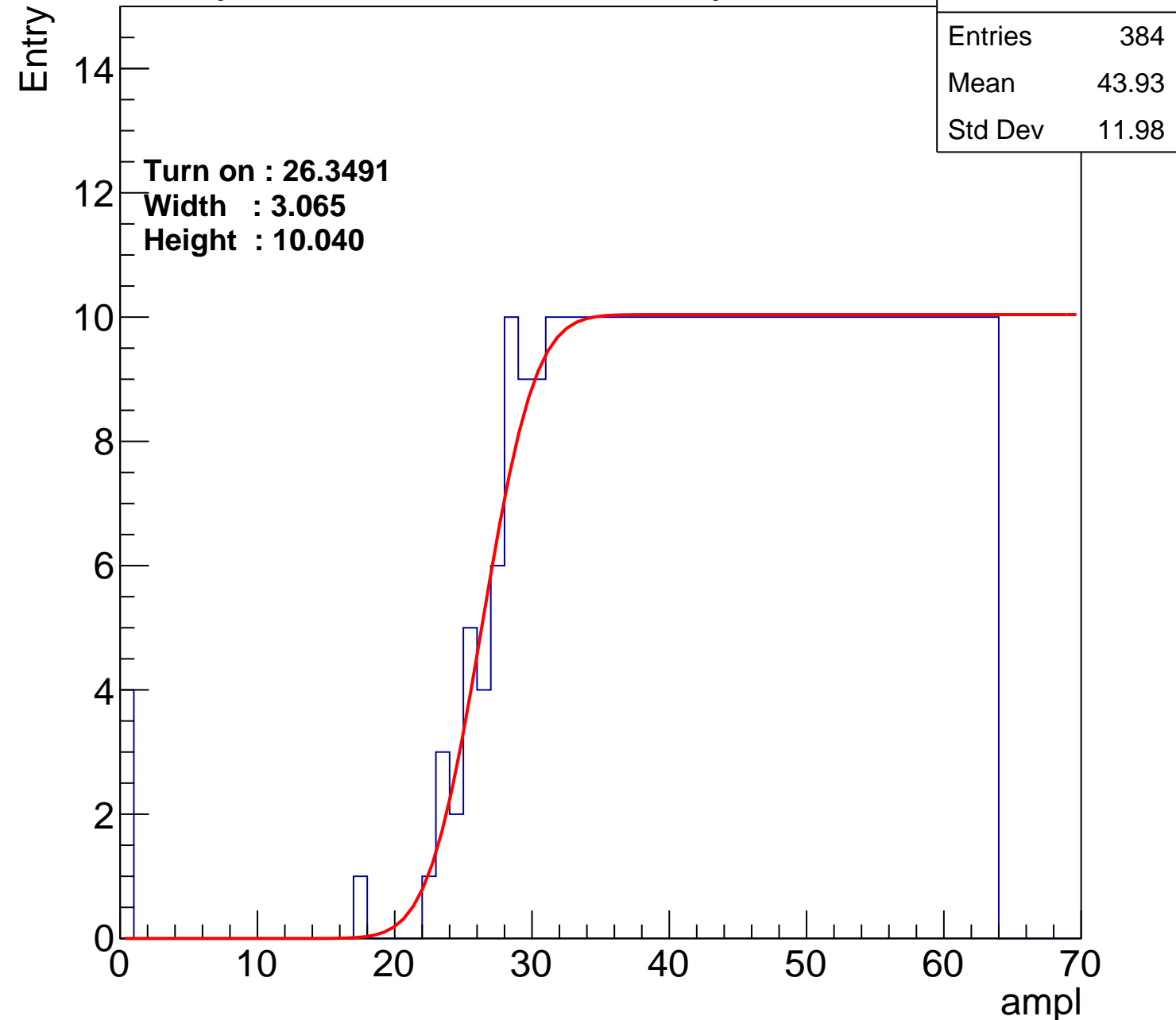
Width : 3.065

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.67
Std Dev	11.47

Turn on : 27.5819

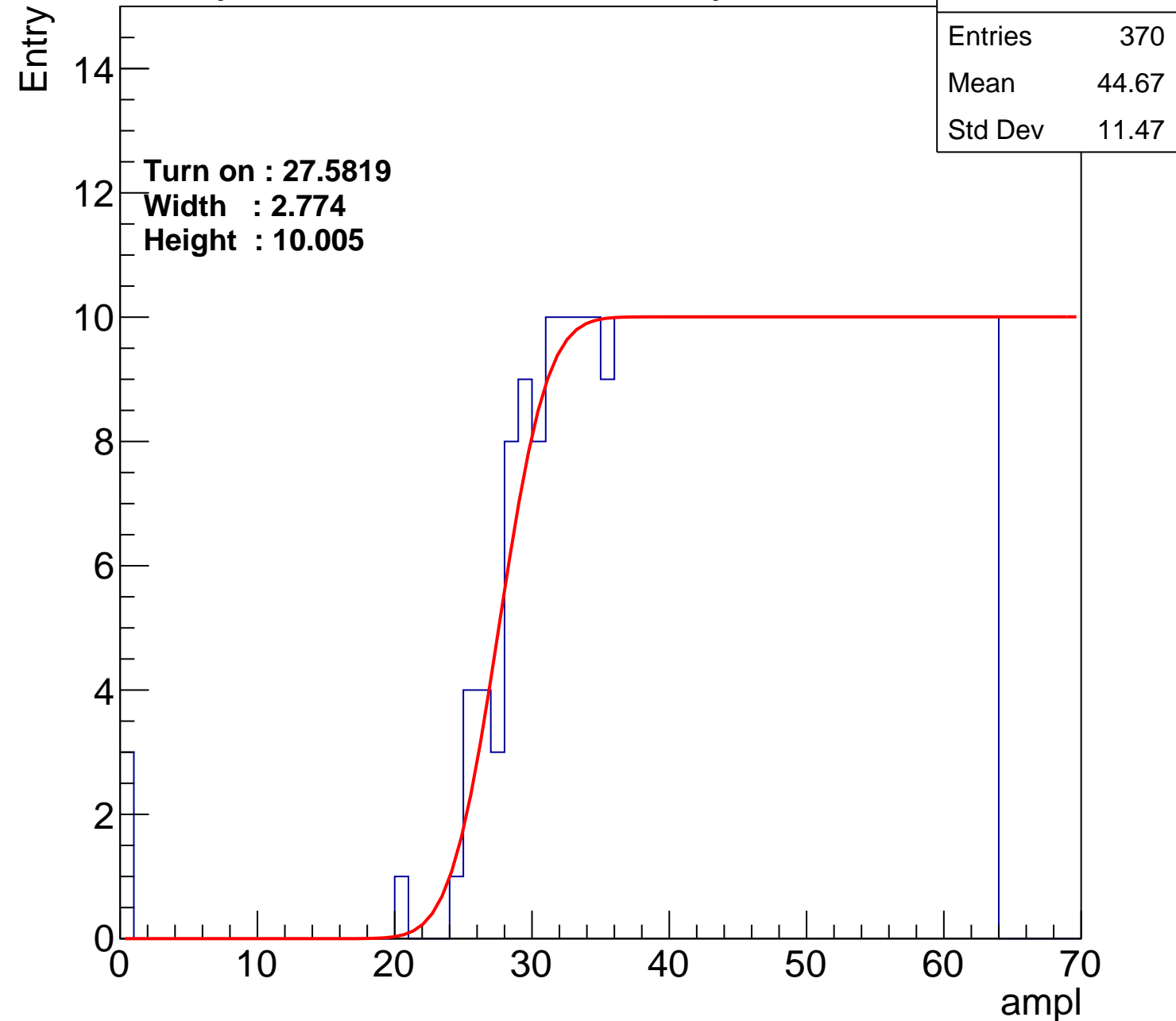
Width : 2.774

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch90

calib_packv5_042523_0143.root, FC#11, port A2

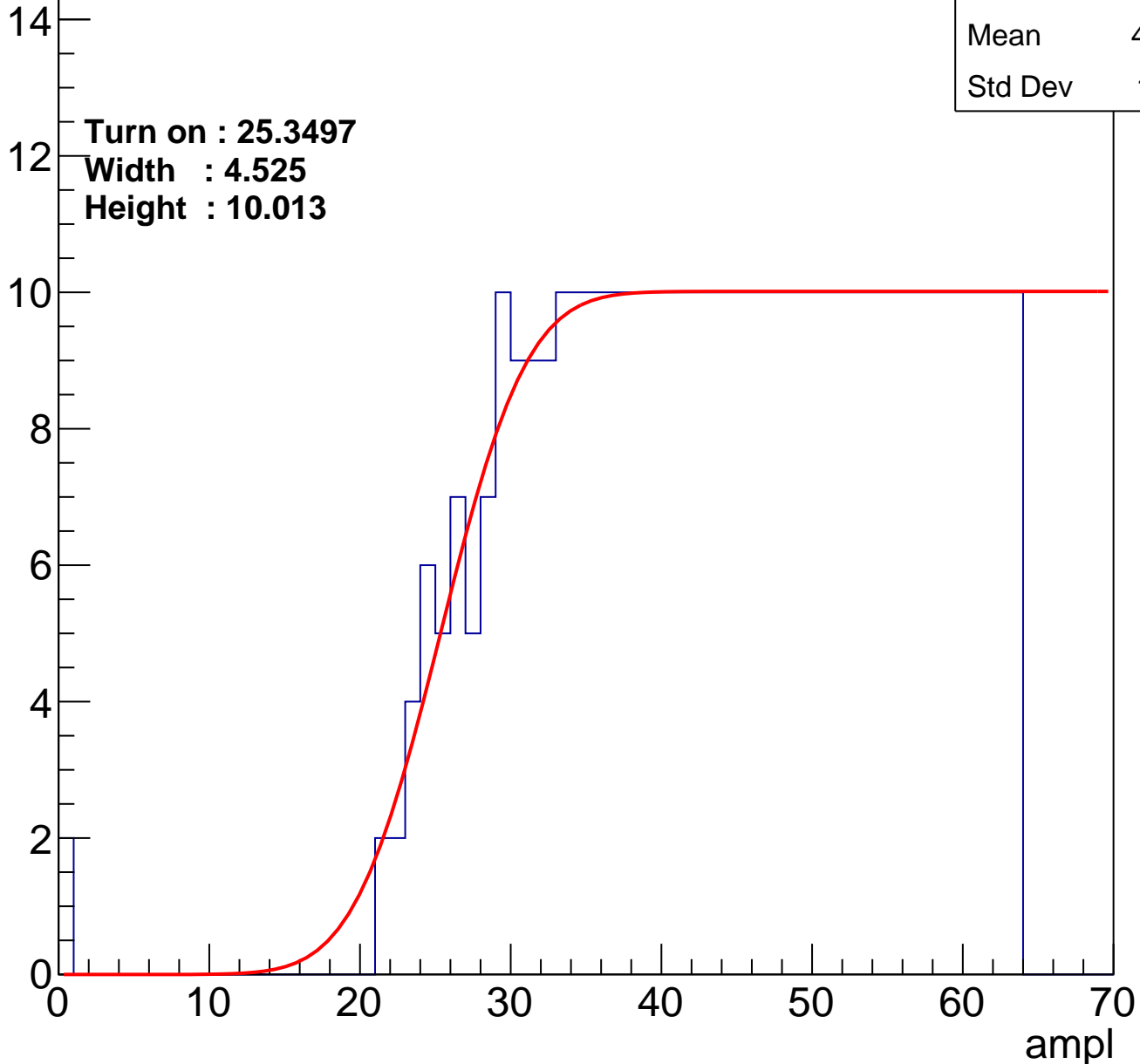
Entries	387
Mean	43.84
Std Dev	11.81

Turn on : 25.3497

Width : 4.525

Height : 10.013

Entry



B1L102S, U24-ch91

calib_packv5_042523_0143.root, FC#11, port A2

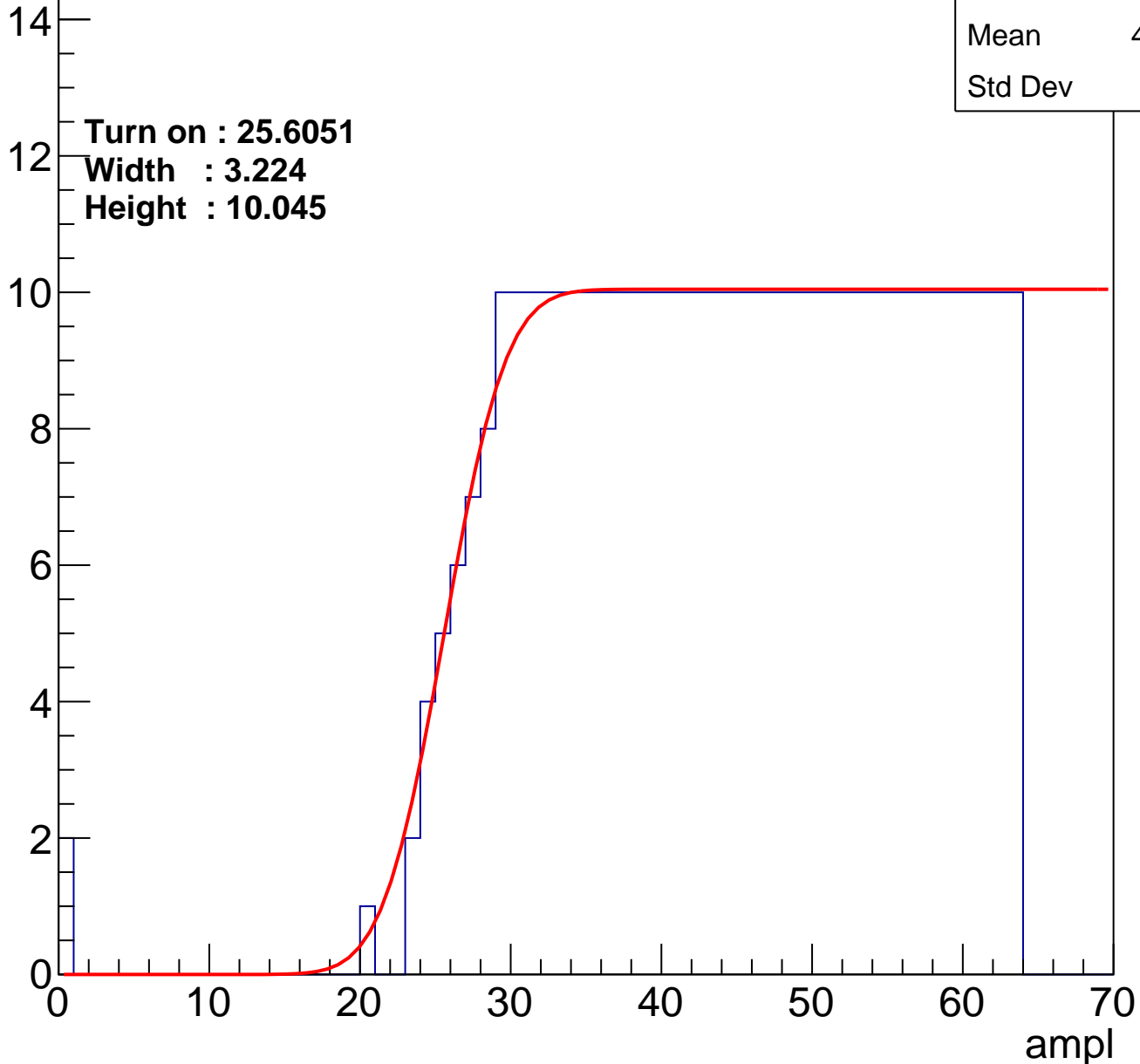
Entries	385
Mean	44.04
Std Dev	11.6

Turn on : 25.6051

Width : 3.224

Height : 10.045

Entry



B1L102S, U24-ch92

calib_packv5_042523_0143.root, FC#11, port A2

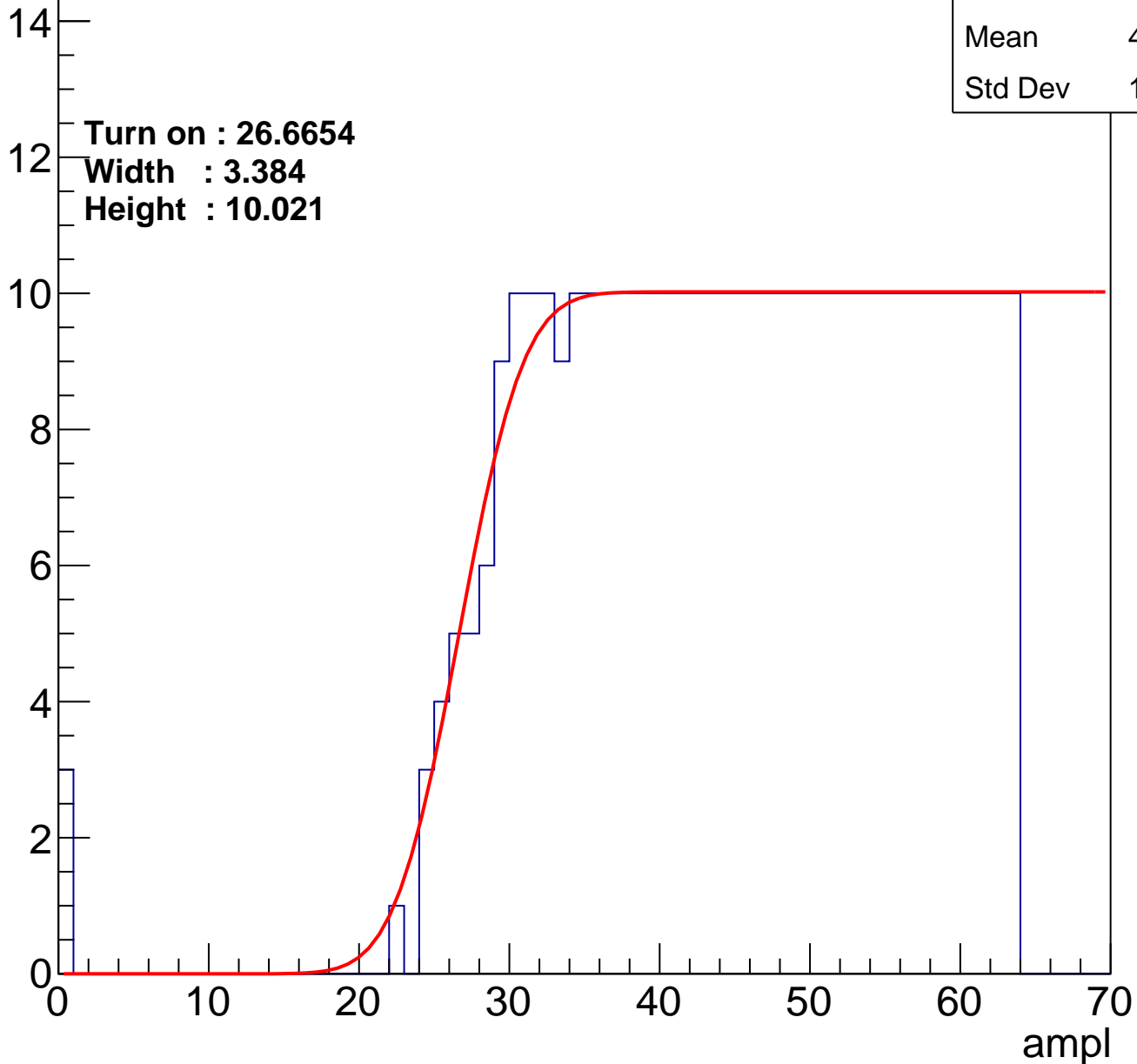
Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 26.6654

Width : 3.384

Height : 10.021

Entry



B1L102S, U24-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.1074

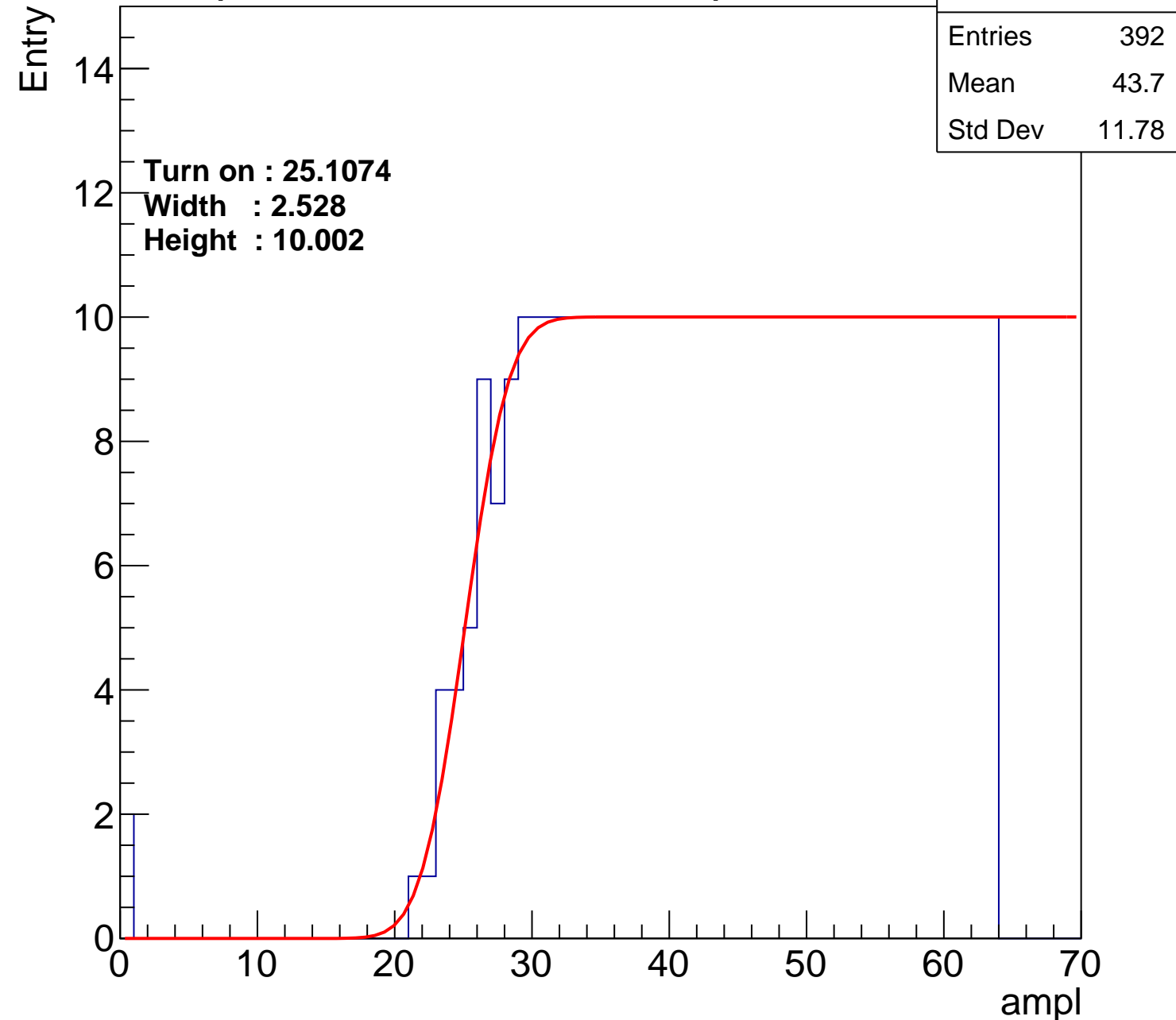
Width : 2.528

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch94

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.45
Std Dev	12.09

Turn on : 25.1814

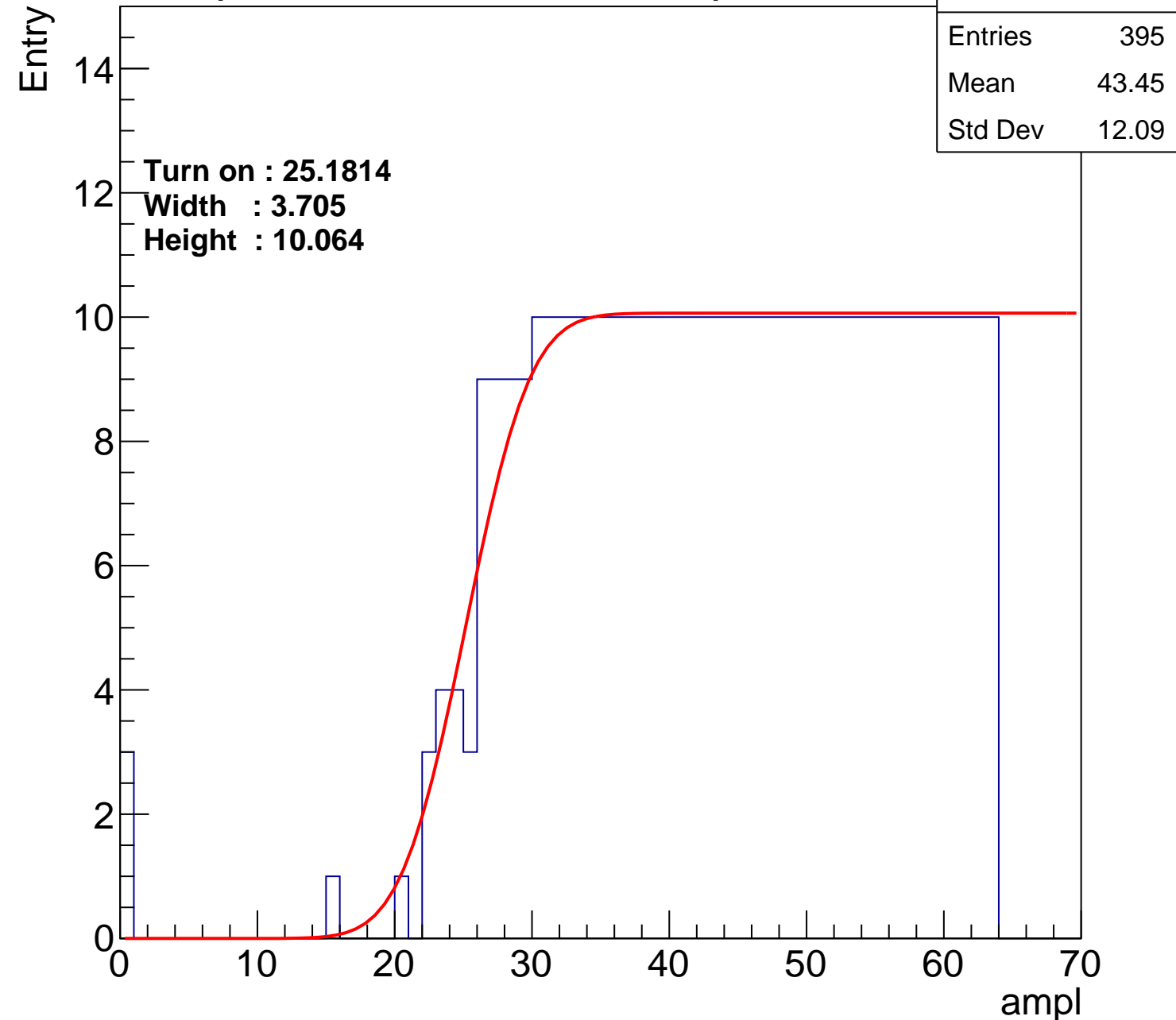
Width : 3.705

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 27.2946

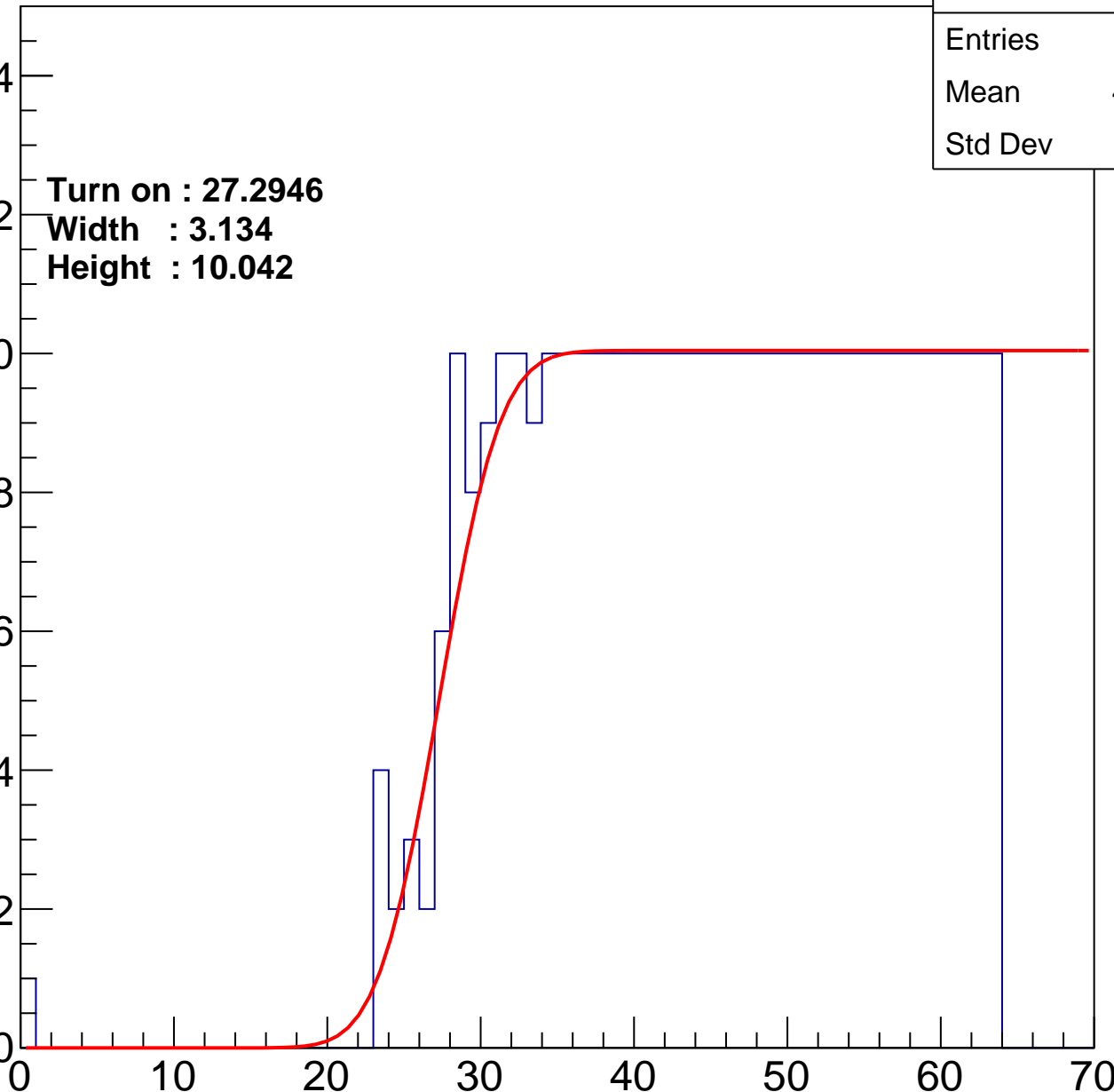
Width : 3.134

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch96

calib_packv5_042523_0143.root, FC#11, port A2

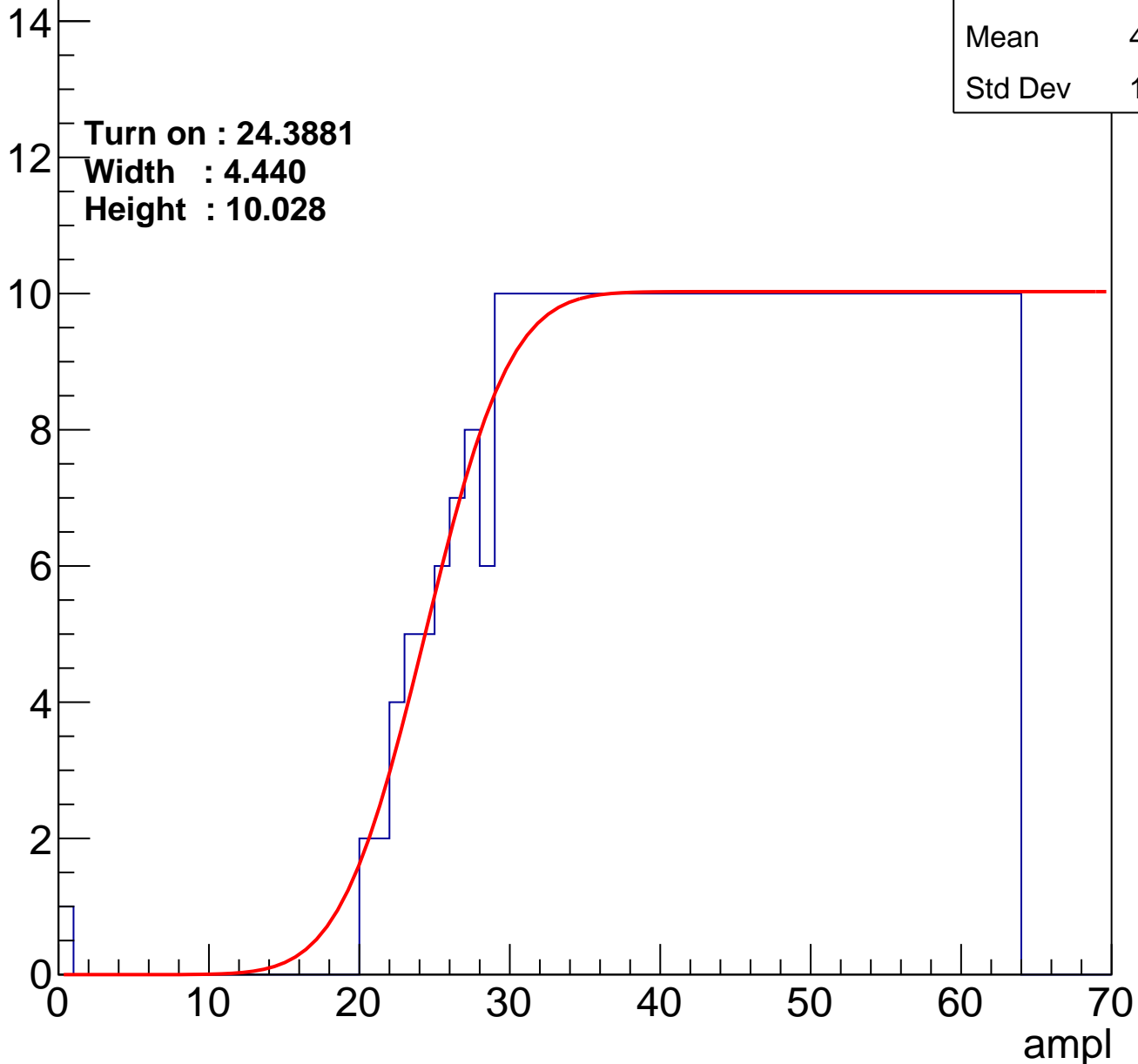
Entries	396
Mean	43.49
Std Dev	11.85

Turn on : 24.3881

Width : 4.440

Height : 10.028

Entry



B1L102S, U24-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	358
Mean	45.32
Std Dev	10.99

Turn on : 28.6809

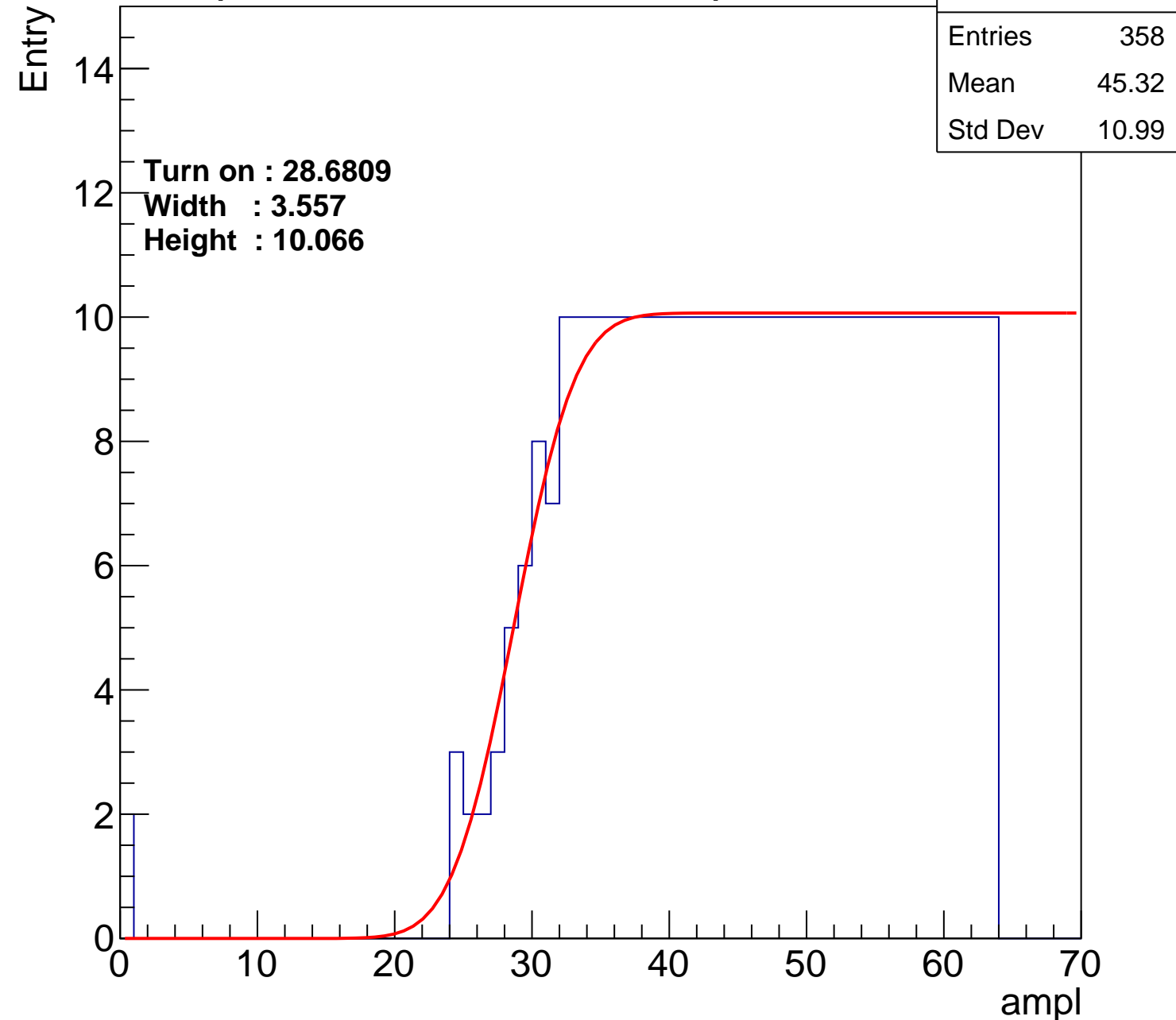
Width : 3.557

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.5
Std Dev	11.45

Turn on : 26.8236

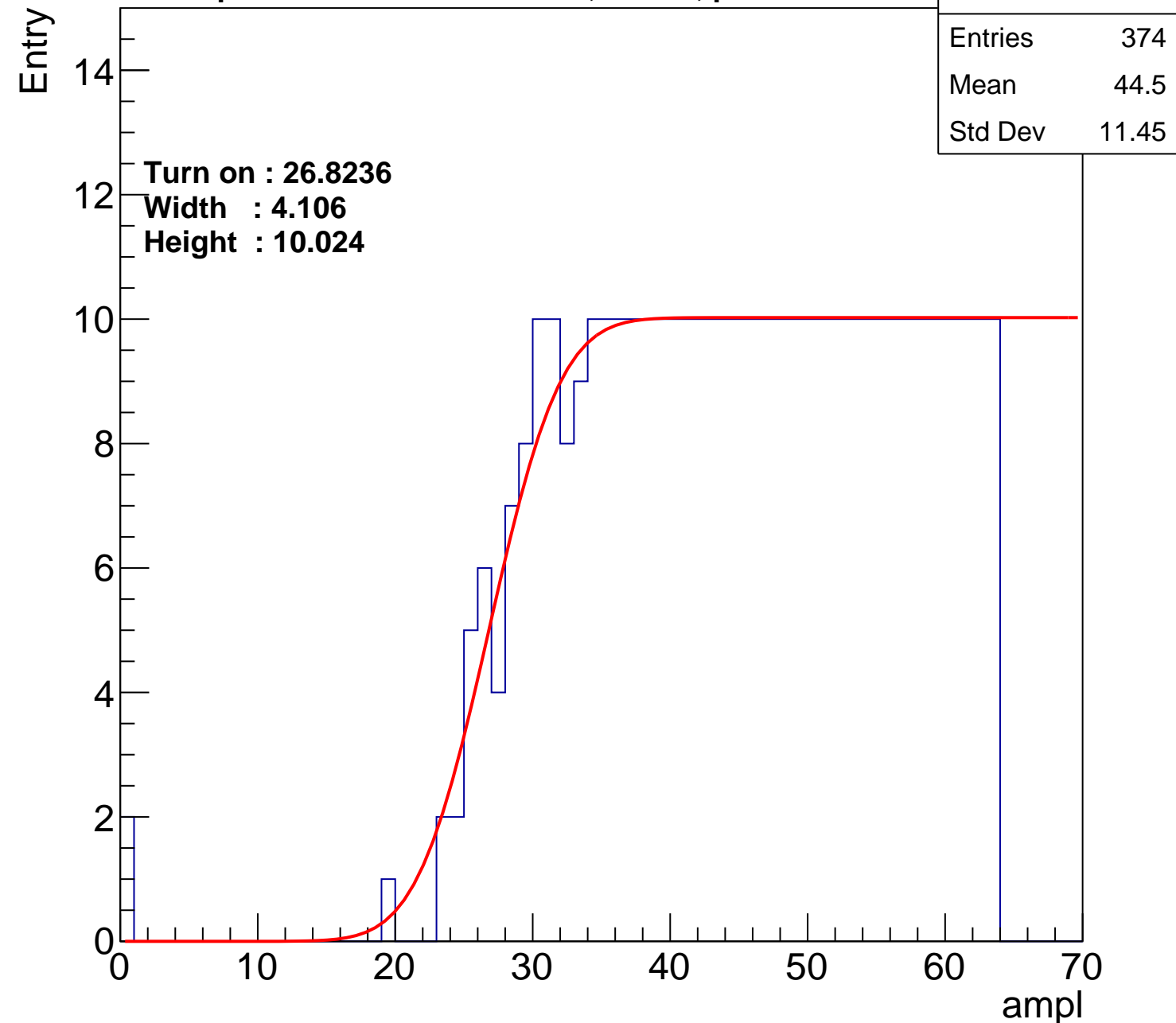
Width : 4.106

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.5375

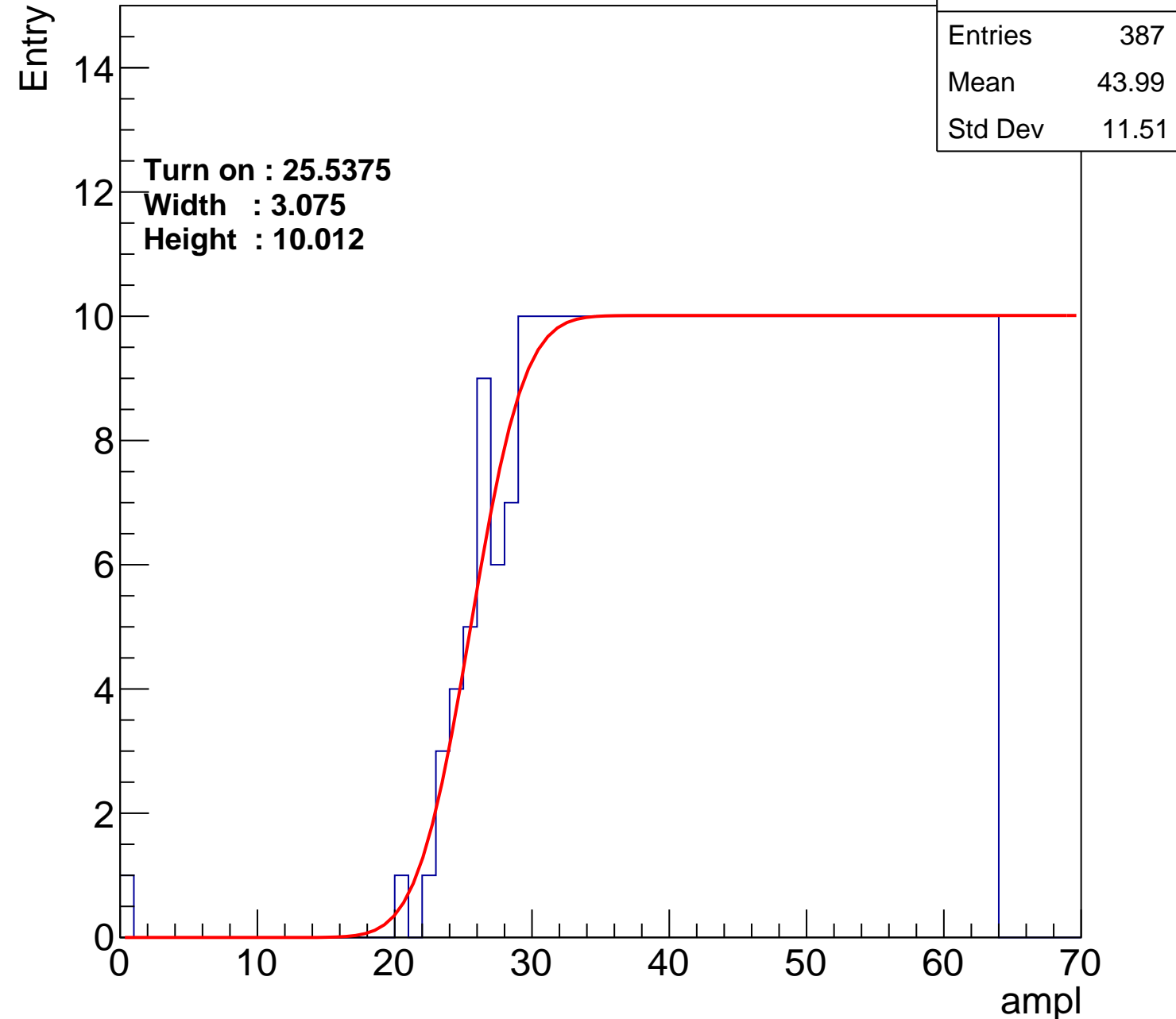
Width : 3.075

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch100

calib_packv5_042523_0143.root, FC#11, port A2

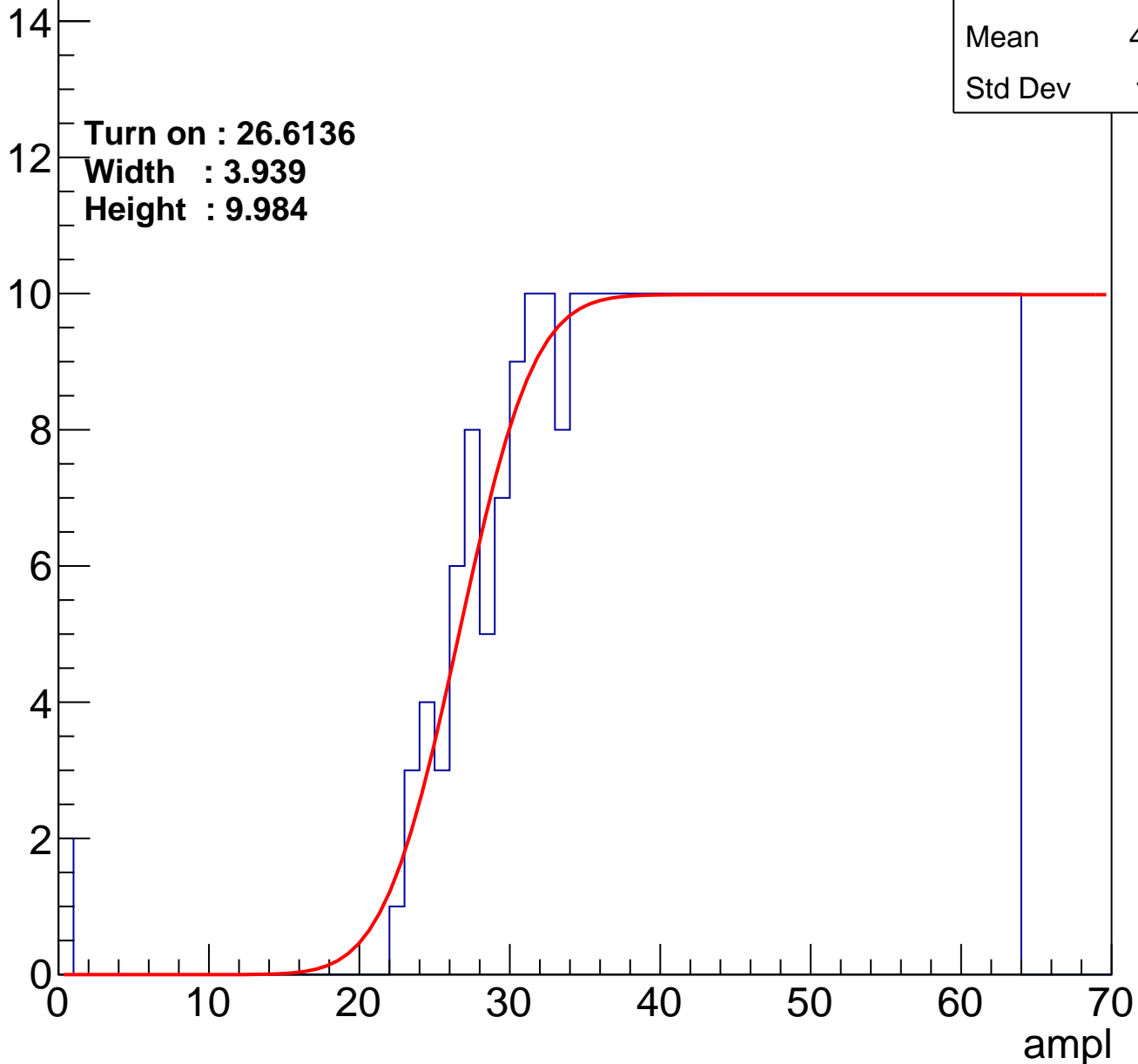
Entries	376
Mean	44.39
Std Dev	11.51

Turn on : 26.6136

Width : 3.939

Height : 9.984

Entry



B1L102S, U24-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.5365

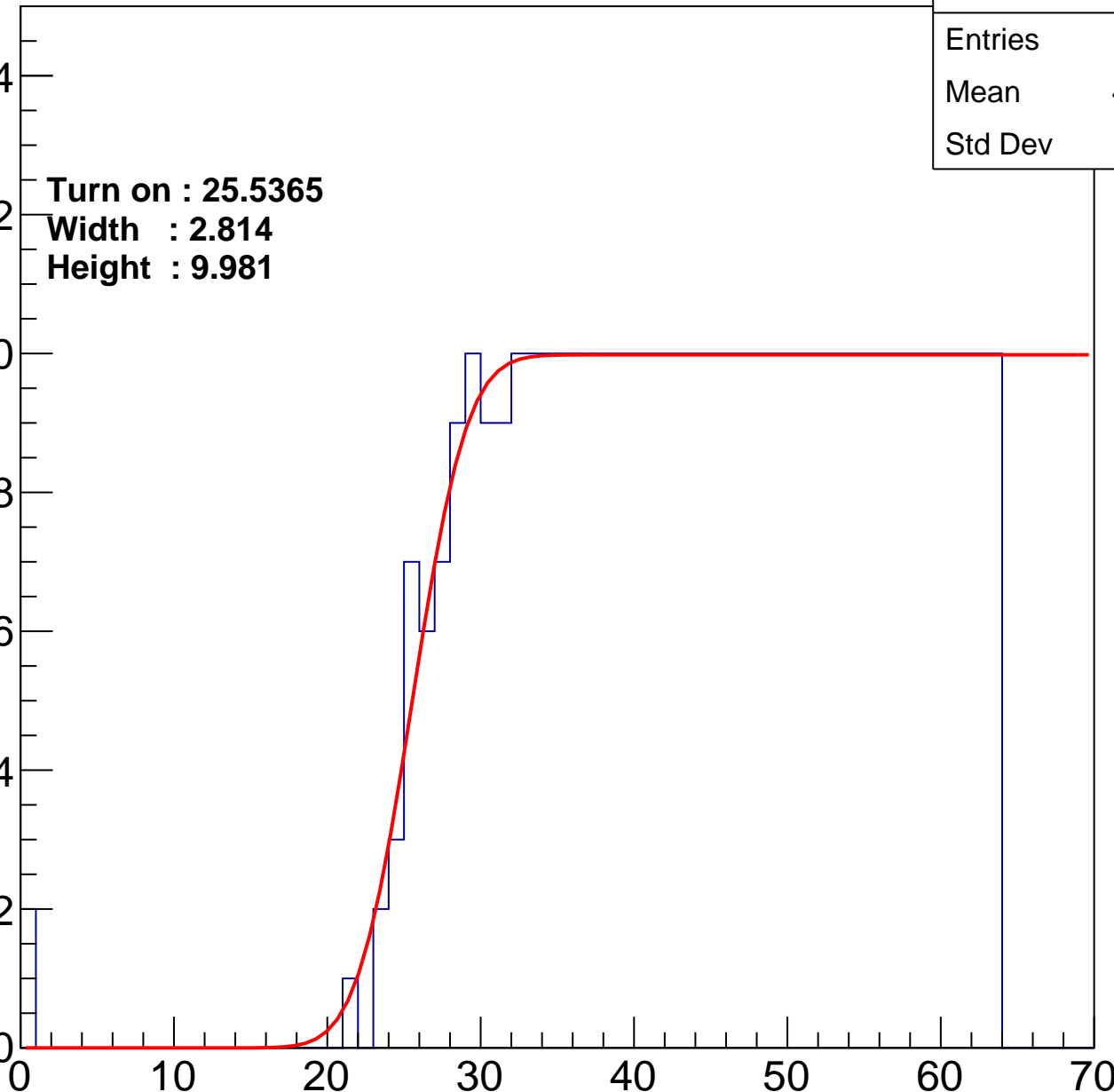
Width : 2.814

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.47
Std Dev	11.93

Turn on : 24.6499

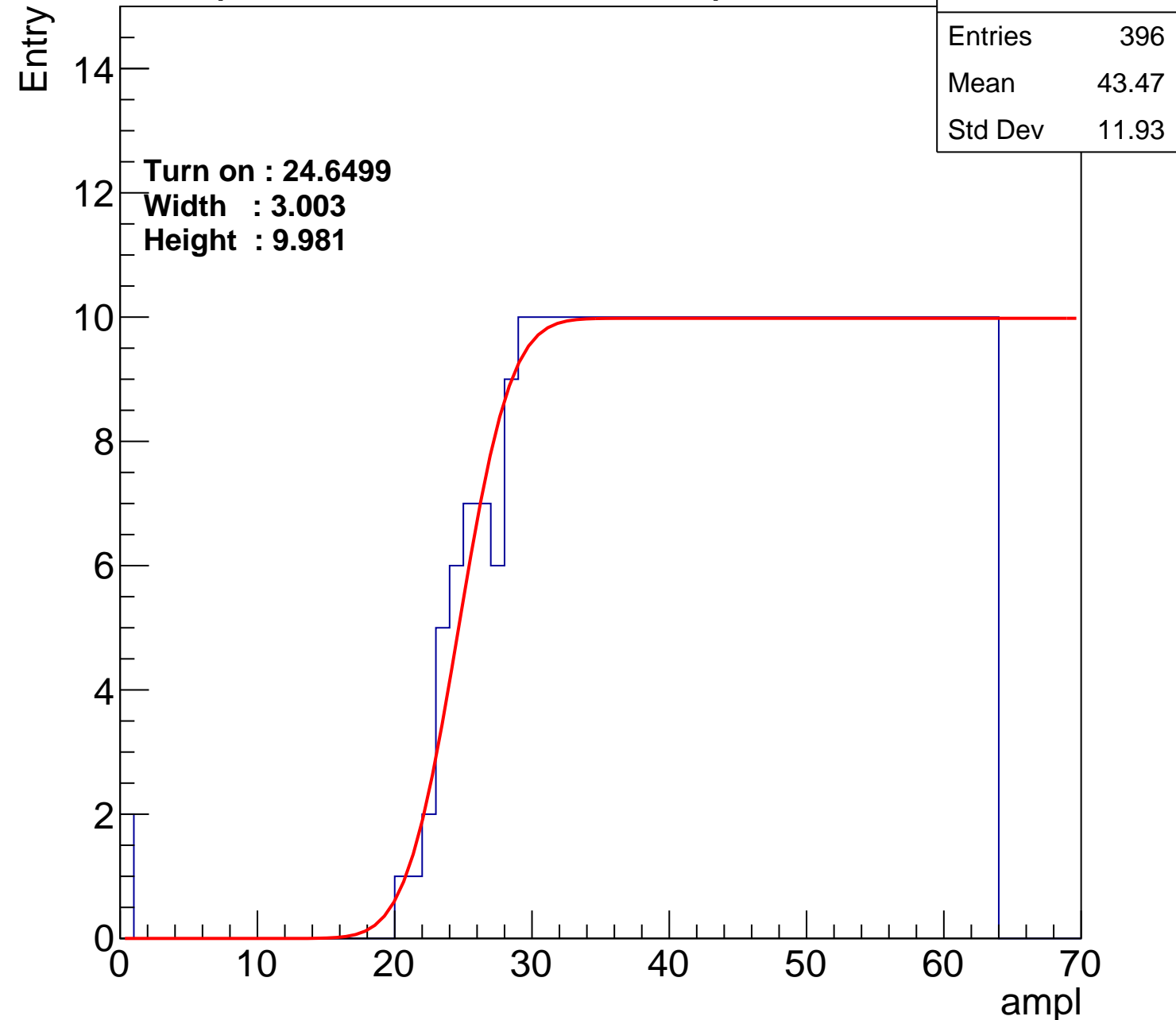
Width : 3.003

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch103

calib_packv5_042523_0143.root, FC#11, port A2

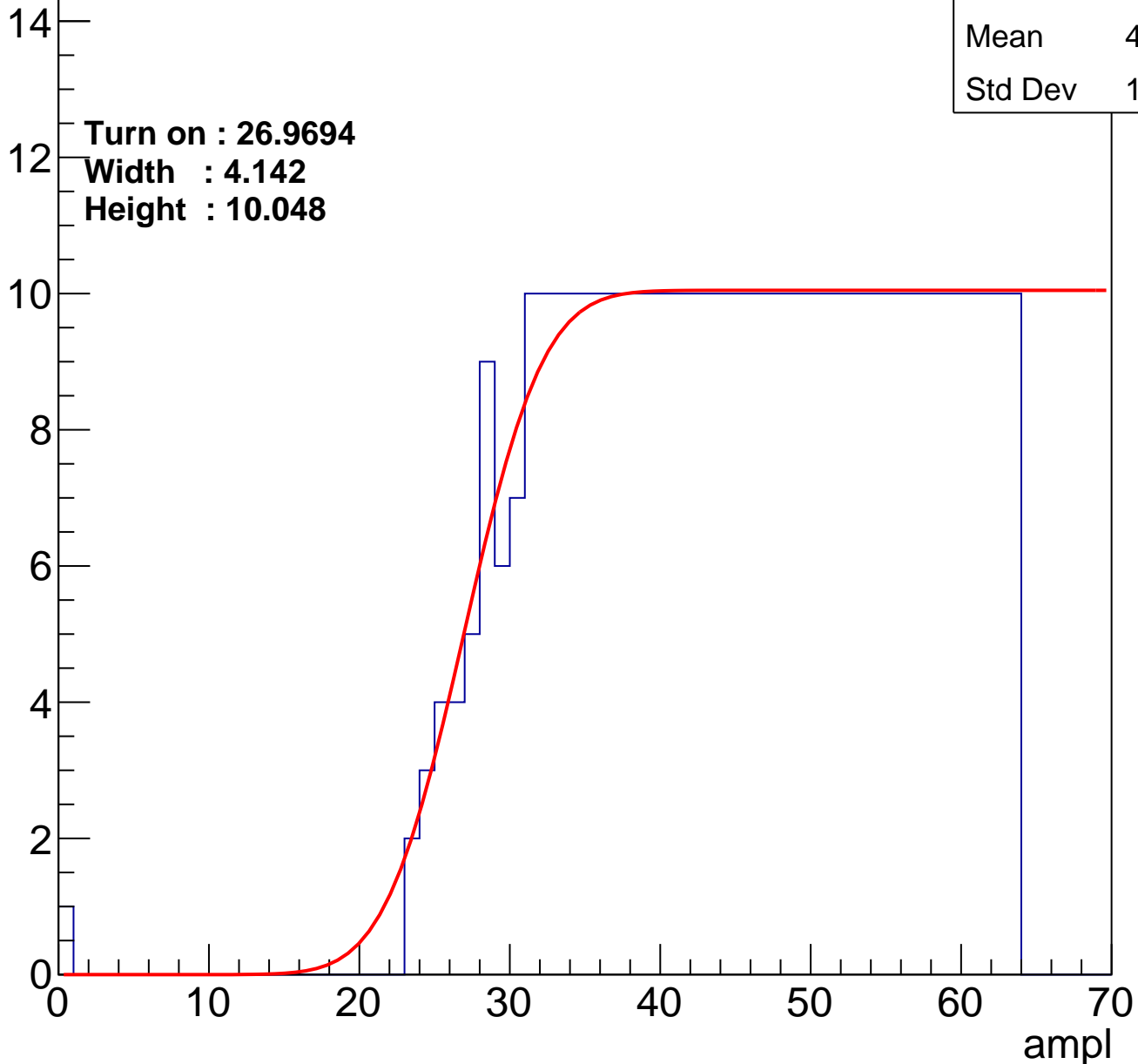
Entries	371
Mean	44.75
Std Dev	11.12

Turn on : 26.9694

Width : 4.142

Height : 10.048

Entry



B1L102S, U24-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 25.6539

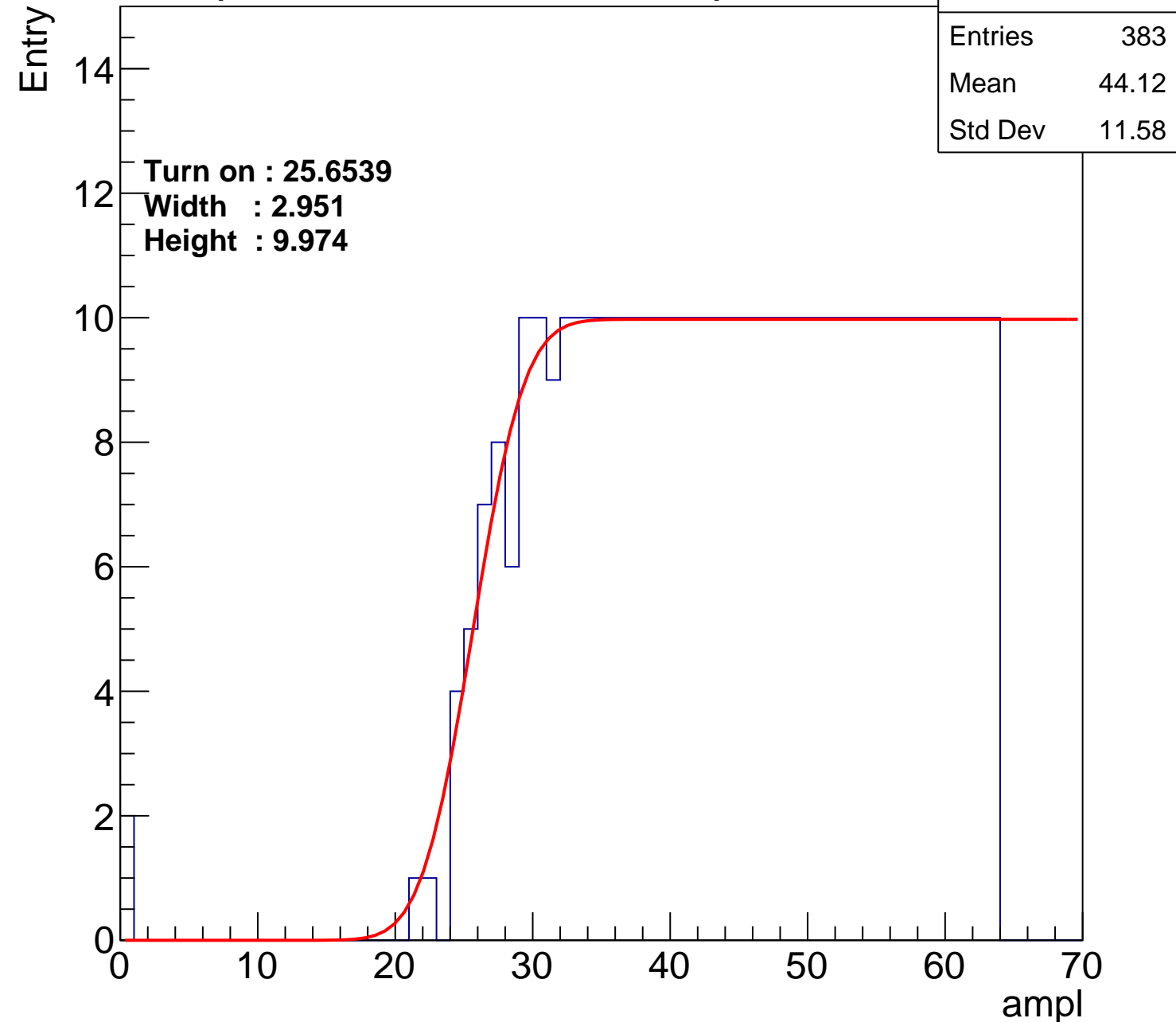
Width : 2.951

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch105

calib_packv5_042523_0143.root, FC#11, port A2

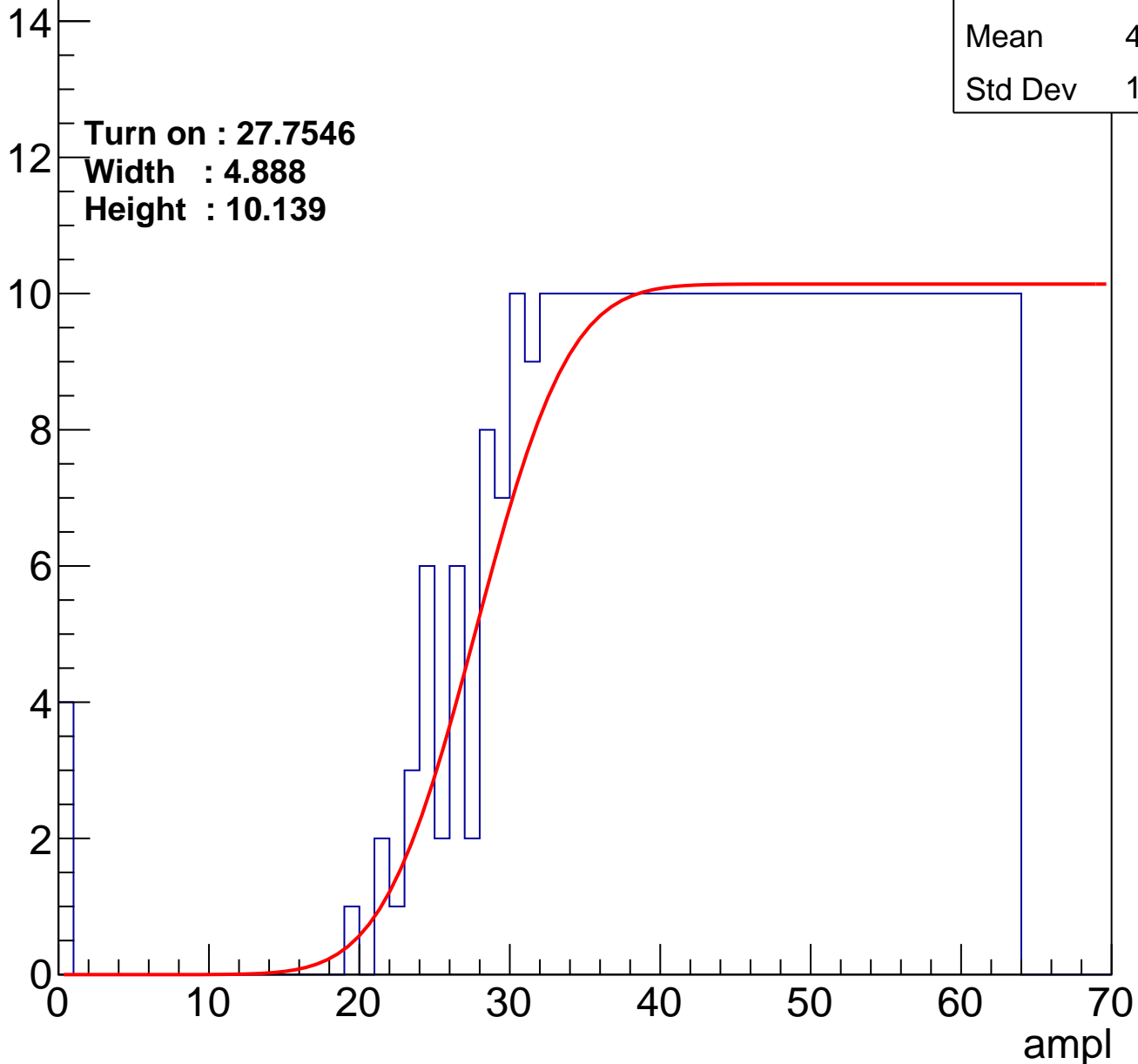
Entries	381
Mean	43.99
Std Dev	12.03

Turn on : 27.7546

Width : 4.888

Height : 10.139

Entry



B1L102S, U24-ch106

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	42.98
Std Dev	12.61

Turn on : 24.4744

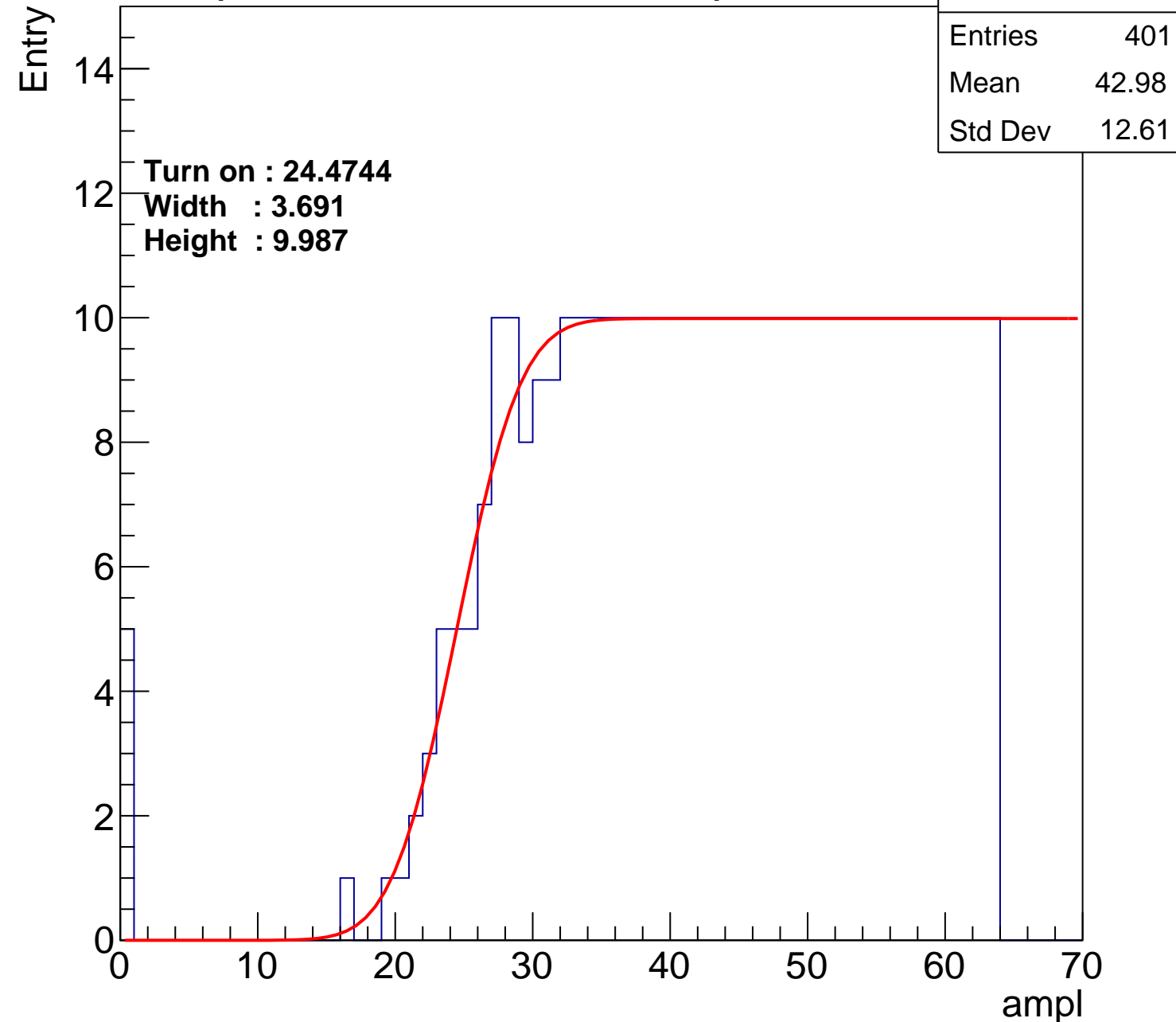
Width : 3.691

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.1722

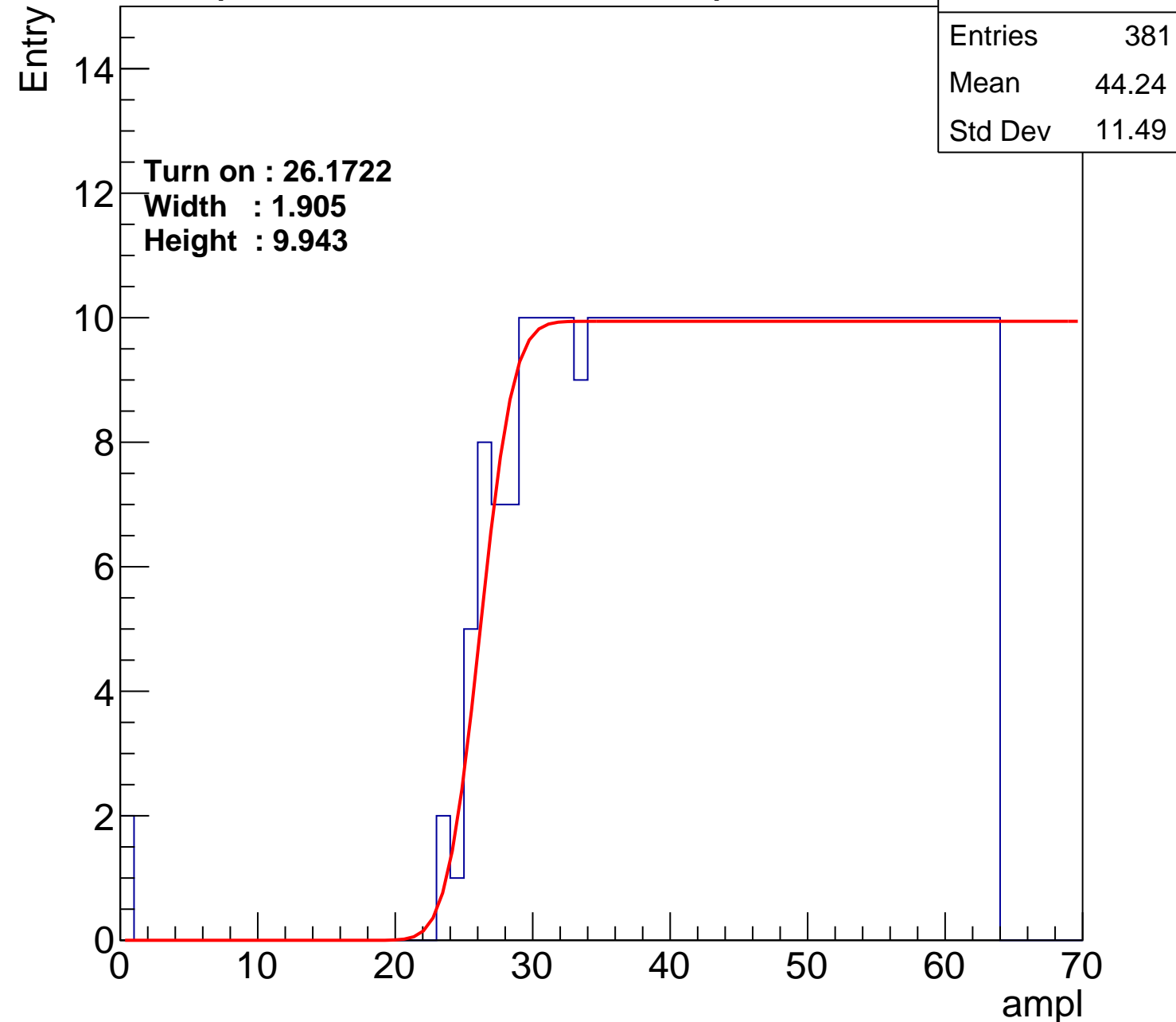
Width : 1.905

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch108

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.2387

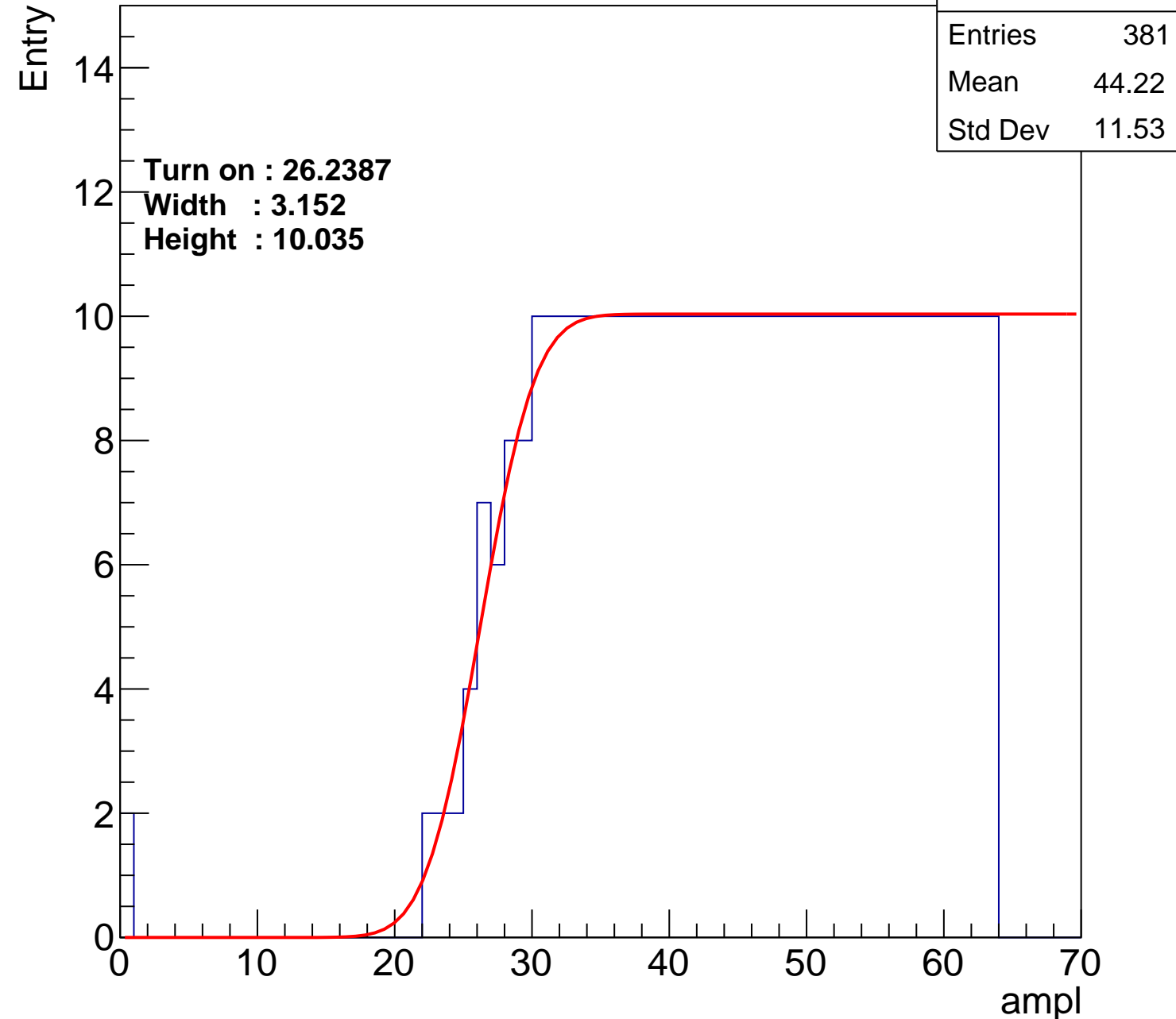
Width : 3.152

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch109

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.38
Std Dev	12.25

Turn on : 26.1035

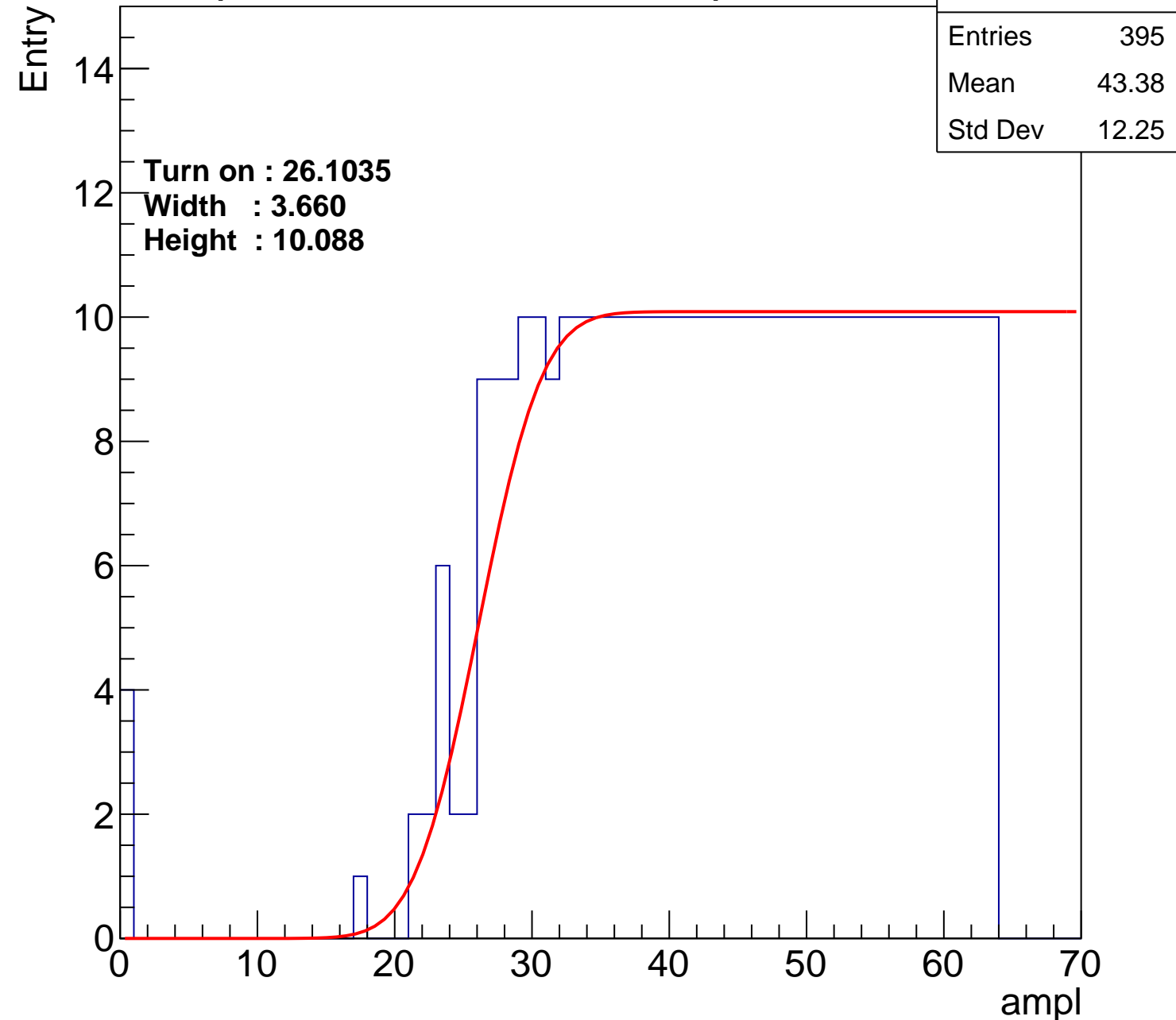
Width : 3.660

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch110

calib_packv5_042523_0143.root, FC#11, port A2

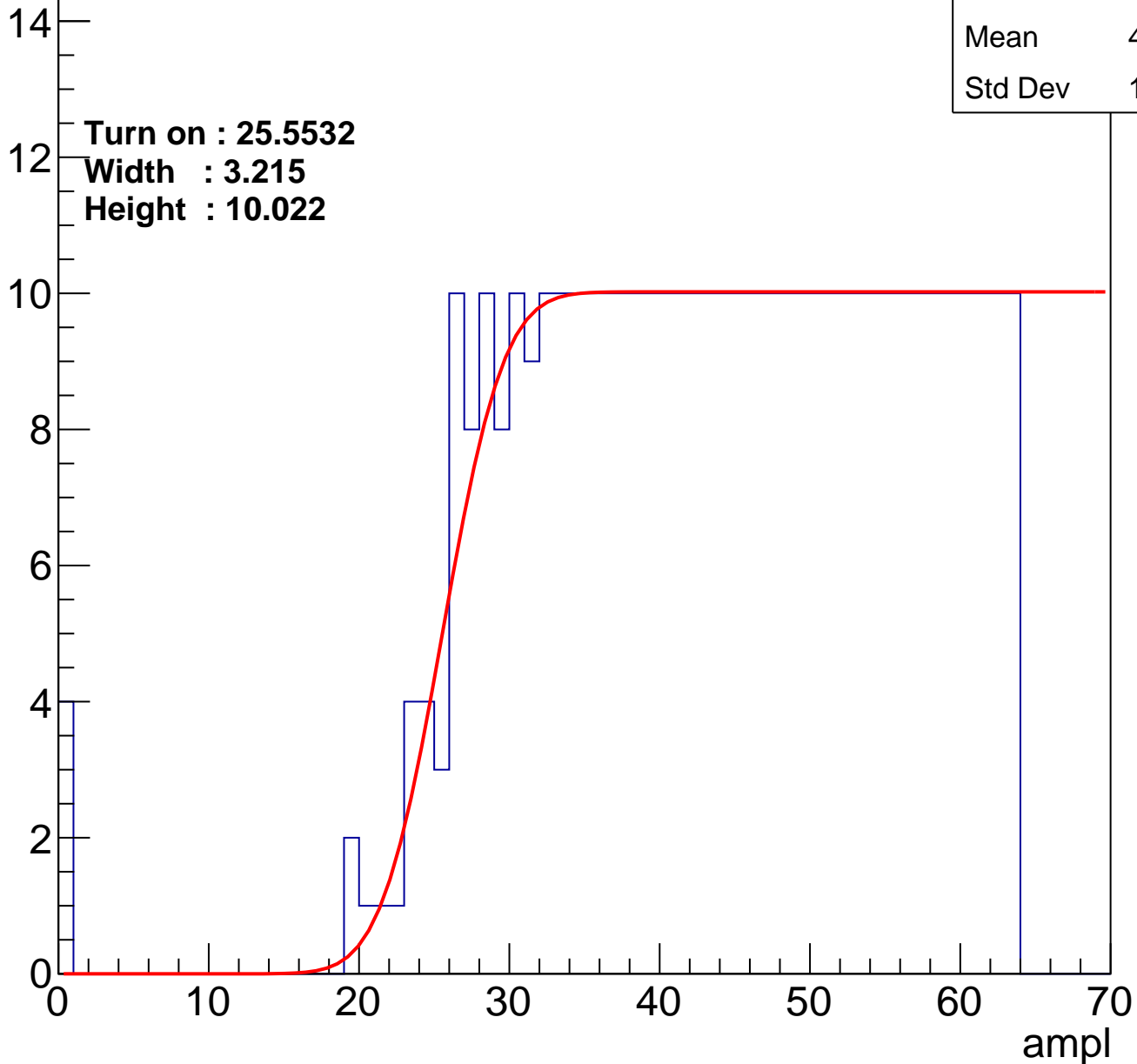
Entries	395
Mean	43.37
Std Dev	12.27

Turn on : 25.5532

Width : 3.215

Height : 10.022

Entry



B1L102S, U24-ch111

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 25.6964

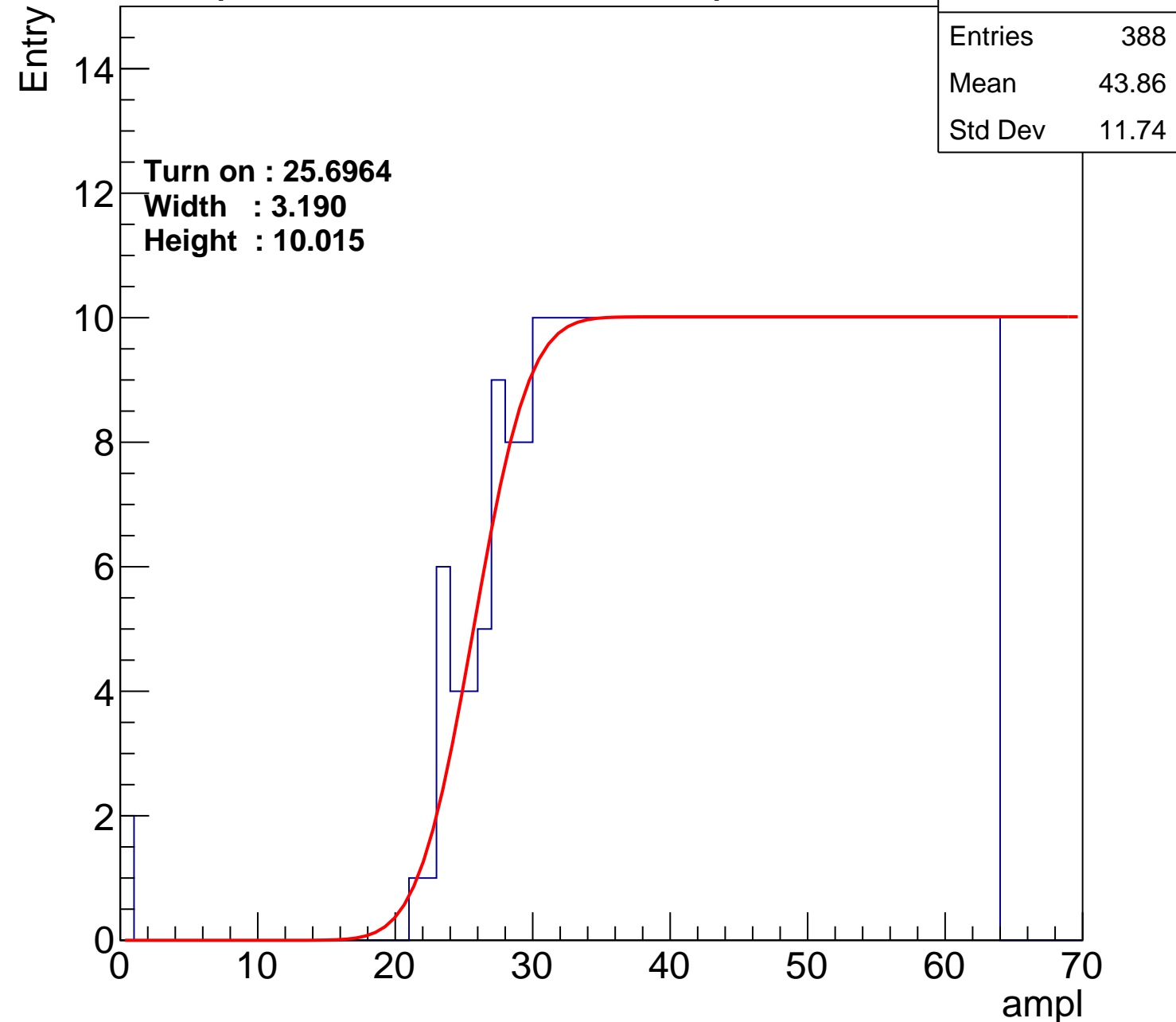
Width : 3.190

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch112

calib_packv5_042523_0143.root, FC#11, port A2

Entries	403
Mean	43.18
Std Dev	11.99

Turn on : 24.1556

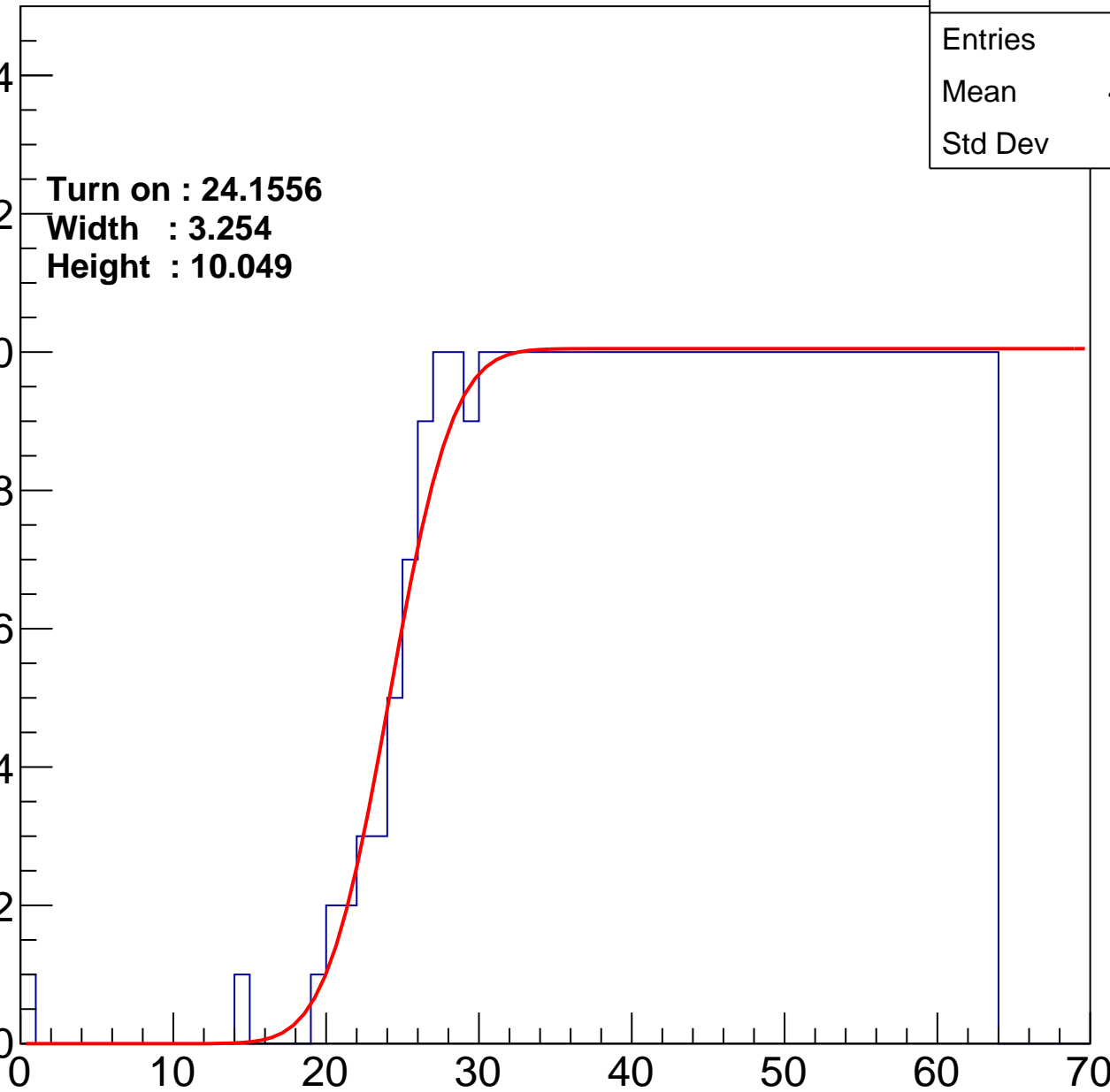
Width : 3.254

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch113

calib_packv5_042523_0143.root, FC#11, port A2

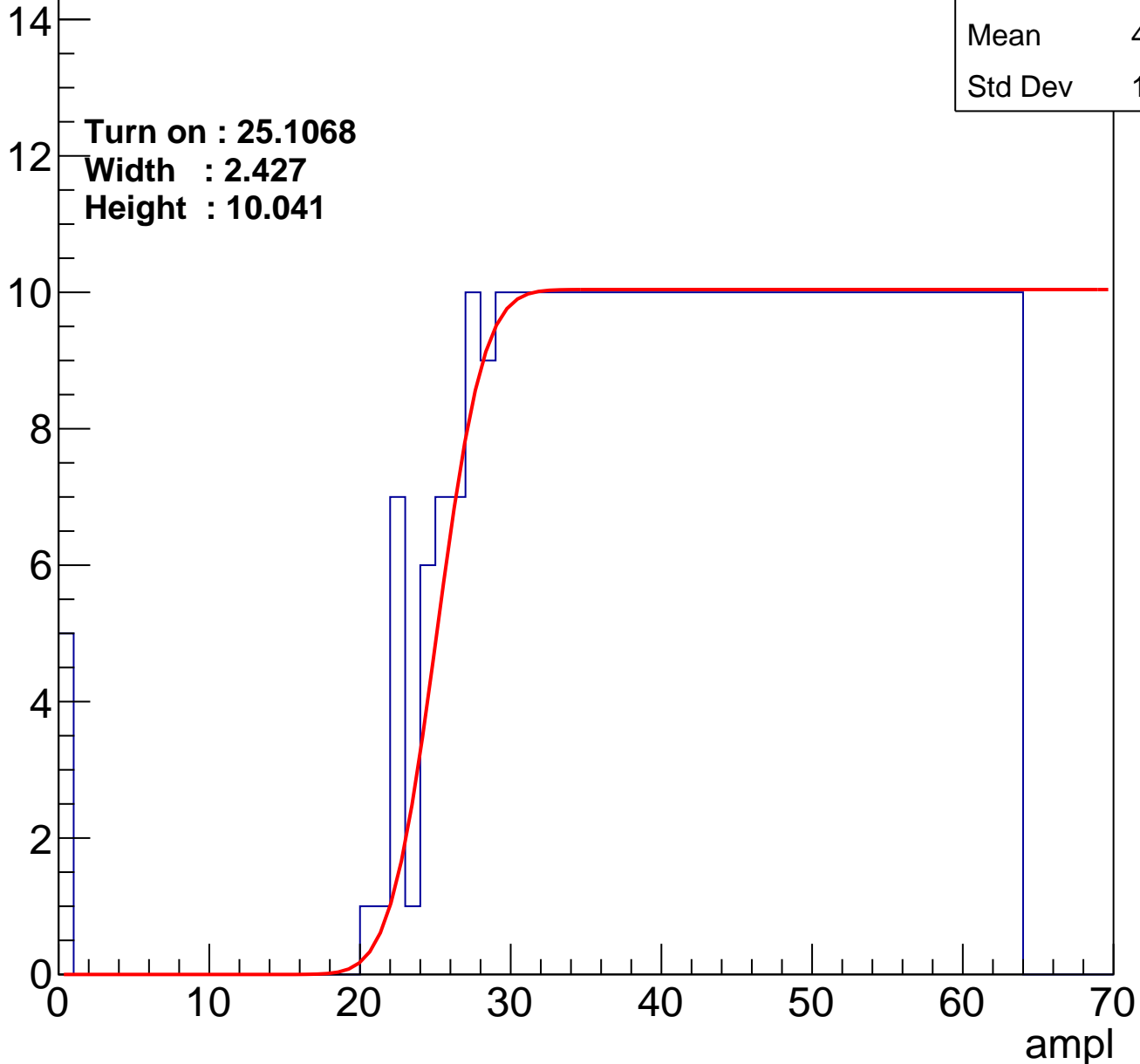
Entries	404
Mean	42.92
Std Dev	12.55

Turn on : 25.1068

Width : 2.427

Height : 10.041

Entry



B1L102S, U24-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 27.0590

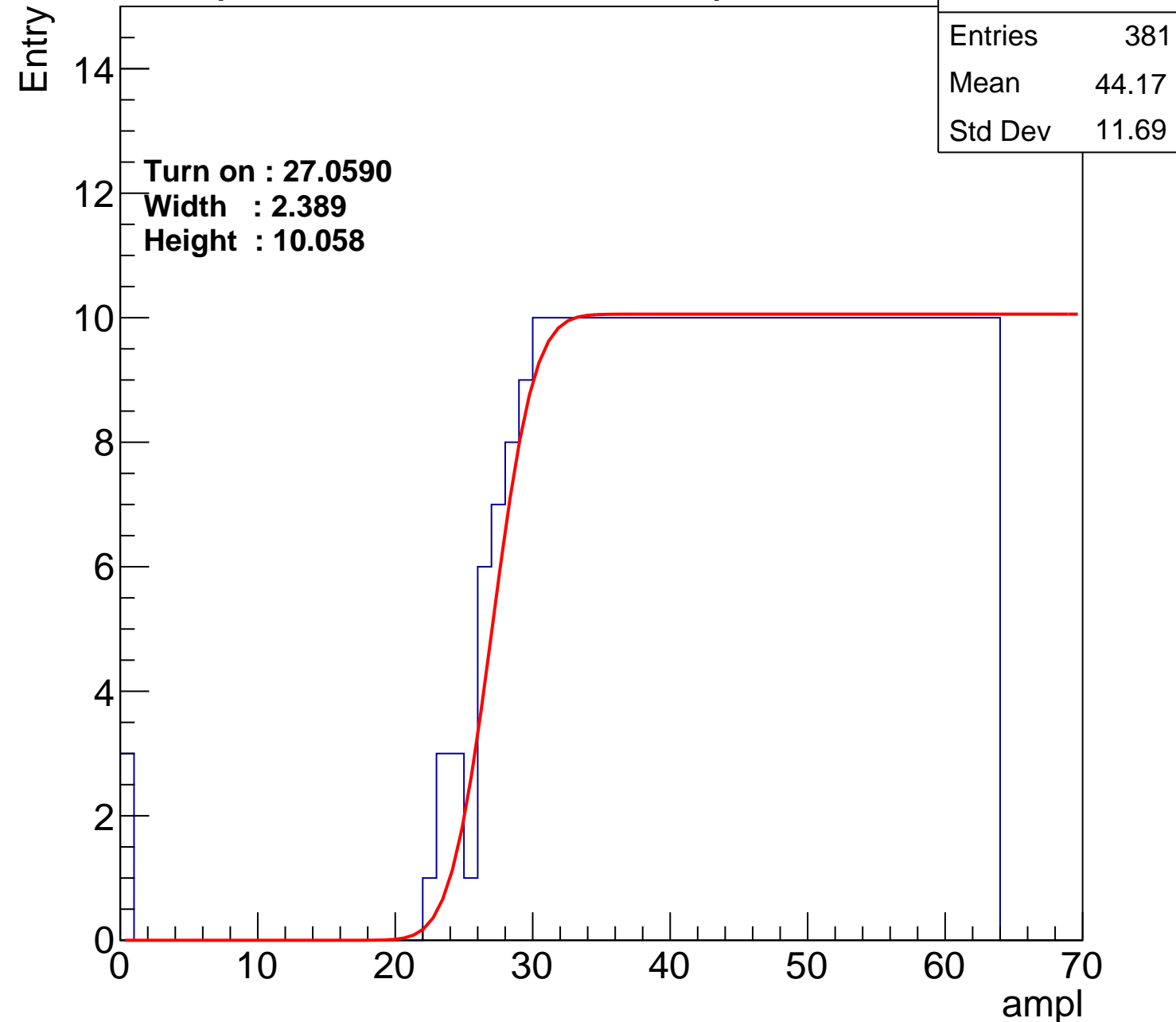
Width : 2.389

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch115

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.04
Std Dev	11.32

Turn on : 28.2091

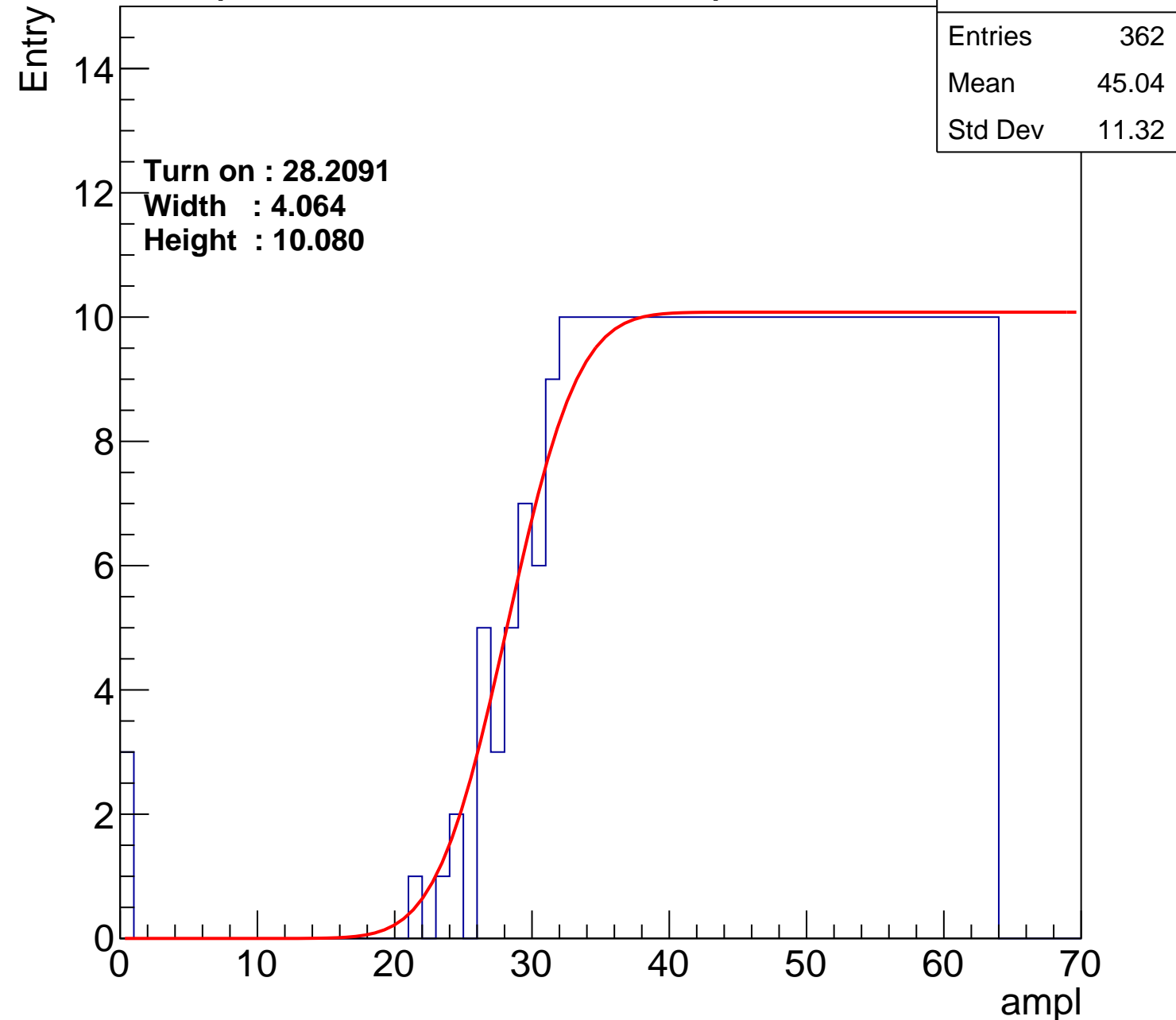
Width : 4.064

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.21
Std Dev	11.61

Turn on : 26.4968

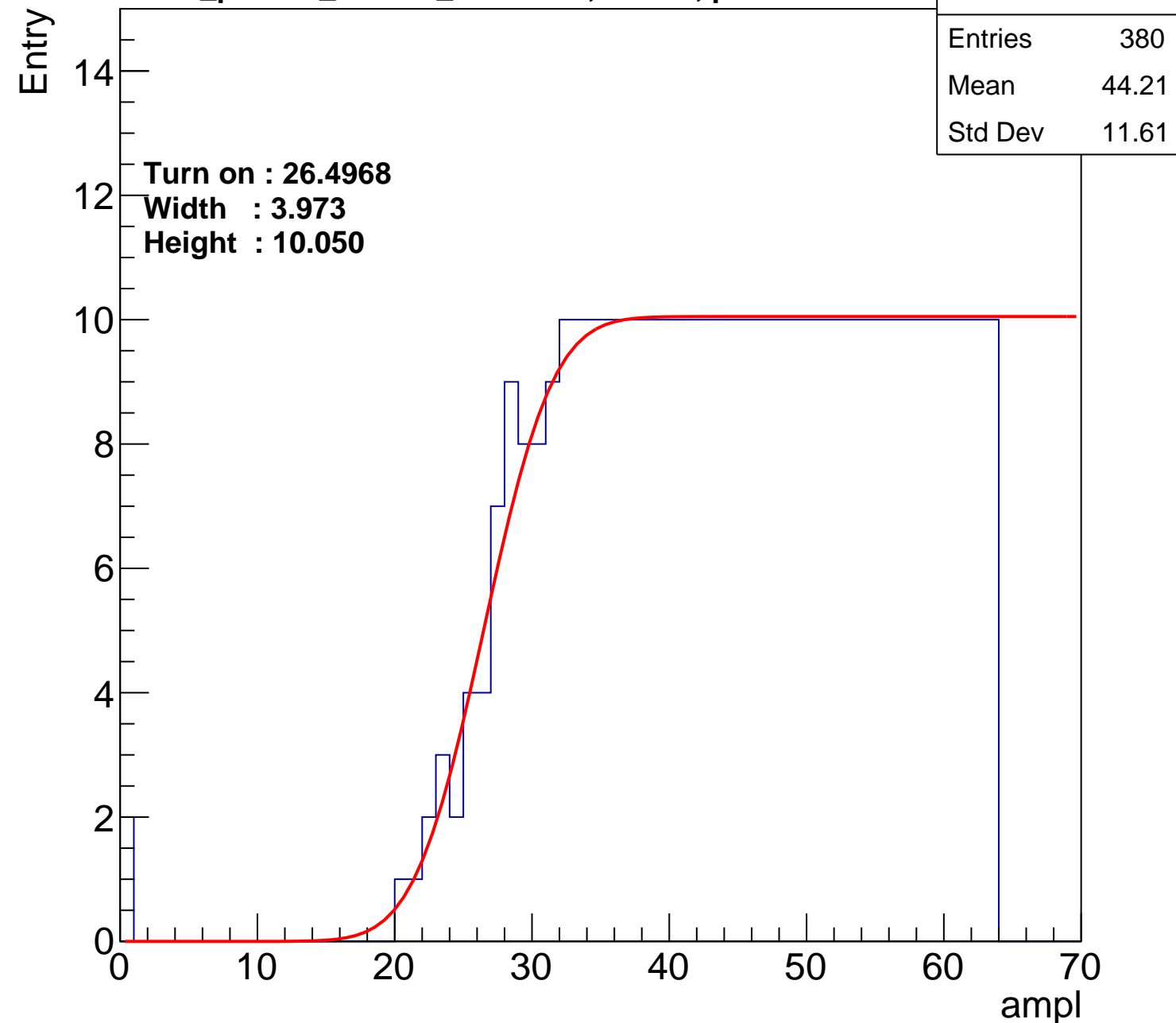
Width : 3.973

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch117

calib_packv5_042523_0143.root, FC#11, port A2

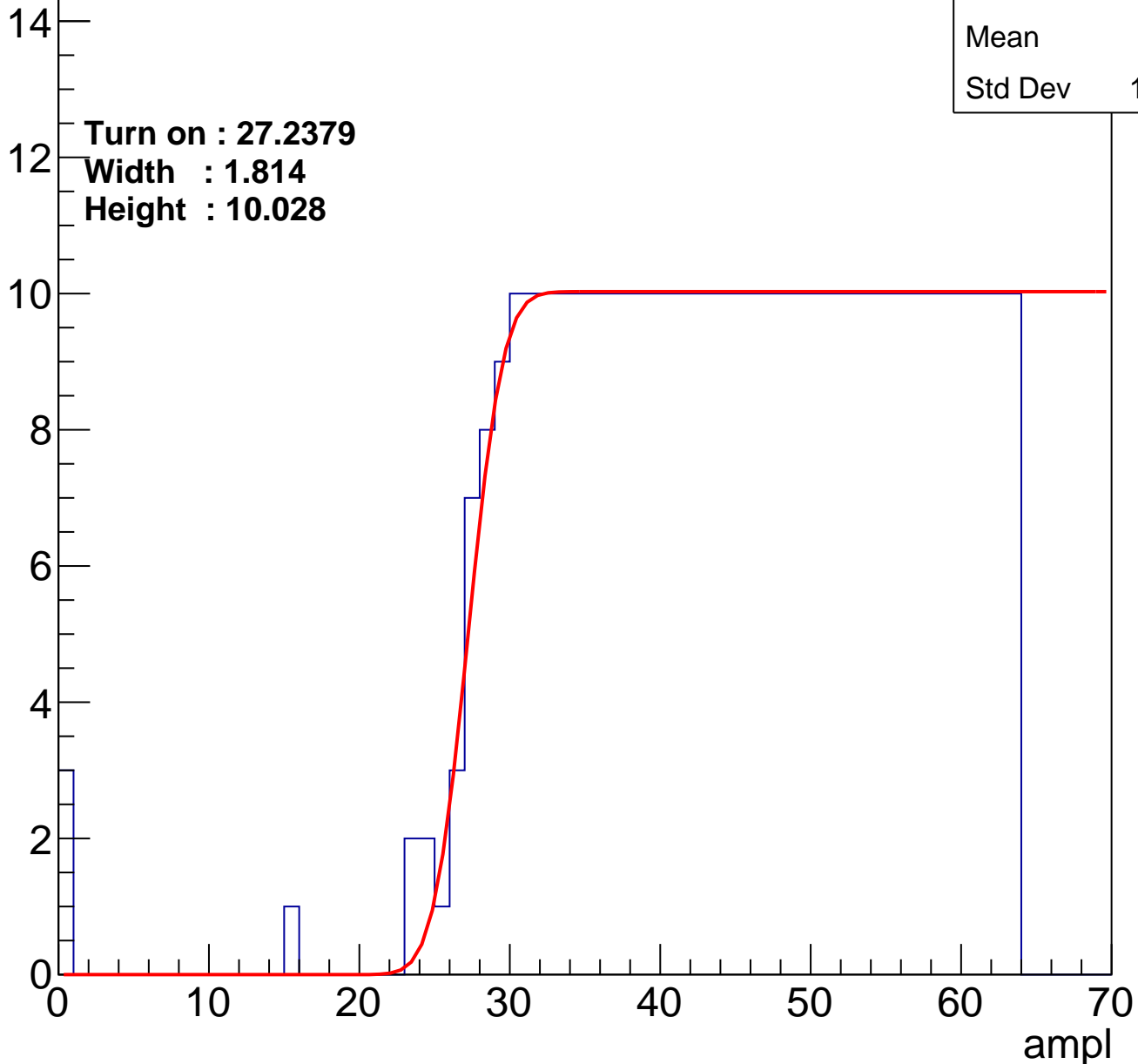
Entries	376
Mean	44.4
Std Dev	11.59

Turn on : 27.2379

Width : 1.814

Height : 10.028

Entry



B1L102S, U24-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43.06
Std Dev	12.17

Turn on : 24.0356

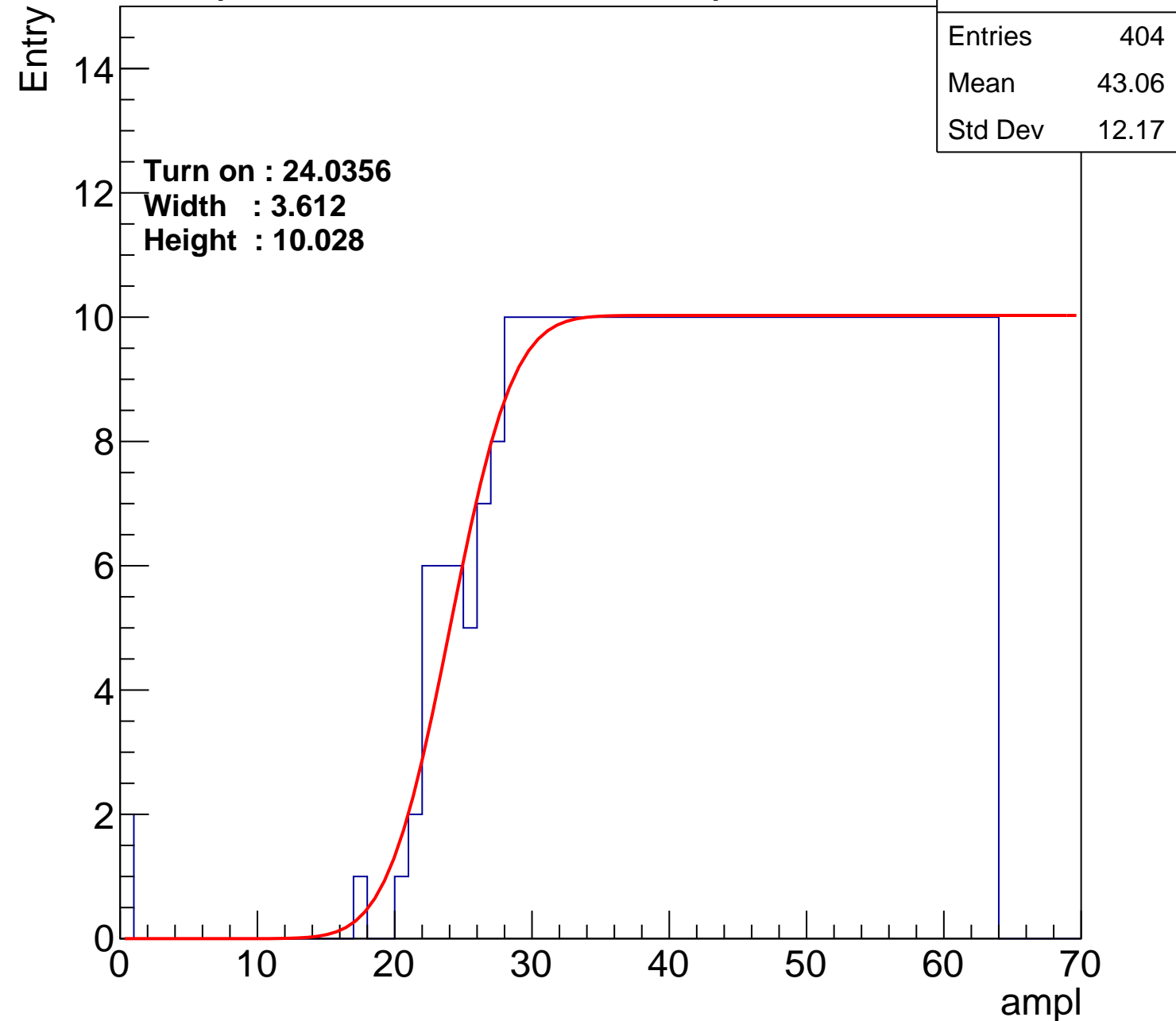
Width : 3.612

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch119

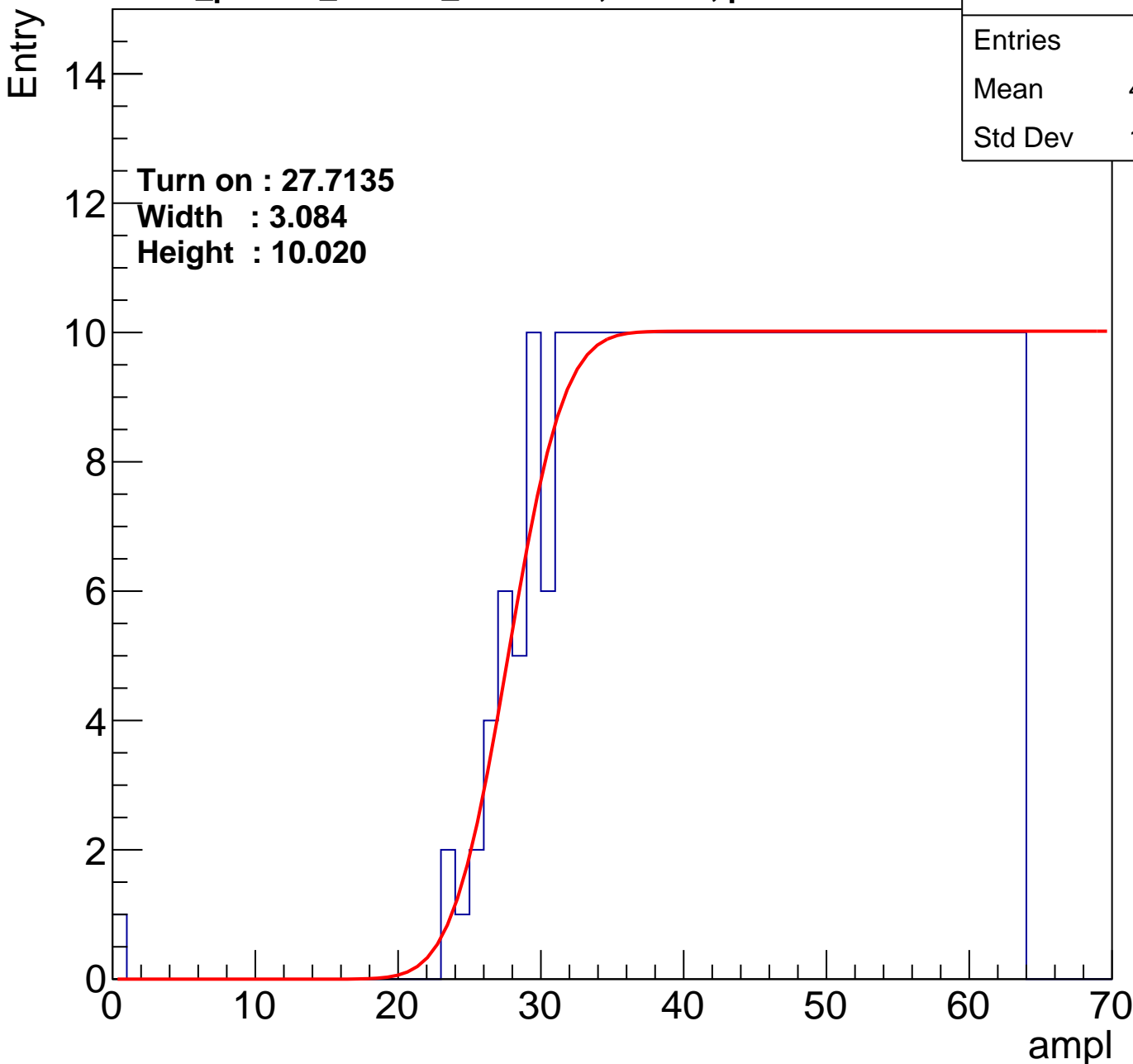
calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.98
Std Dev	10.98

Turn on : 27.7135

Width : 3.084

Height : 10.020



B1L102S, U24-ch120

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.37
Std Dev	11.52

Turn on : 26.8161

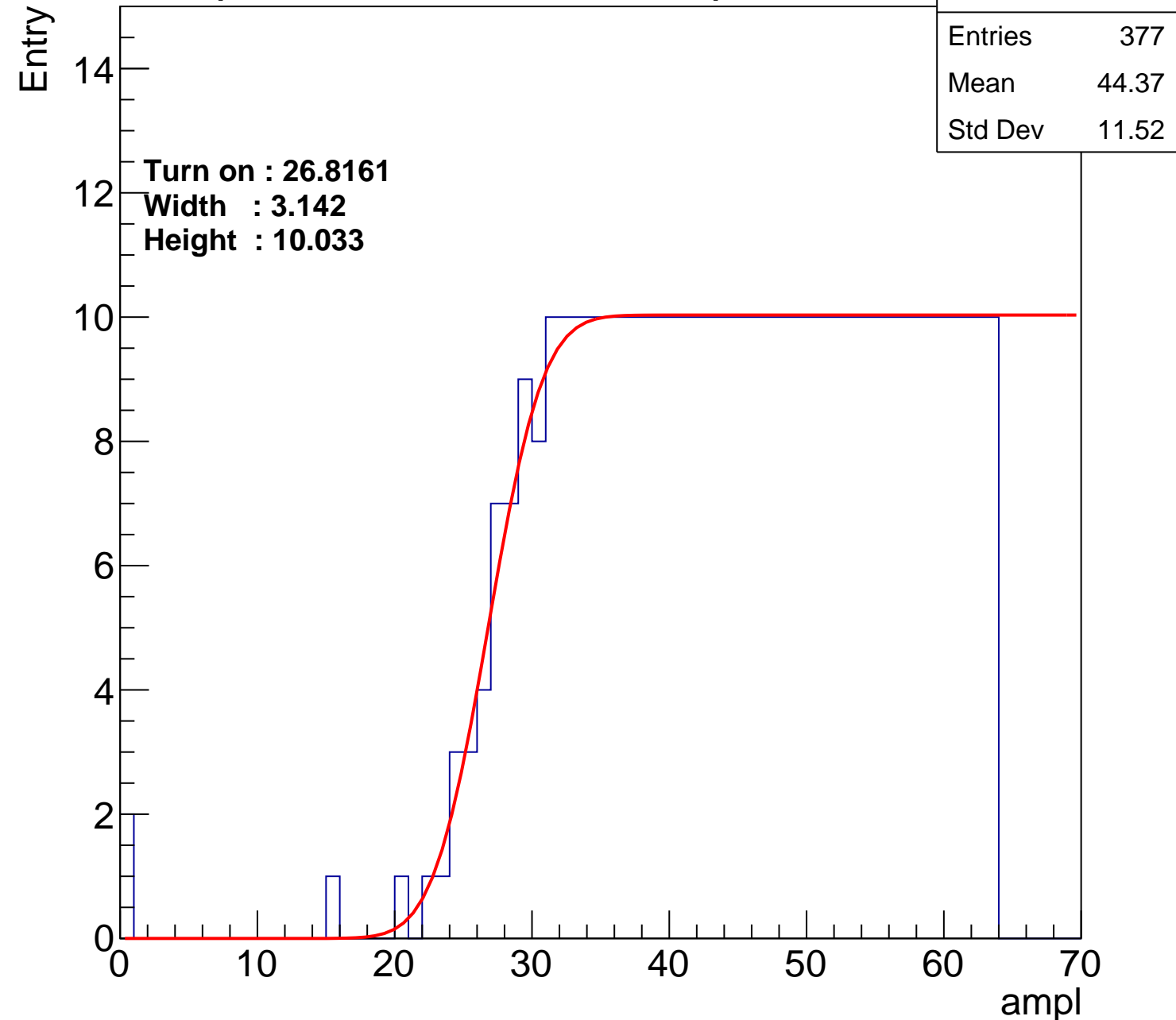
Width : 3.142

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.63
Std Dev	11.63

Turn on : 27.4981

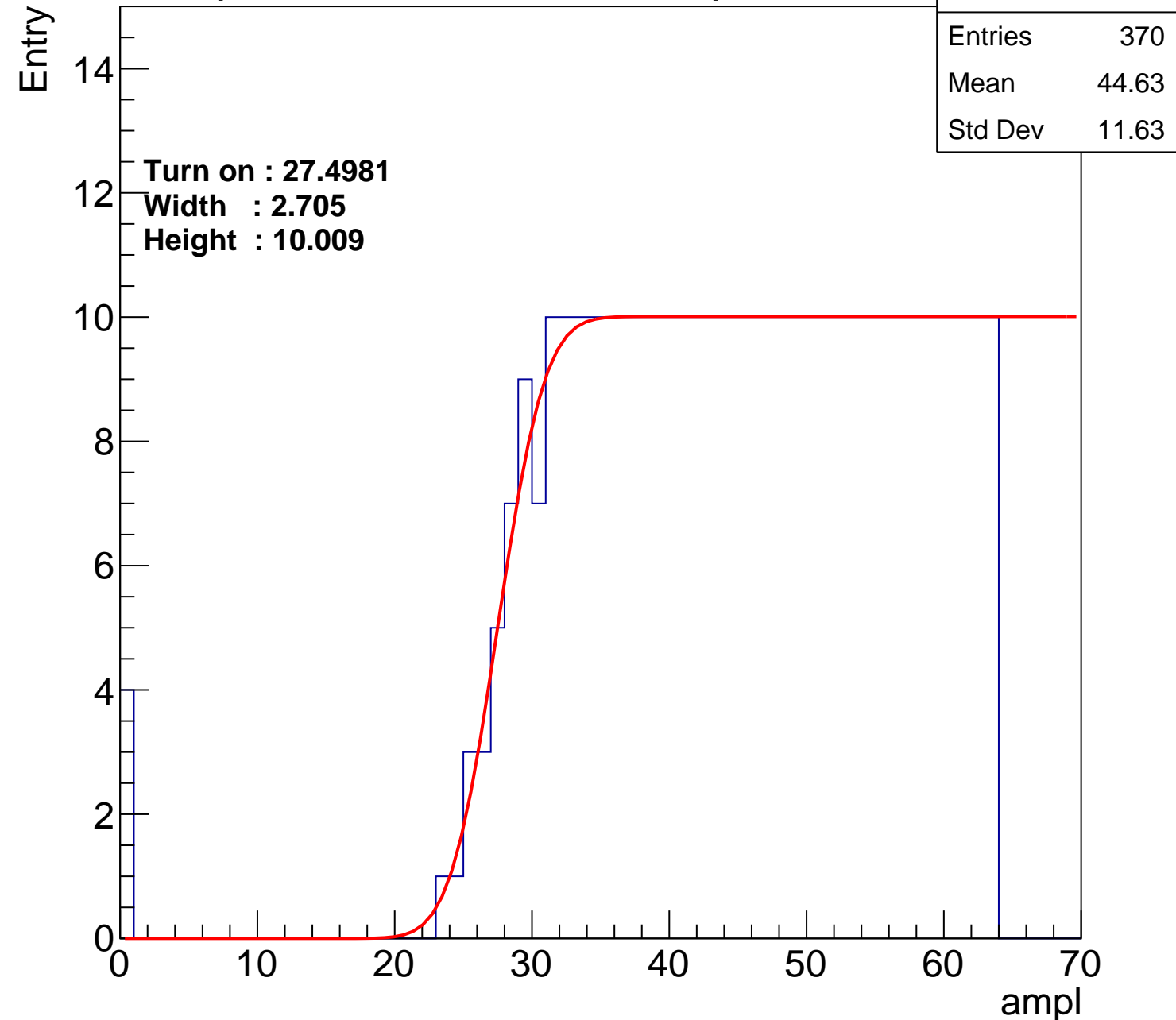
Width : 2.705

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch122

calib_packv5_042523_0143.root, FC#11, port A2

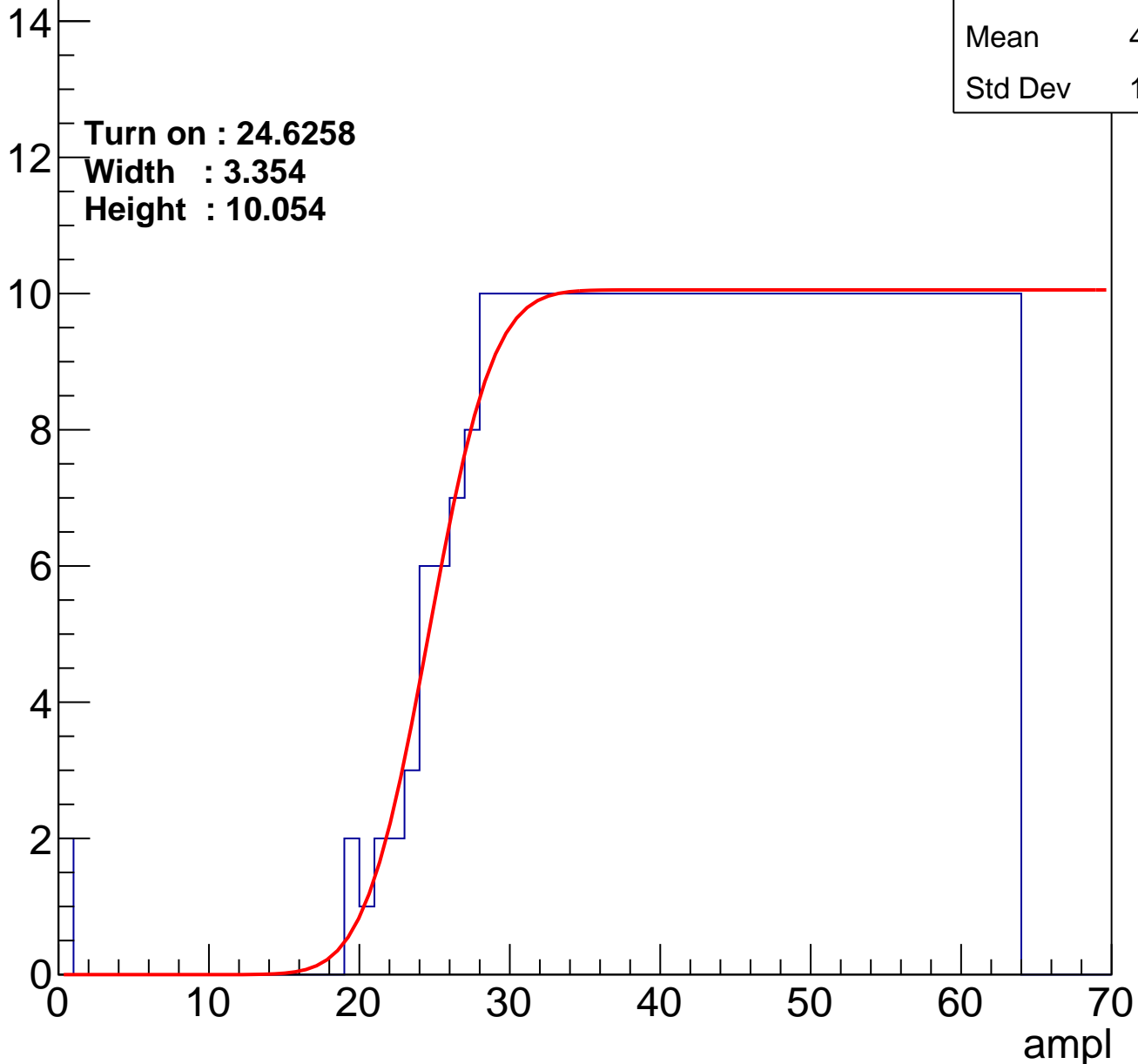
Entries	399
Mean	43.32
Std Dev	12.02

Turn on : 24.6258

Width : 3.354

Height : 10.054

Entry



B1L102S, U24-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.76
Std Dev	11.96

Turn on : 27.0337

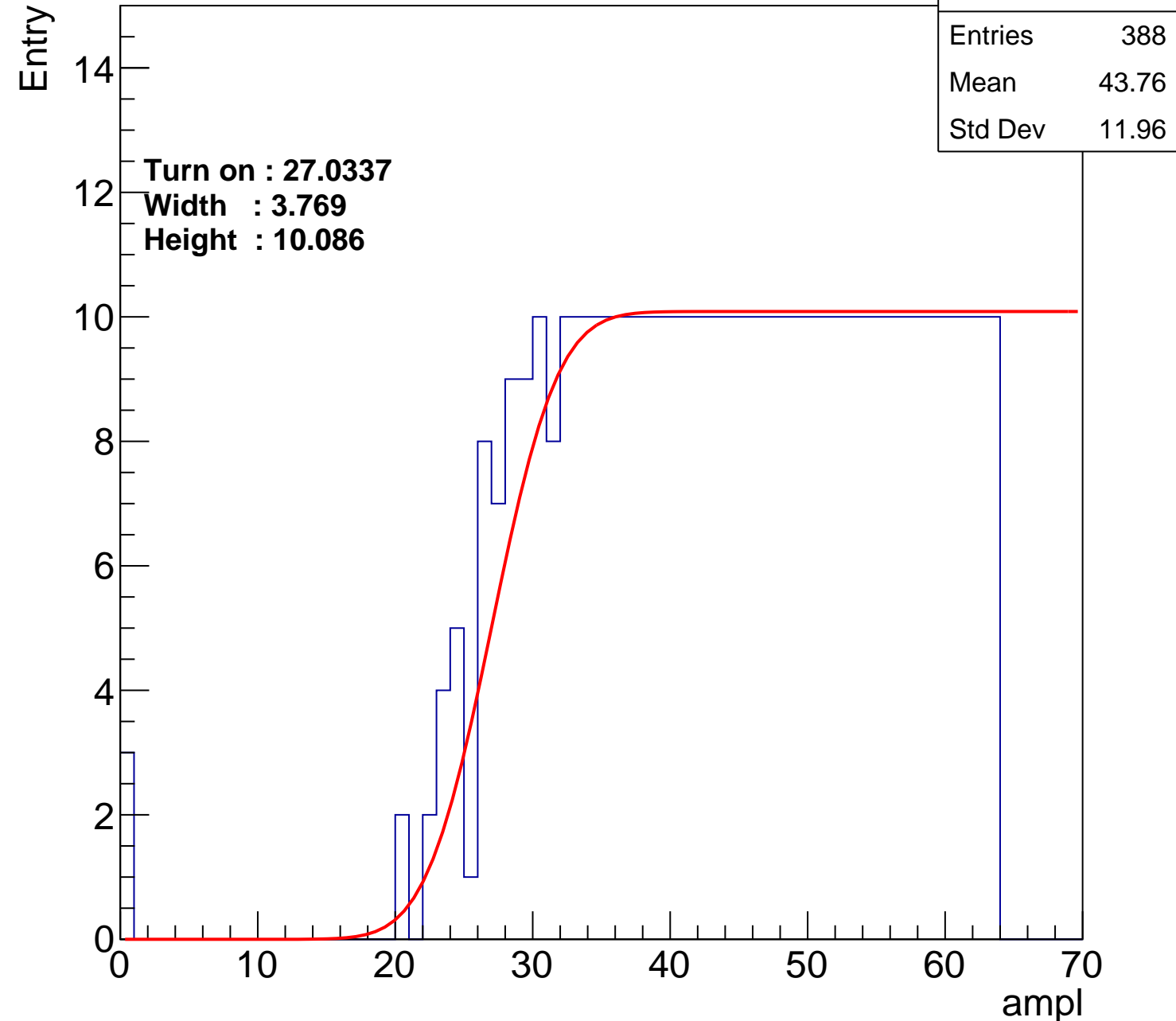
Width : 3.769

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch124

calib_packv5_042523_0143.root, FC#11, port A2

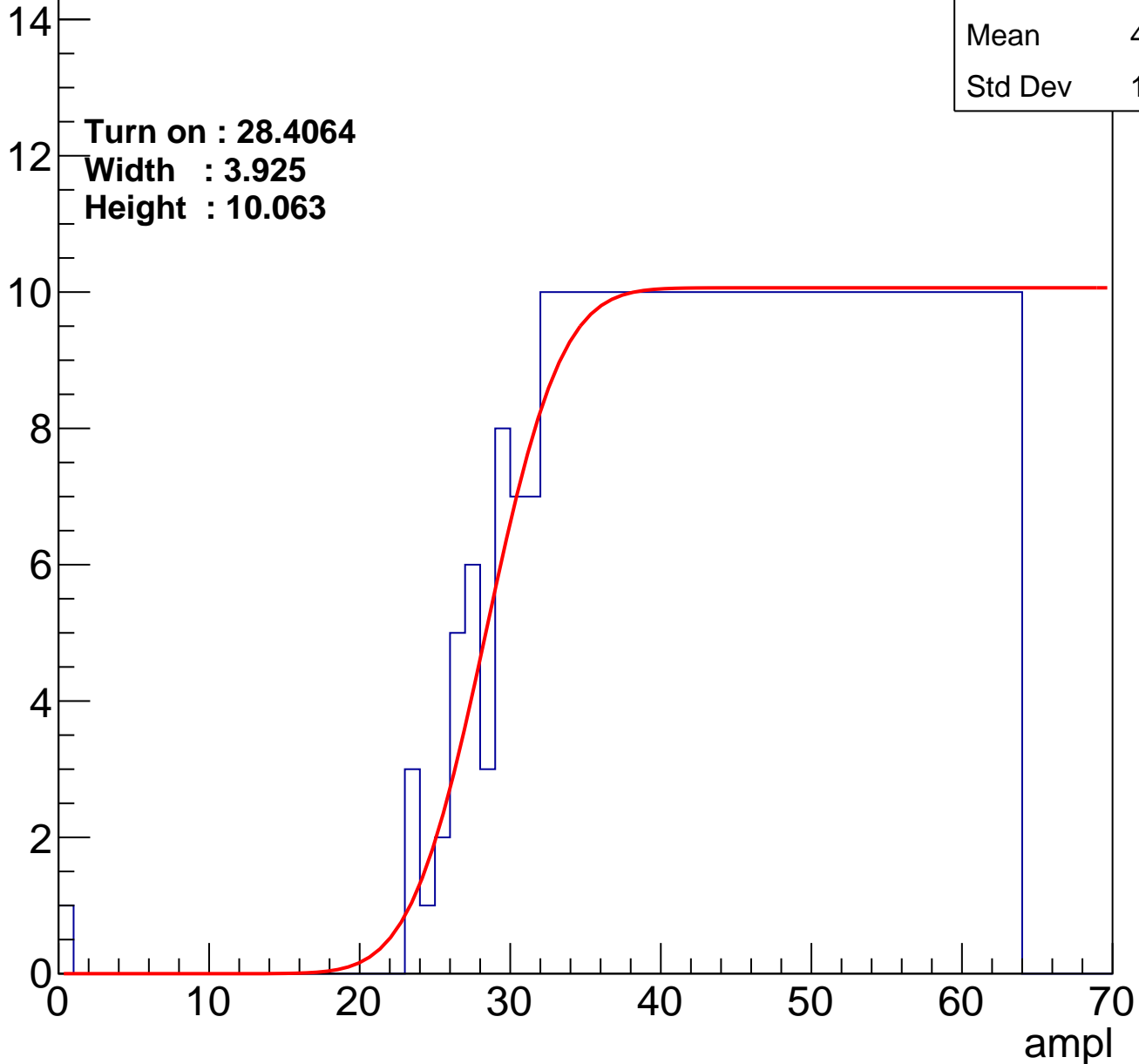
Entries	363
Mean	45.12
Std Dev	10.96

Turn on : 28.4064

Width : 3.925

Height : 10.063

Entry



B1L102S, U24-ch125

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.04
Std Dev	11.9

Turn on : 26.1364

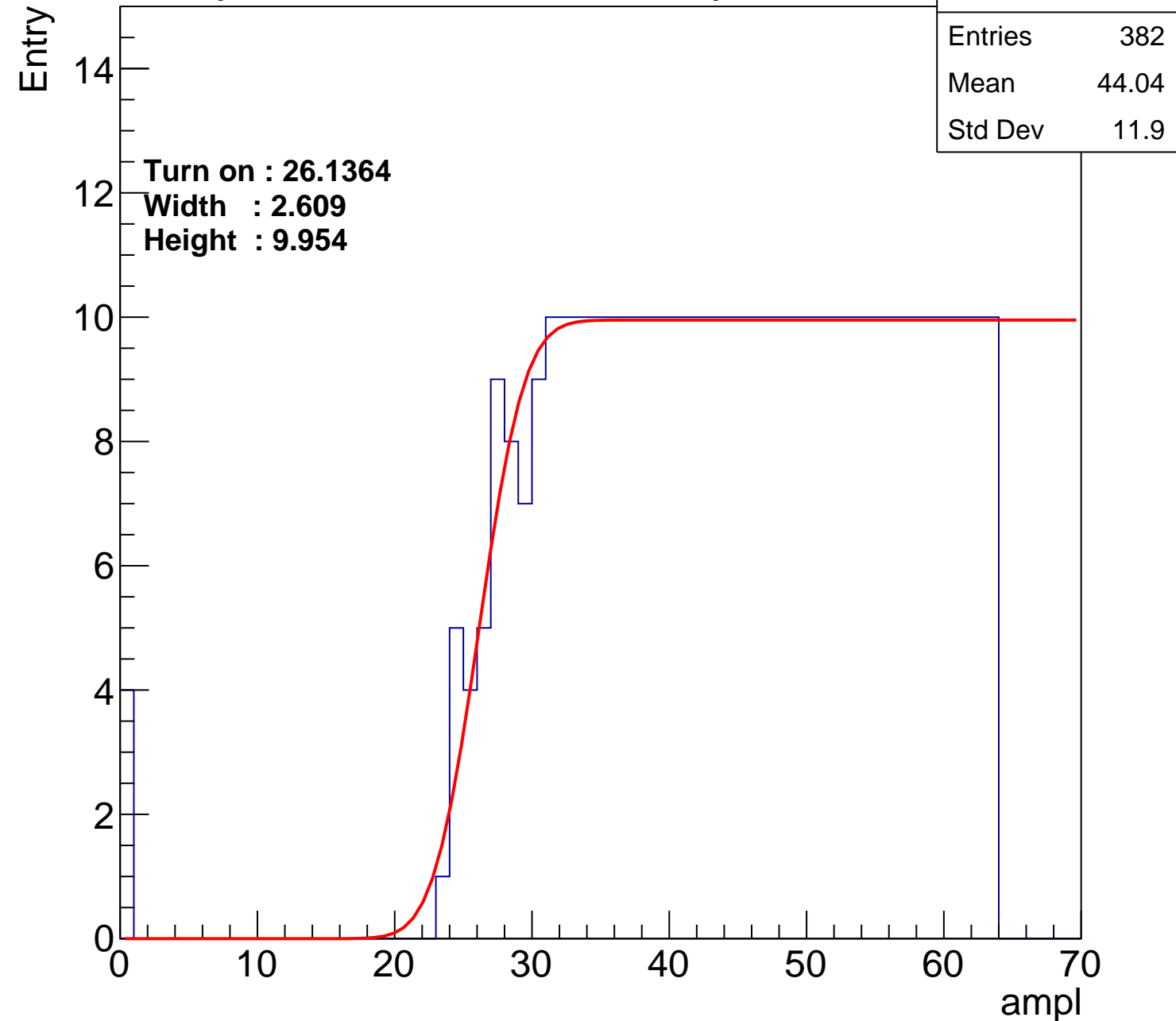
Width : 2.609

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.82
Std Dev	11.93

Turn on : 27.8398

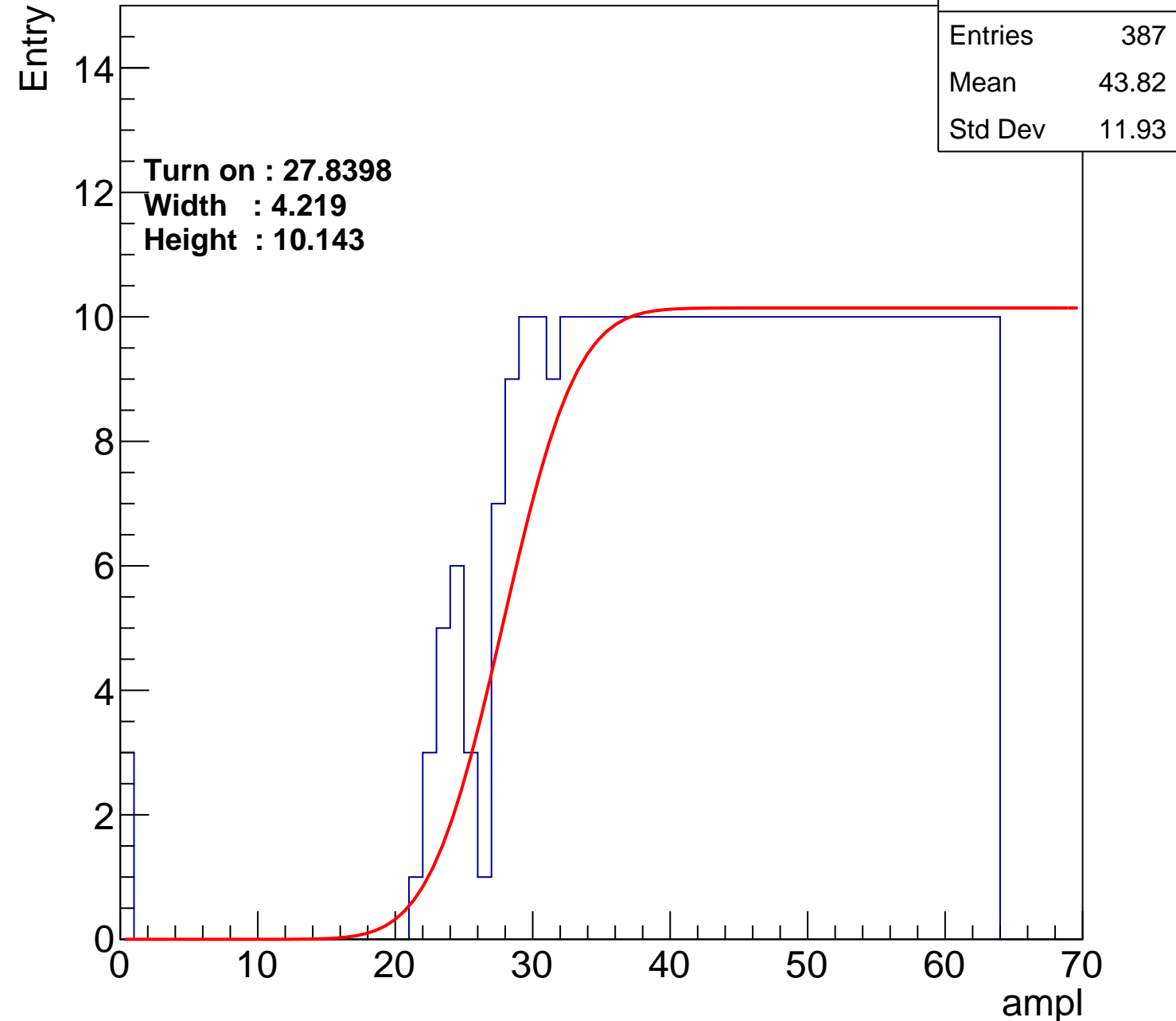
Width : 4.219

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.44
Std Dev	12.29

Turn on : 25.5967

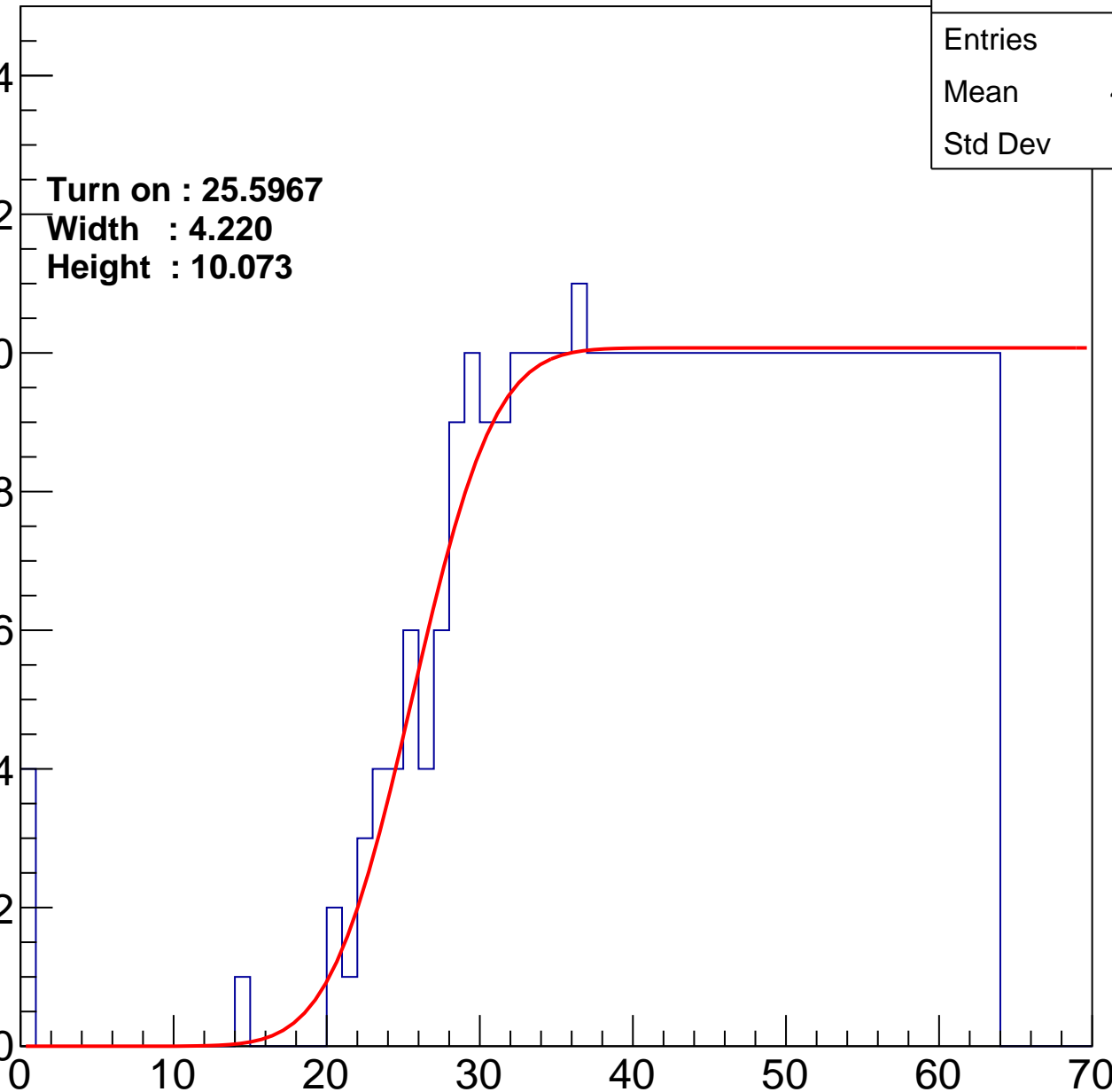
Width : 4.220

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U24-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.44
Std Dev	12.29

Turn on : 25.5967

Width : 4.220

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl

