

B1L103S, U11-ch0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.06
Std Dev	12.5

Turn on : 24.8186

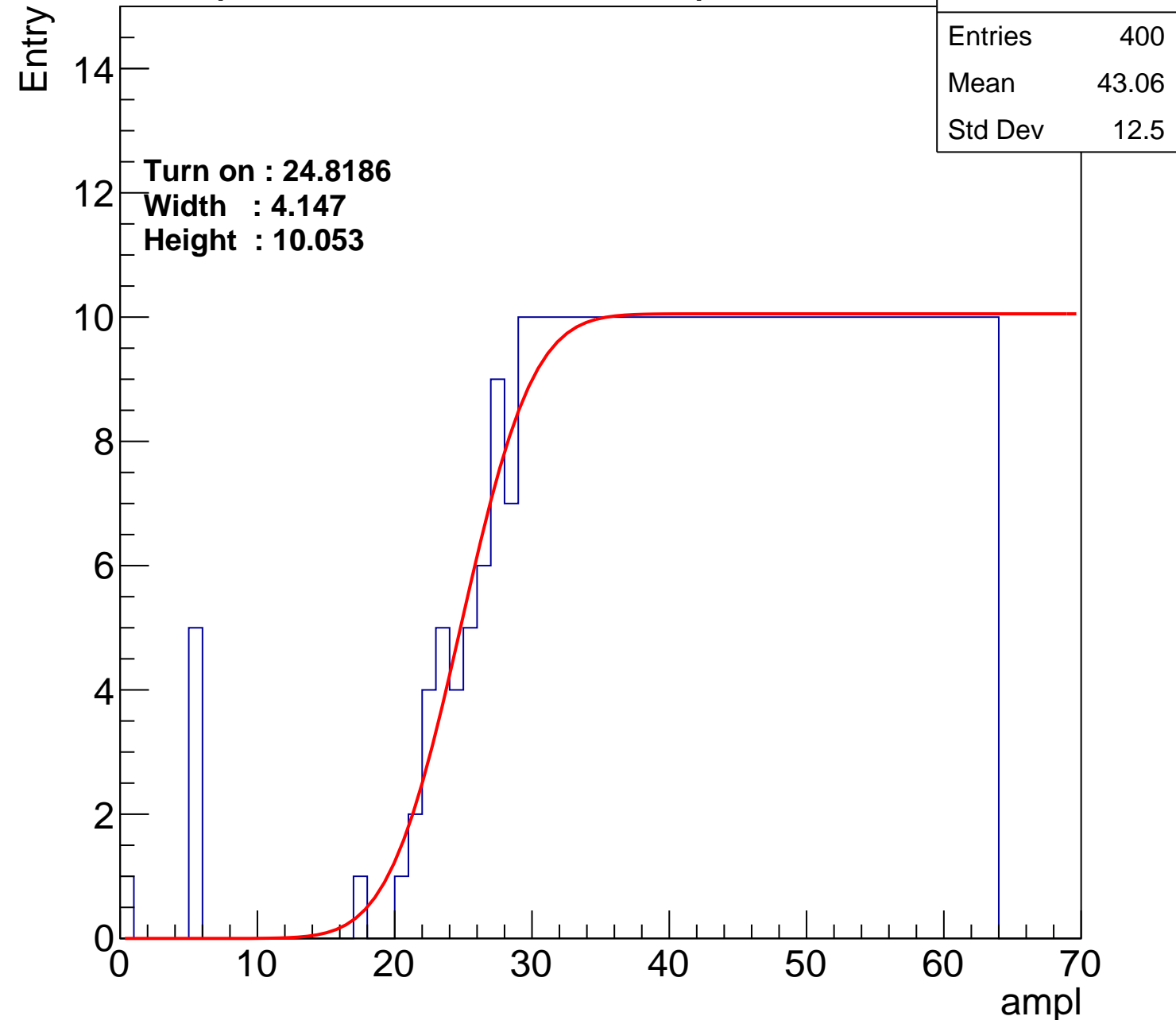
Width : 4.147

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch1

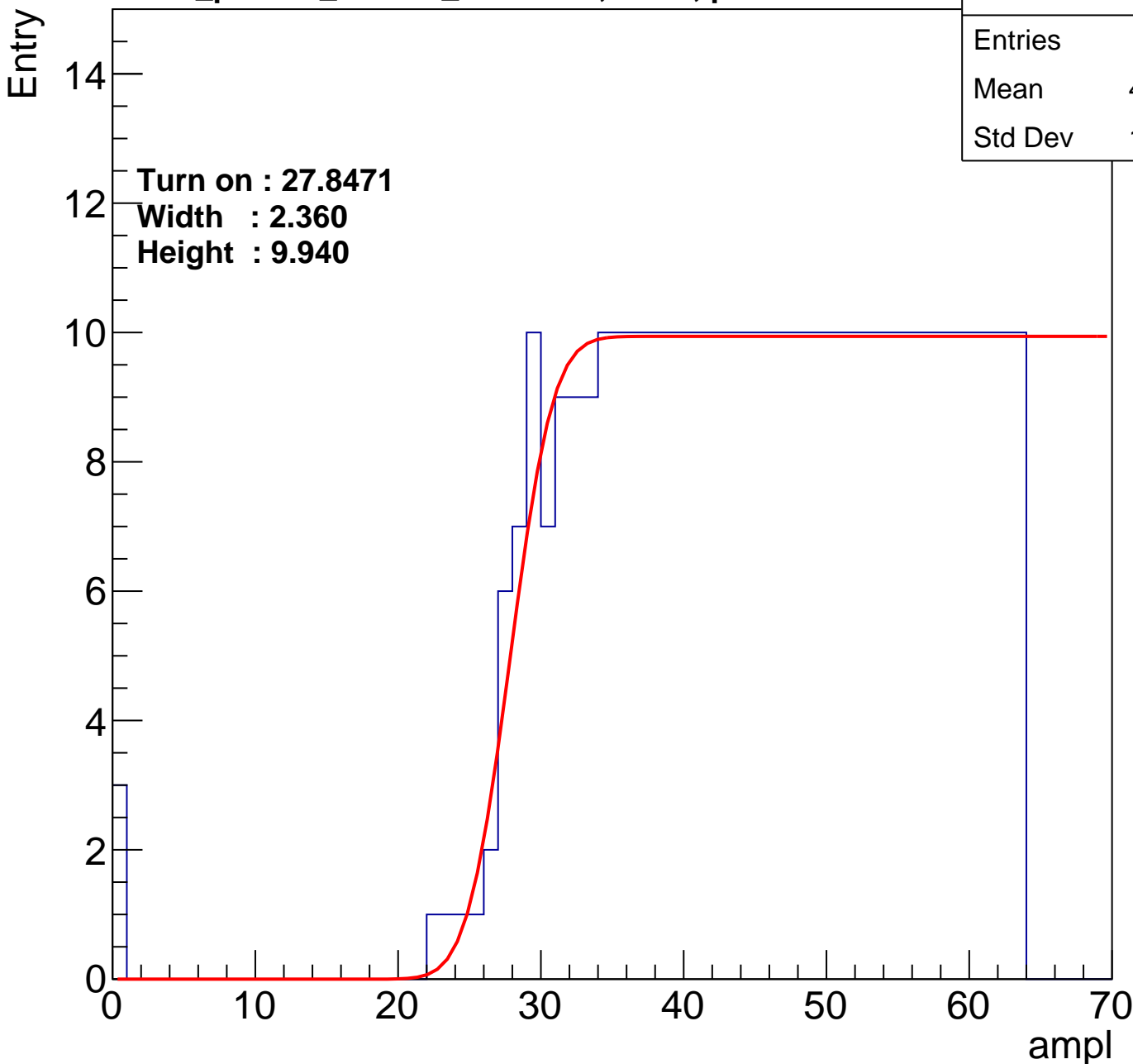
calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.8471

Width : 2.360

Height : 9.940



B1L103S, U11-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.89
Std Dev	12.04

Turn on : 26.3674

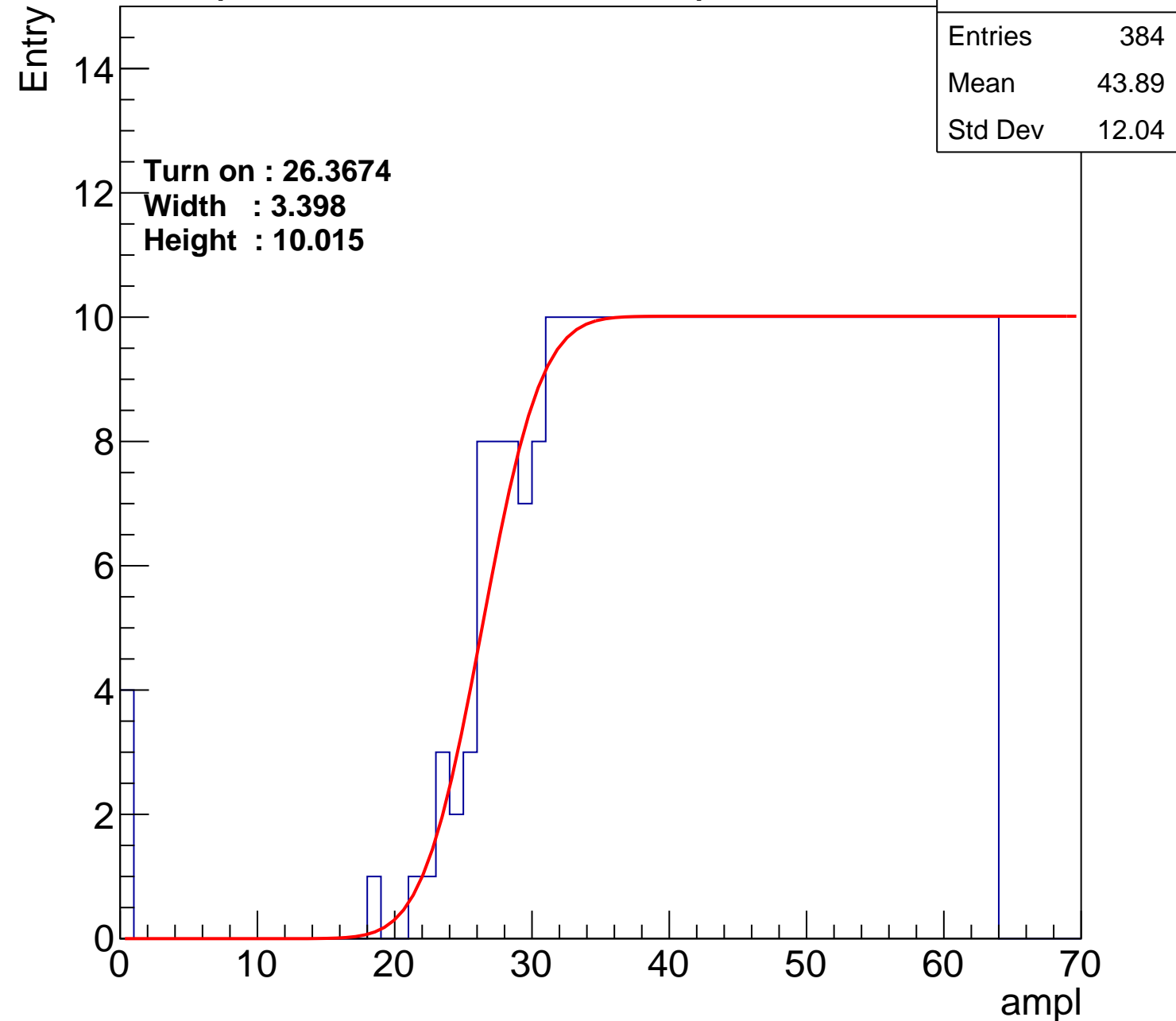
Width : 3.398

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch3

calib_packv5_042523_0143.root, FC#7, port C2

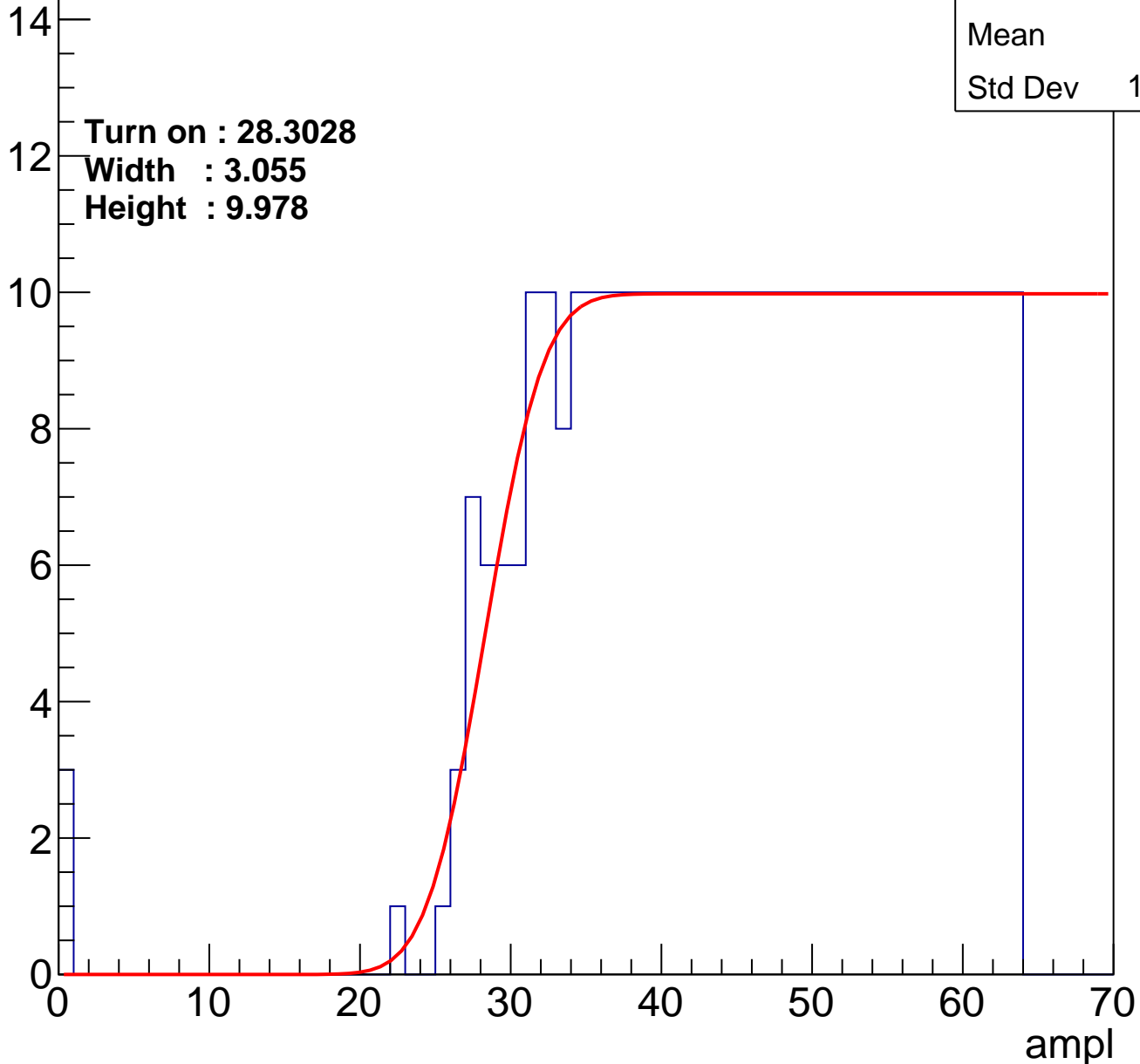
Entries	361
Mean	45.1
Std Dev	11.28

Turn on : 28.3028

Width : 3.055

Height : 9.978

Entry



B1L103S, U11-ch4

calib_packv5_042523_0143.root, FC#7, port C2

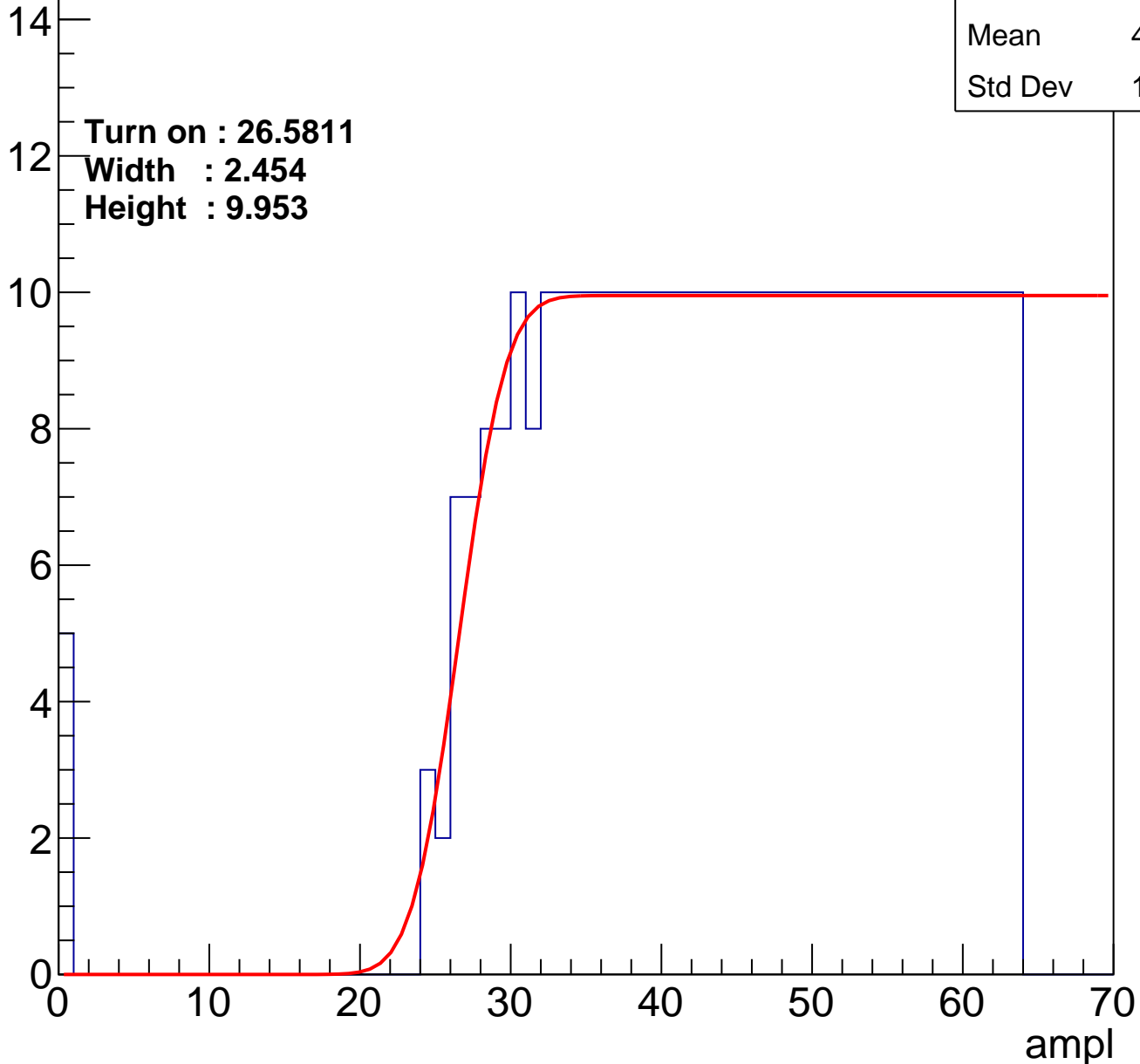
Entries	378
Mean	44.17
Std Dev	11.98

Turn on : 26.5811

Width : 2.454

Height : 9.953

Entry



B1L103S, U11-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	350
Mean	45.66
Std Dev	10.98

Turn on : 29.0015

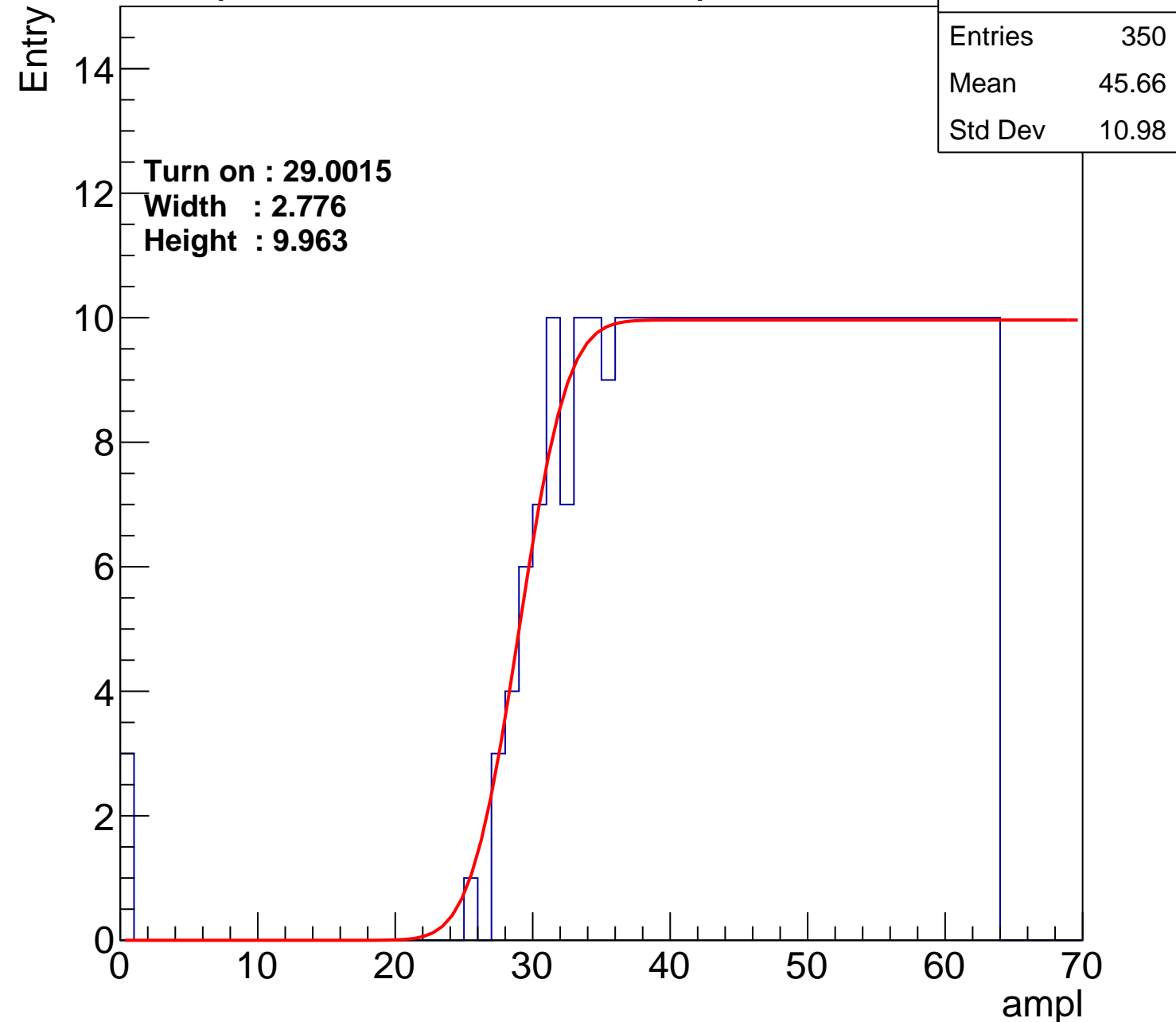
Width : 2.776

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 26.9010

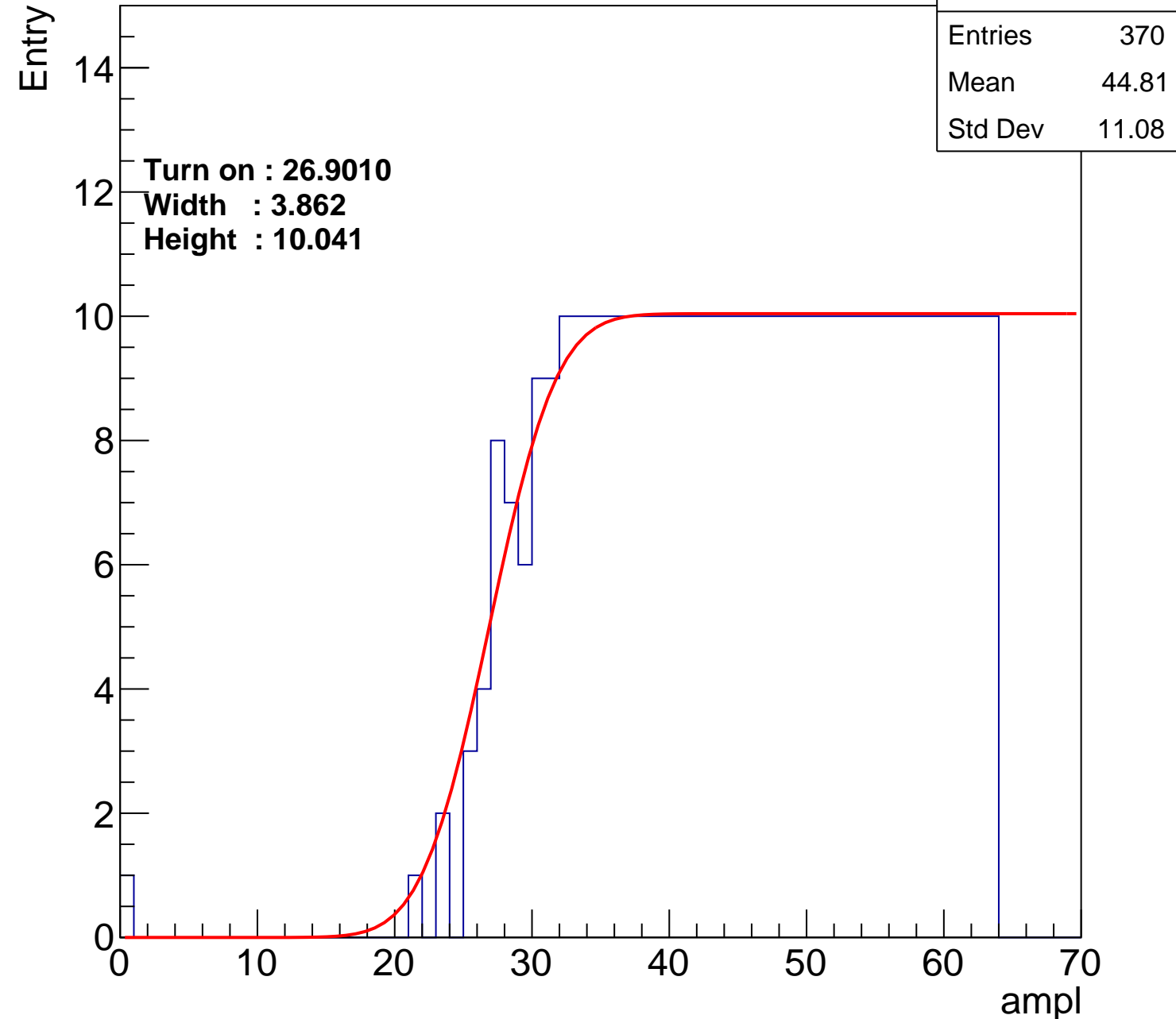
Width : 3.862

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.67
Std Dev	12.06

Turn on : 25.7226

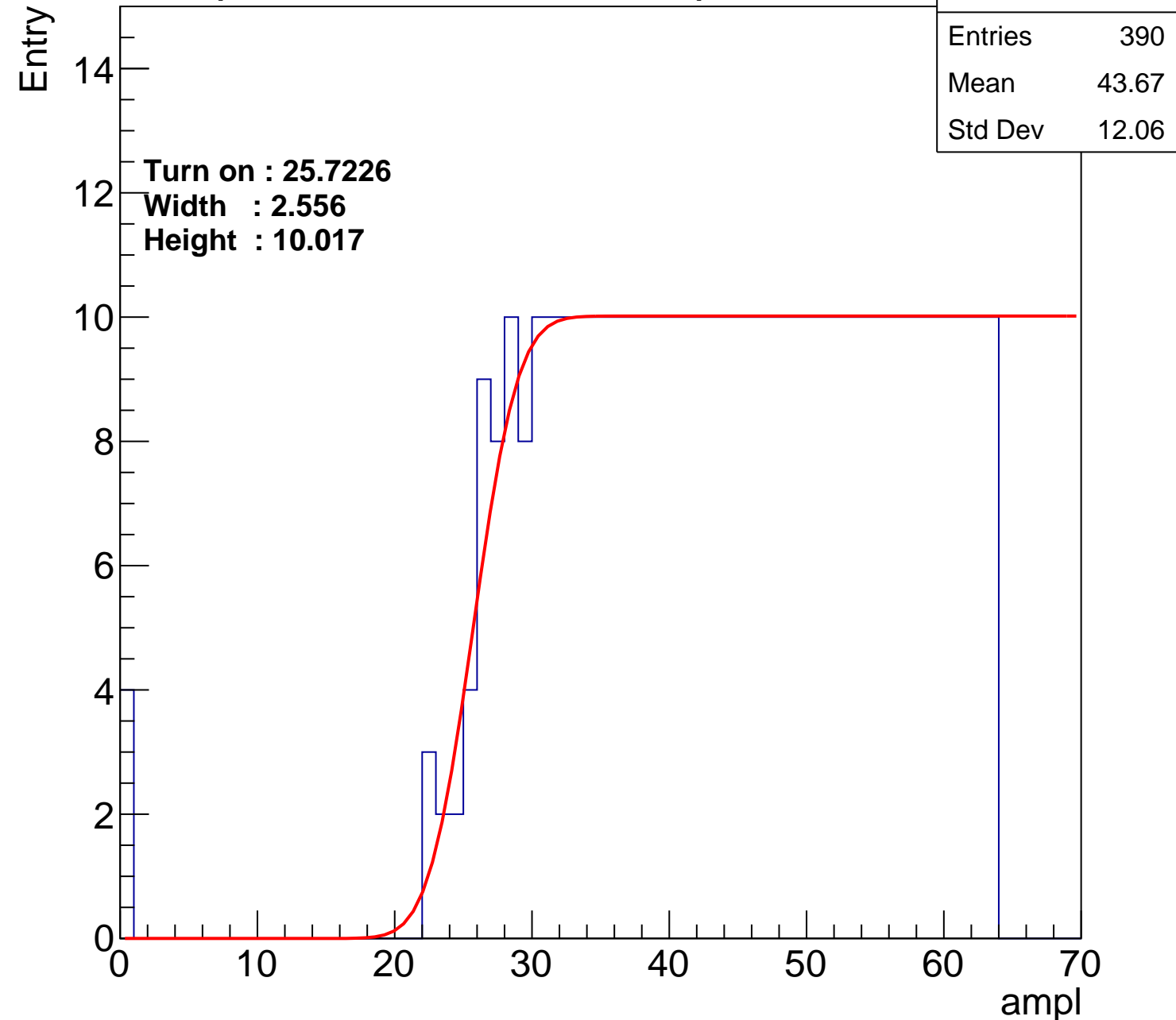
Width : 2.556

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.53
Std Dev	11.43

Turn on : 27.6623

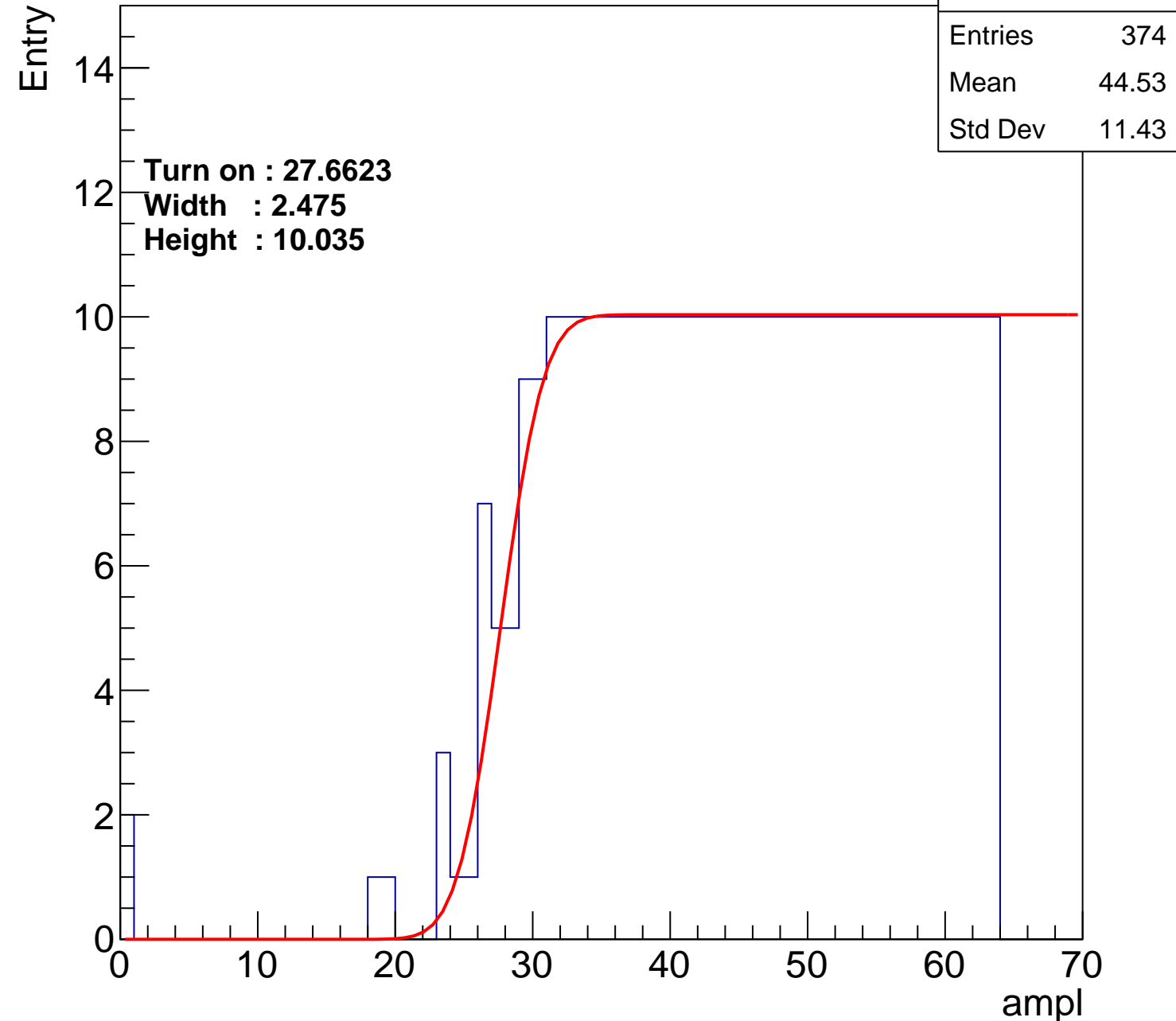
Width : 2.475

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.33
Std Dev	10.79

Turn on : 28.2445

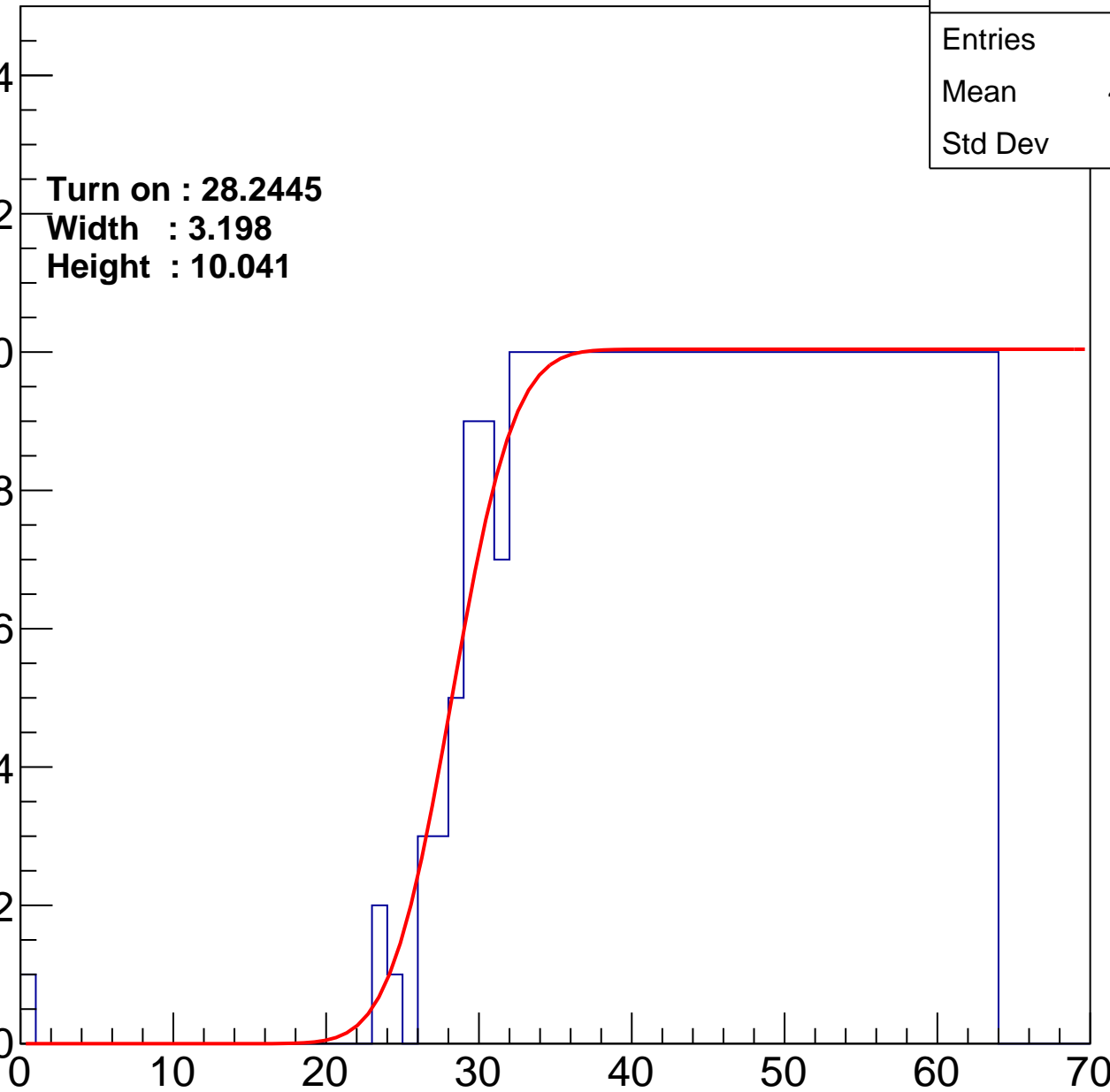
Width : 3.198

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.46
Std Dev	11.6

Turn on : 27.5898

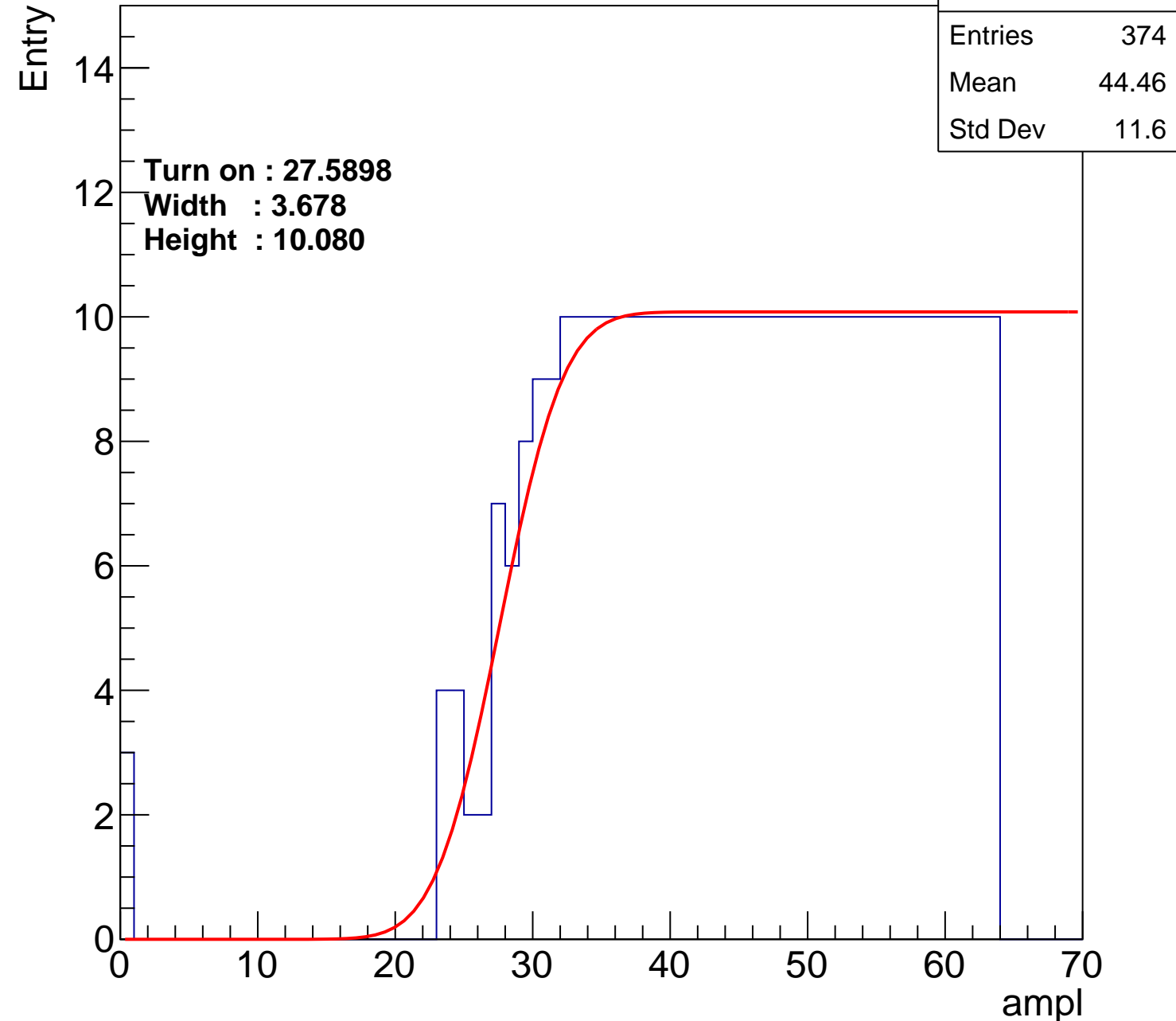
Width : 3.678

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch11

calib_packv5_042523_0143.root, FC#7, port C2

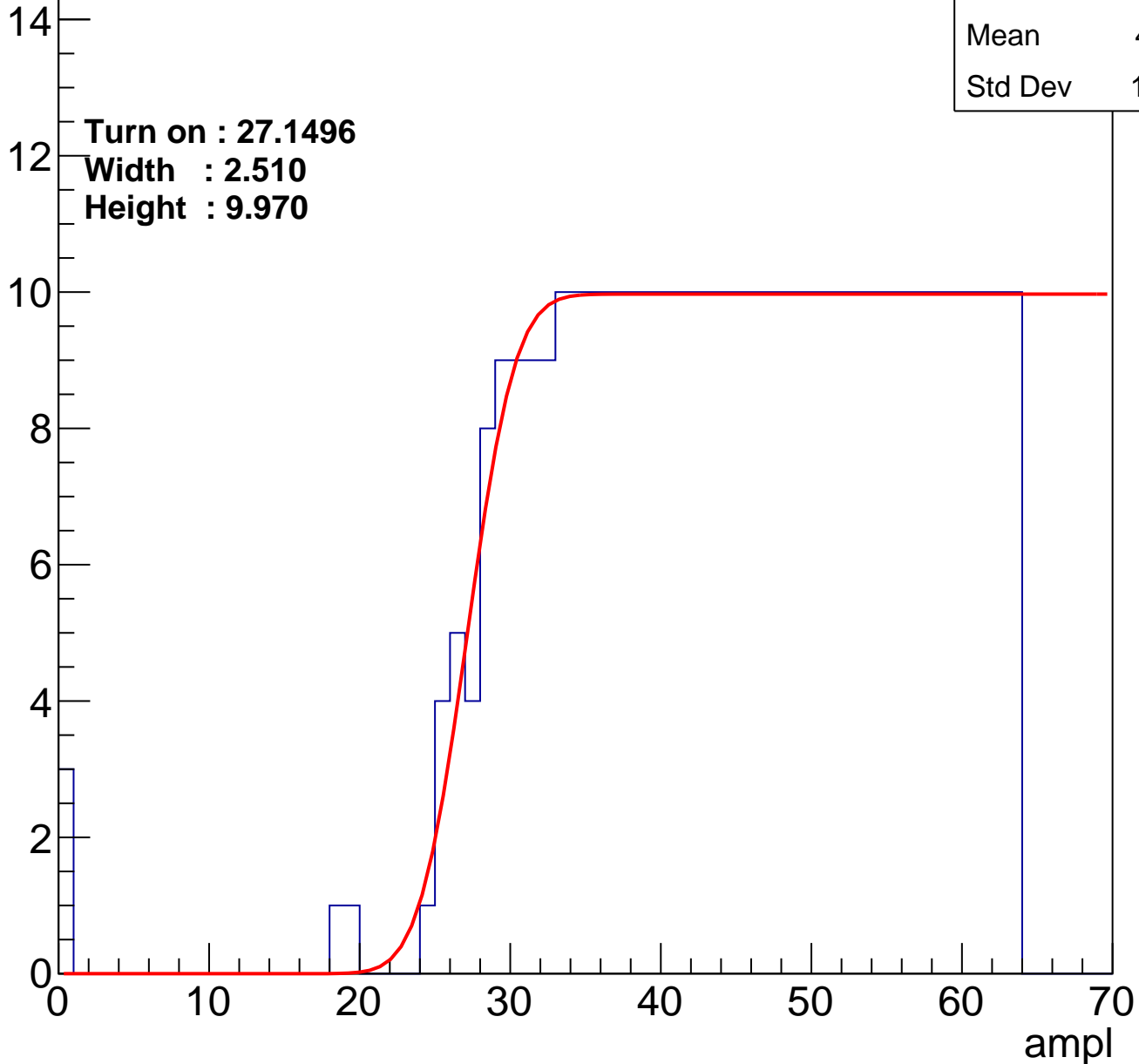
Entries	373
Mean	44.51
Std Dev	11.58

Turn on : 27.1496

Width : 2.510

Height : 9.970

Entry



B1L103S, U11-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.06
Std Dev	11.3

Turn on : 28.8072

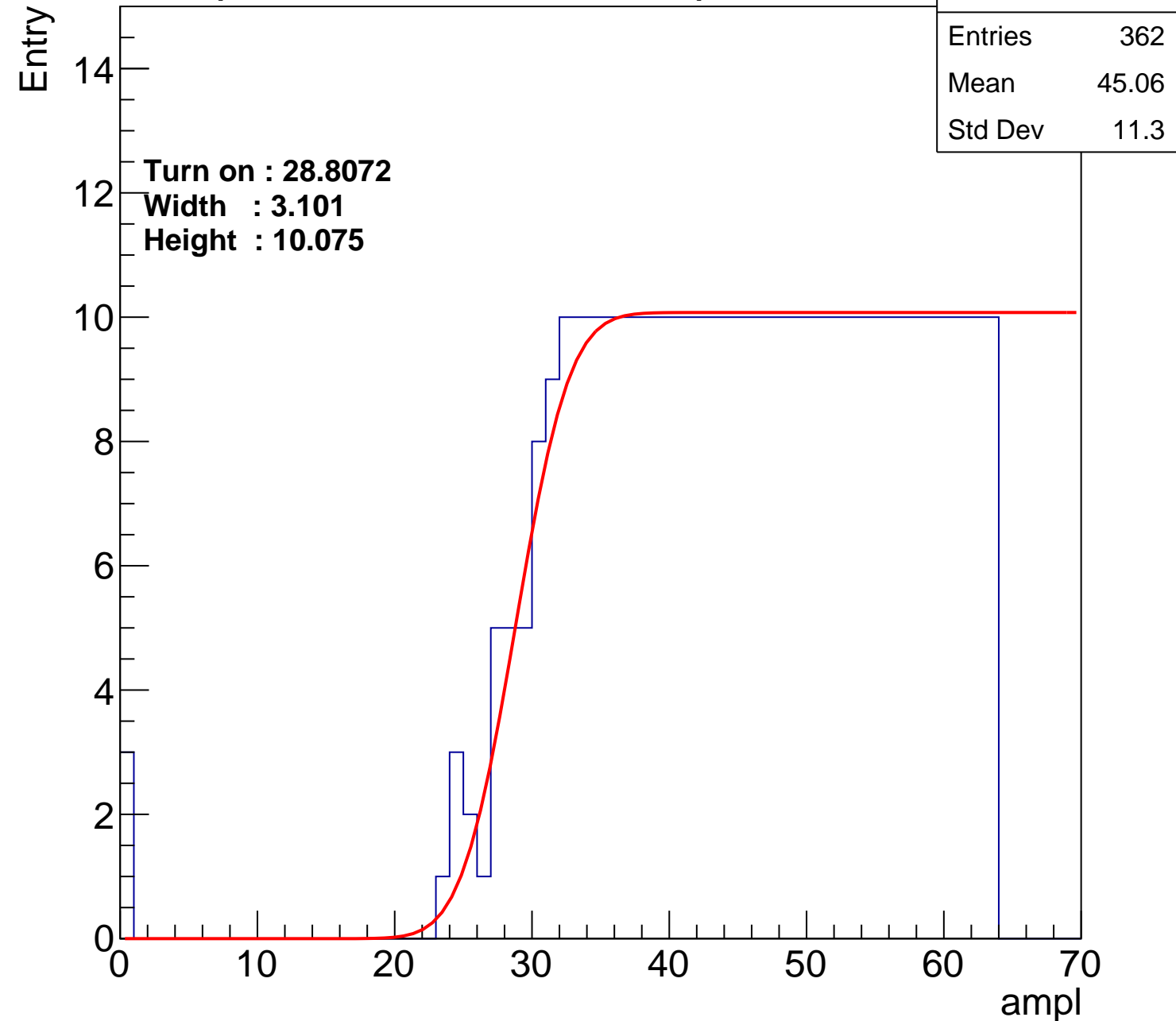
Width : 3.101

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch13

calib_packv5_042523_0143.root, FC#7, port C2

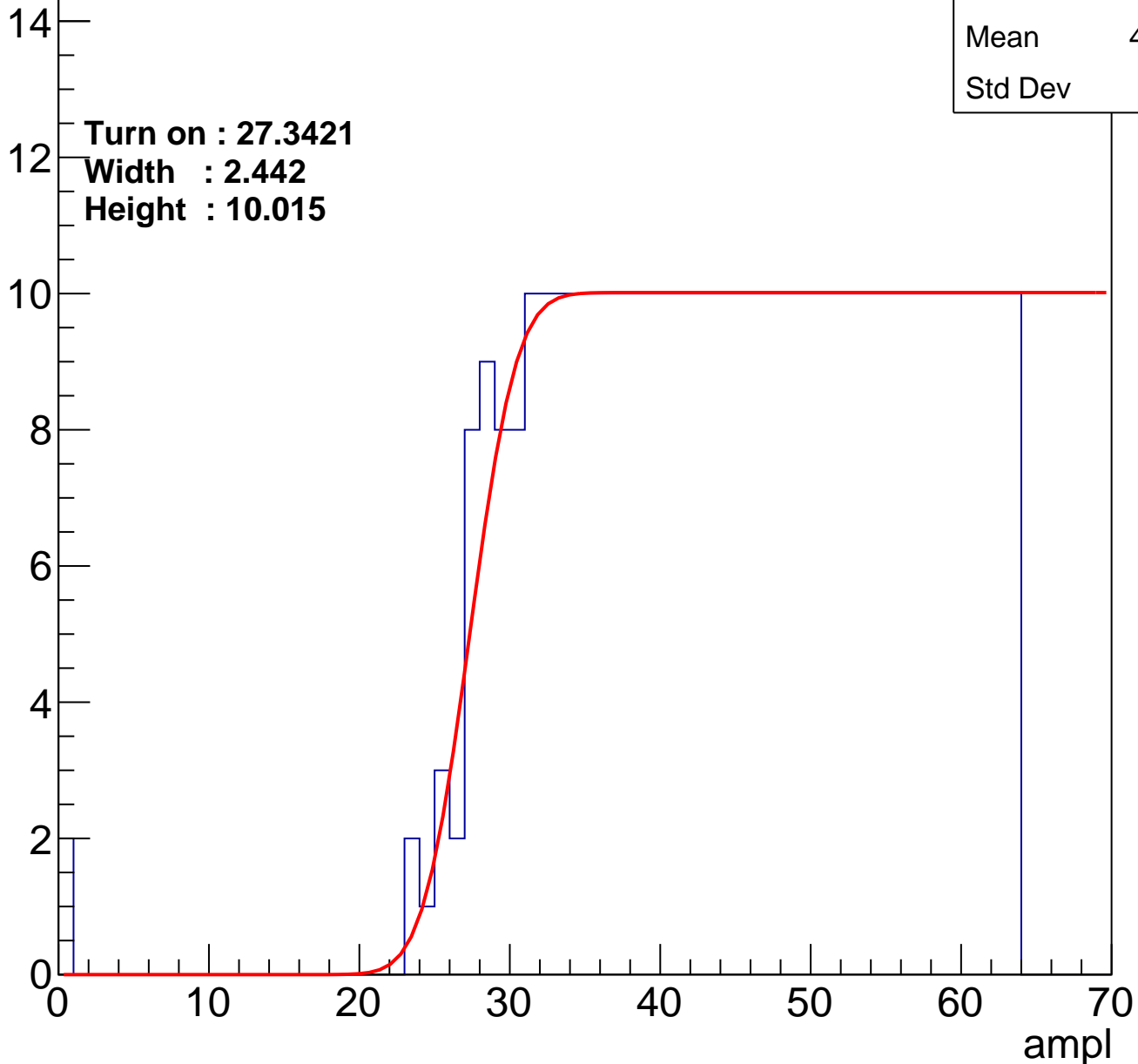
Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.3421

Width : 2.442

Height : 10.015

Entry



B1L103S, U11-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.82
Std Dev	11.41

Turn on : 28.4076

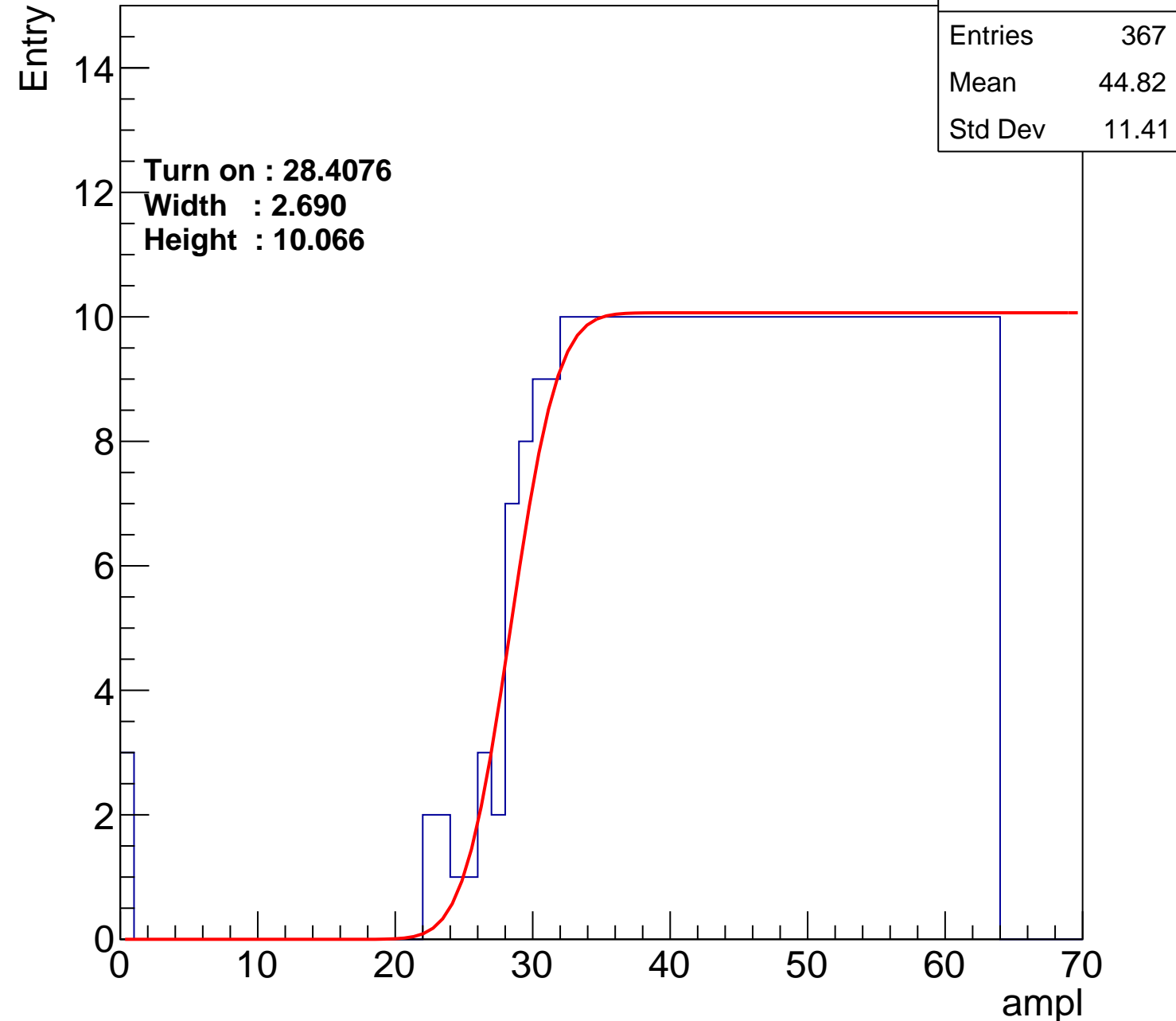
Width : 2.690

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.81
Std Dev	11.55

Turn on : 27.8088

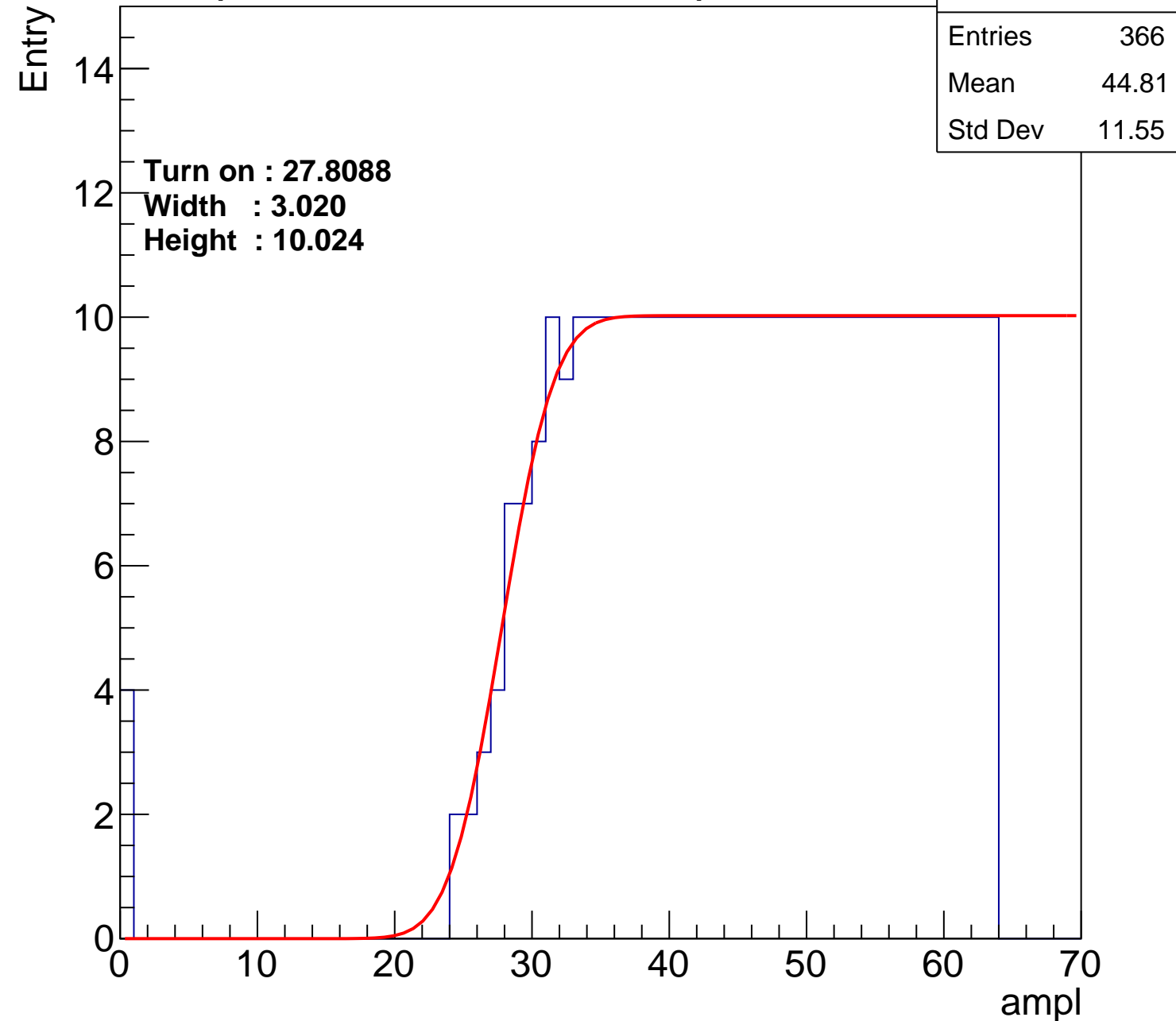
Width : 3.020

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch16

calib_packv5_042523_0143.root, FC#7, port C2

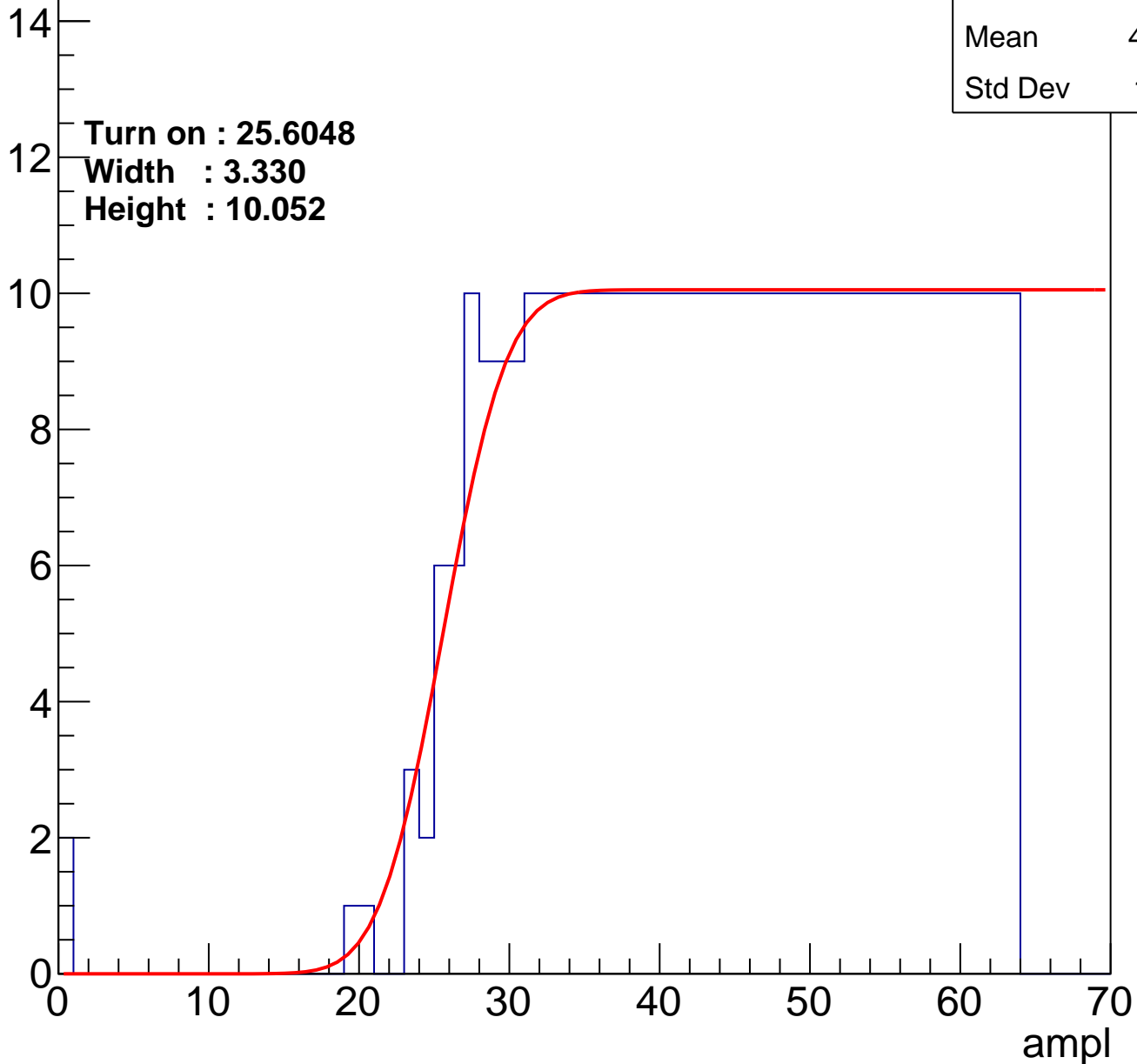
Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.6048

Width : 3.330

Height : 10.052

Entry



B1L103S, U11-ch17

calib_packv5_042523_0143.root, FC#7, port C2

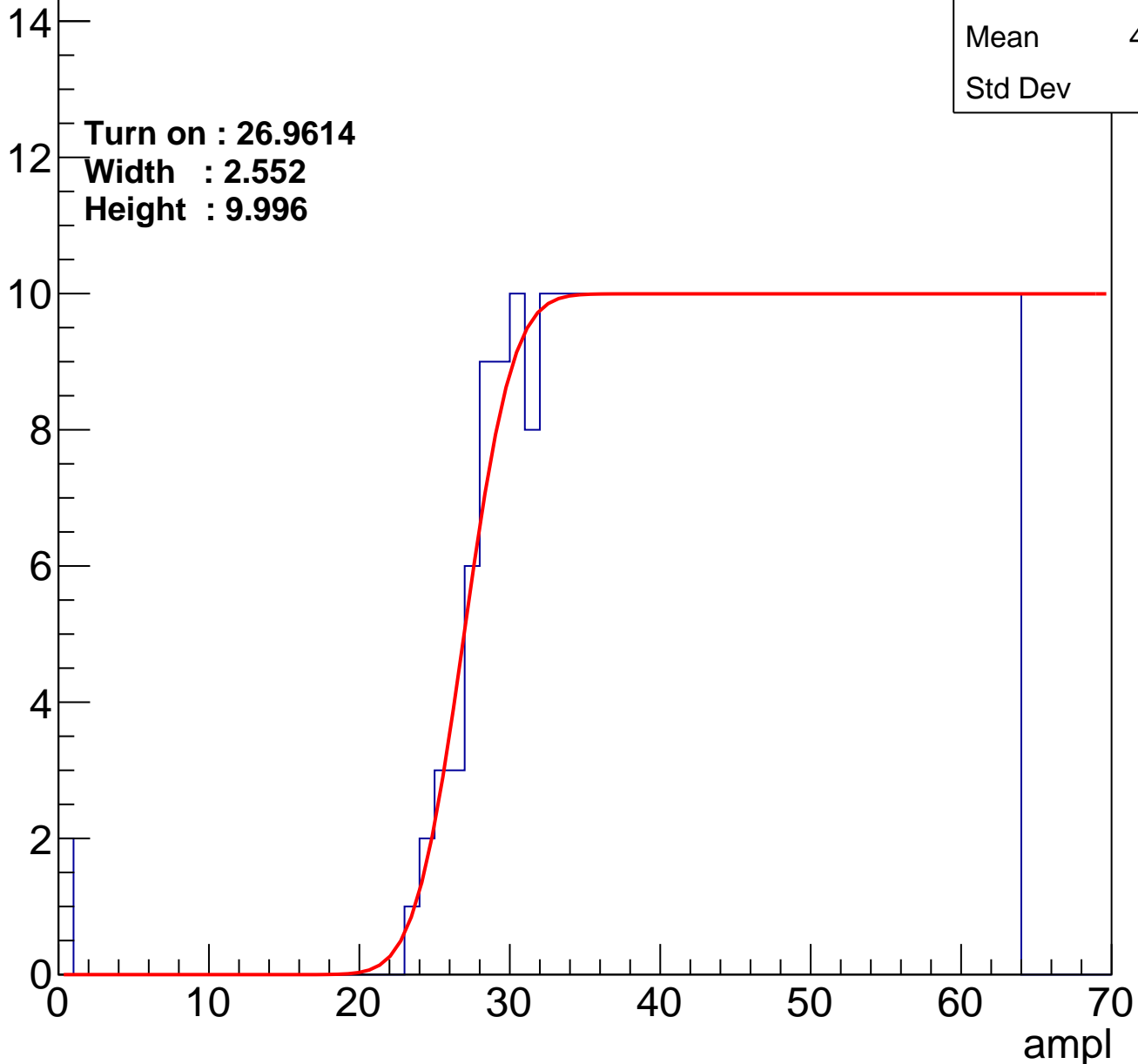
Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.9614

Width : 2.552

Height : 9.996

Entry



B1L103S, U11-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 27.9058

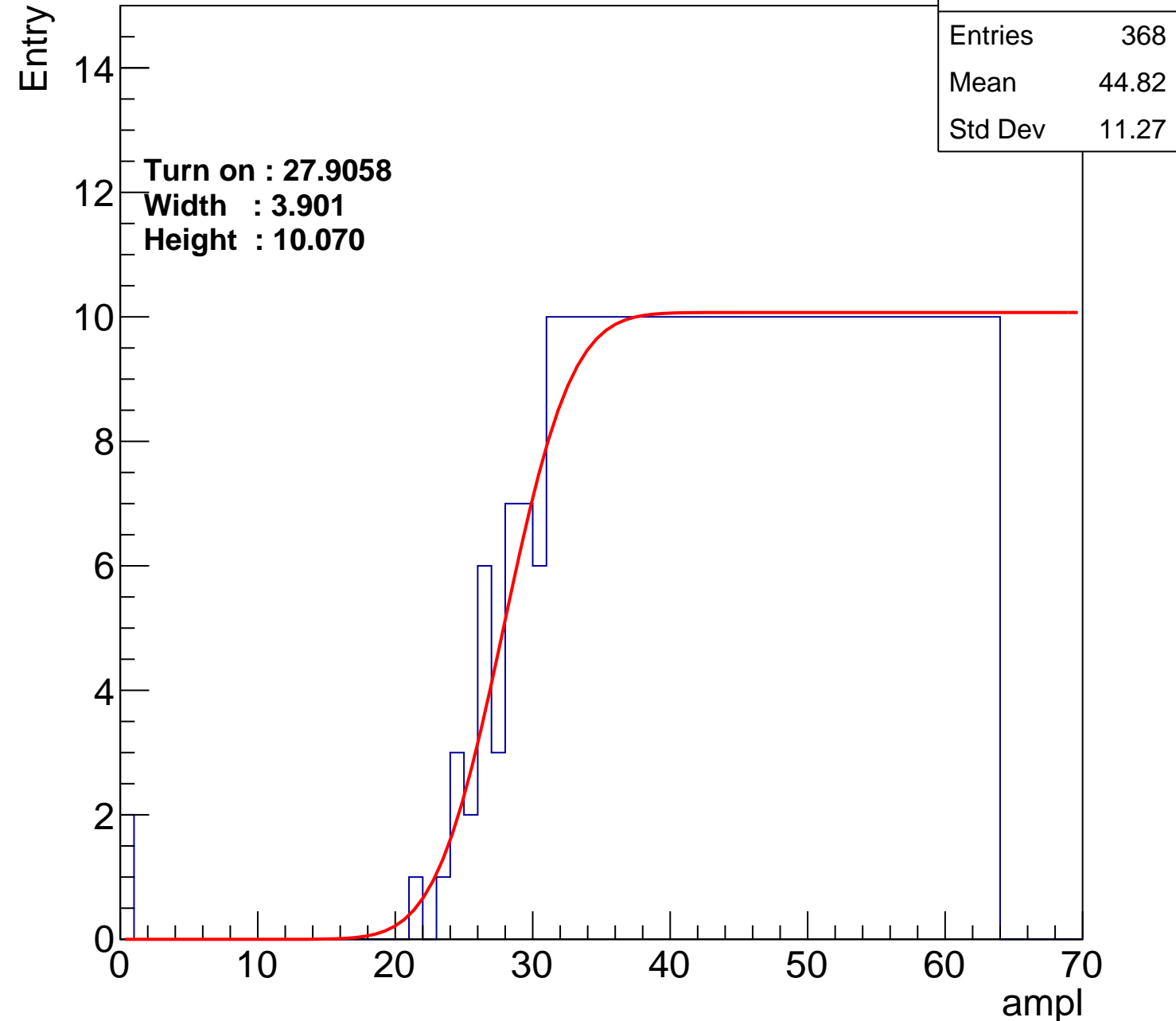
Width : 3.901

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.6389

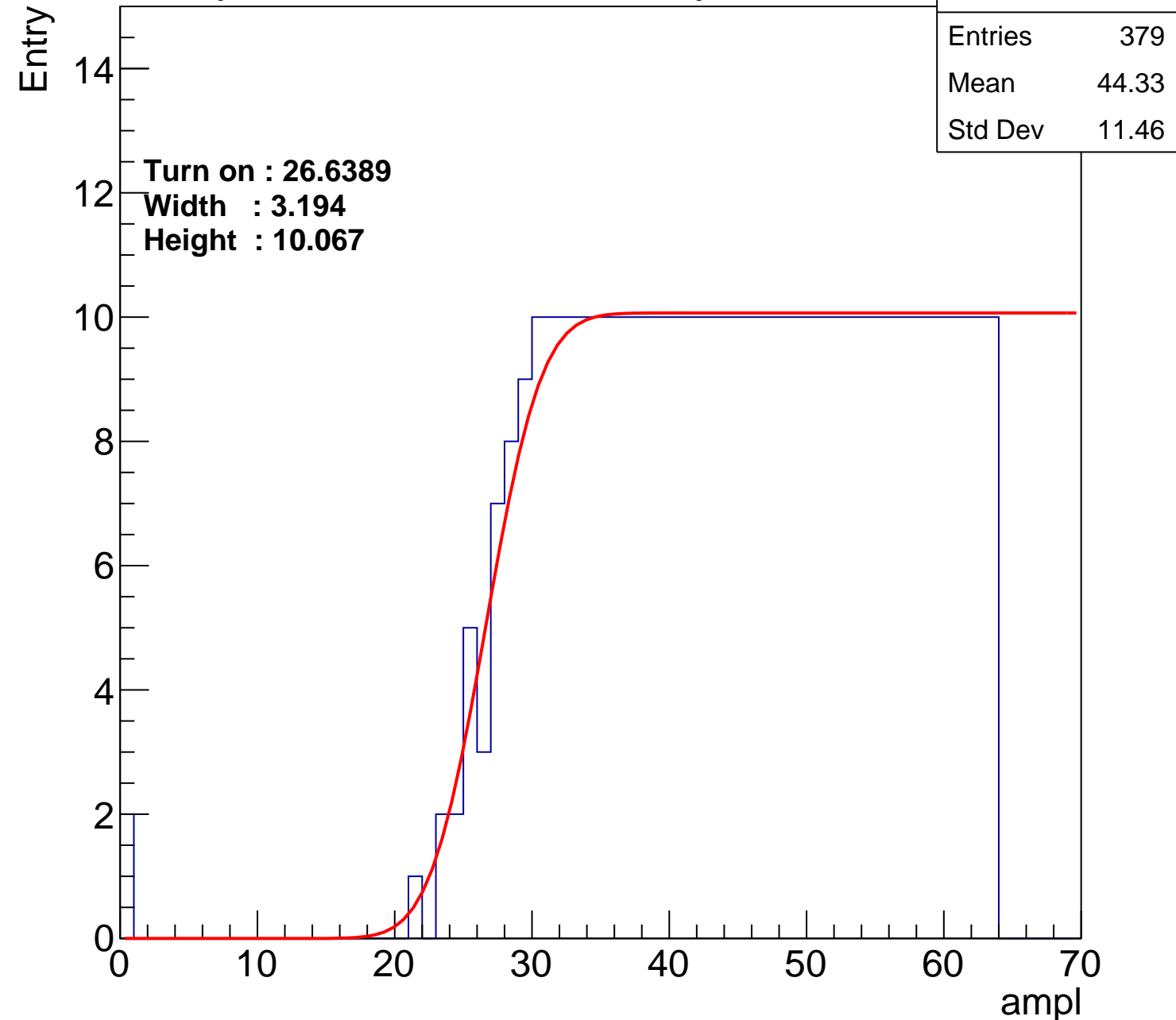
Width : 3.194

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch20

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.3757

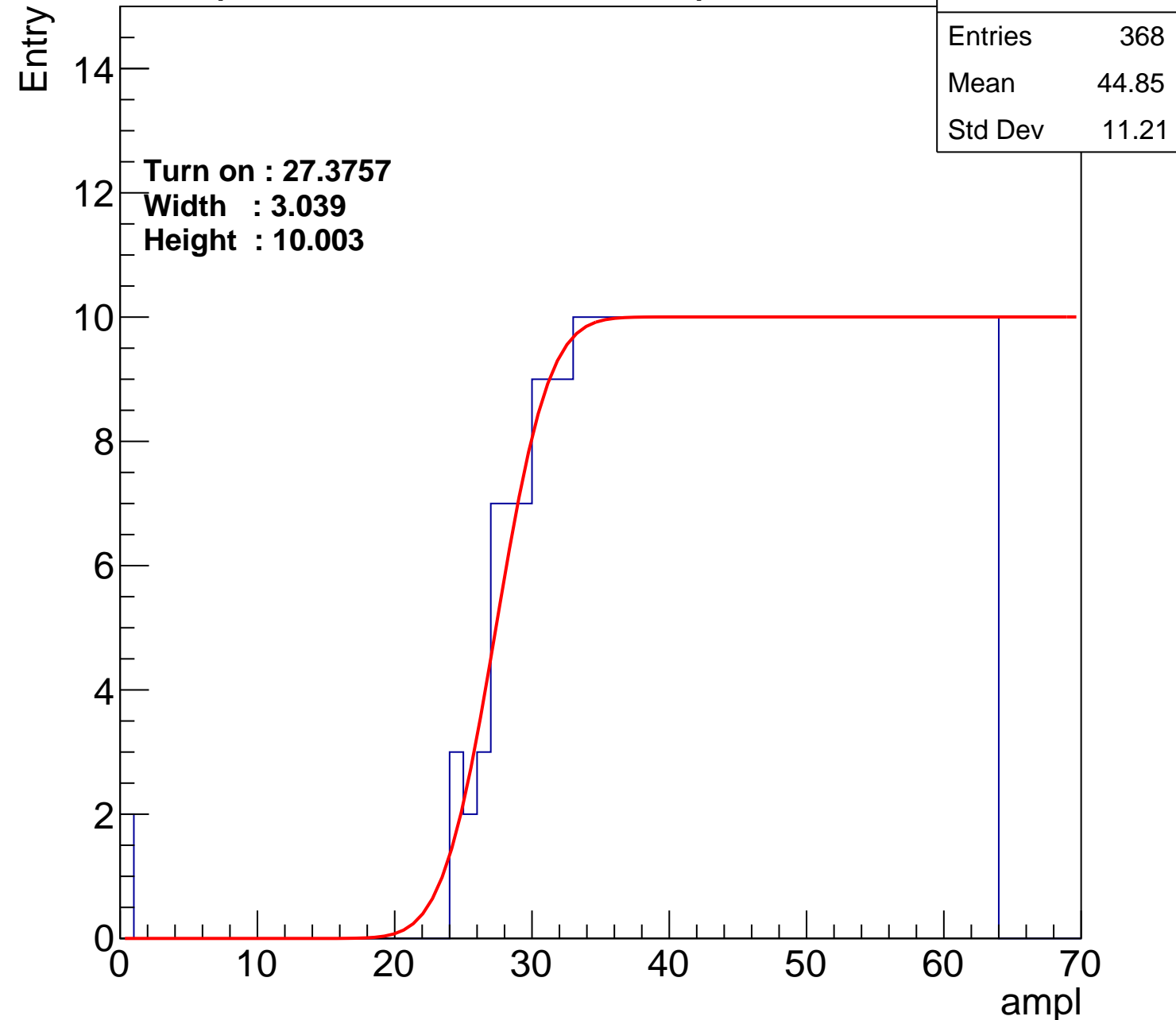
Width : 3.039

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	354
Mean	45.44
Std Dev	11.12

Turn on : 29.4834

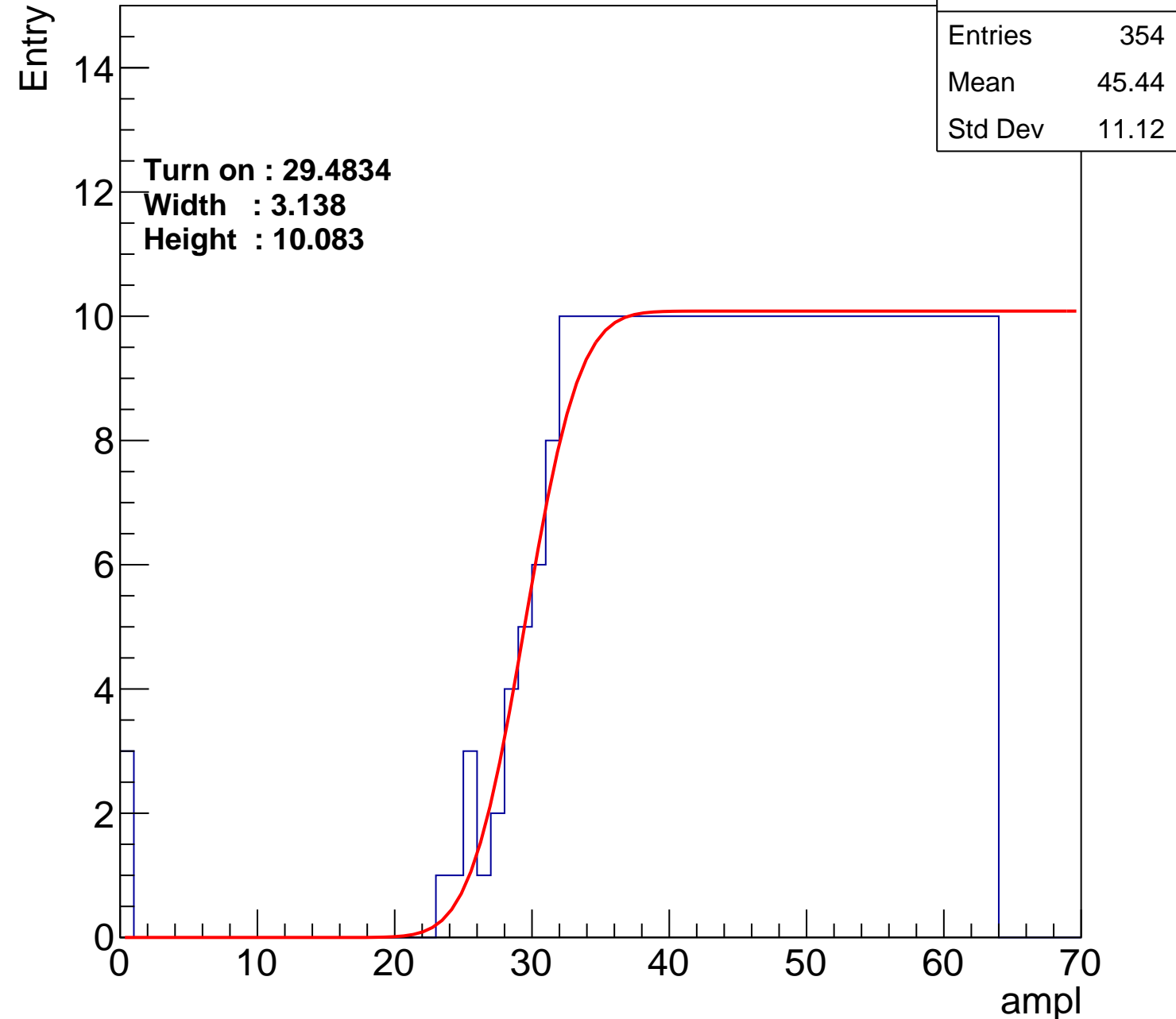
Width : 3.138

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.28
Std Dev	11.8

Turn on : 27.0405

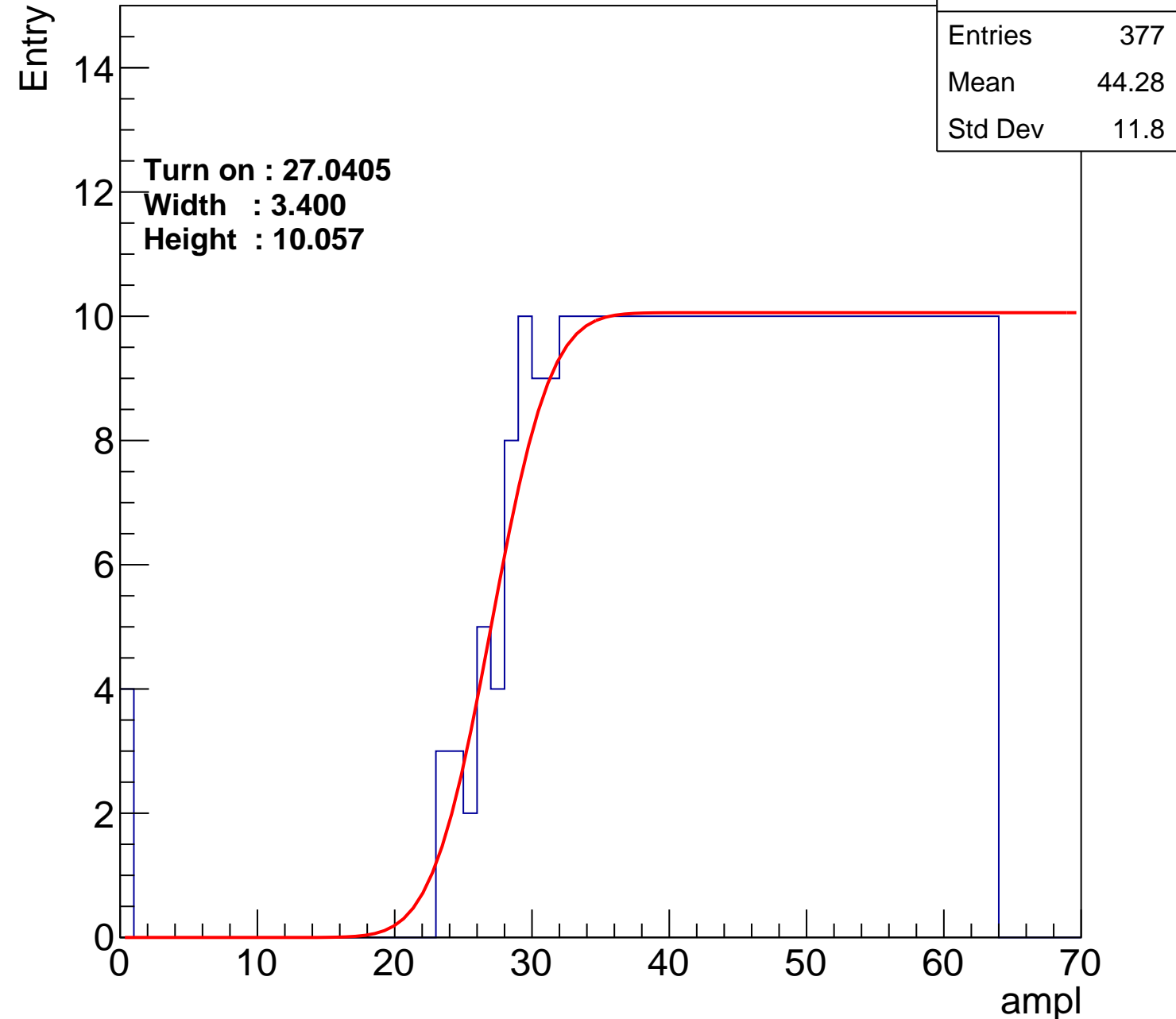
Width : 3.400

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.23
Std Dev	11.18

Turn on : 28.0490

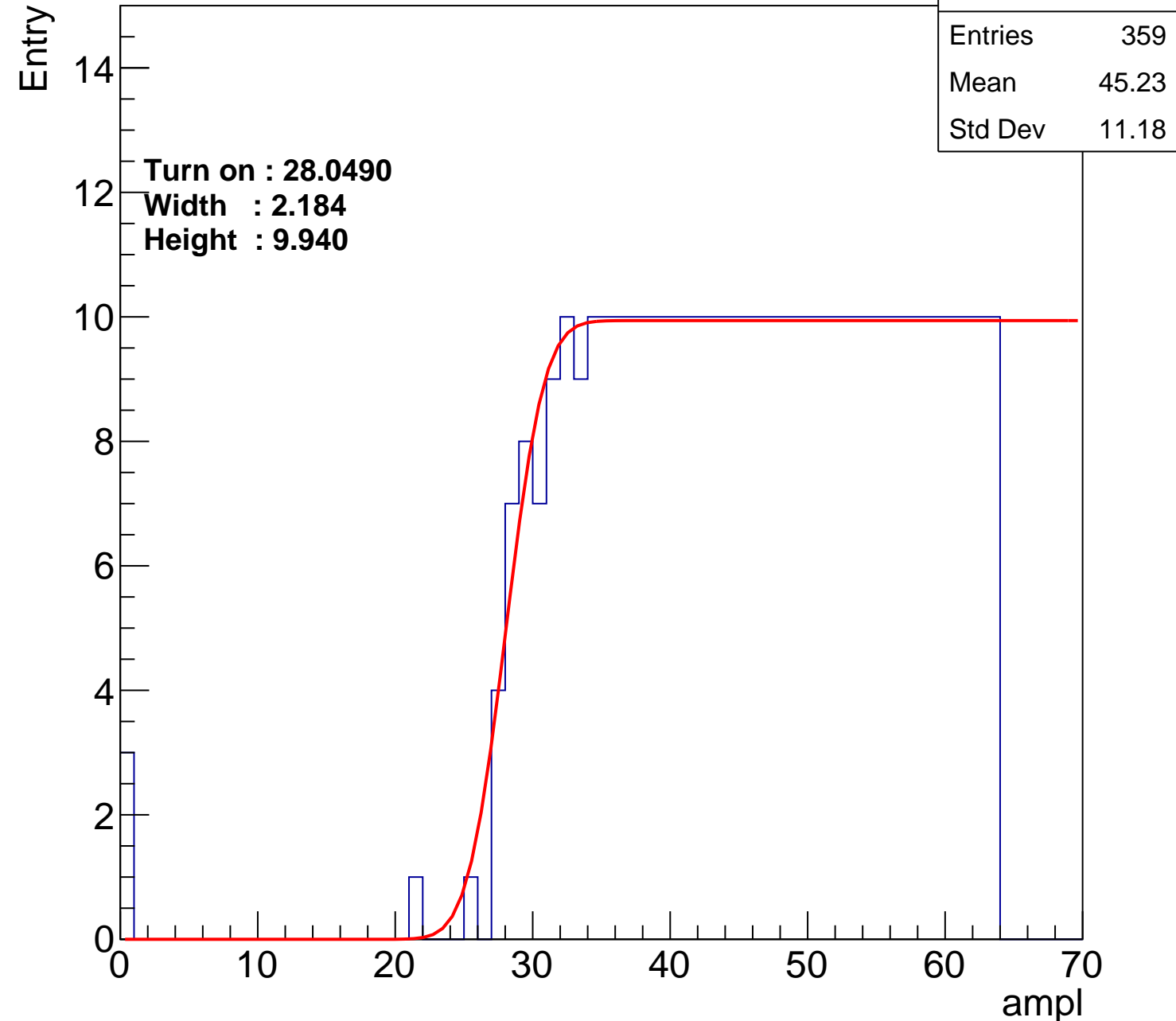
Width : 2.184

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.57
Std Dev	11.39

Turn on : 26.9184

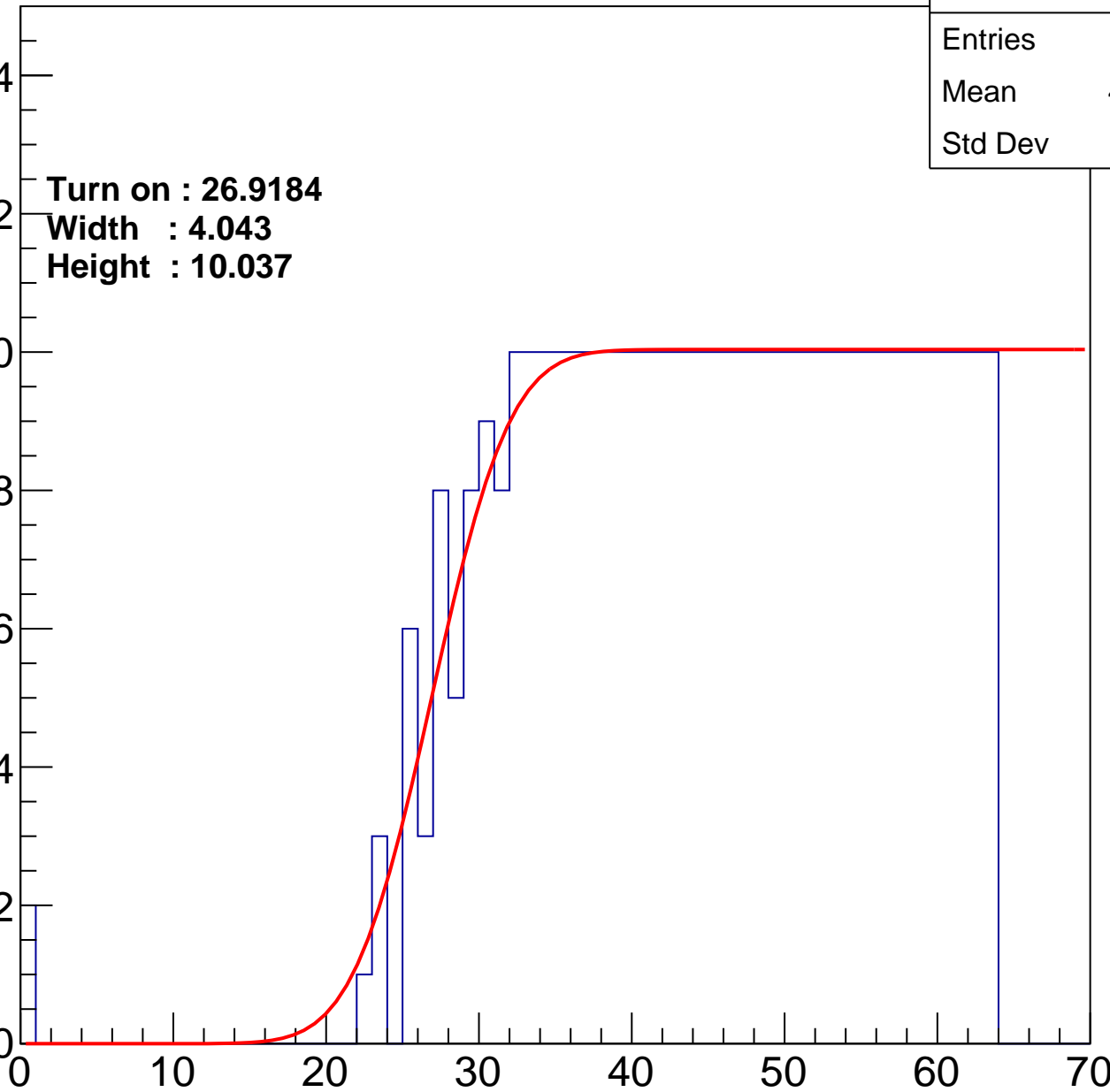
Width : 4.043

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 27.3546

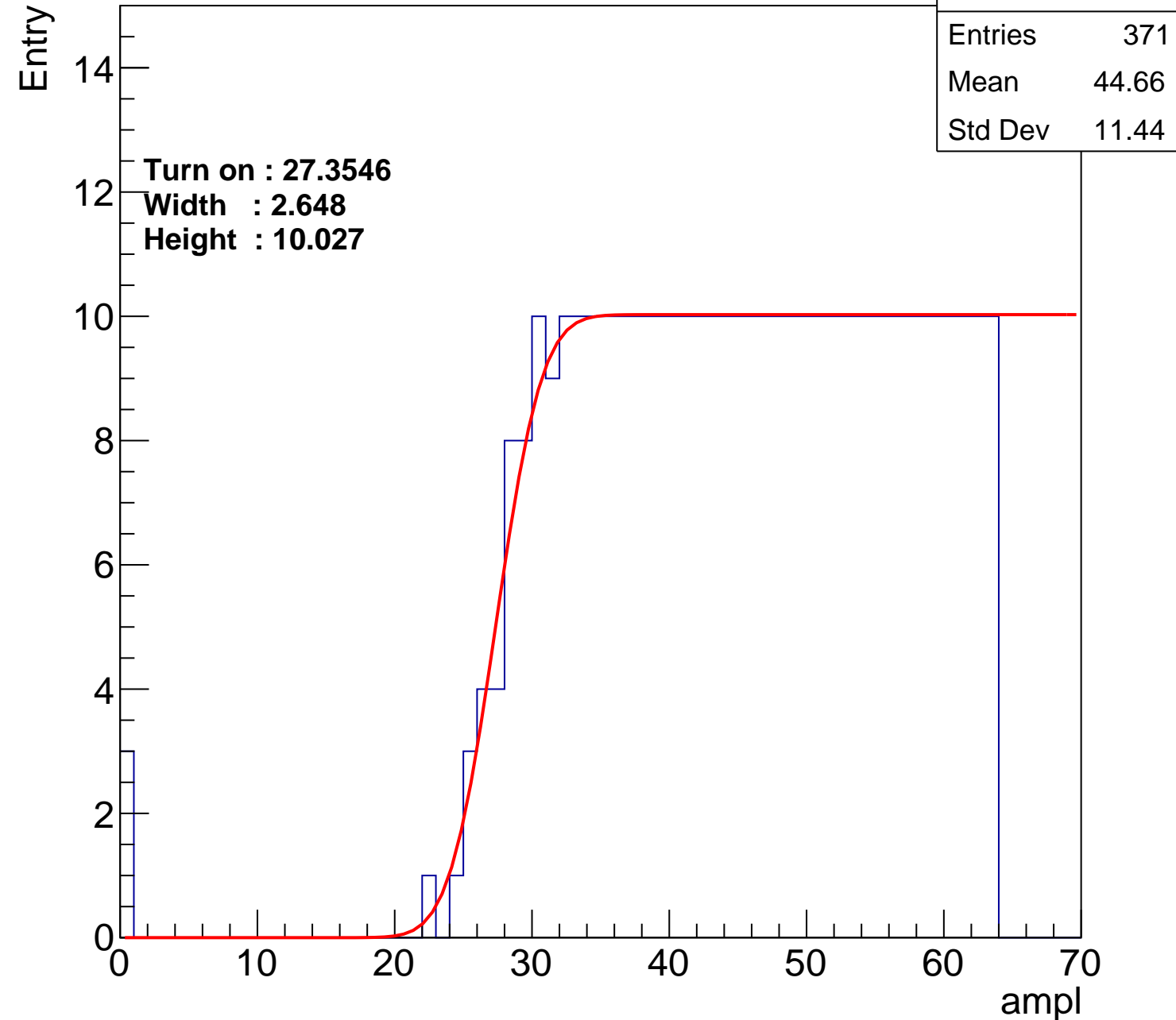
Width : 2.648

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch26

calib_packv5_042523_0143.root, FC#7, port C2

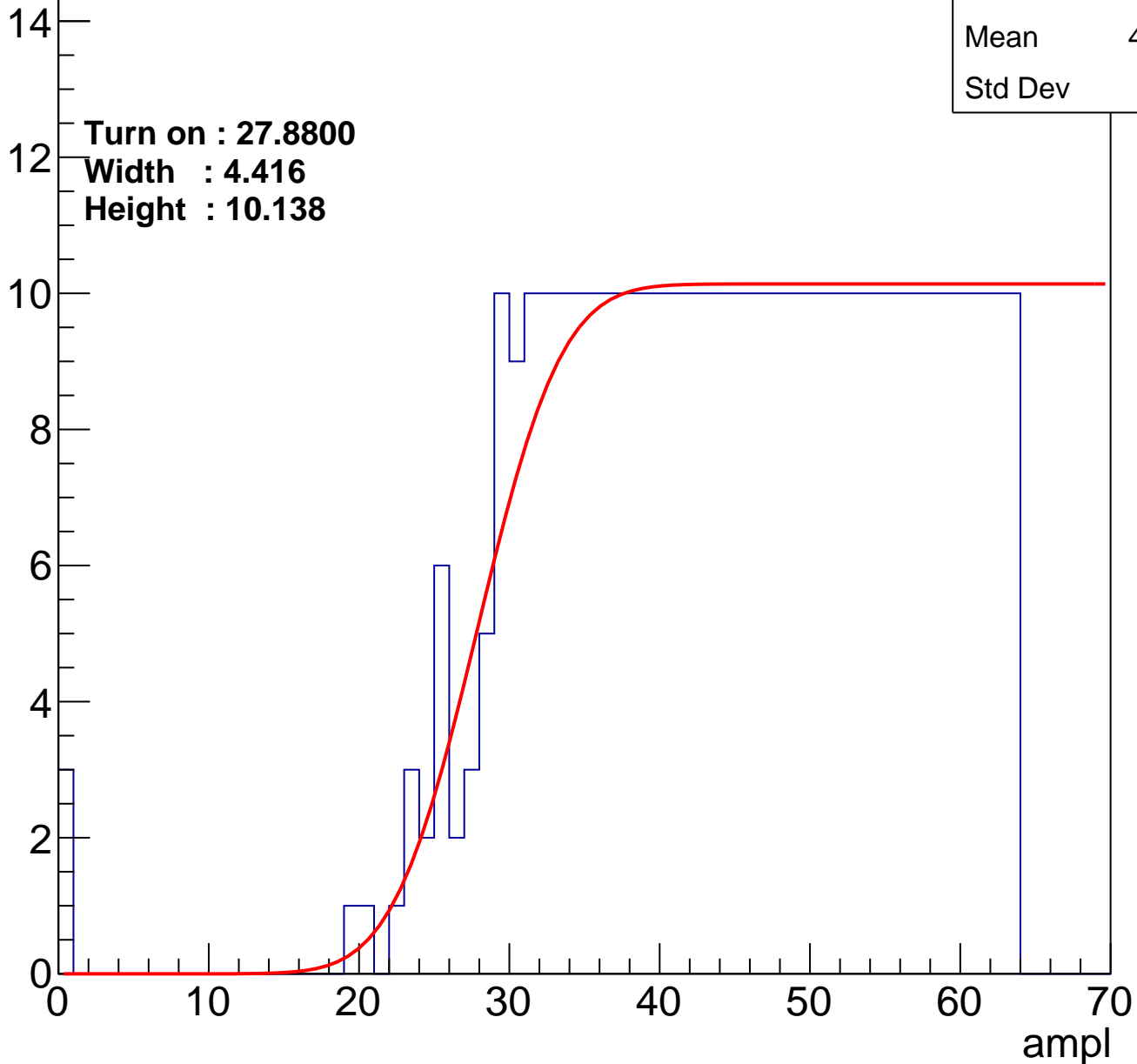
Entries	376
Mean	44.34
Std Dev	11.7

Turn on : 27.8800

Width : 4.416

Height : 10.138

Entry



B1L103S, U11-ch27

calib_packv5_042523_0143.root, FC#7, port C2

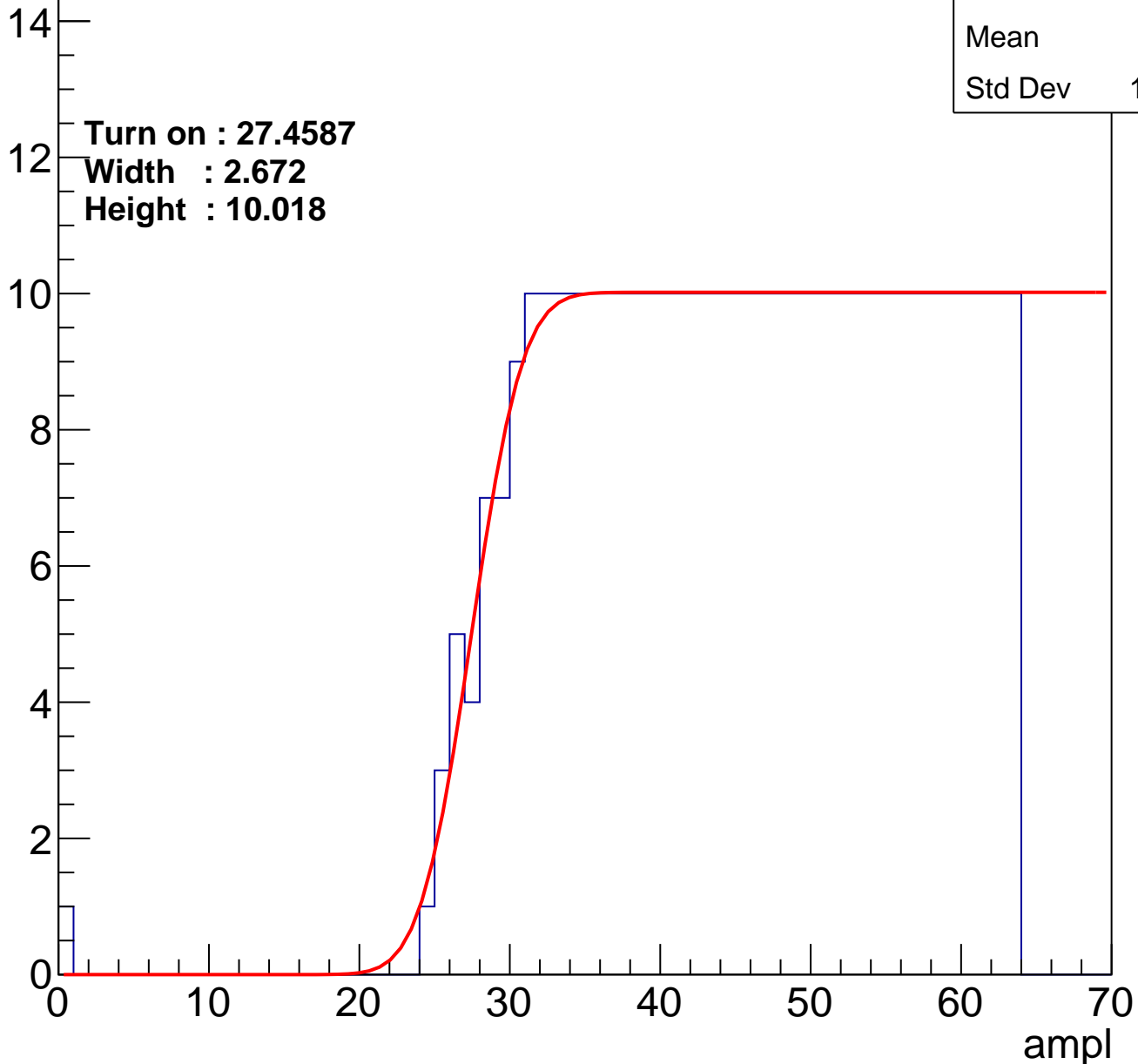
Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.4587

Width : 2.672

Height : 10.018

Entry



B1L103S, U11-ch28

calib_packv5_042523_0143.root, FC#7, port C2

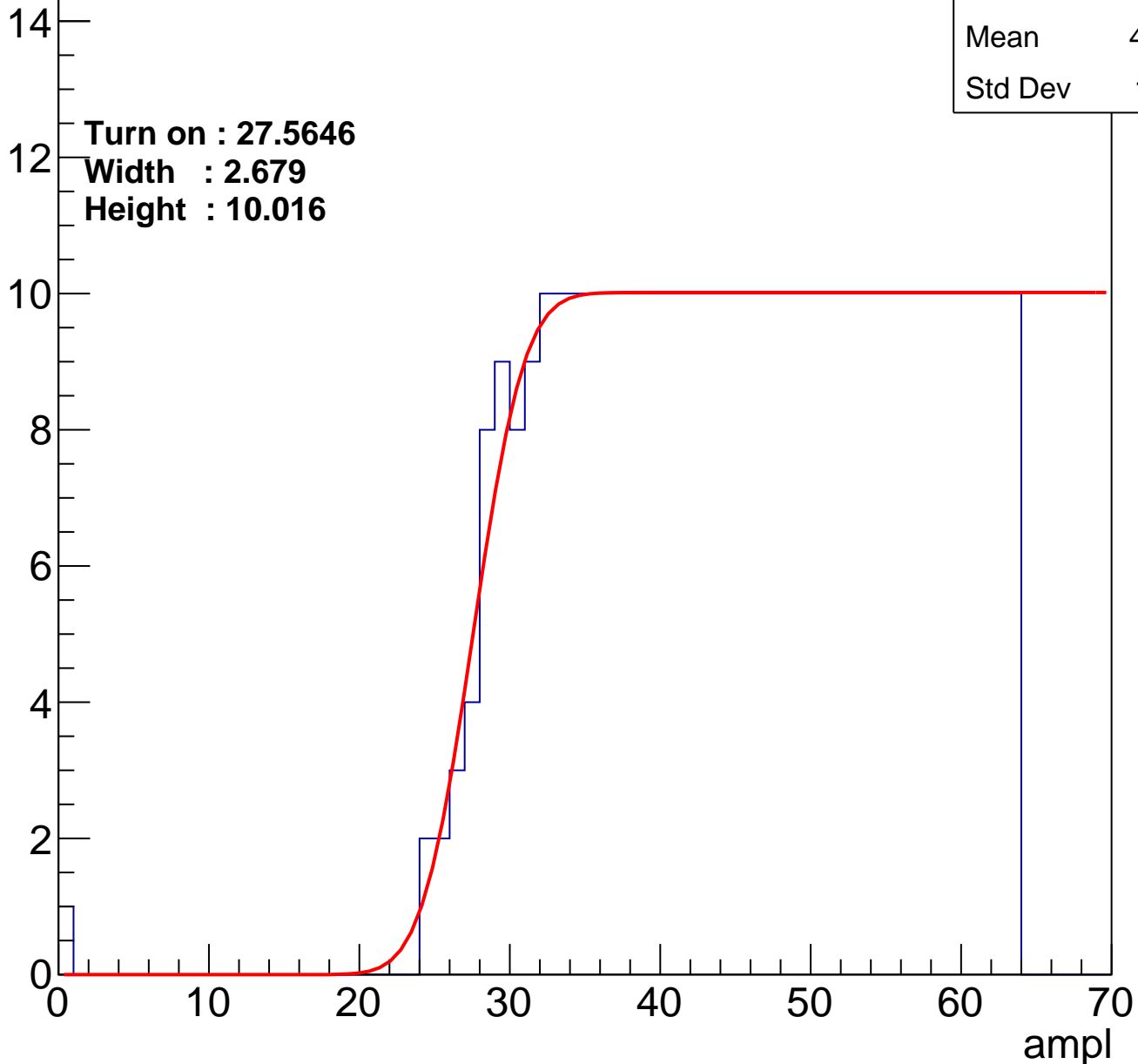
Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.5646

Width : 2.679

Height : 10.016

Entry



B1L103S, U11-ch29

calib_packv5_042523_0143.root, FC#7, port C2

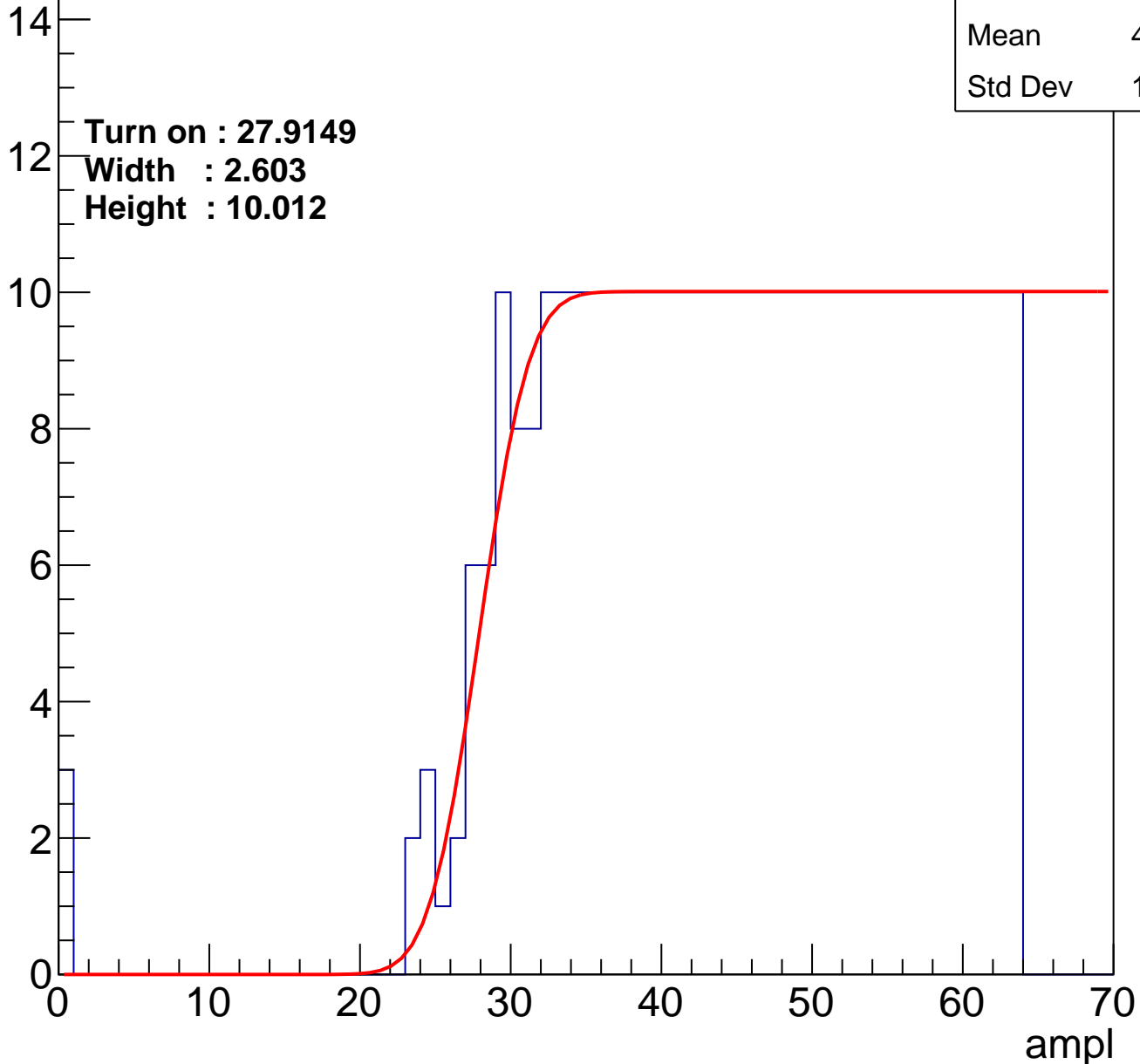
Entries	369
Mean	44.72
Std Dev	11.45

Turn on : 27.9149

Width : 2.603

Height : 10.012

Entry



B1L103S, U11-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.7124

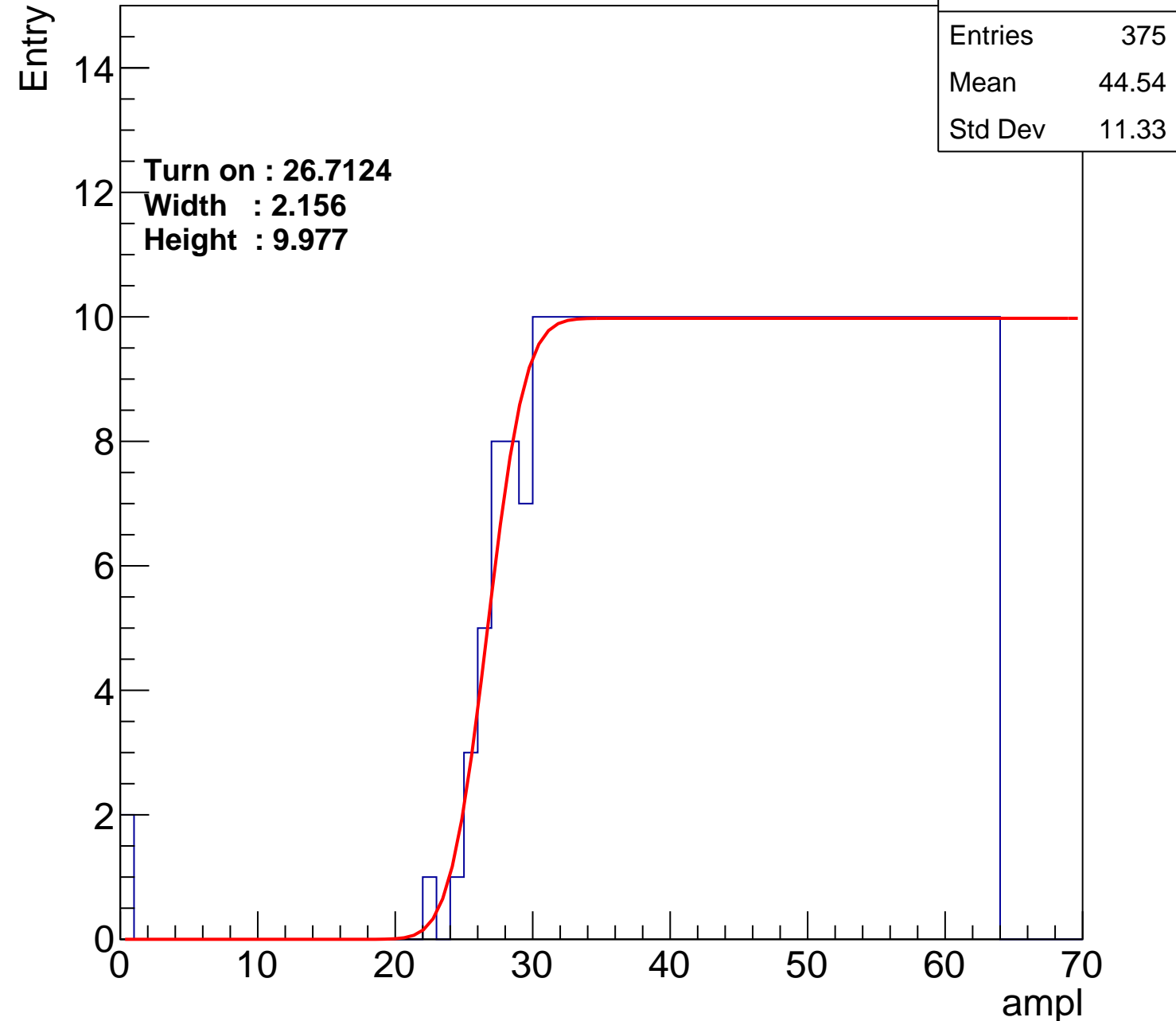
Width : 2.156

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	345
Mean	45.98
Std Dev	10.65

Turn on : 30.3017

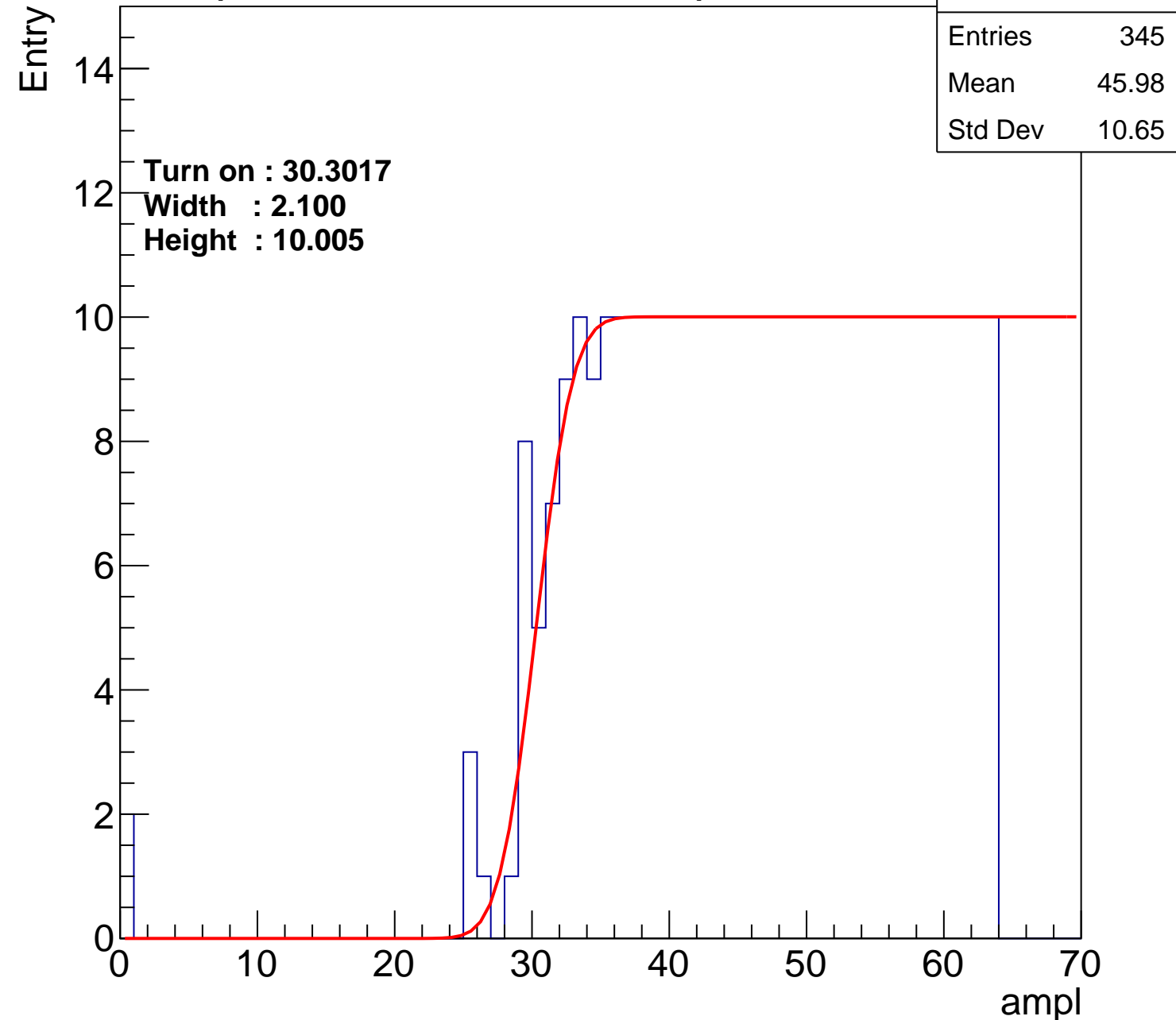
Width : 2.100

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch32

calib_packv5_042523_0143.root, FC#7, port C2

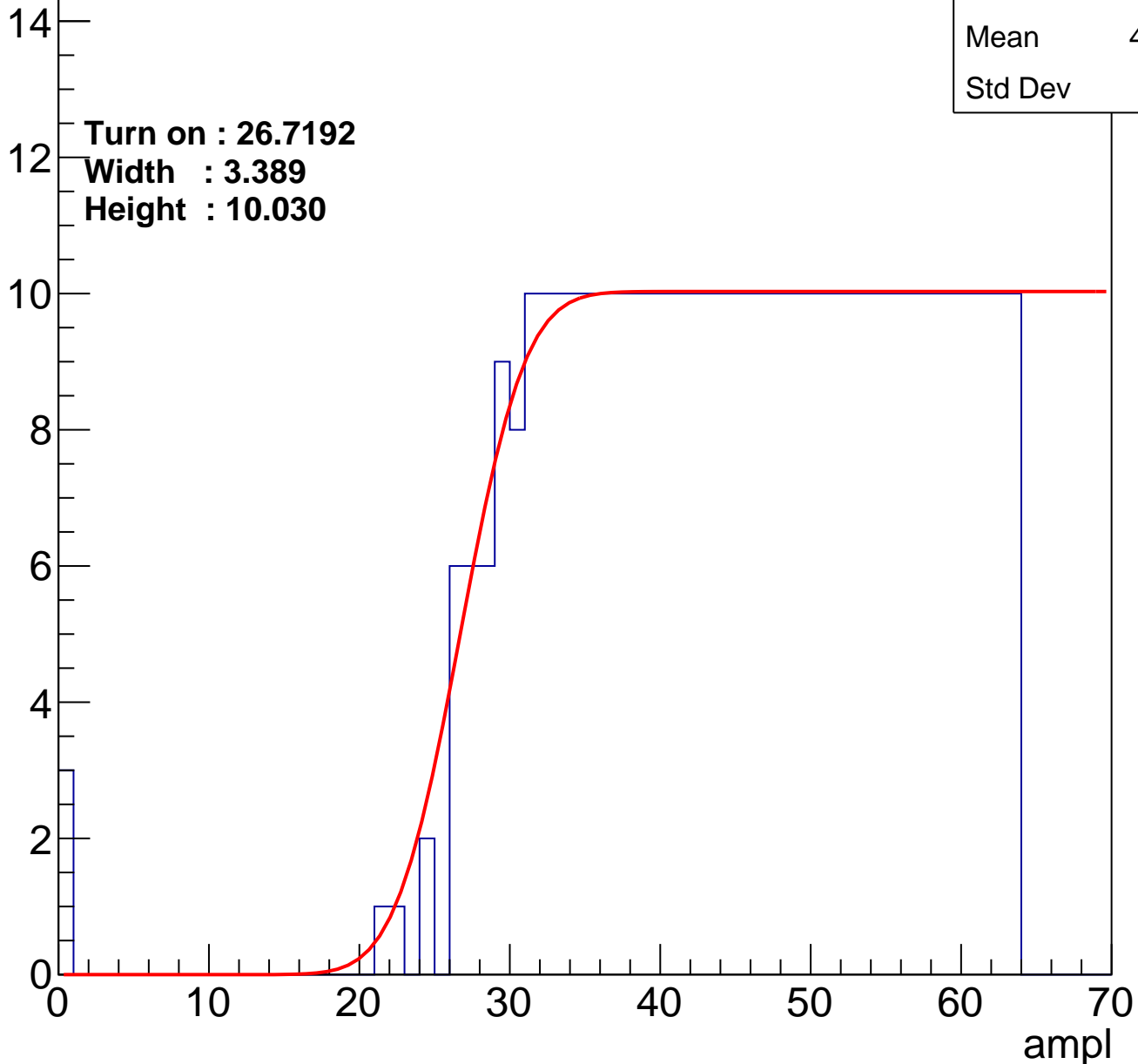
Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 26.7192

Width : 3.389

Height : 10.030

Entry



B1L103S, U11-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 27.9669

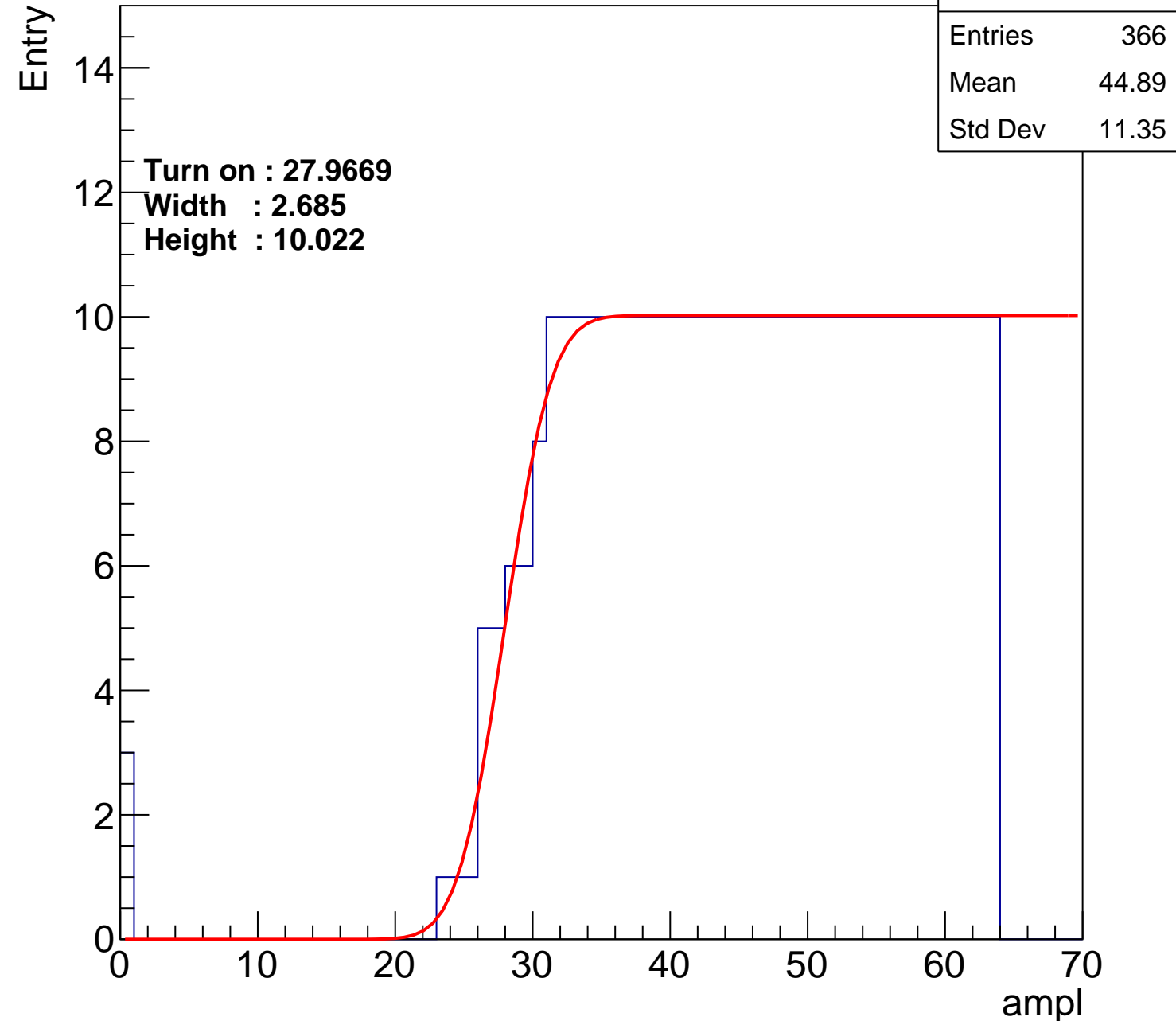
Width : 2.685

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.6732

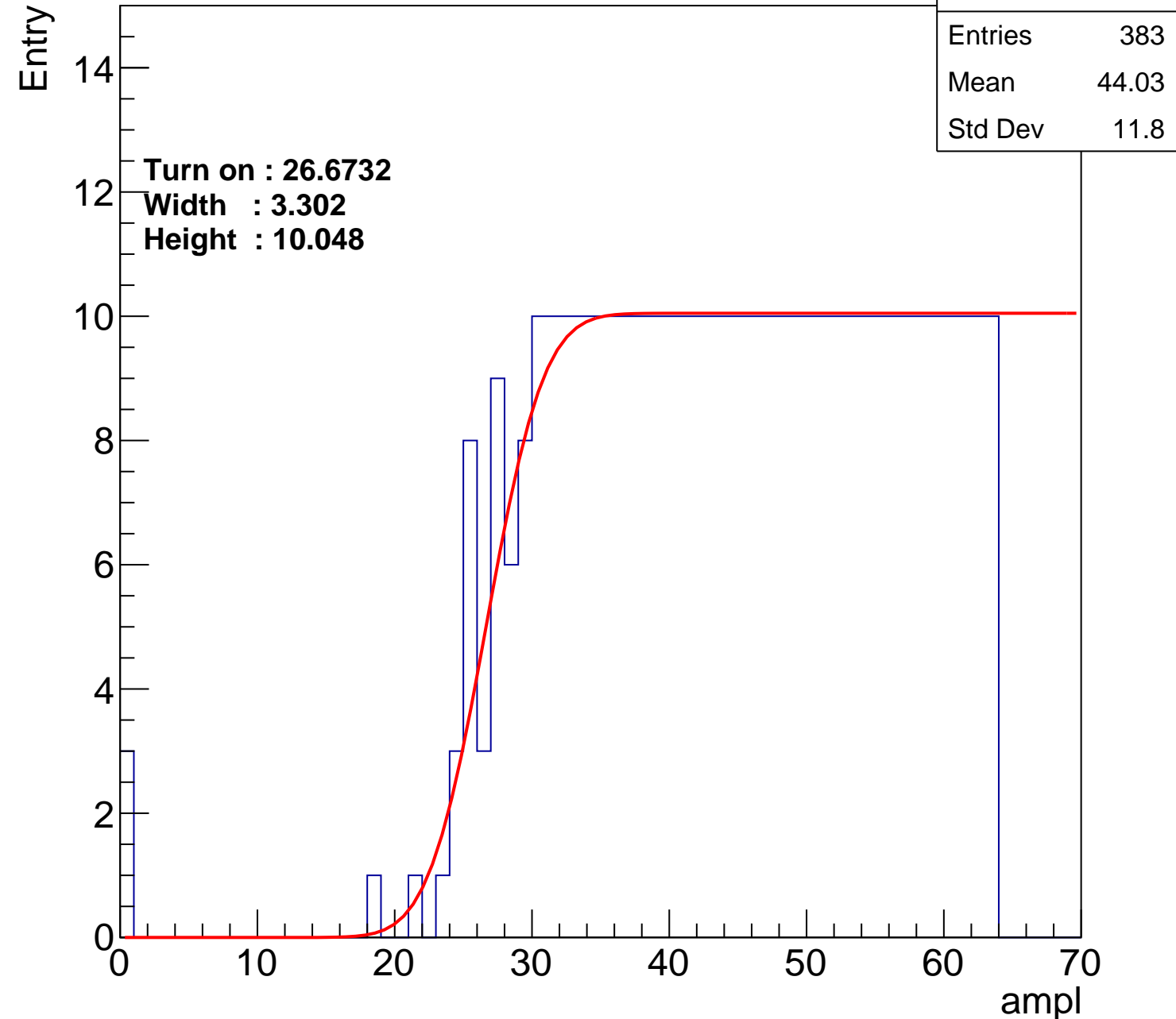
Width : 3.302

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.82
Std Dev	11.02

Turn on : 27.1193

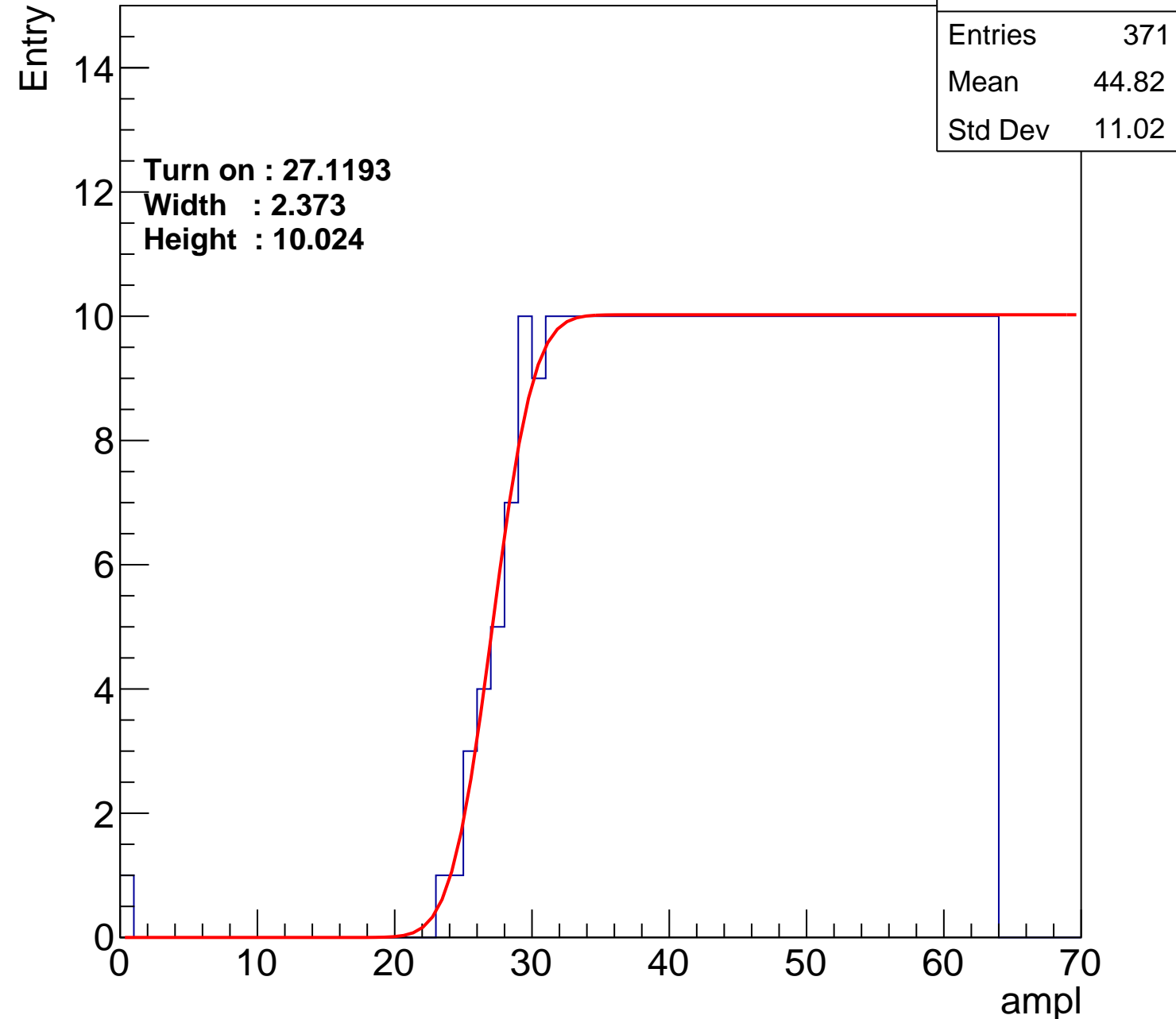
Width : 2.373

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch36

calib_packv5_042523_0143.root, FC#7, port C2

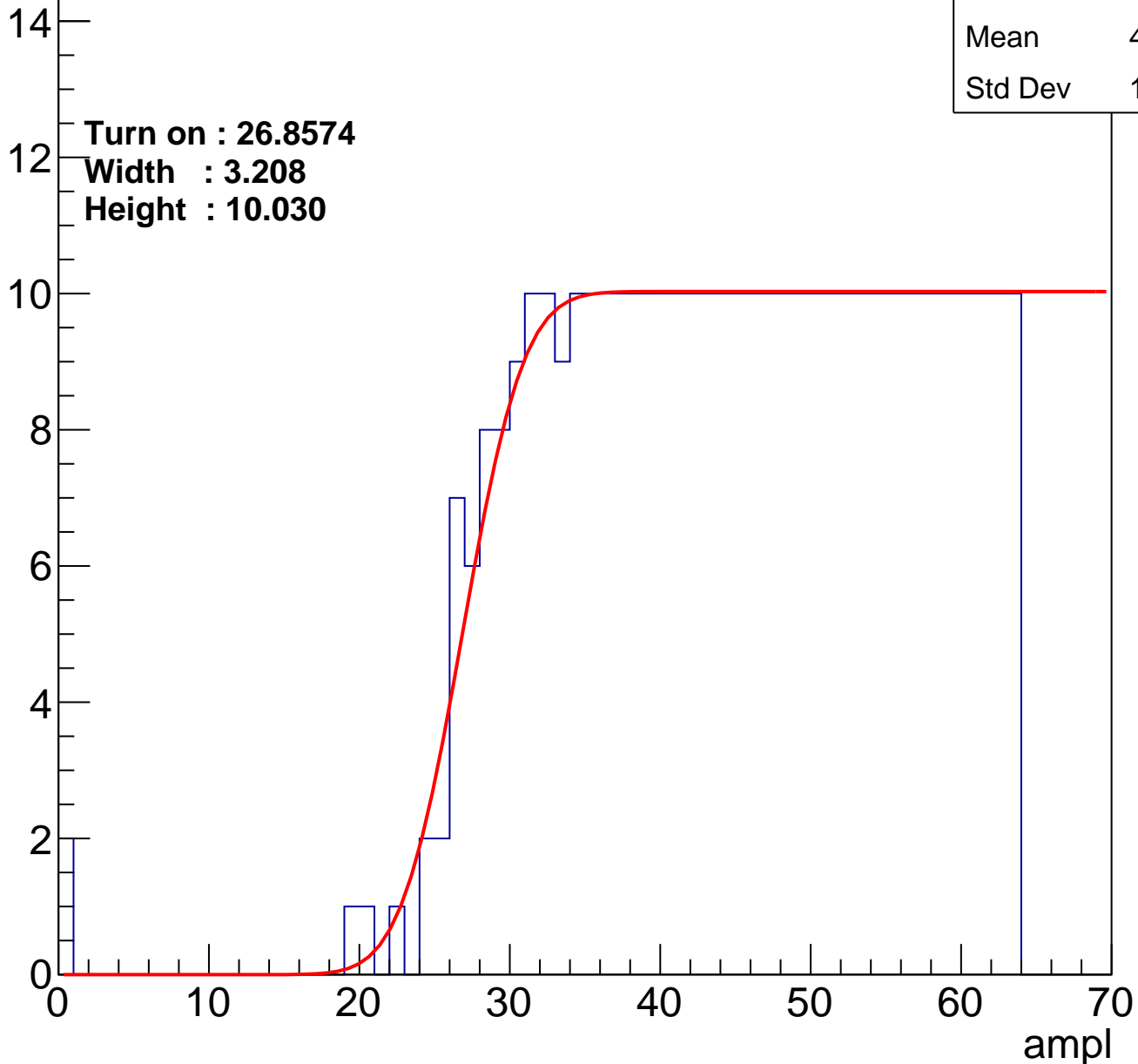
Entries	376
Mean	44.43
Std Dev	11.46

Turn on : 26.8574

Width : 3.208

Height : 10.030

Entry



B1L103S, U11-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.6
Std Dev	11.29

Turn on : 26.7253

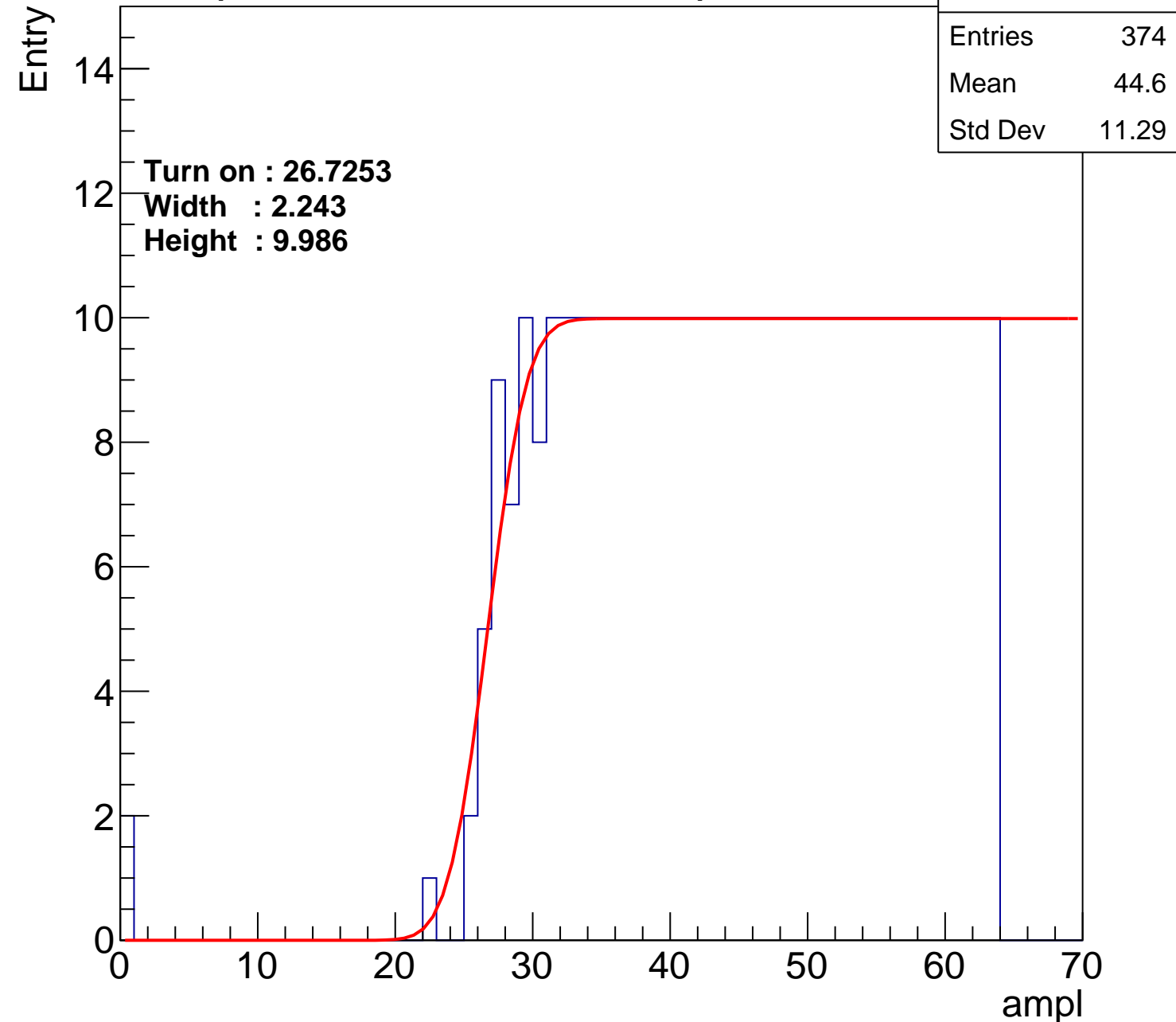
Width : 2.243

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch38

calib_packv5_042523_0143.root, FC#7, port C2

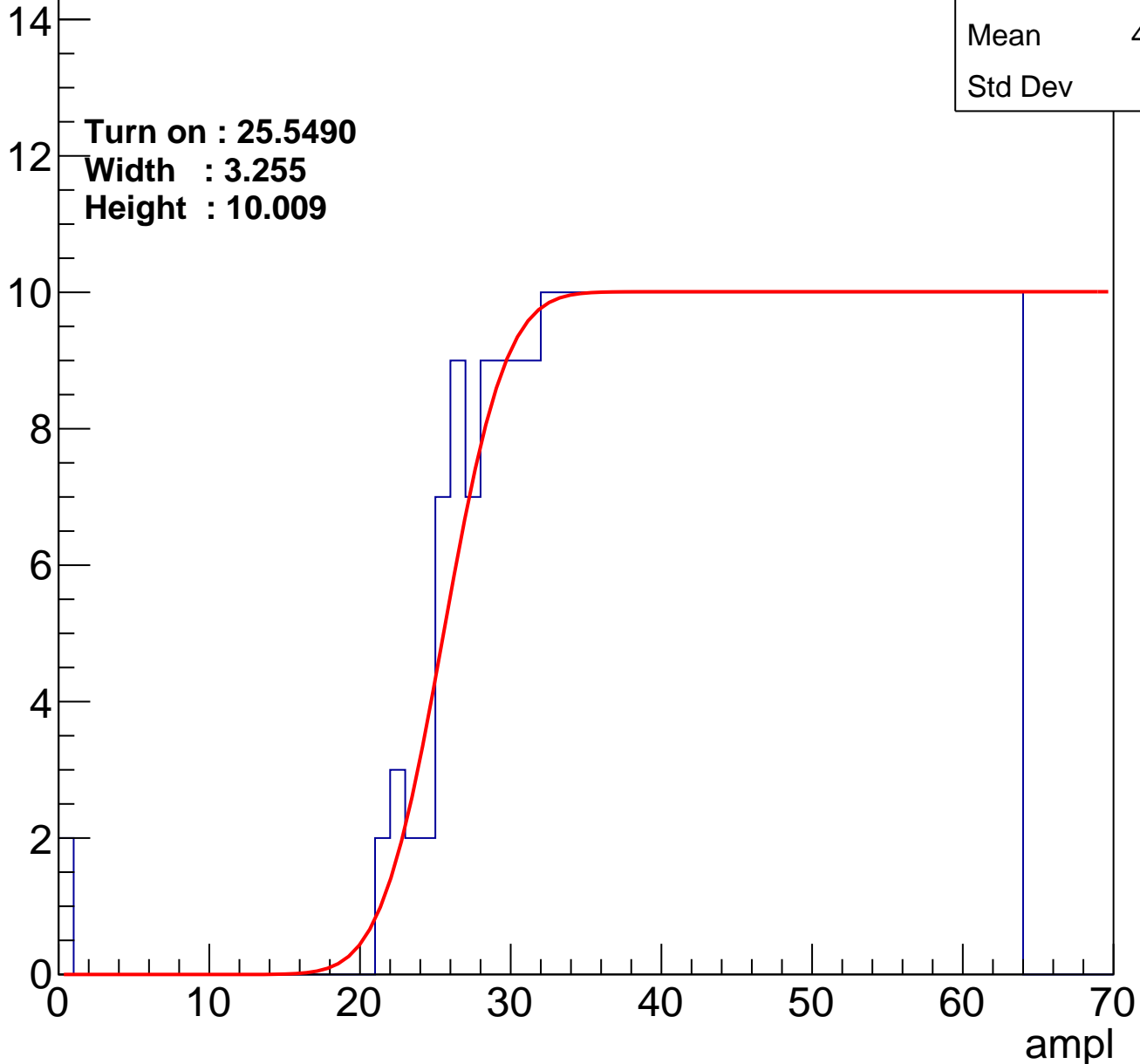
Entries	390
Mean	43.75
Std Dev	11.8

Turn on : 25.5490

Width : 3.255

Height : 10.009

Entry



B1L103S, U11-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.82
Std Dev	11.35

Turn on : 27.8983

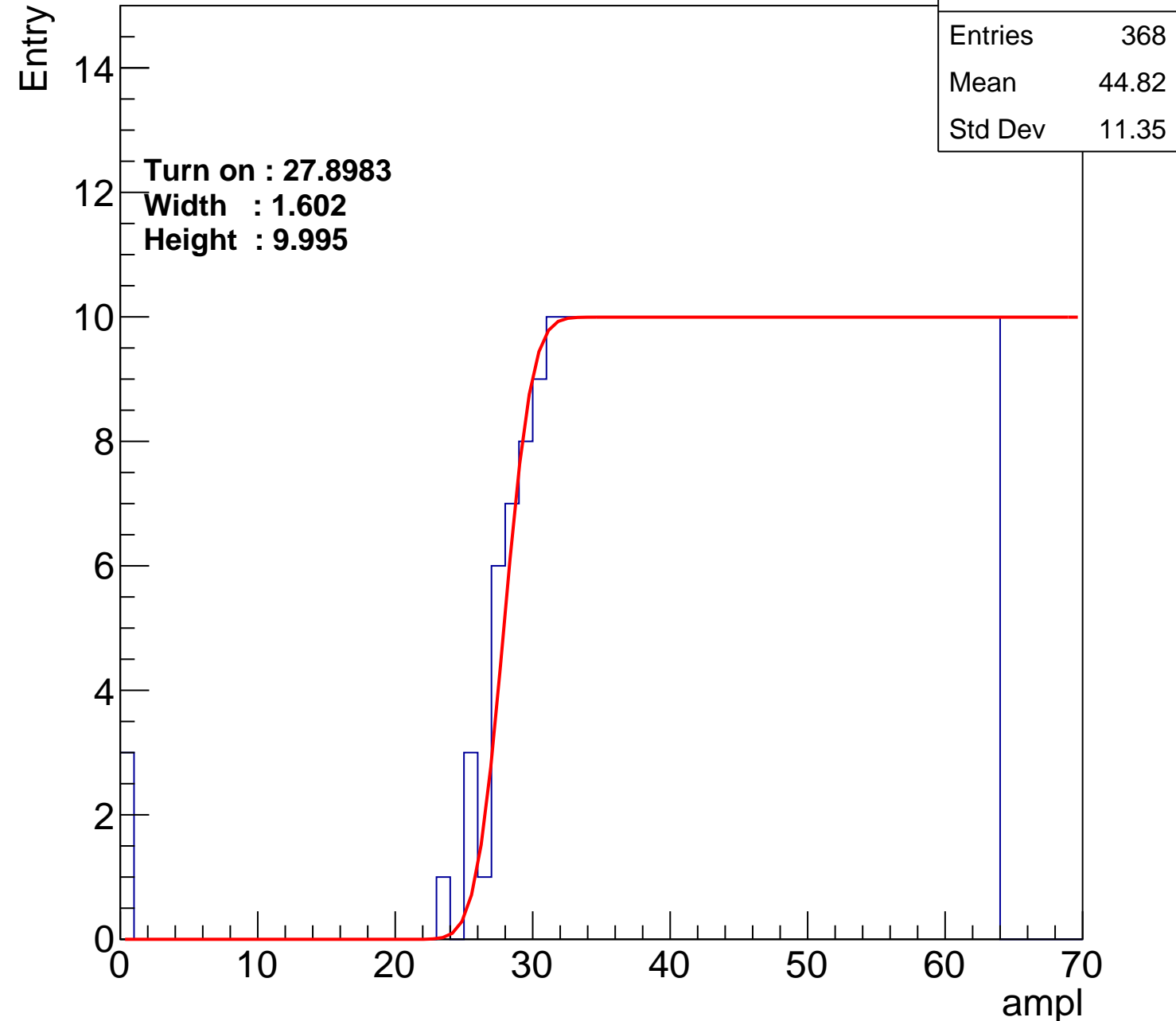
Width : 1.602

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch40

calib_packv5_042523_0143.root, FC#7, port C2

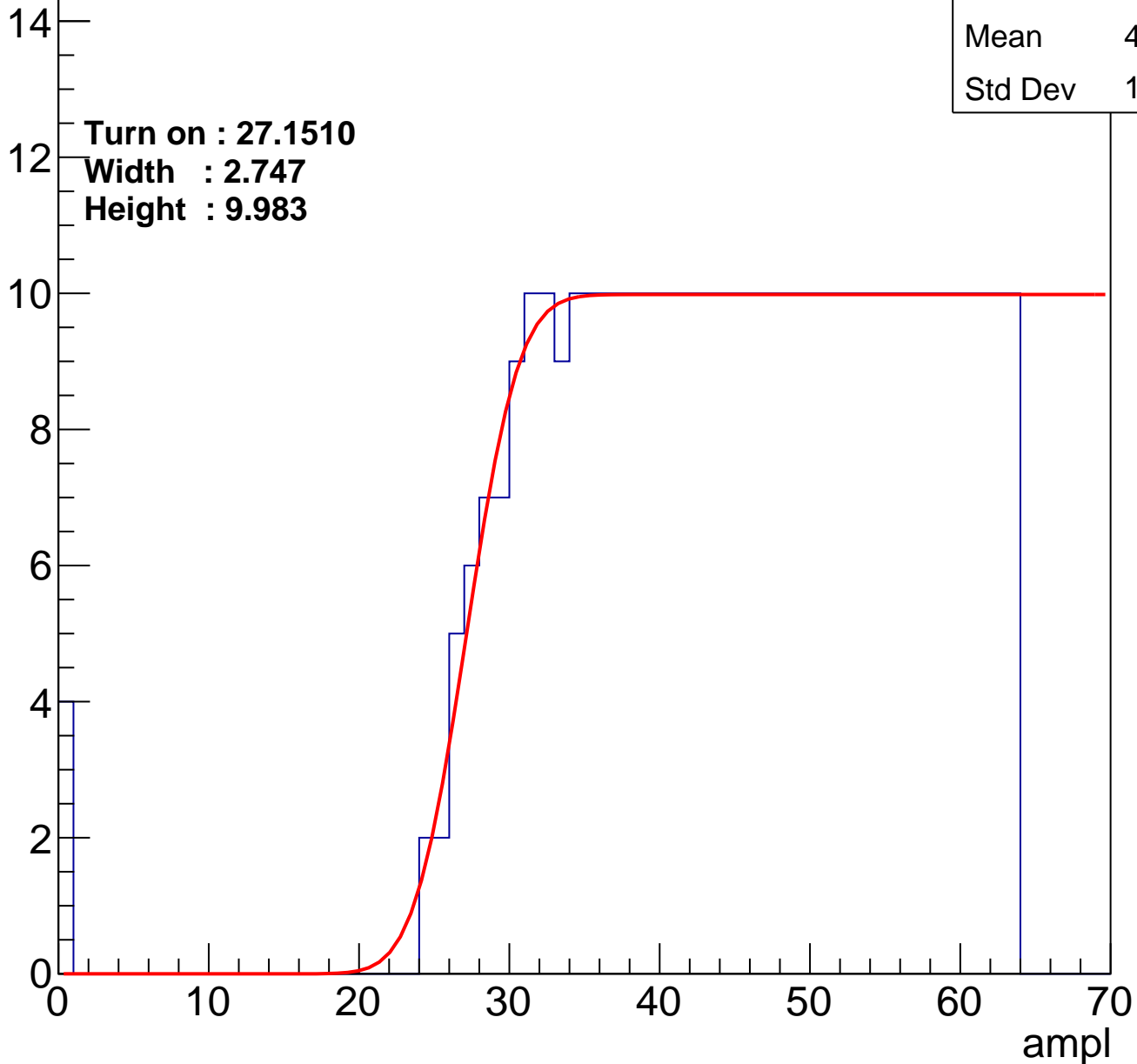
Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 27.1510

Width : 2.747

Height : 9.983

Entry



B1L103S, U11-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.51
Std Dev	11.58

Turn on : 27.7357

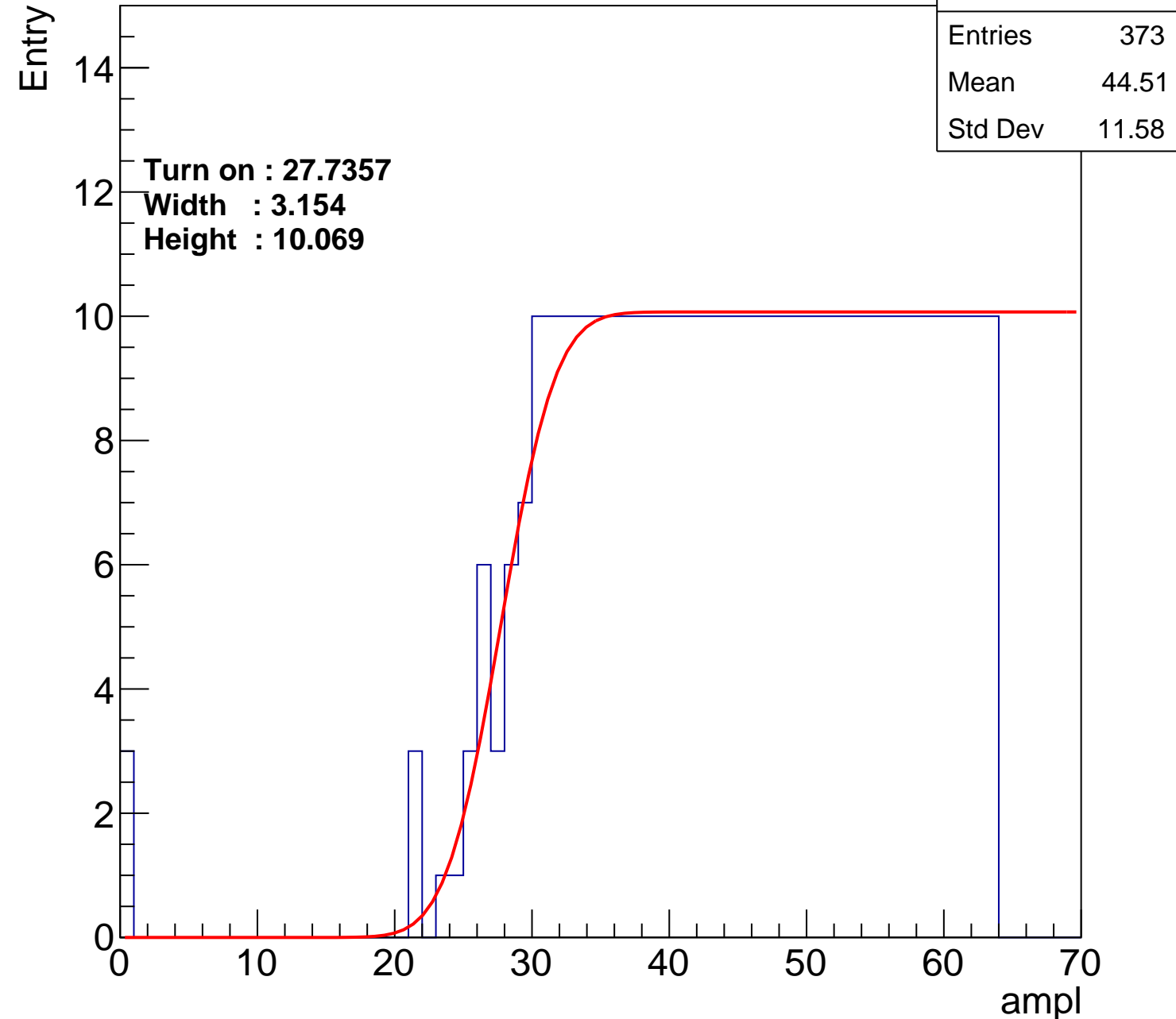
Width : 3.154

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.39
Std Dev	10.99

Turn on : 29.0652

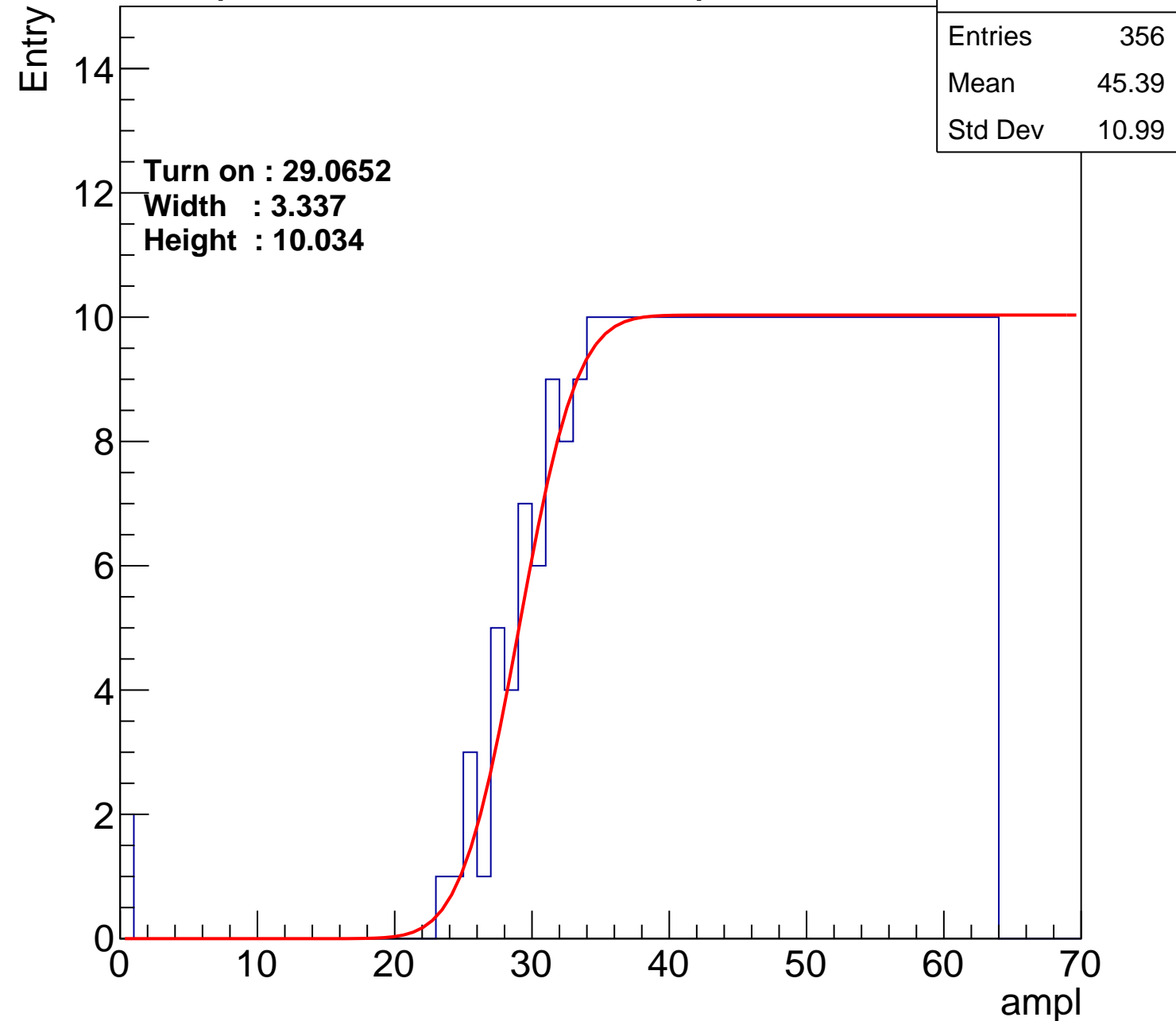
Width : 3.337

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.08
Std Dev	11.11

Turn on : 27.7379

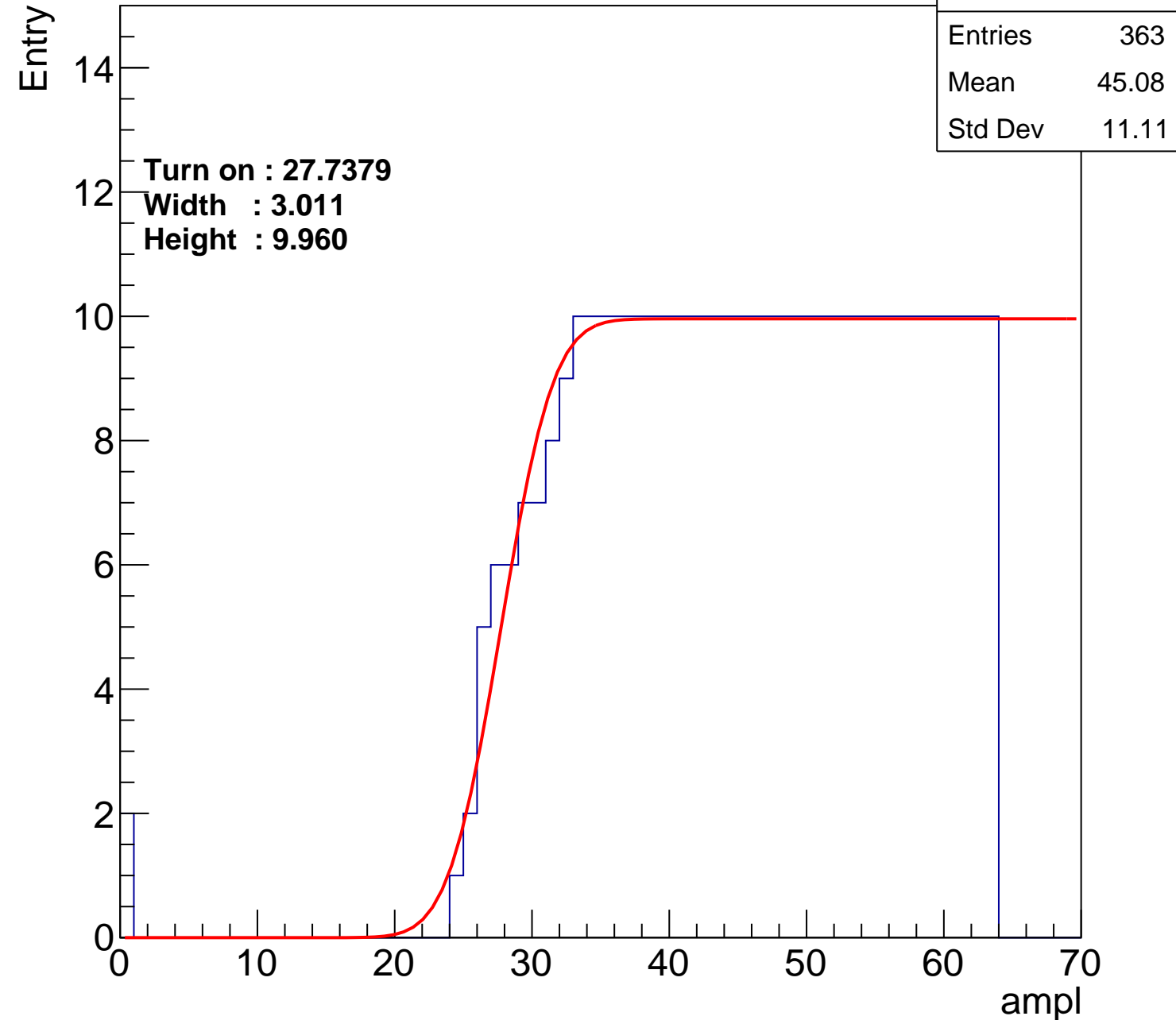
Width : 3.011

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch44

calib_packv5_042523_0143.root, FC#7, port C2

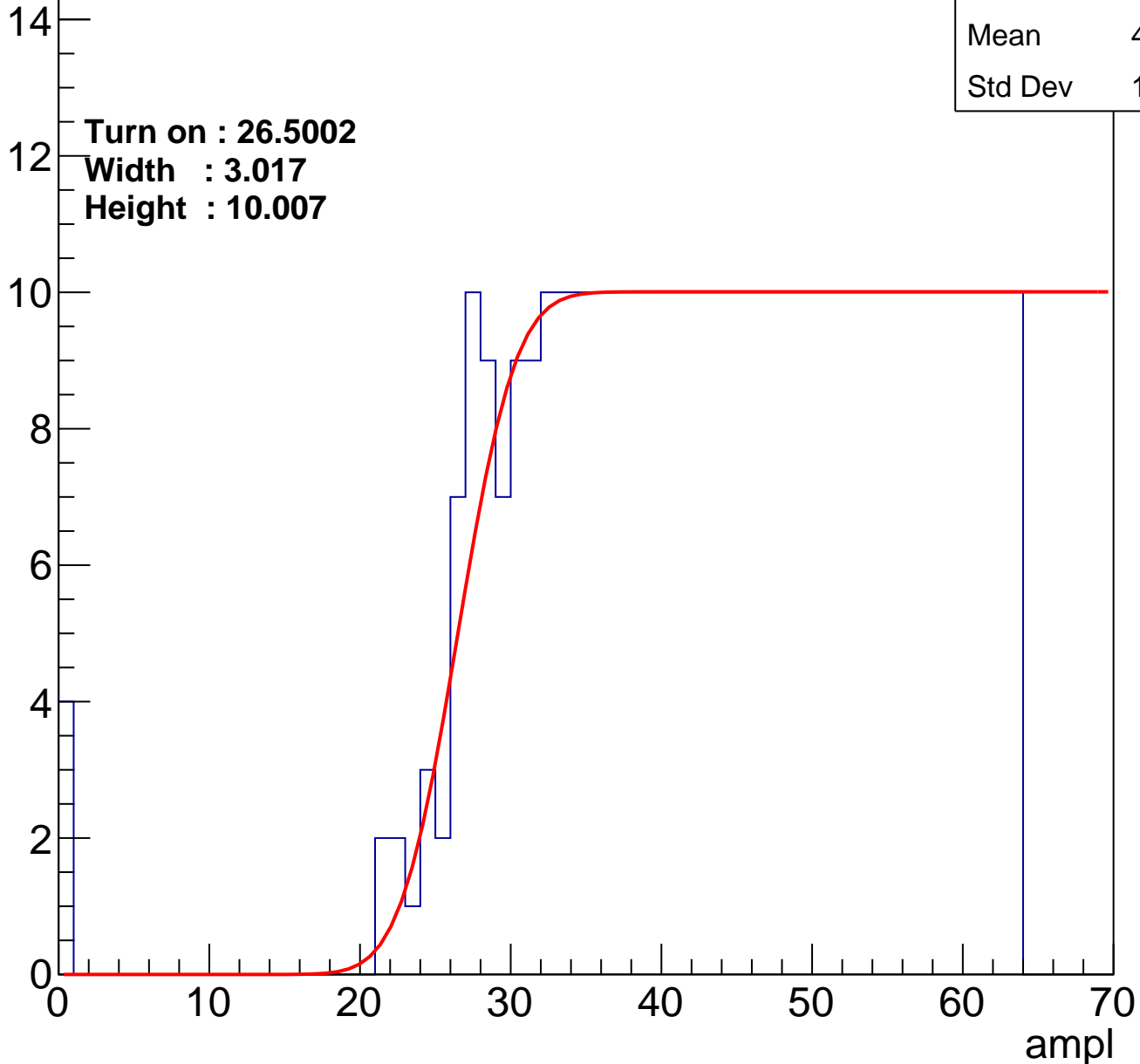
Entries	385
Mean	43.86
Std Dev	12.02

Turn on : 26.5002

Width : 3.017

Height : 10.007

Entry



B1L103S, U11-ch45

calib_packv5_042523_0143.root, FC#7, port C2

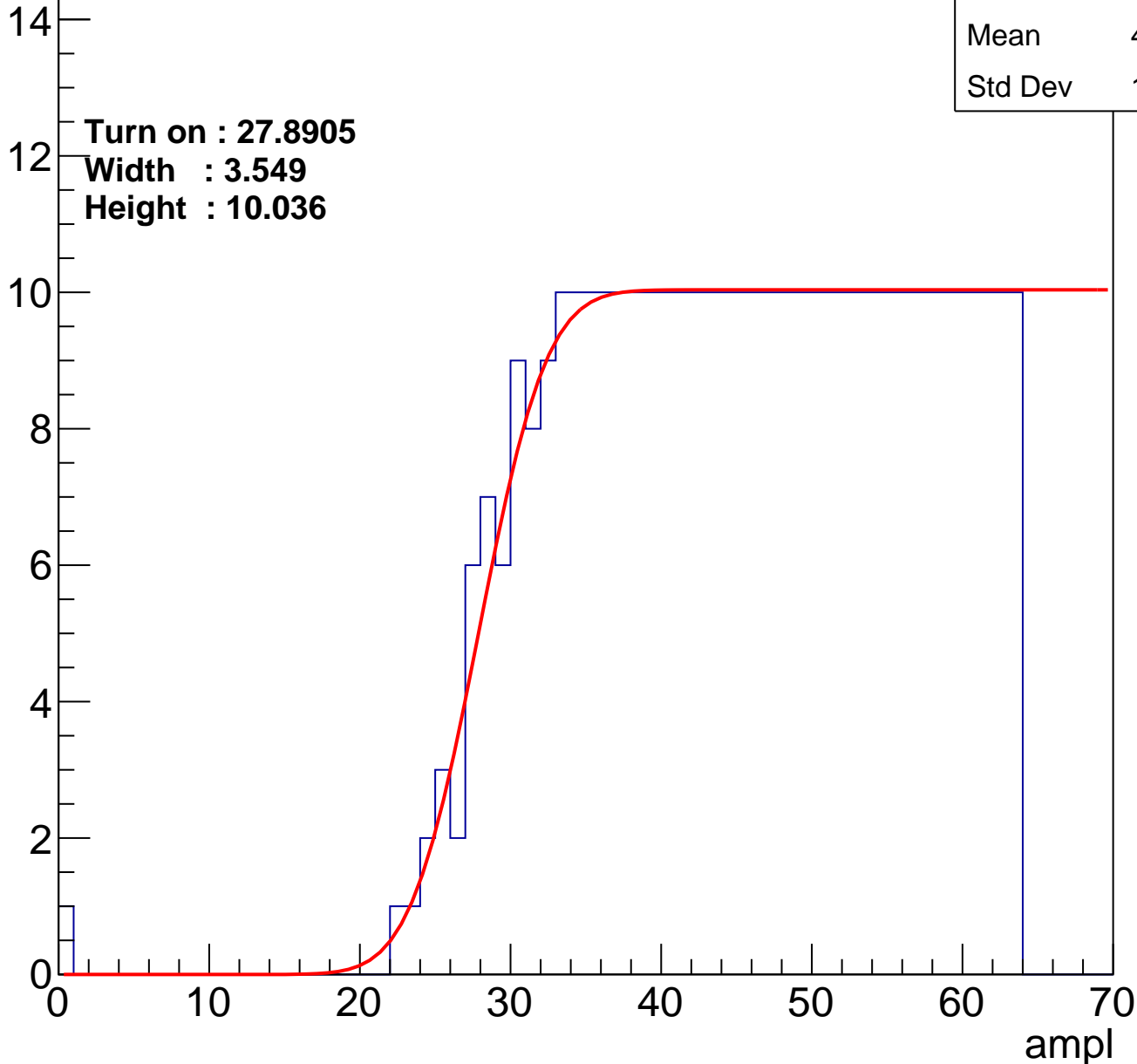
Entries	365
Mean	45.04
Std Dev	10.99

Turn on : 27.8905

Width : 3.549

Height : 10.036

Entry



B1L103S, U11-ch46

calib_packv5_042523_0143.root, FC#7, port C2

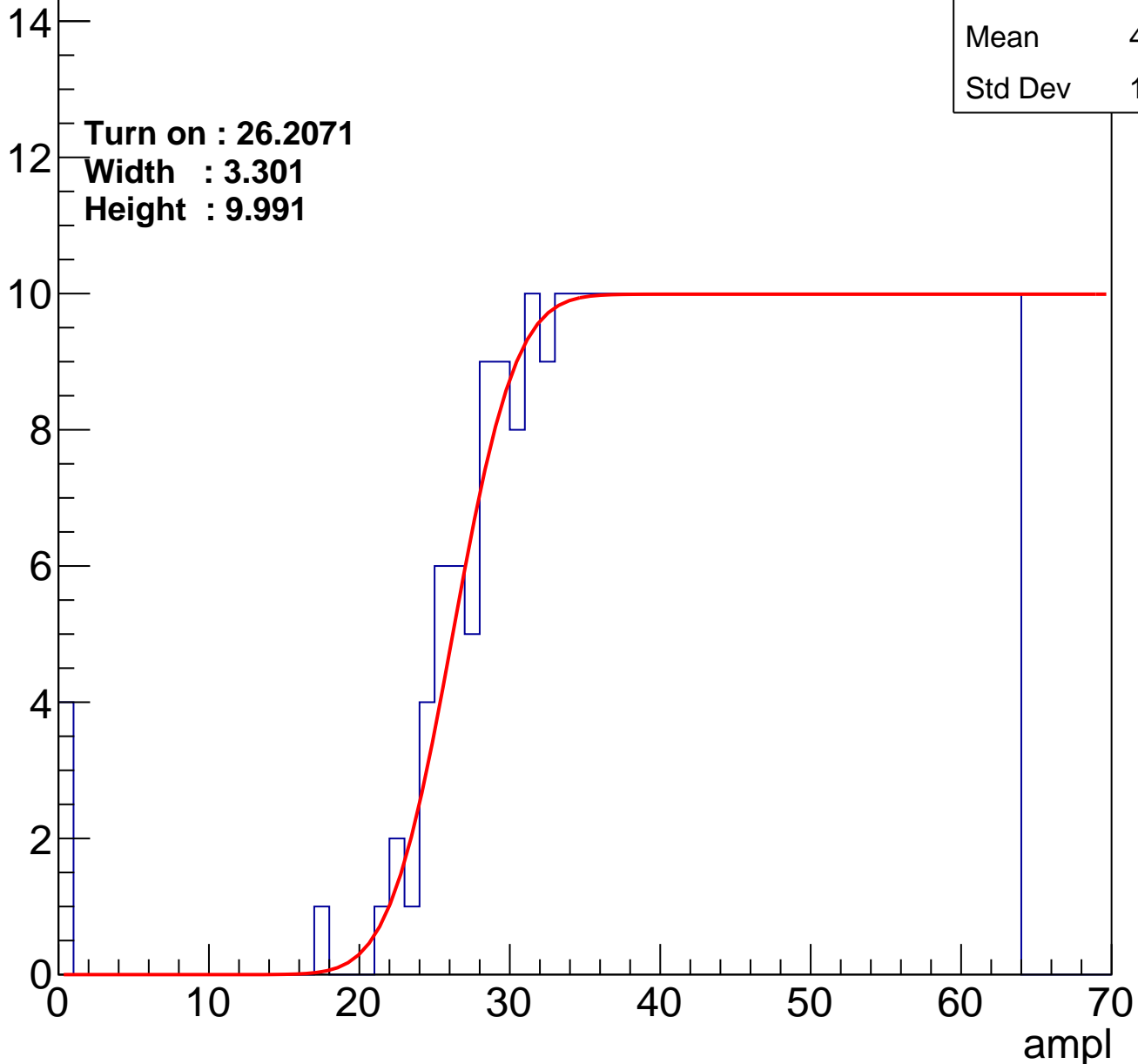
Entries	385
Mean	43.83
Std Dev	12.08

Turn on : 26.2071

Width : 3.301

Height : 9.991

Entry



B1L103S, U11-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.3
Std Dev	10.98

Turn on : 29.0833

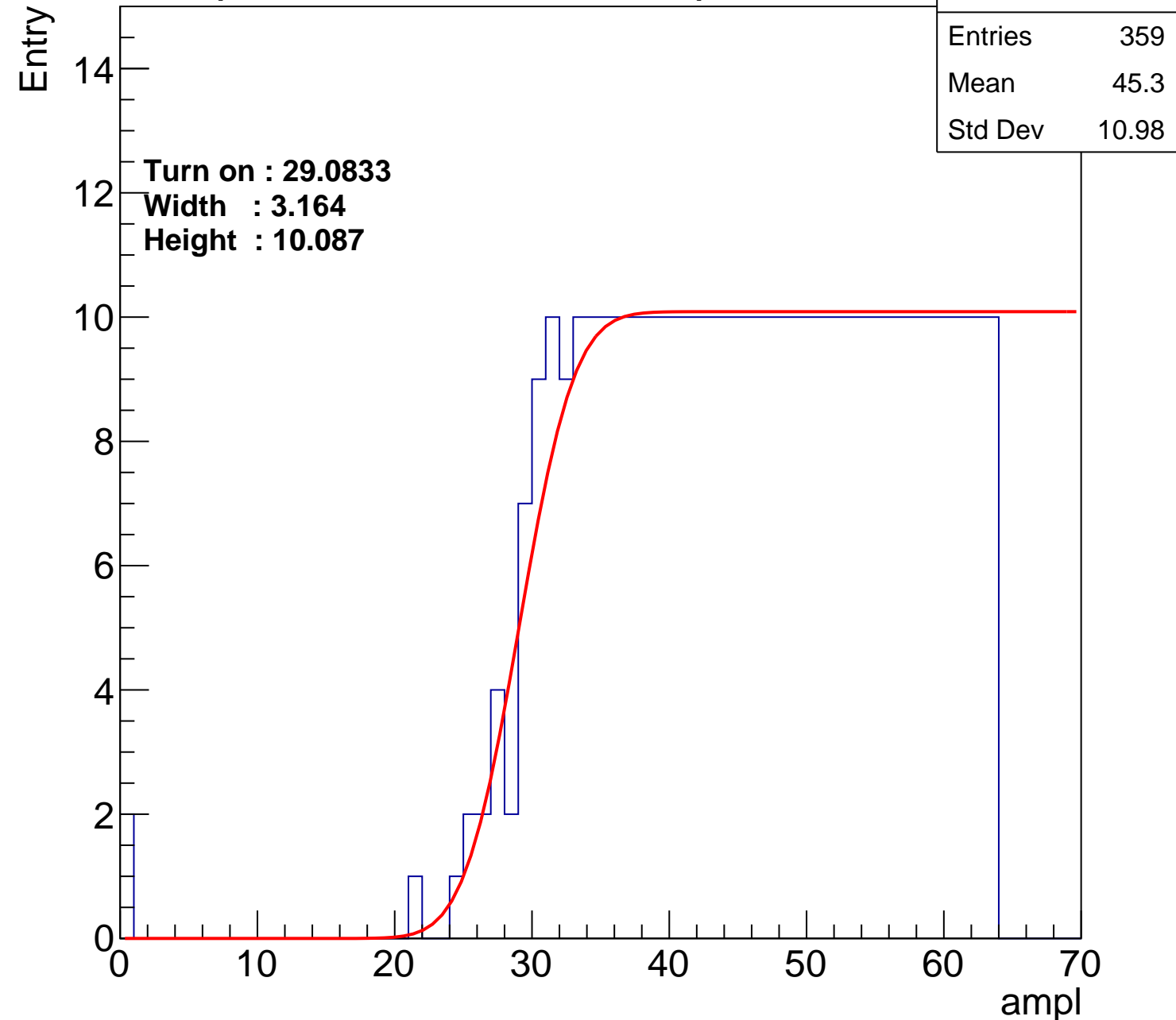
Width : 3.164

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.6973

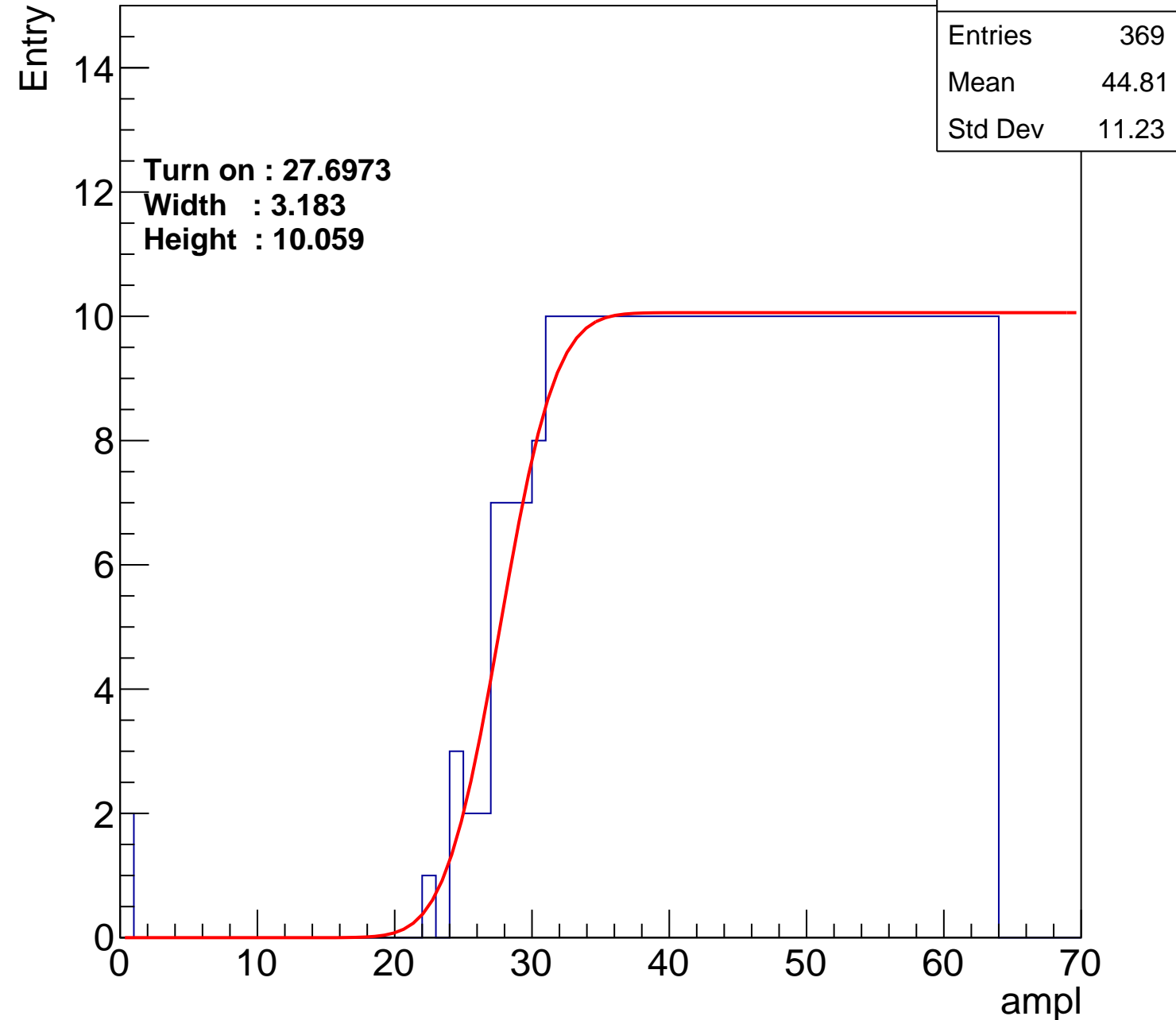
Width : 3.183

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch49

calib_packv5_042523_0143.root, FC#7, port C2

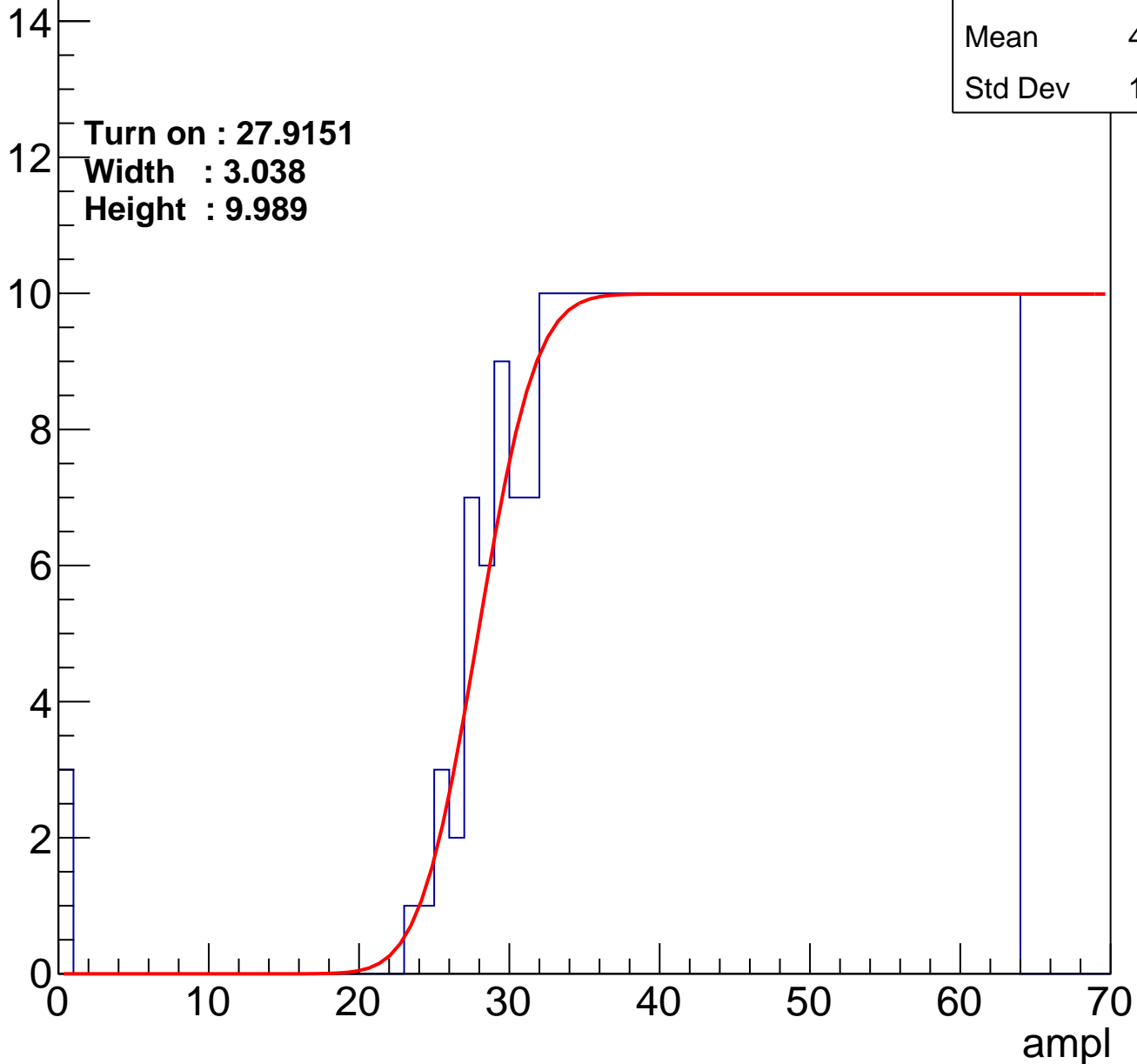
Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.9151

Width : 3.038

Height : 9.989

Entry



B1L103S, U11-ch50

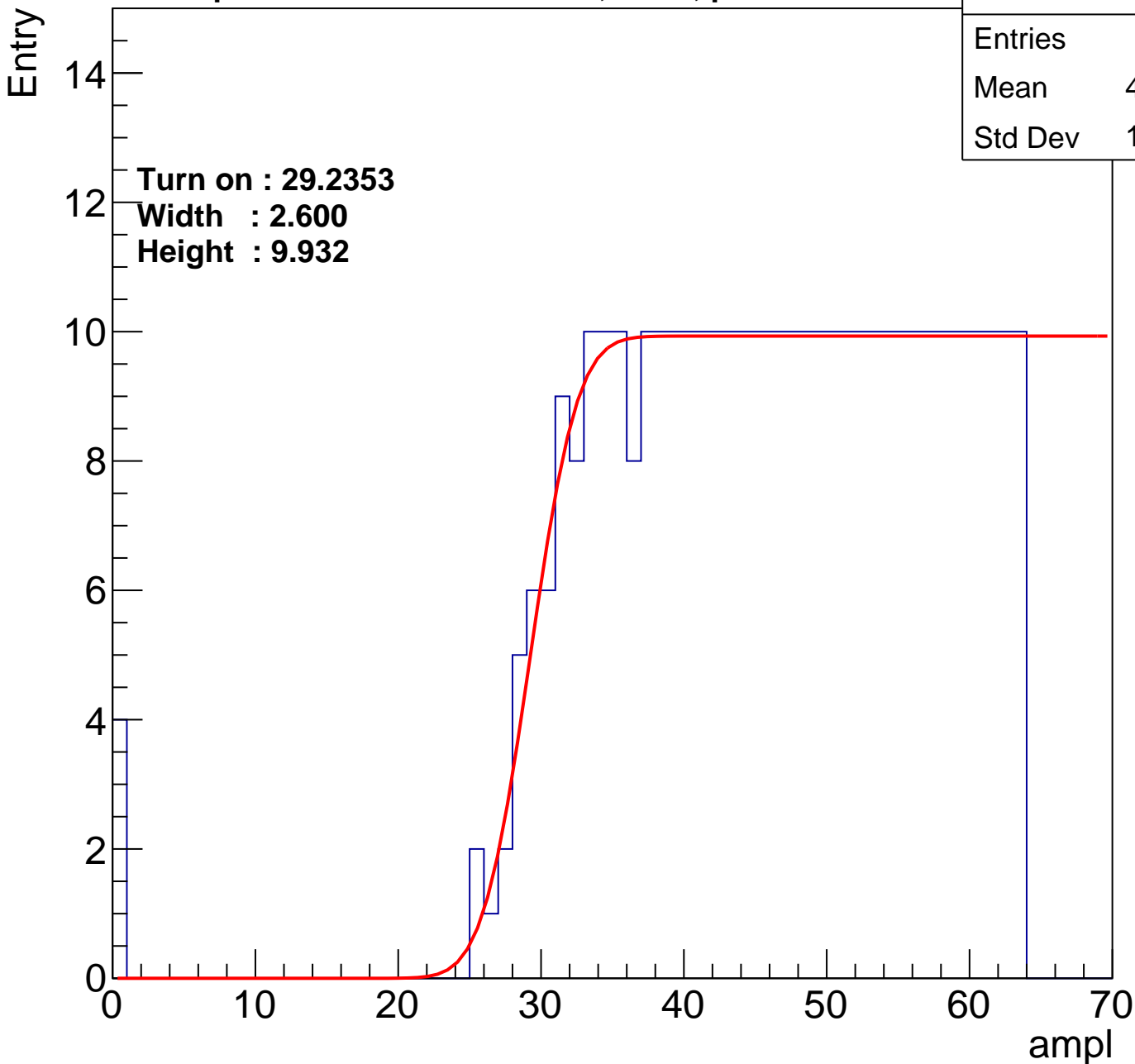
calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.49
Std Dev	11.28

Turn on : 29.2353

Width : 2.600

Height : 9.932



B1L103S, U11-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.53
Std Dev	11.46

Turn on : 26.9239

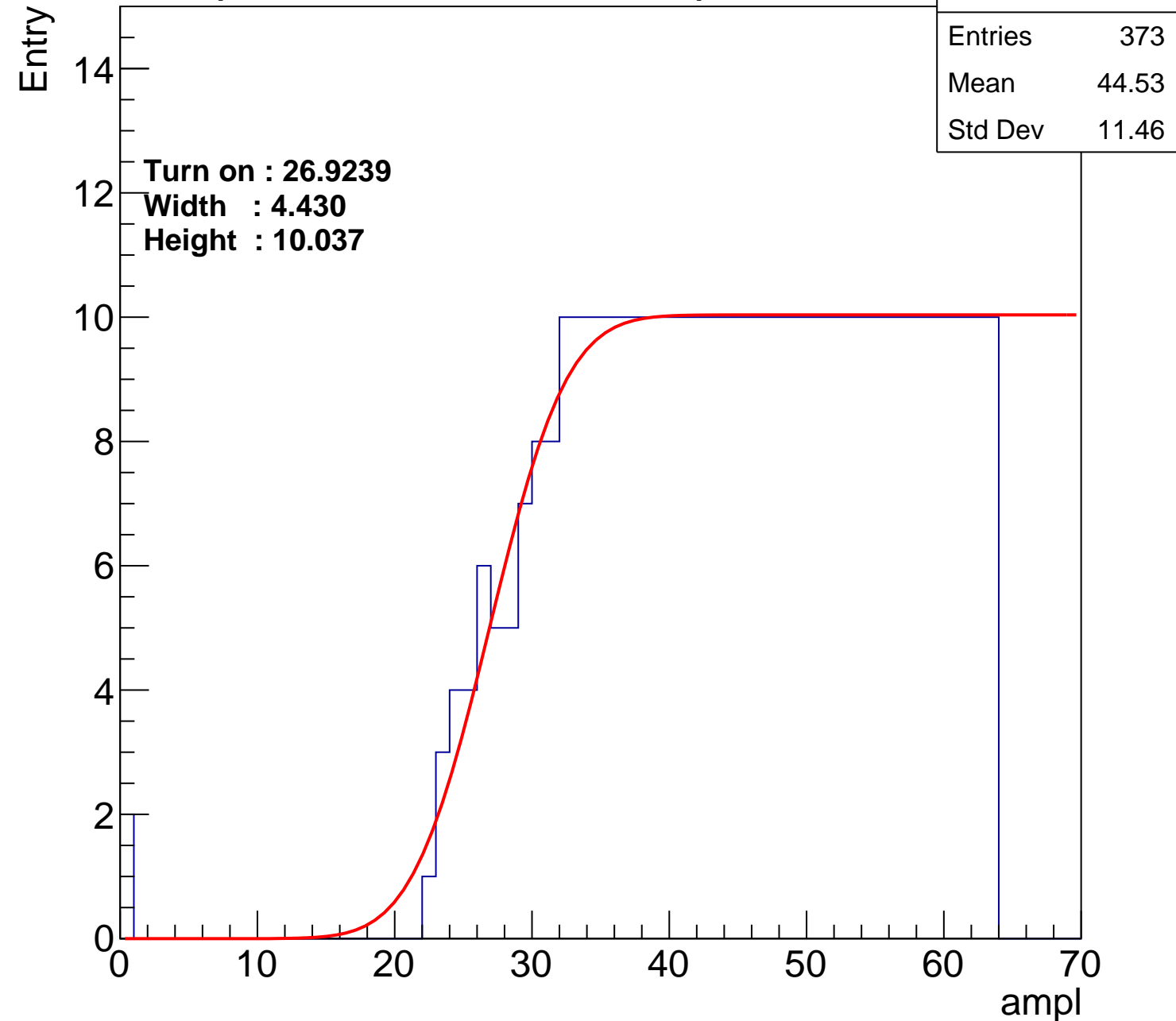
Width : 4.430

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.6760

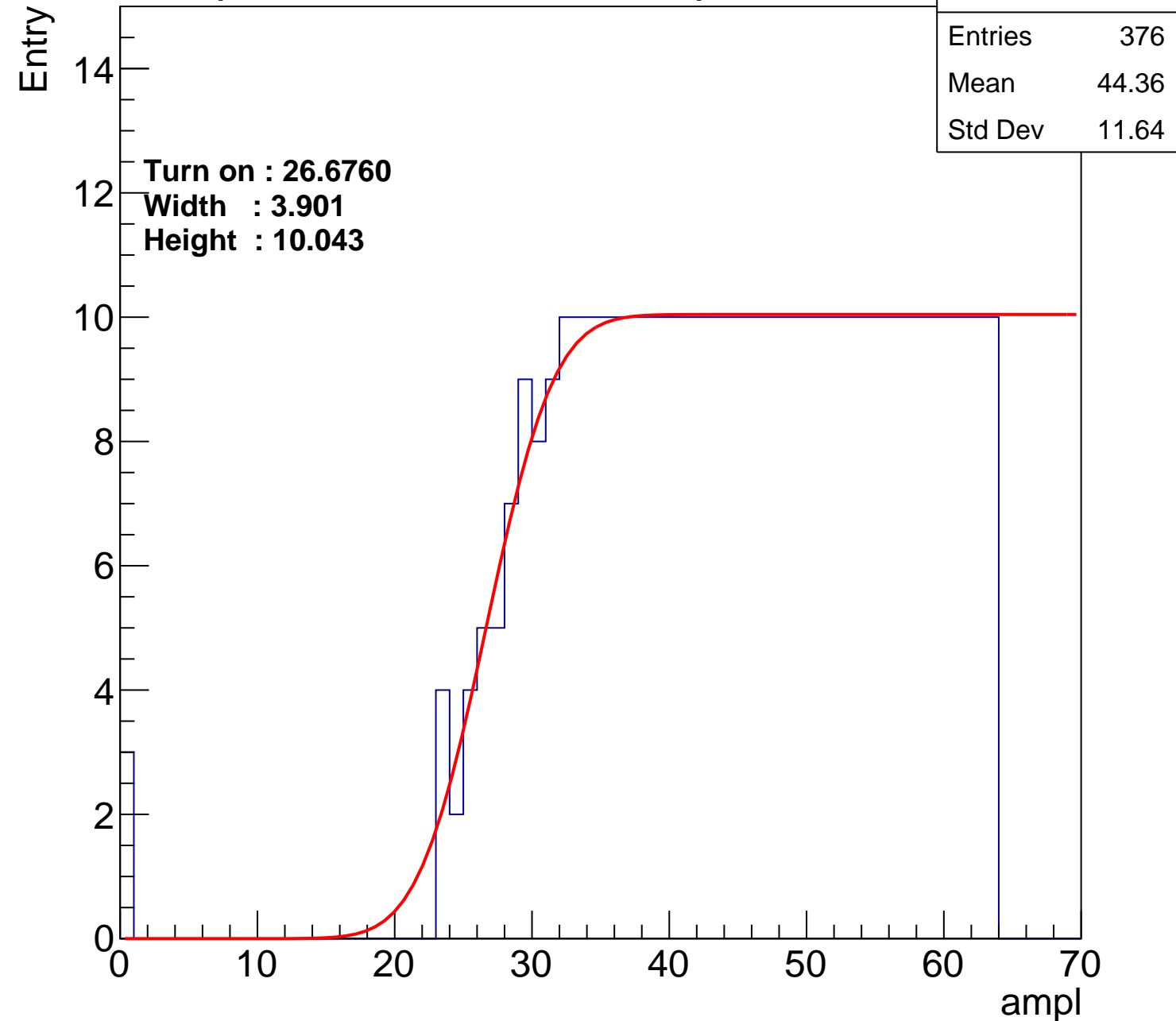
Width : 3.901

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.01
Std Dev	11.46

Turn on : 28.4625

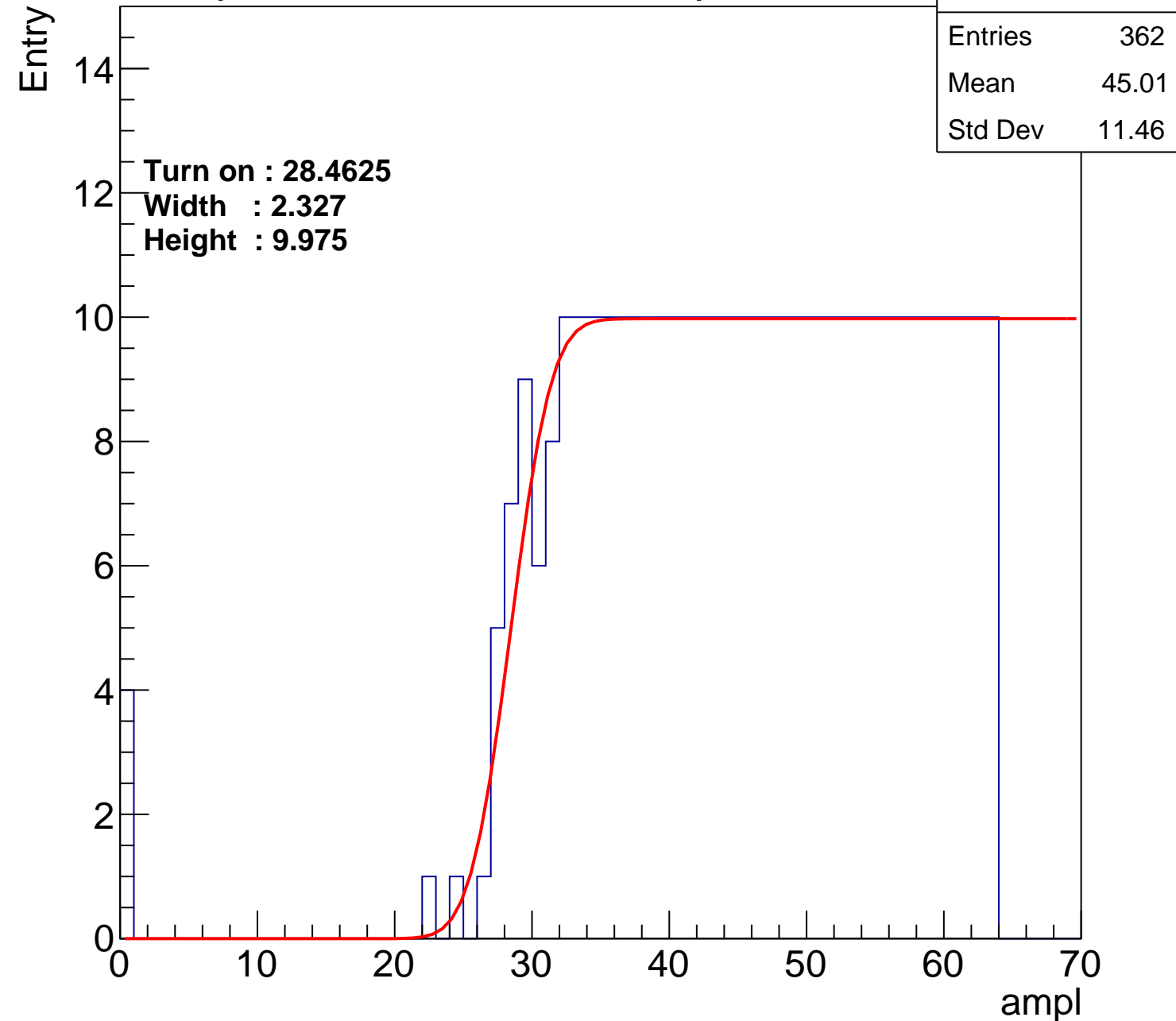
Width : 2.327

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.48
Std Dev	10.69

Turn on : 28.1838

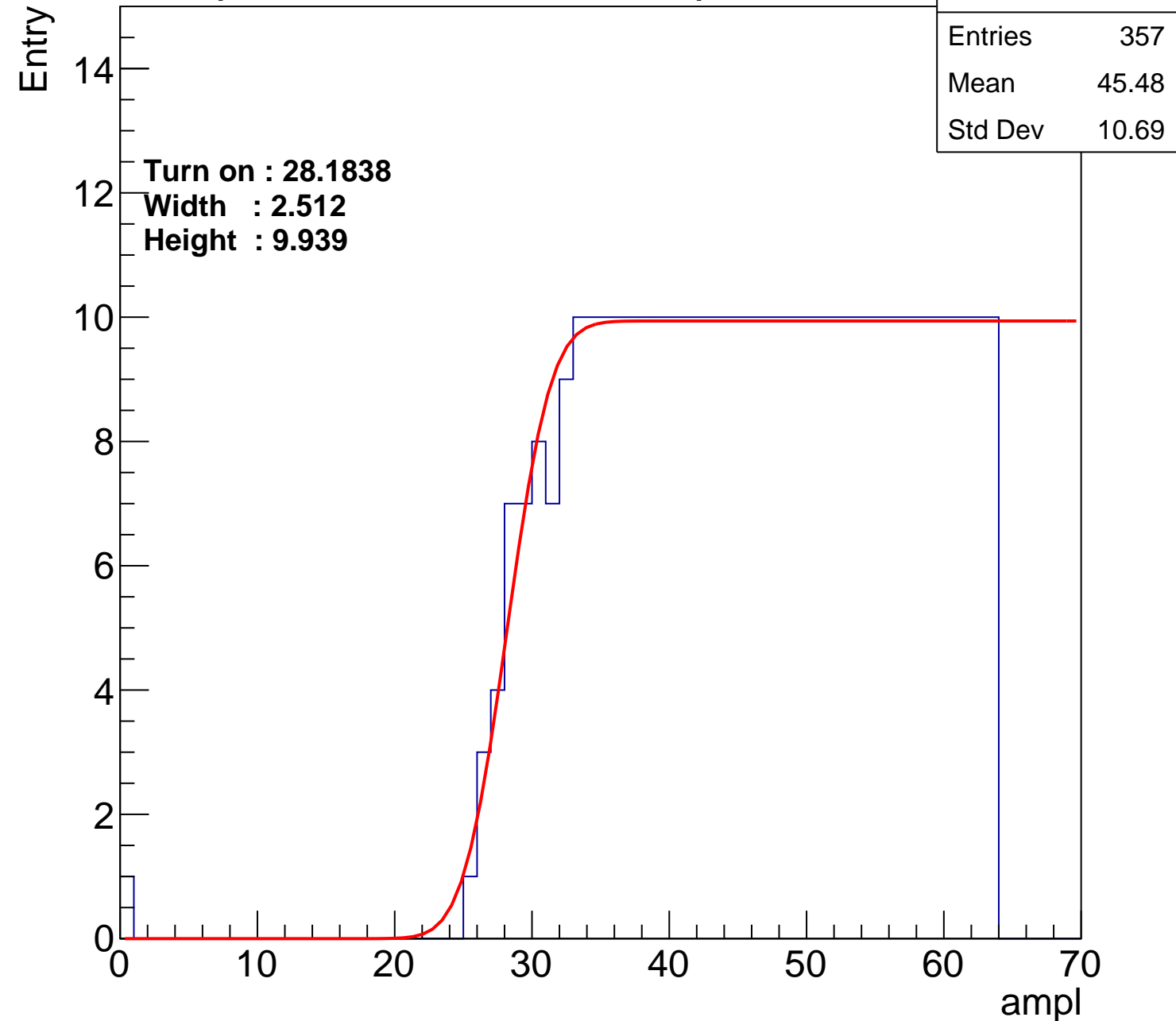
Width : 2.512

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.96
Std Dev	11

Turn on : 27.1087

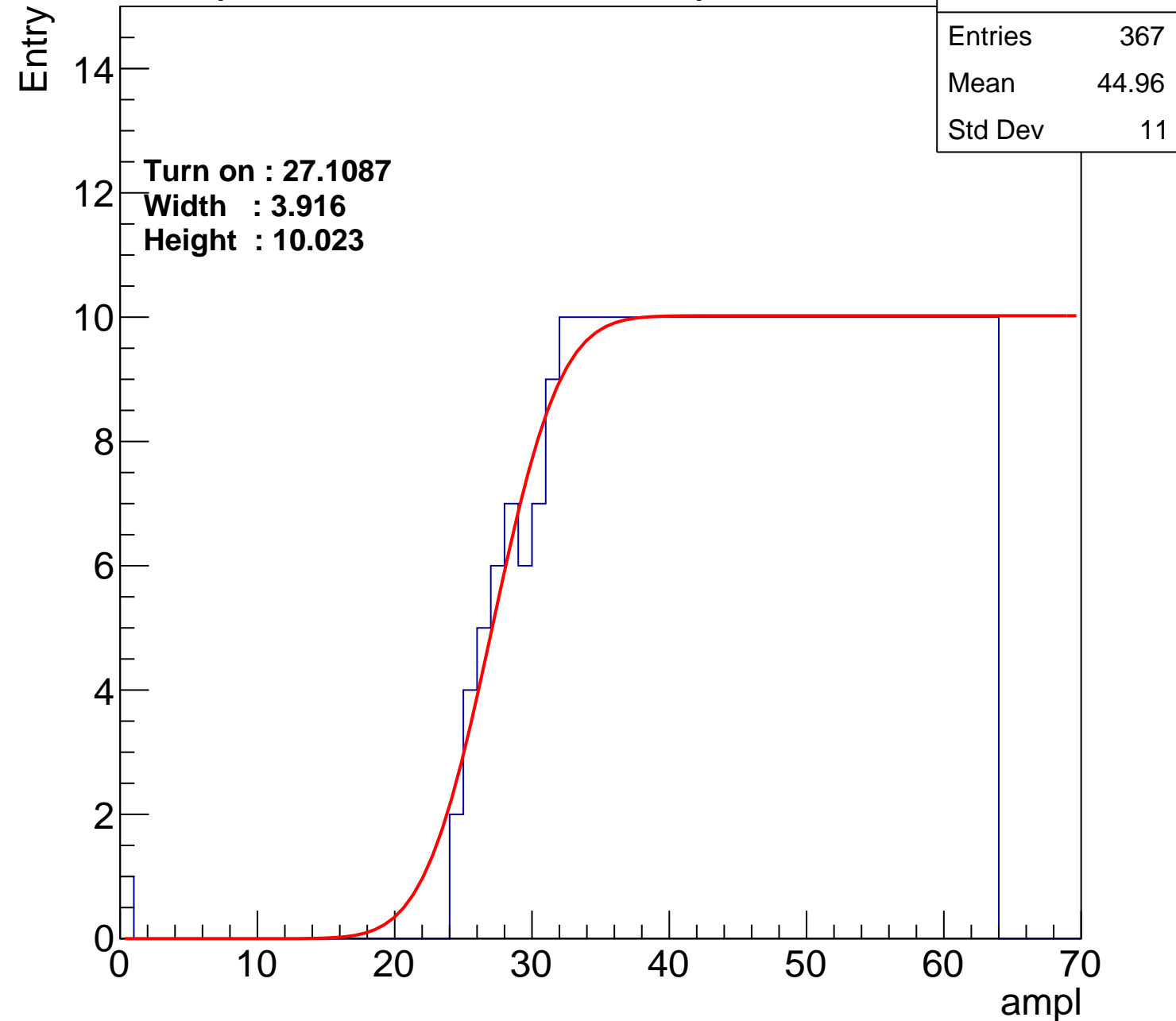
Width : 3.916

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch56

calib_packv5_042523_0143.root, FC#7, port C2

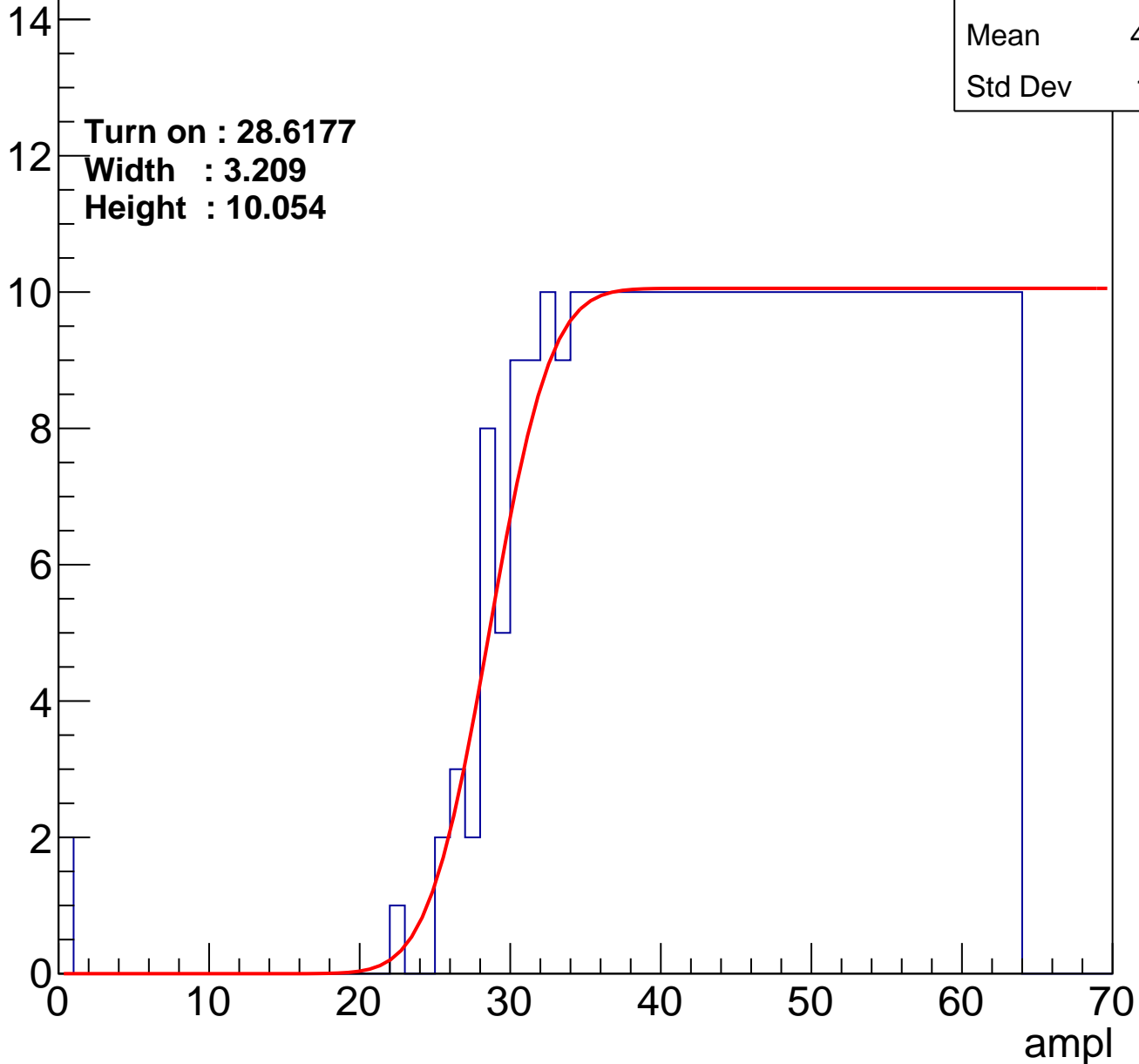
Entries	360
Mean	45.25
Std Dev	11.01

Turn on : 28.6177

Width : 3.209

Height : 10.054

Entry



B1L103S, U11-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.8
Std Dev	11.23

Turn on : 28.1479

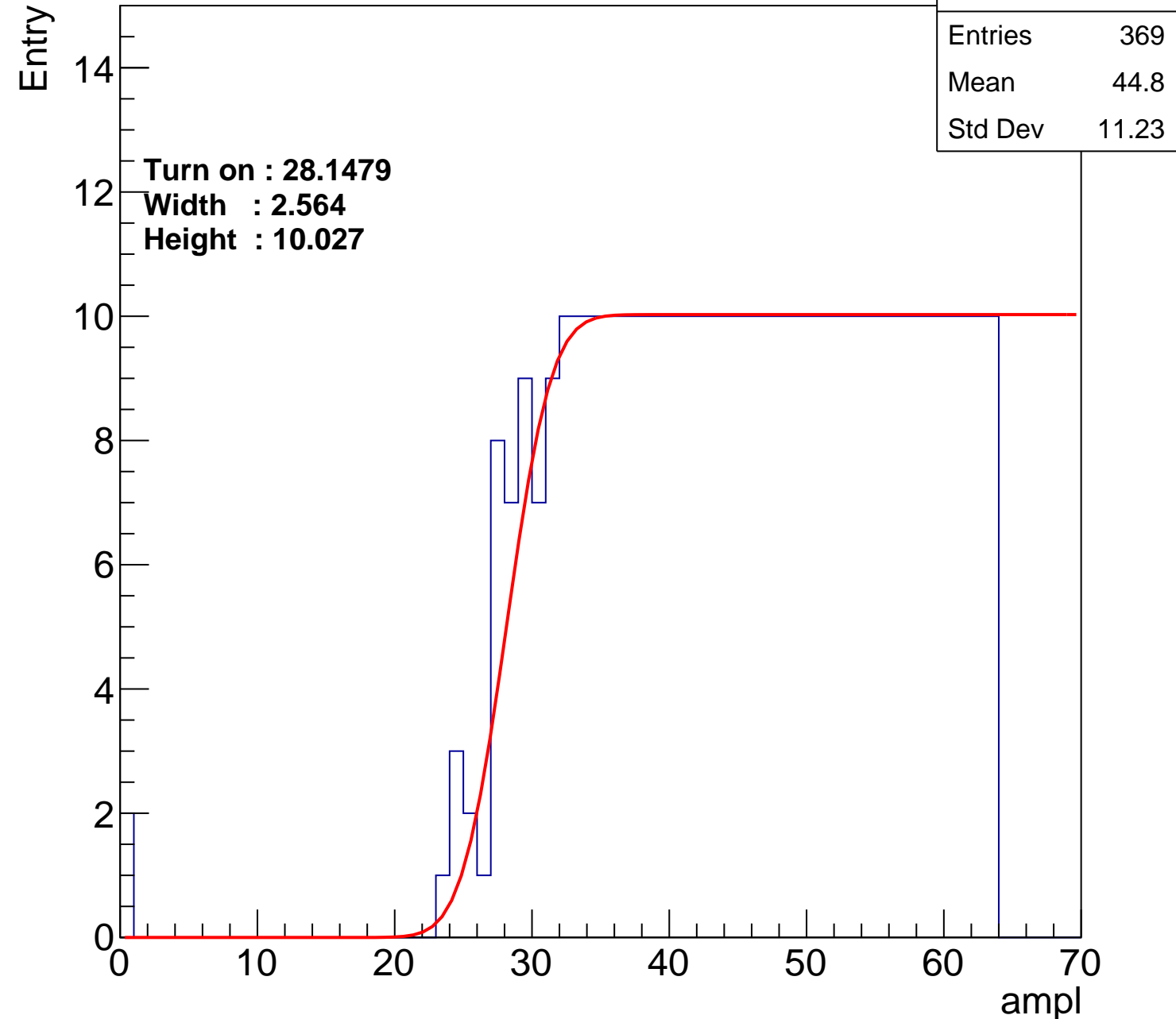
Width : 2.564

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.49
Std Dev	11.43

Turn on : 27.1946

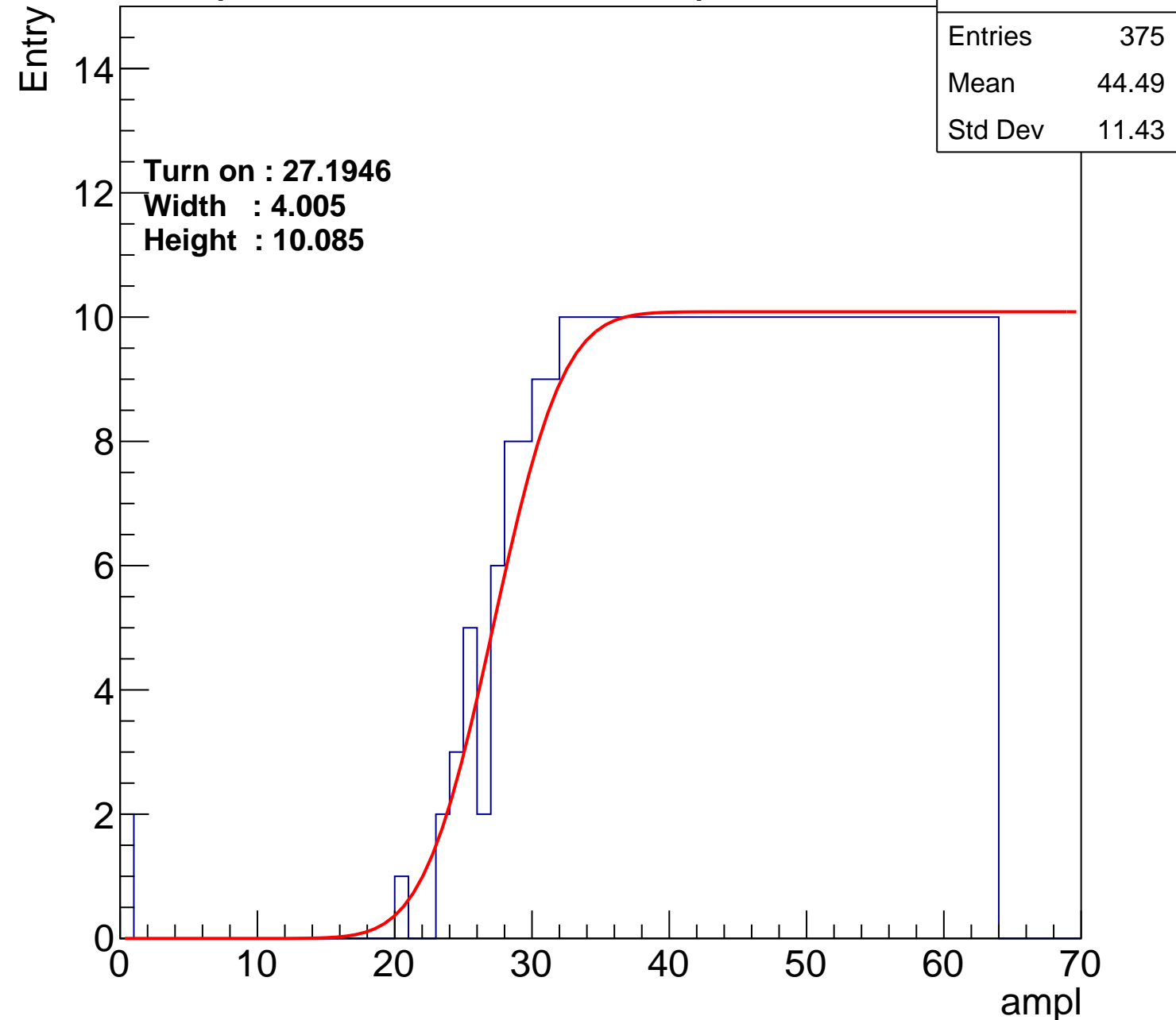
Width : 4.005

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	45
Std Dev	11.14

Turn on : 28.0775

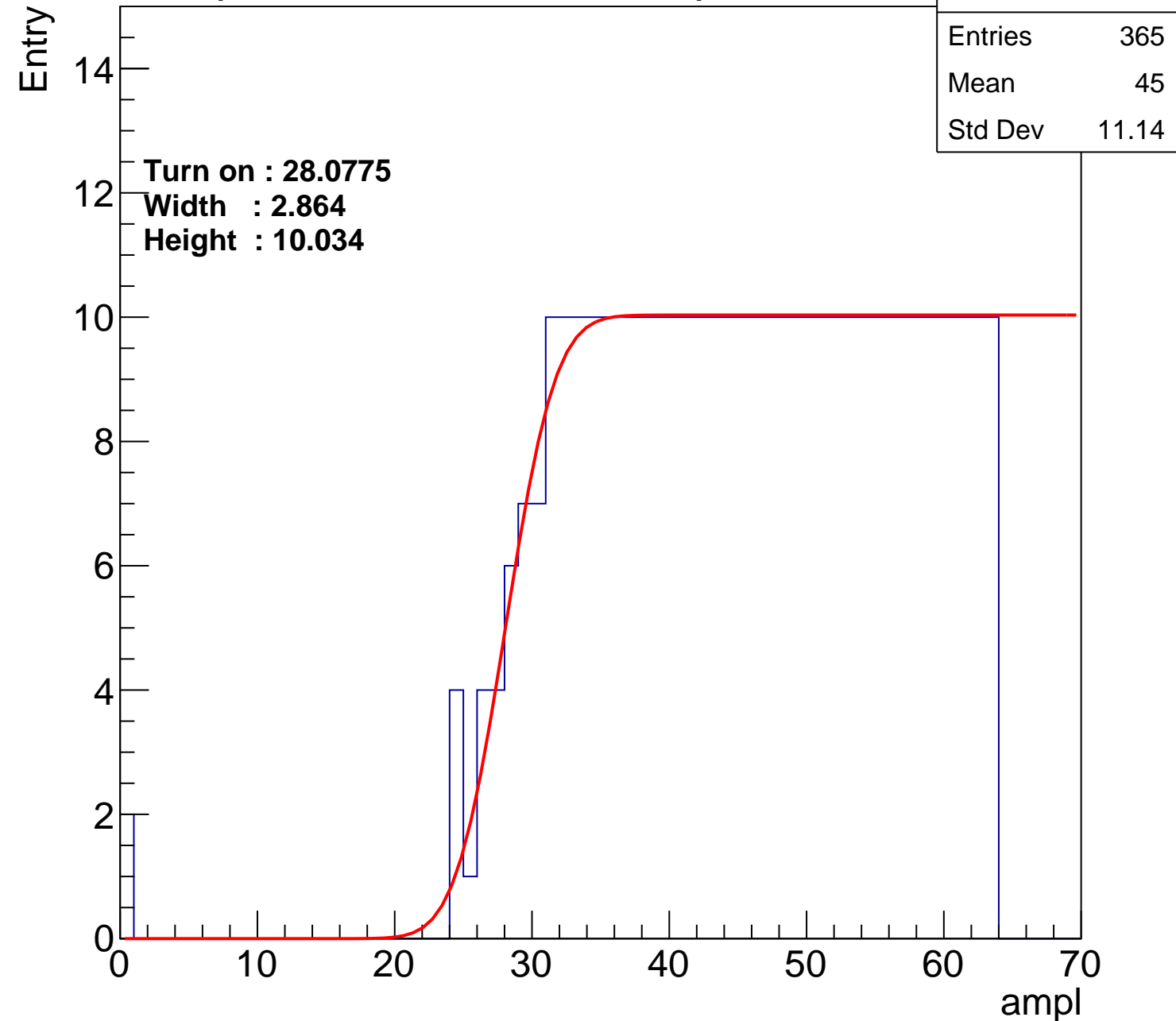
Width : 2.864

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch60

calib_packv5_042523_0143.root, FC#7, port C2

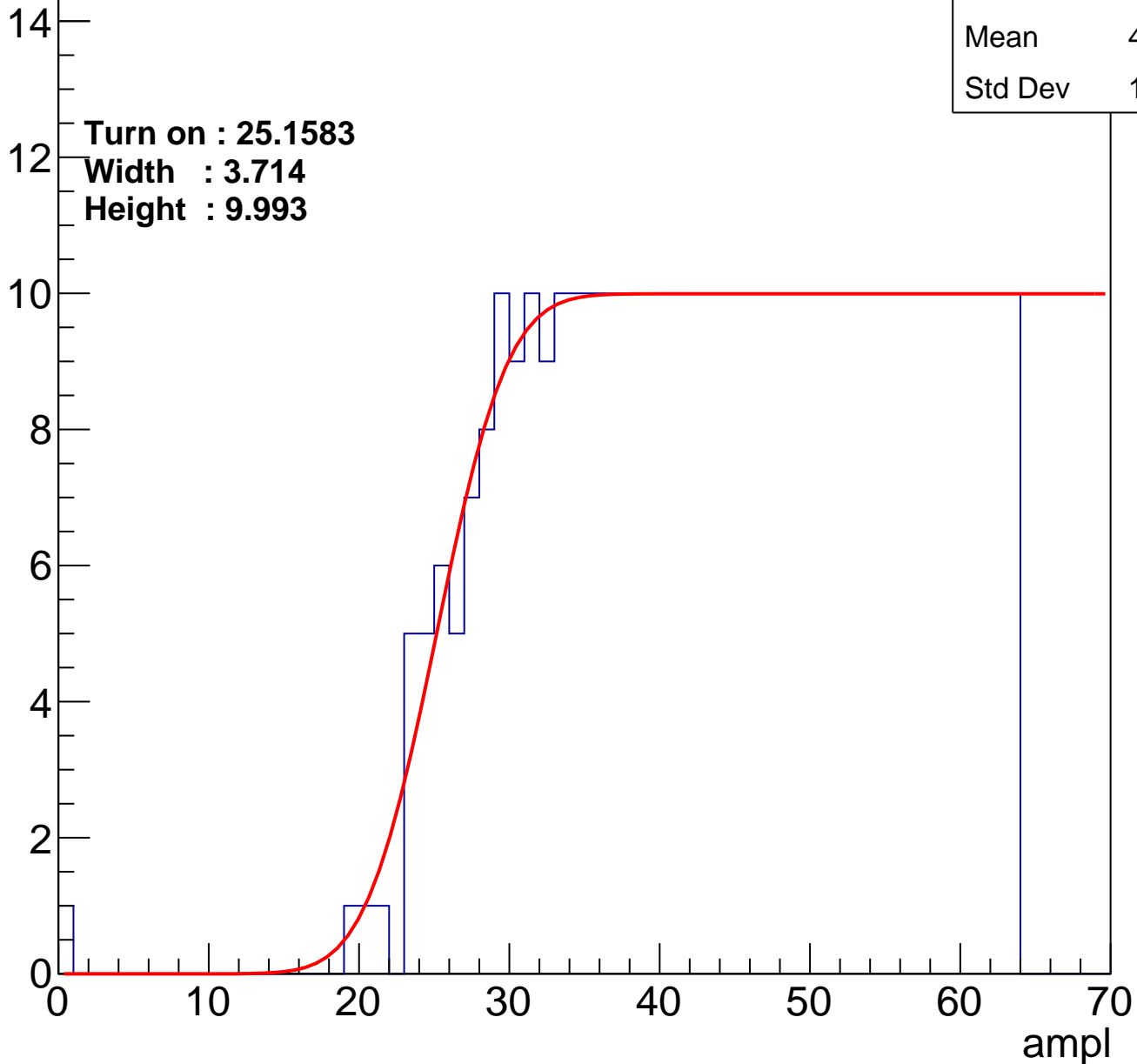
Entries	388
Mean	43.88
Std Dev	11.63

Turn on : 25.1583

Width : 3.714

Height : 9.993

Entry



B1L103S, U11-ch61

calib_packv5_042523_0143.root, FC#7, port C2

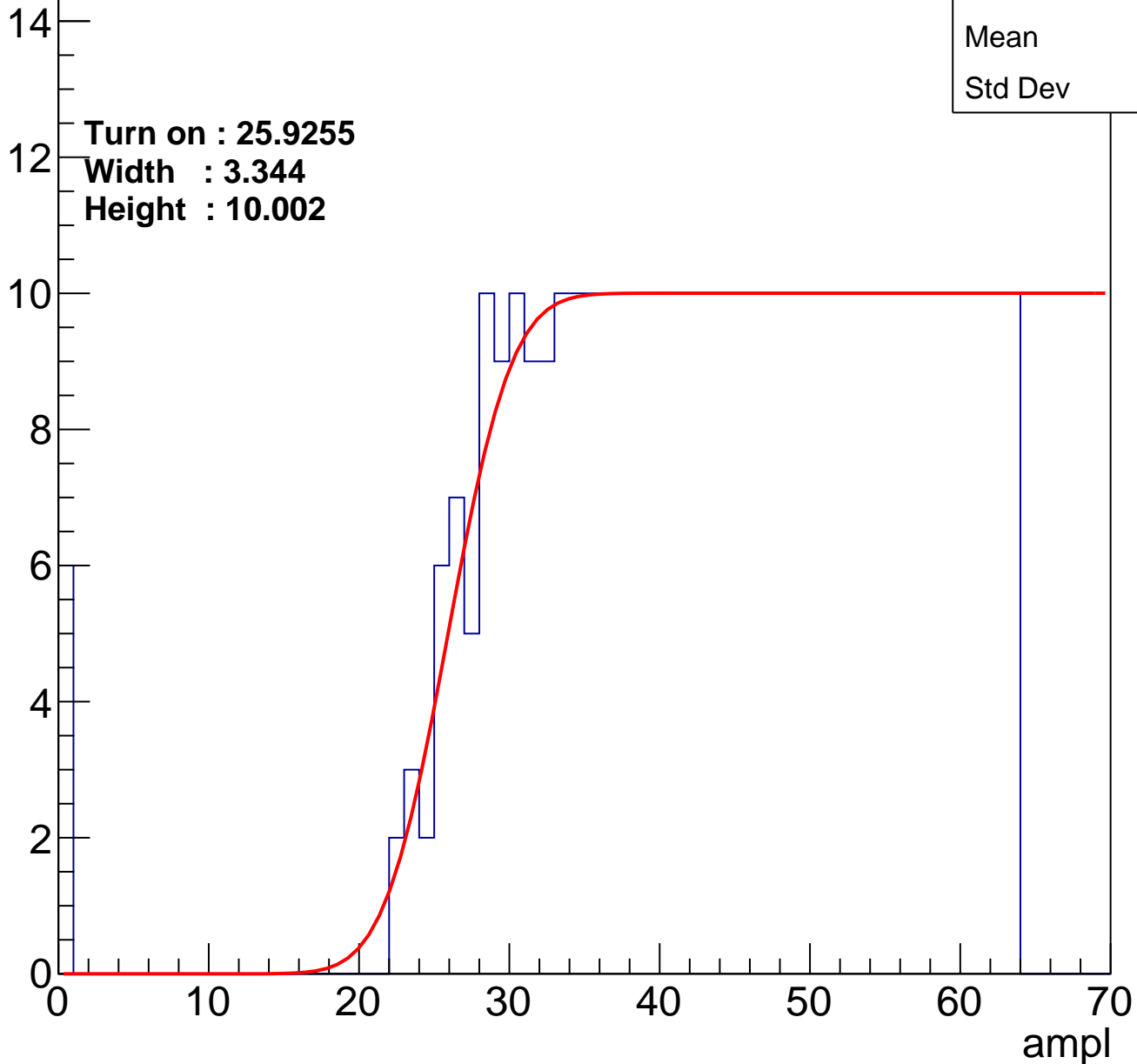
Entries	388
Mean	43.6
Std Dev	12.4

Turn on : 25.9255

Width : 3.344

Height : 10.002

Entry



B1L103S, U11-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.9992

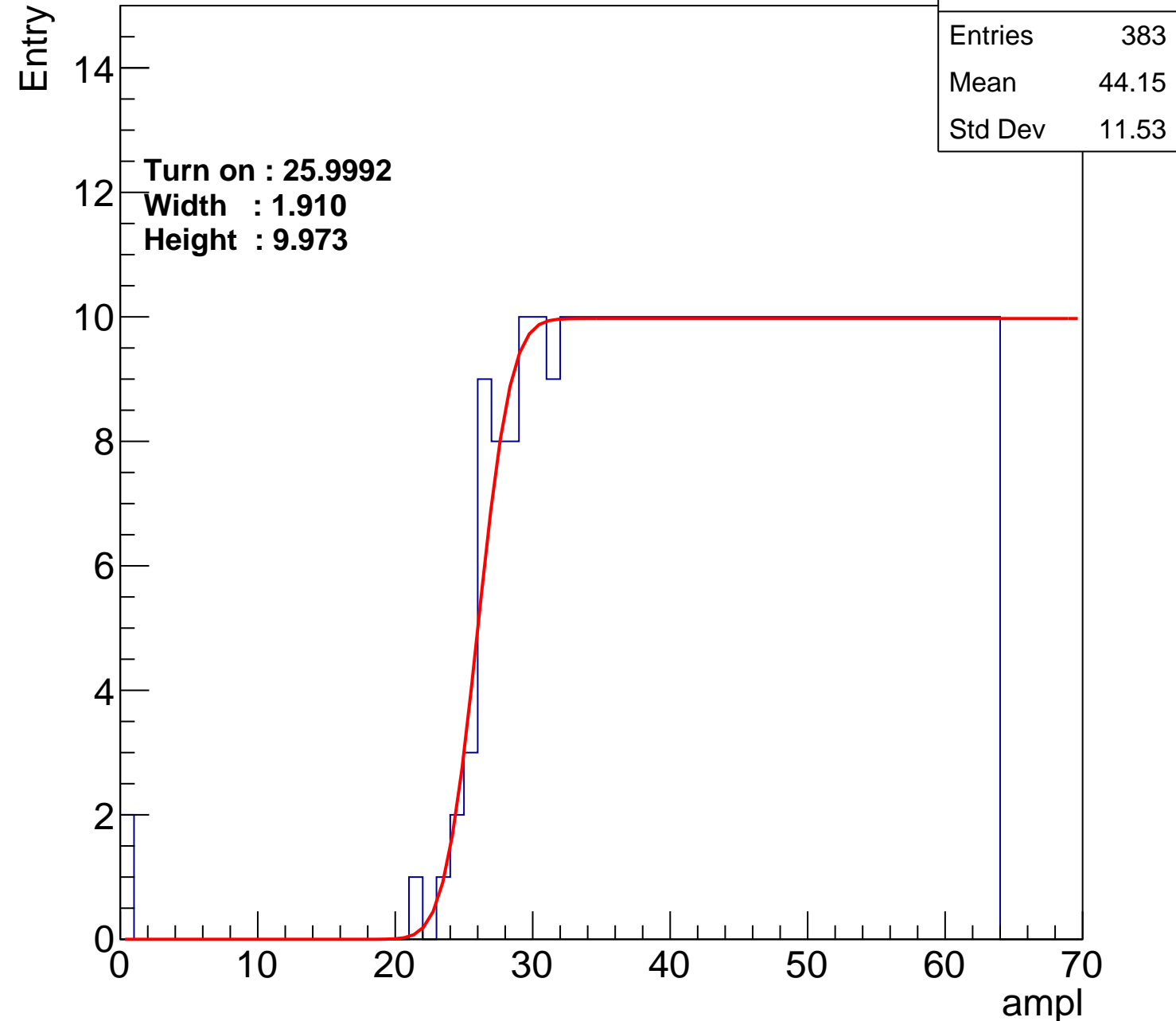
Width : 1.910

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.28
Std Dev	11.06

Turn on : 28.9596

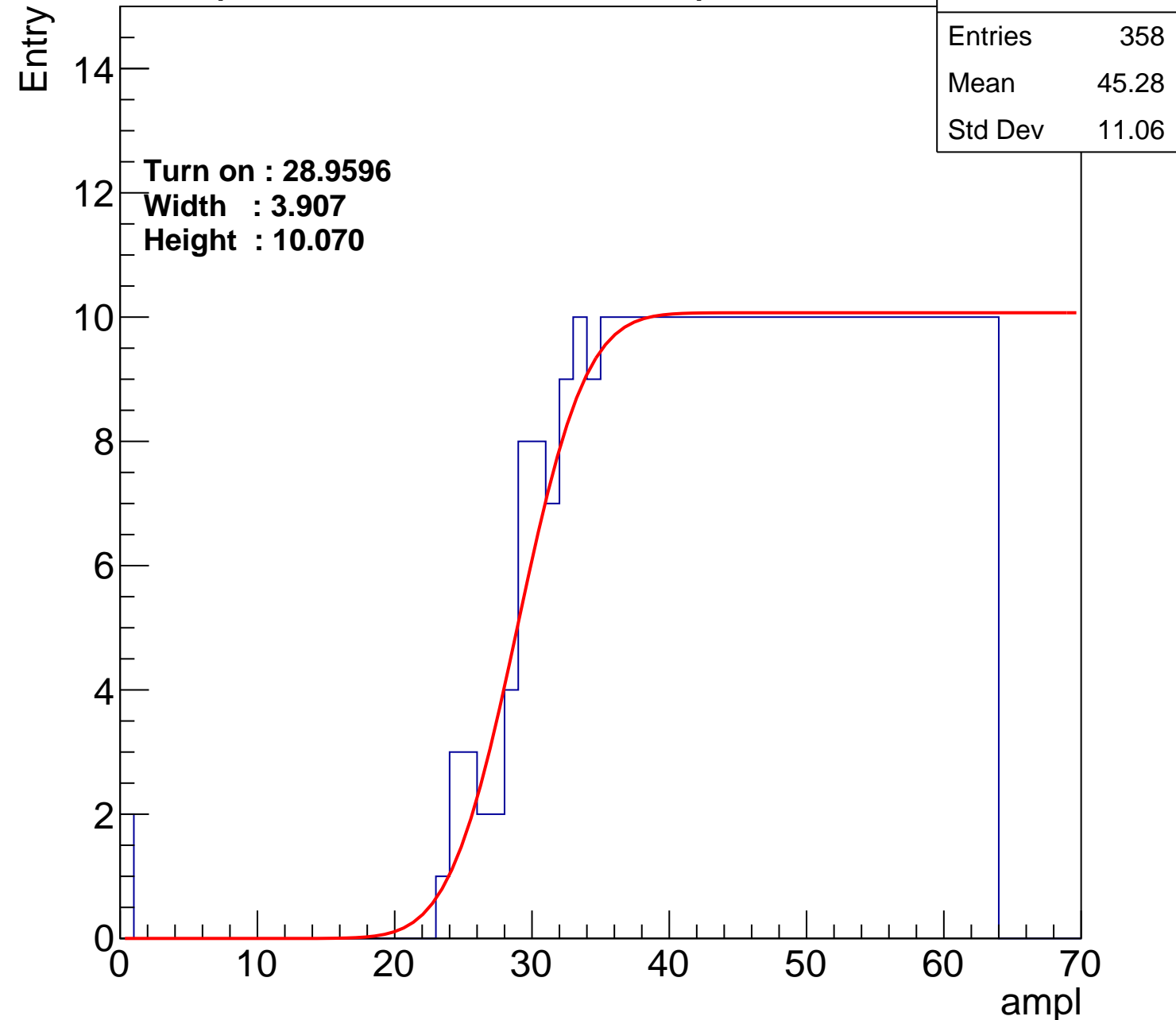
Width : 3.907

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.55
Std Dev	11.7

Turn on : 27.8403

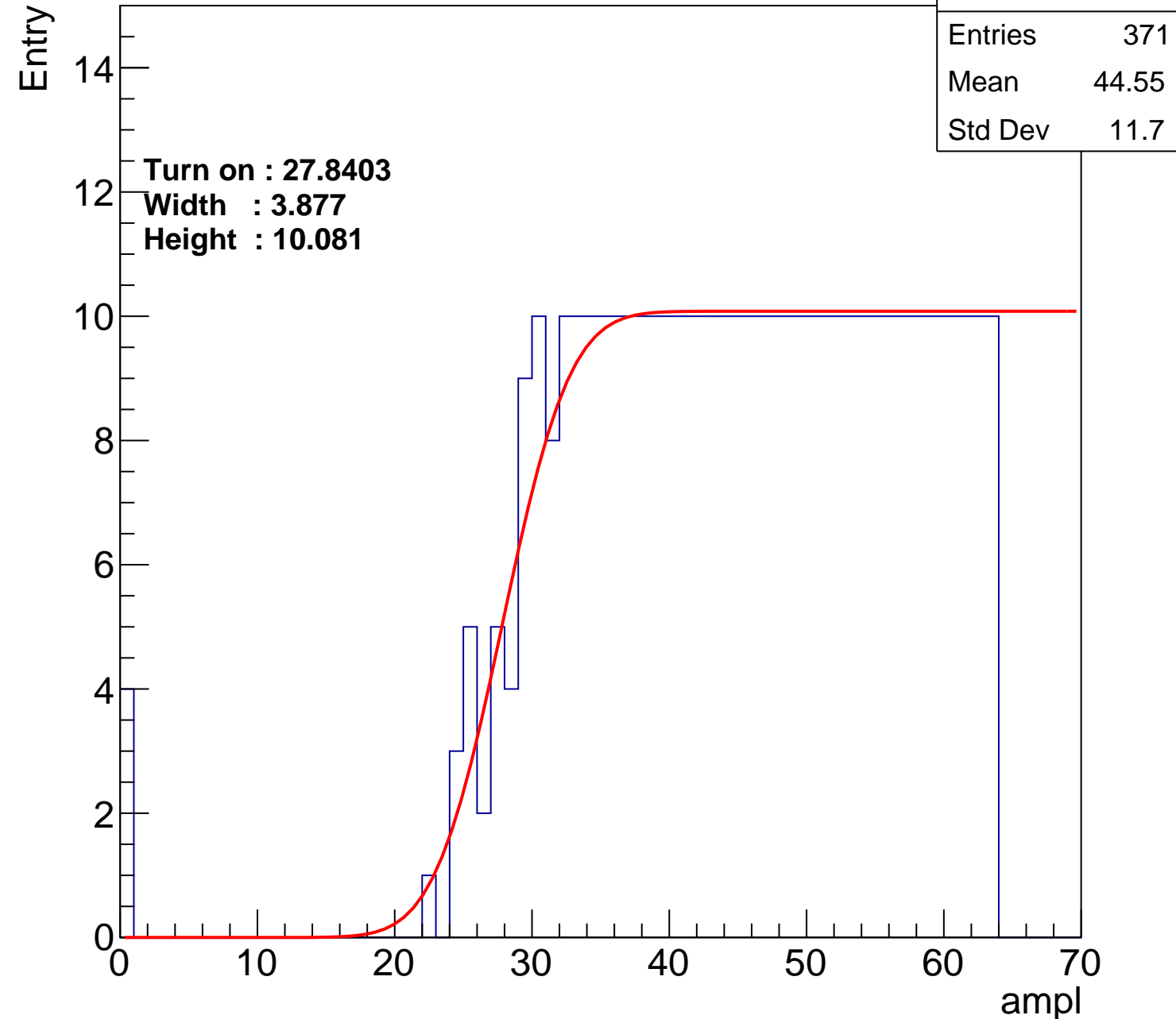
Width : 3.877

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	43.88
Std Dev	12.46

Turn on : 26.7571

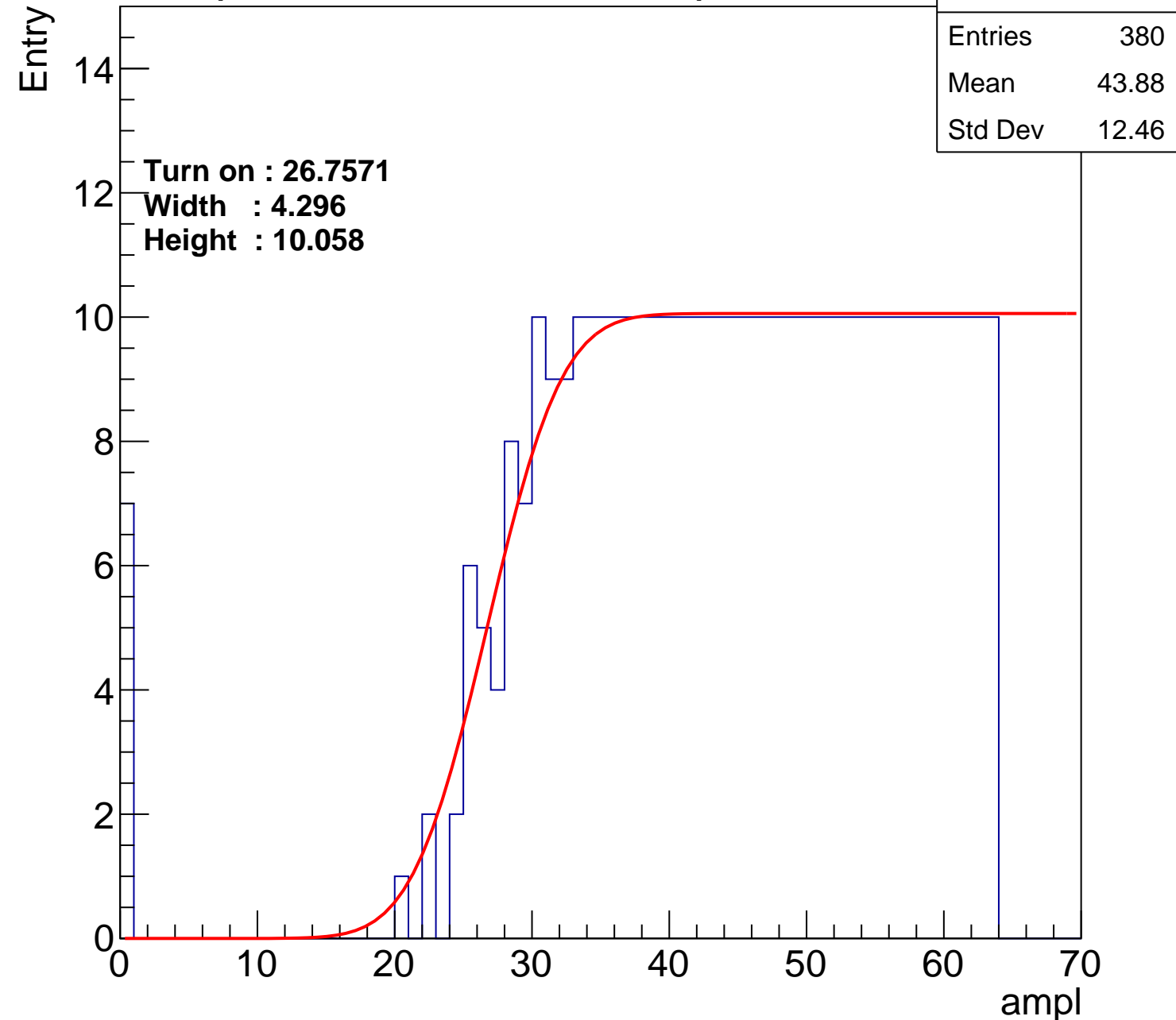
Width : 4.296

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch66

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.17
Std Dev	11.22

Turn on : 28.6629

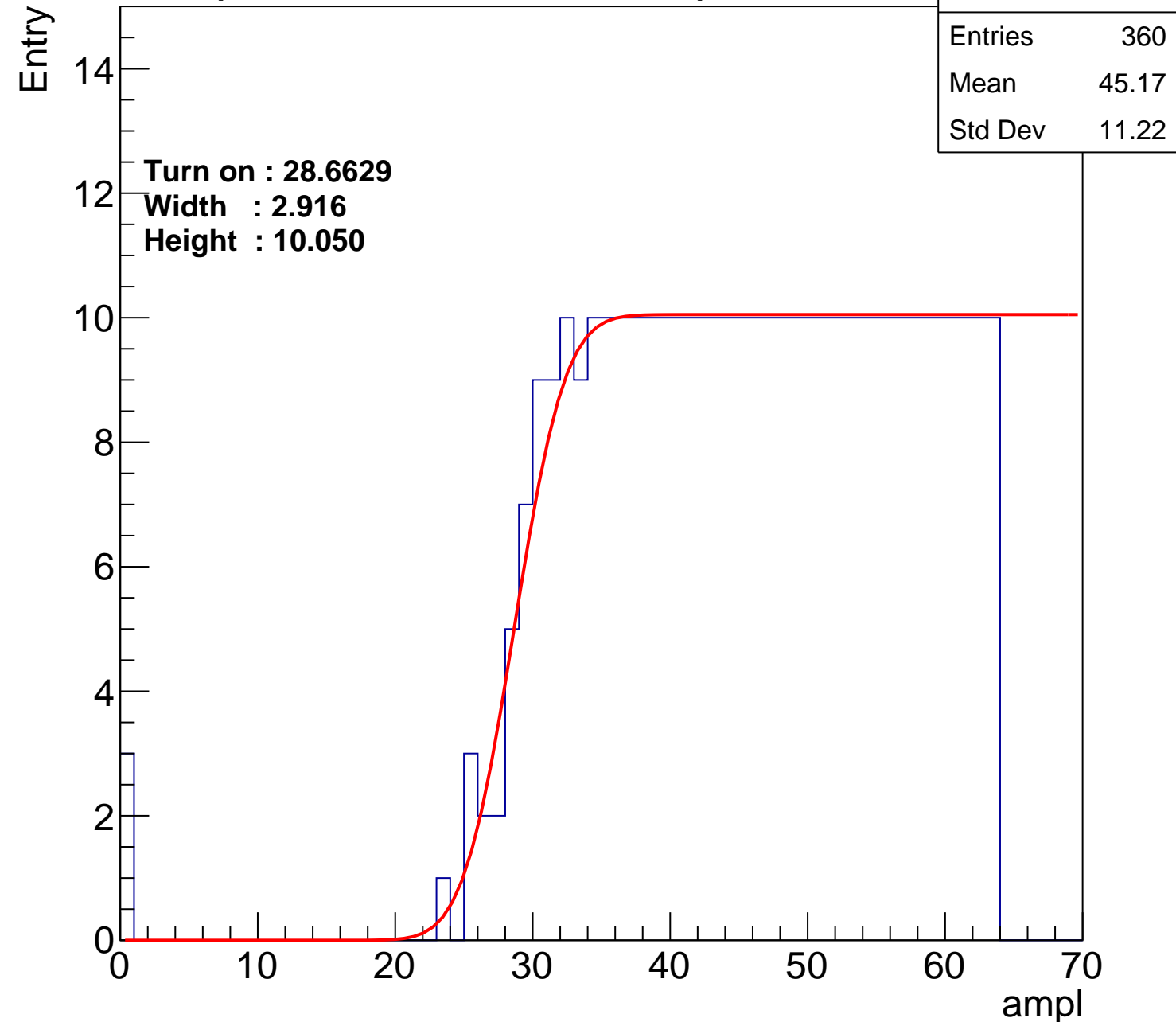
Width : 2.916

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.29
Std Dev	11.19

Turn on : 28.8260

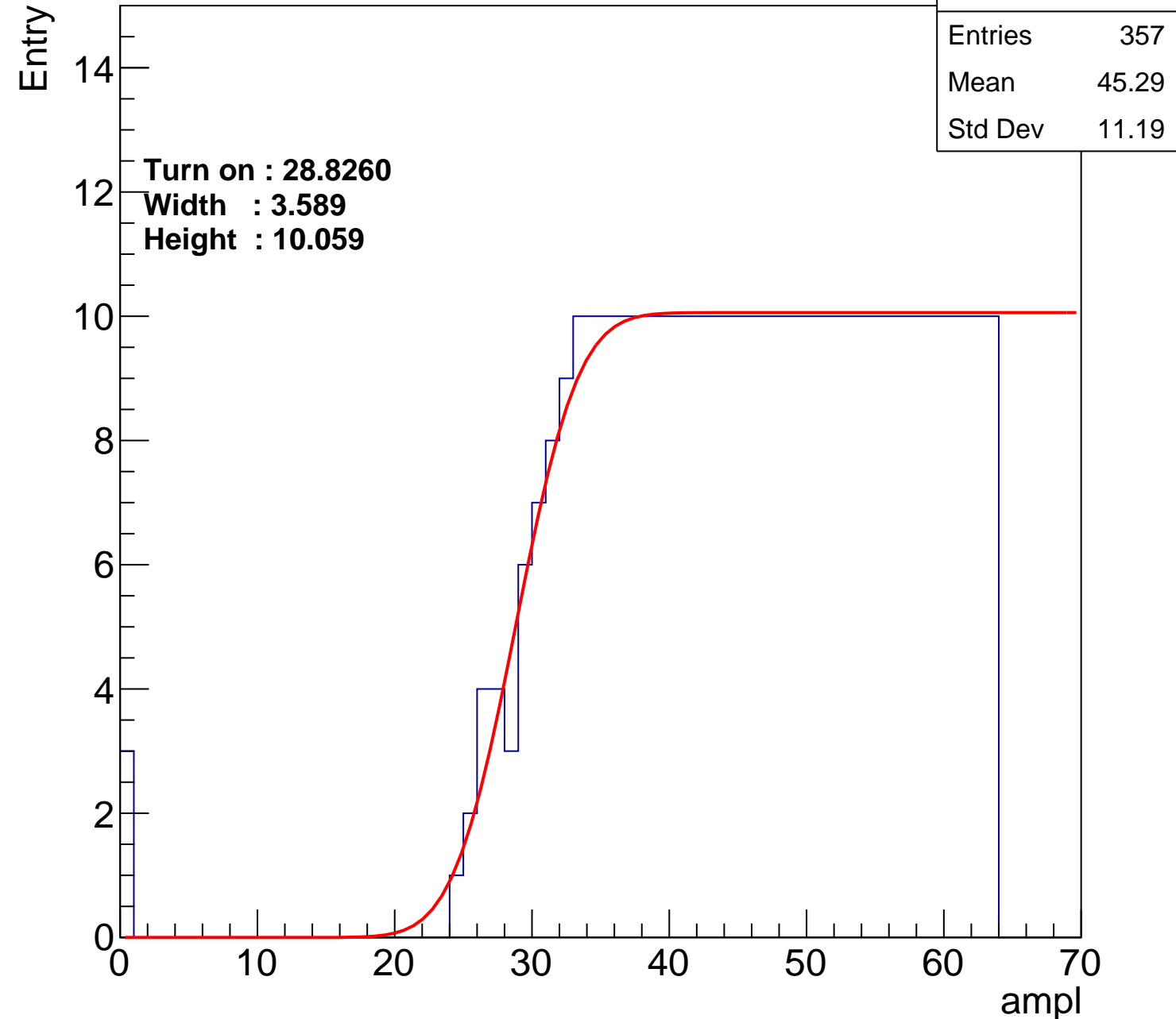
Width : 3.589

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.14
Std Dev	10.93

Turn on : 28.3557

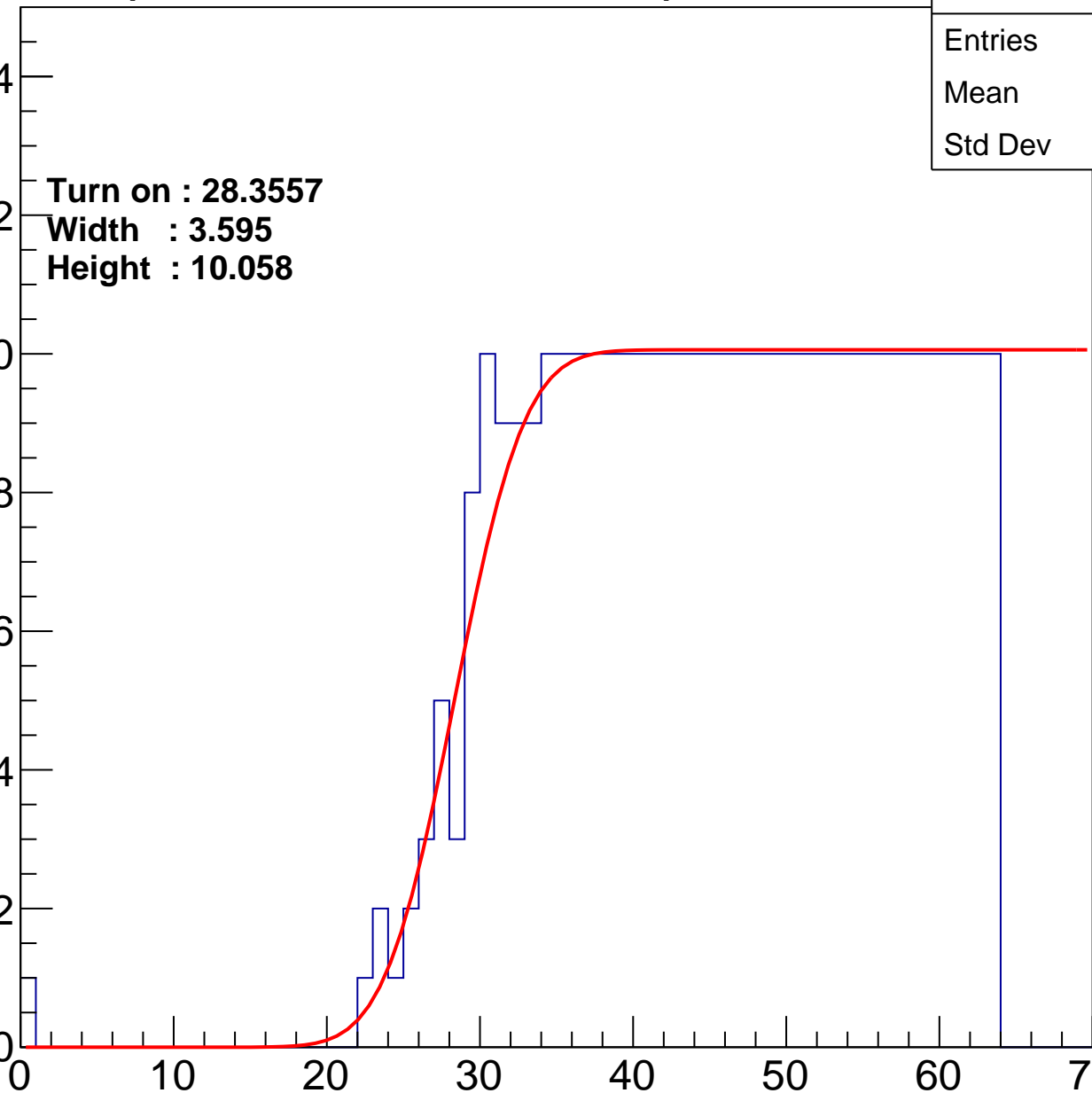
Width : 3.595

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.2
Std Dev	11.29

Turn on : 28.9298

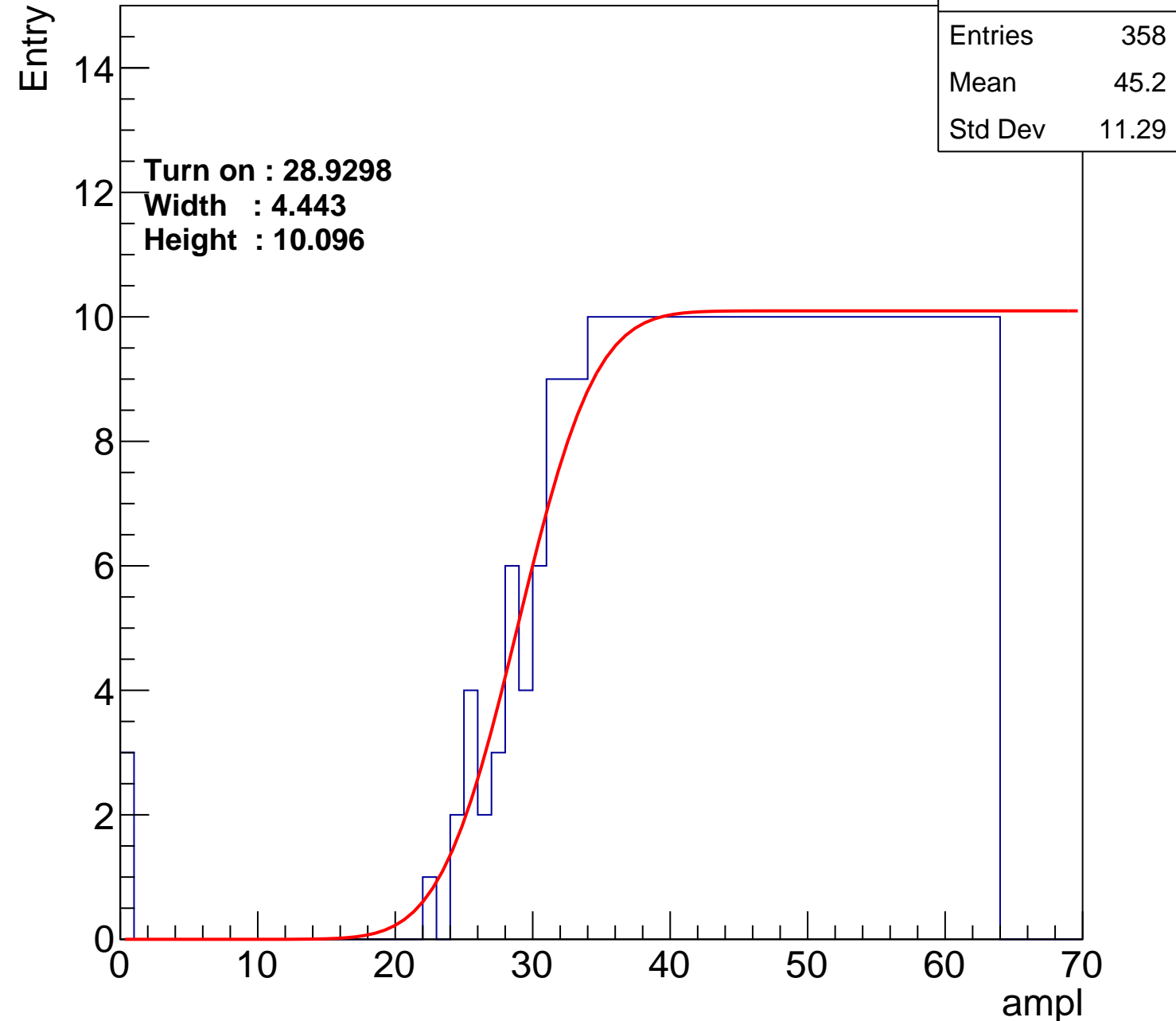
Width : 4.443

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch70

calib_packv5_042523_0143.root, FC#7, port C2

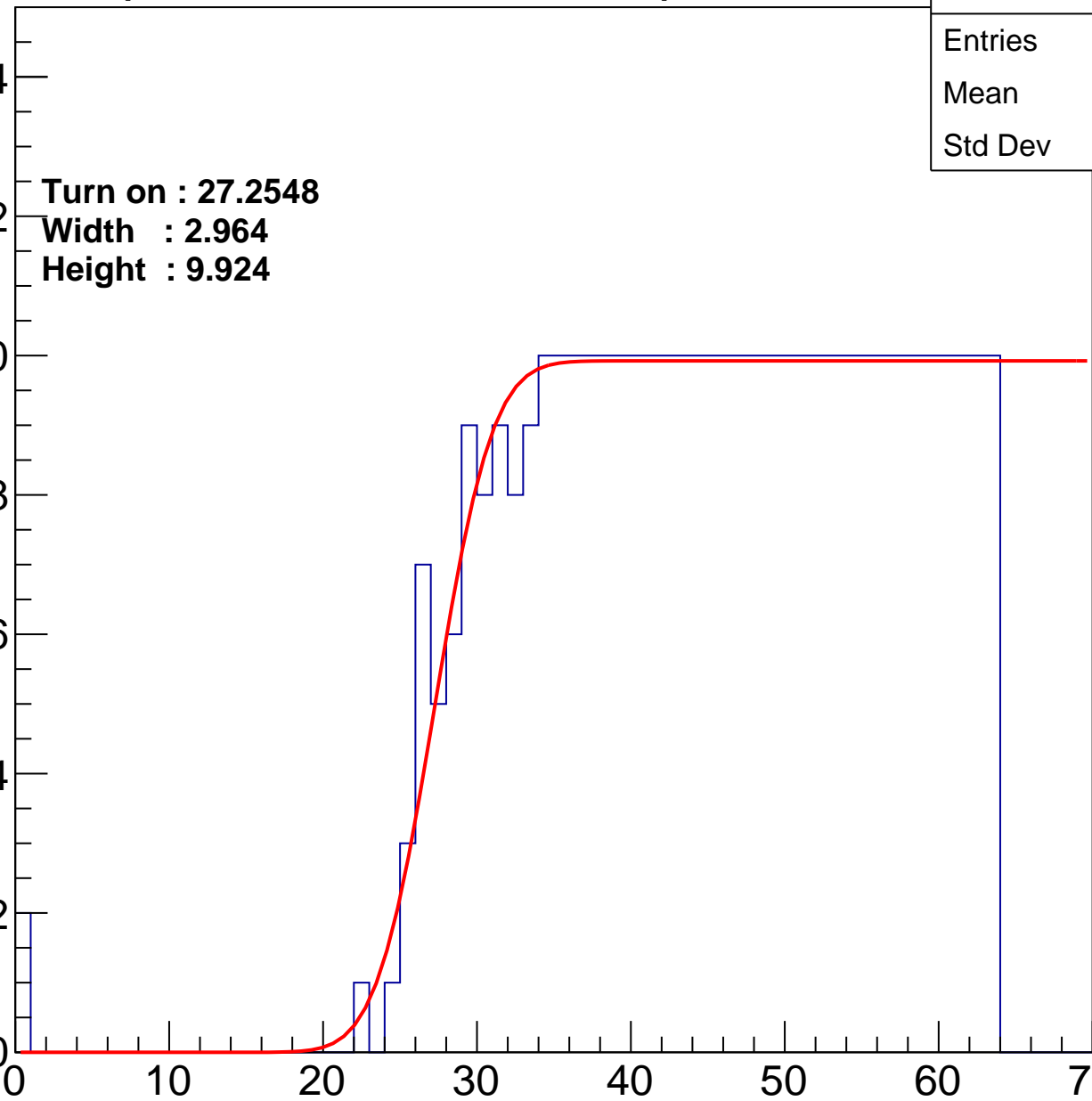
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2548
Width : 2.964
Height : 9.924

Entries	368
Mean	44.81
Std Dev	11.27

ampl



B1L103S, U11-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.2
Std Dev	10.87

Turn on : 27.9591

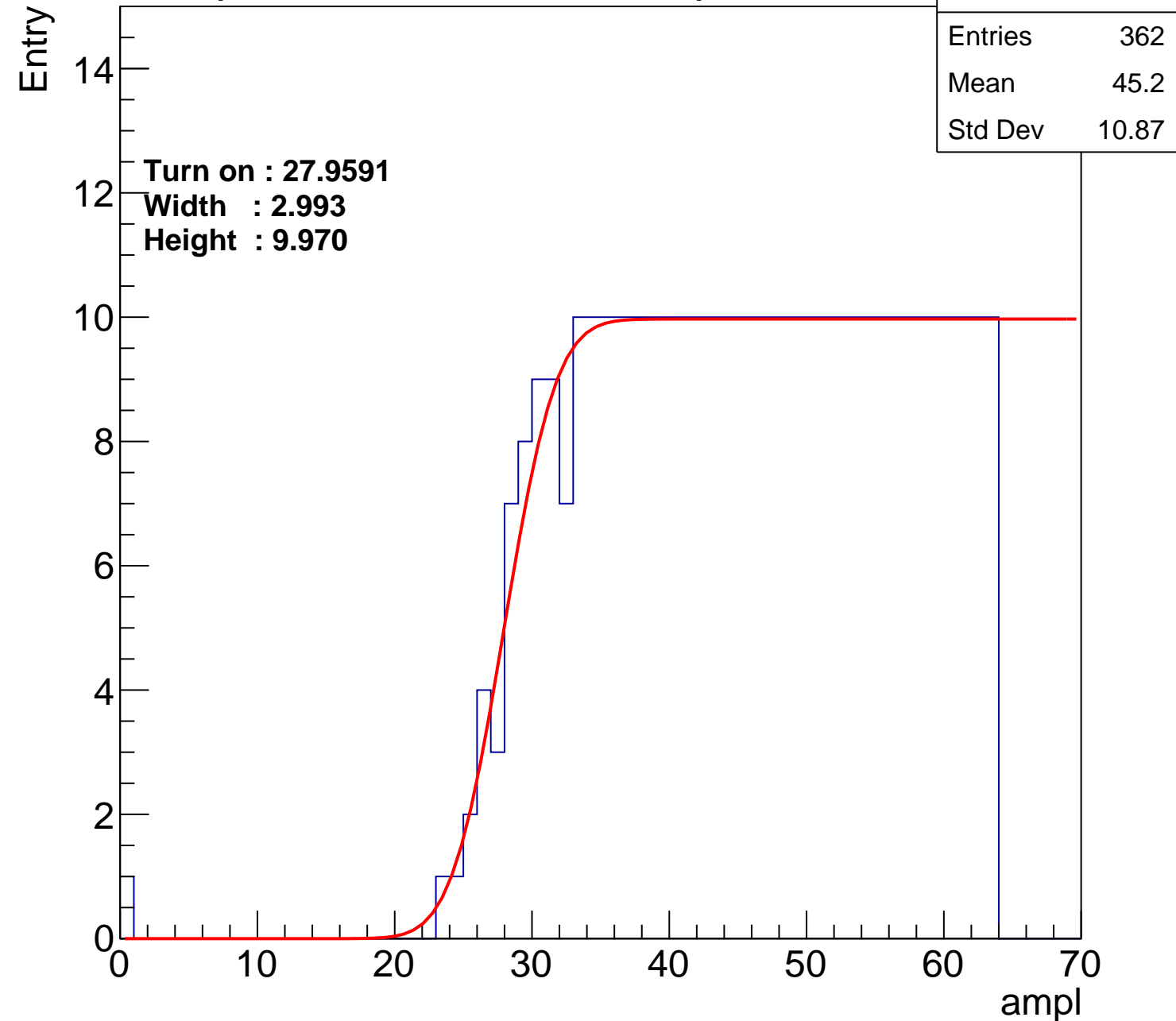
Width : 2.993

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch72

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.91
Std Dev	11.88

Turn on : 27.3491

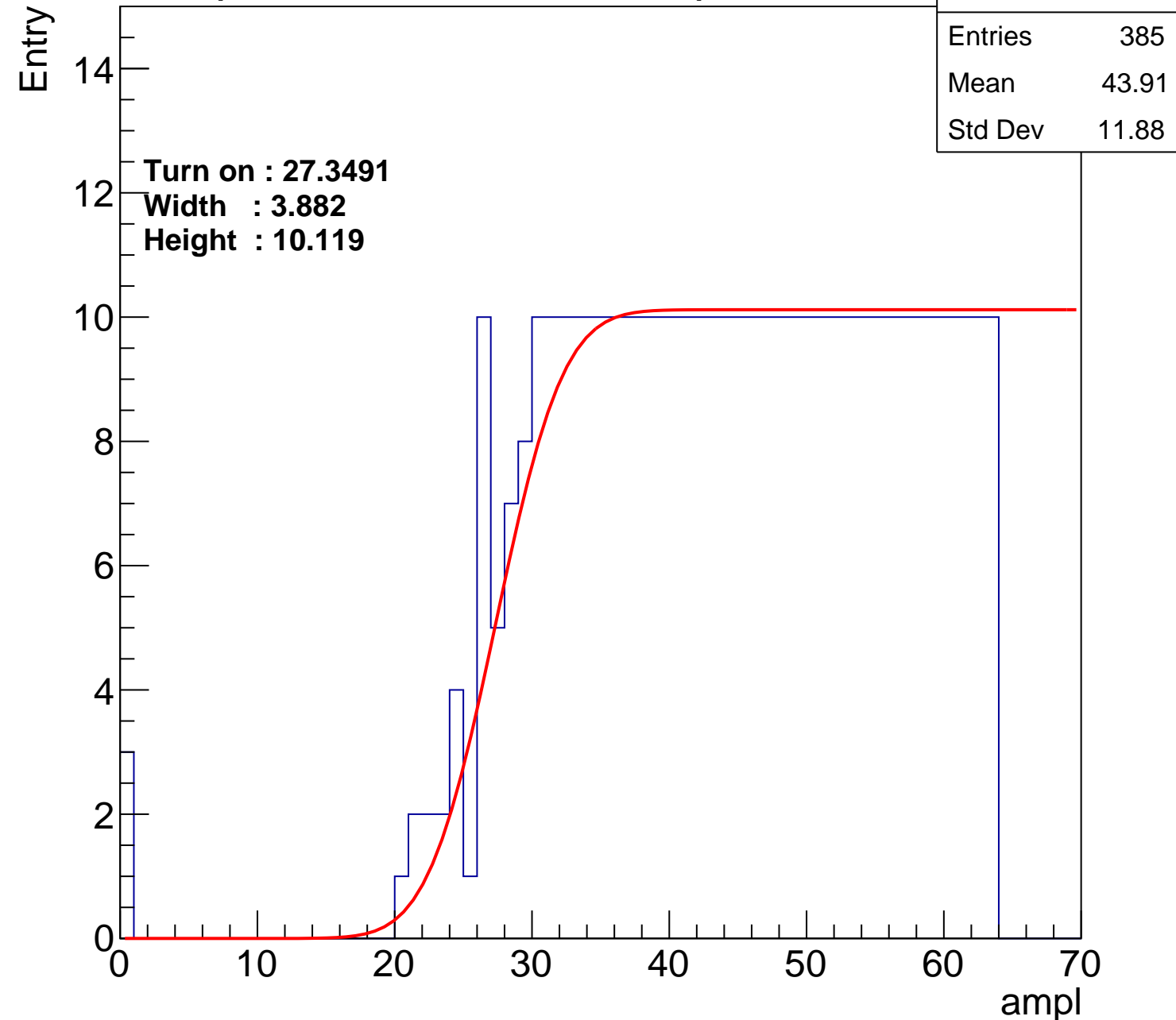
Width : 3.882

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch73

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 27.0458

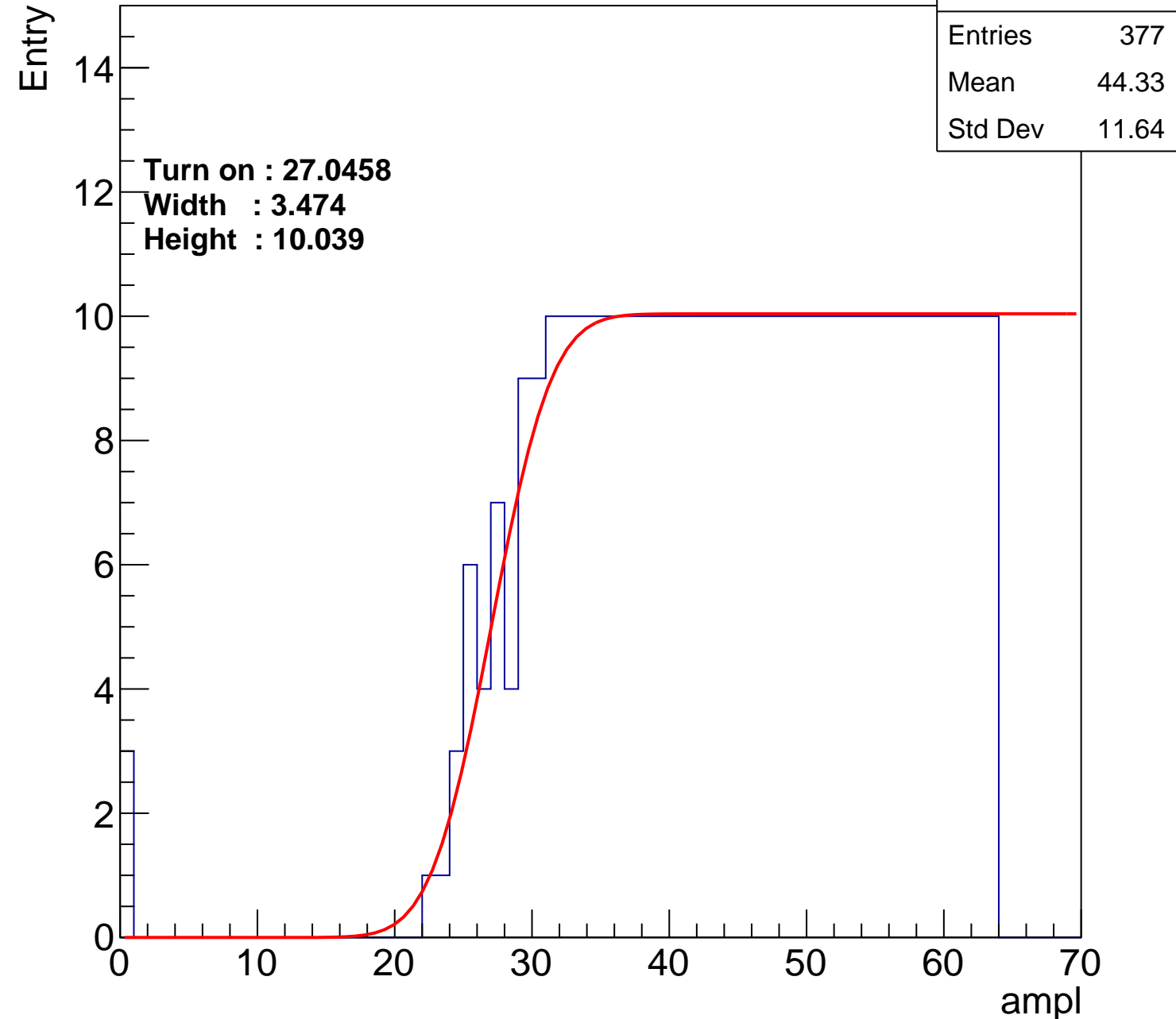
Width : 3.474

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch74

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.62
Std Dev	11.5

Turn on : 27.7106

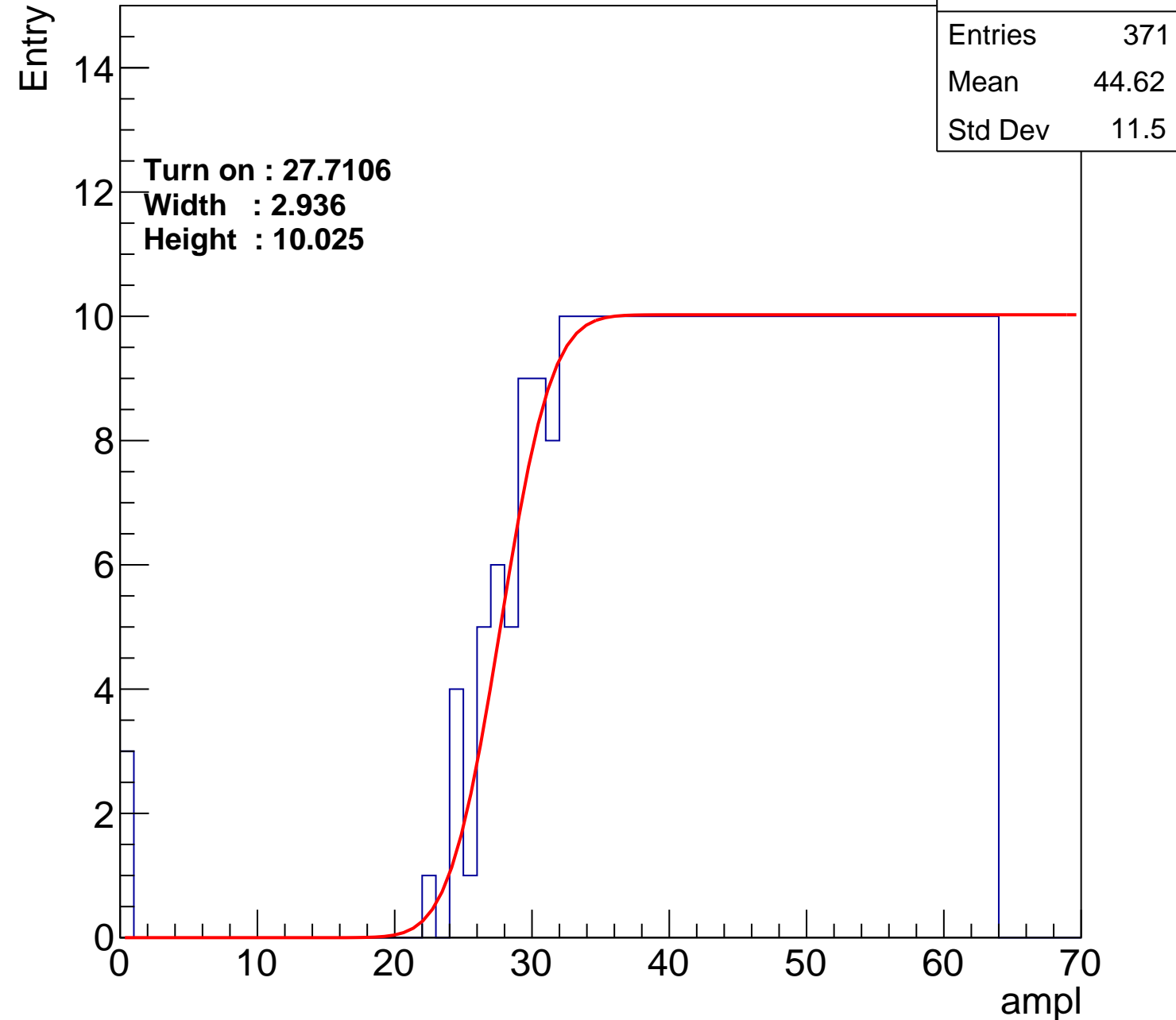
Width : 2.936

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.39
Std Dev	10.74

Turn on : 28.6624

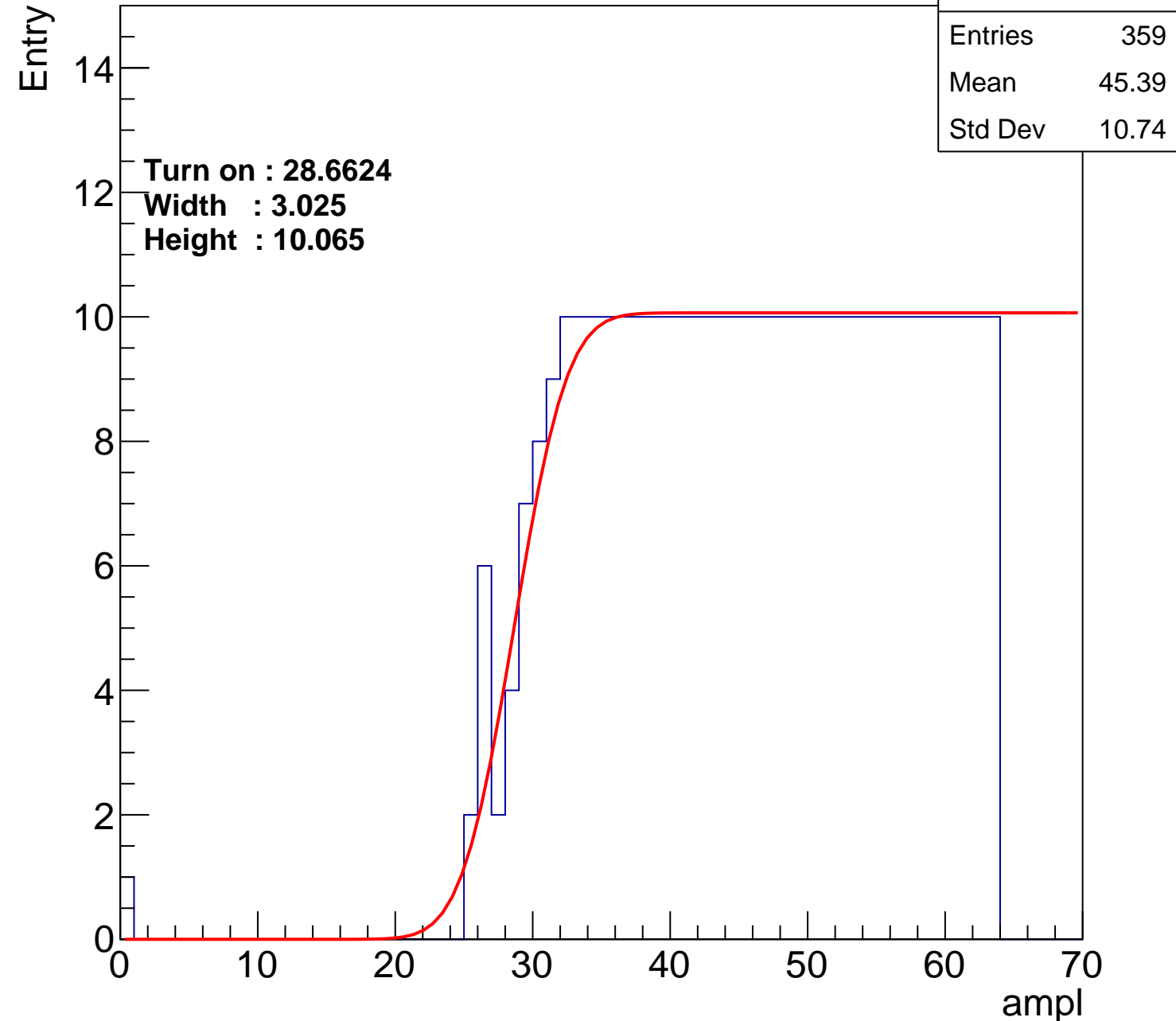
Width : 3.025

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch76

calib_packv5_042523_0143.root, FC#7, port C2

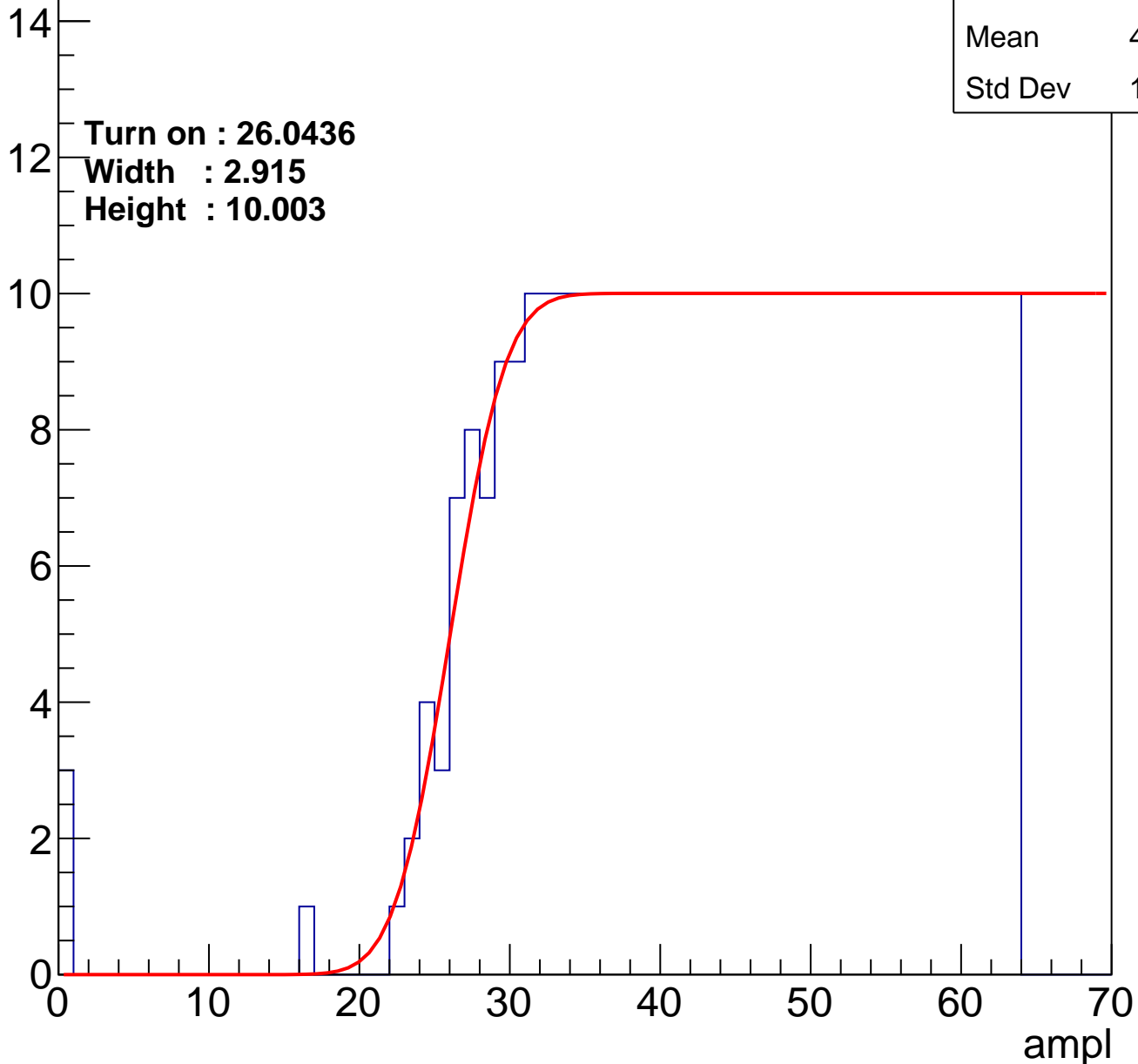
Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 26.0436

Width : 2.915

Height : 10.003

Entry



B1L103S, U11-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.17
Std Dev	11.44

Turn on : 26.2570

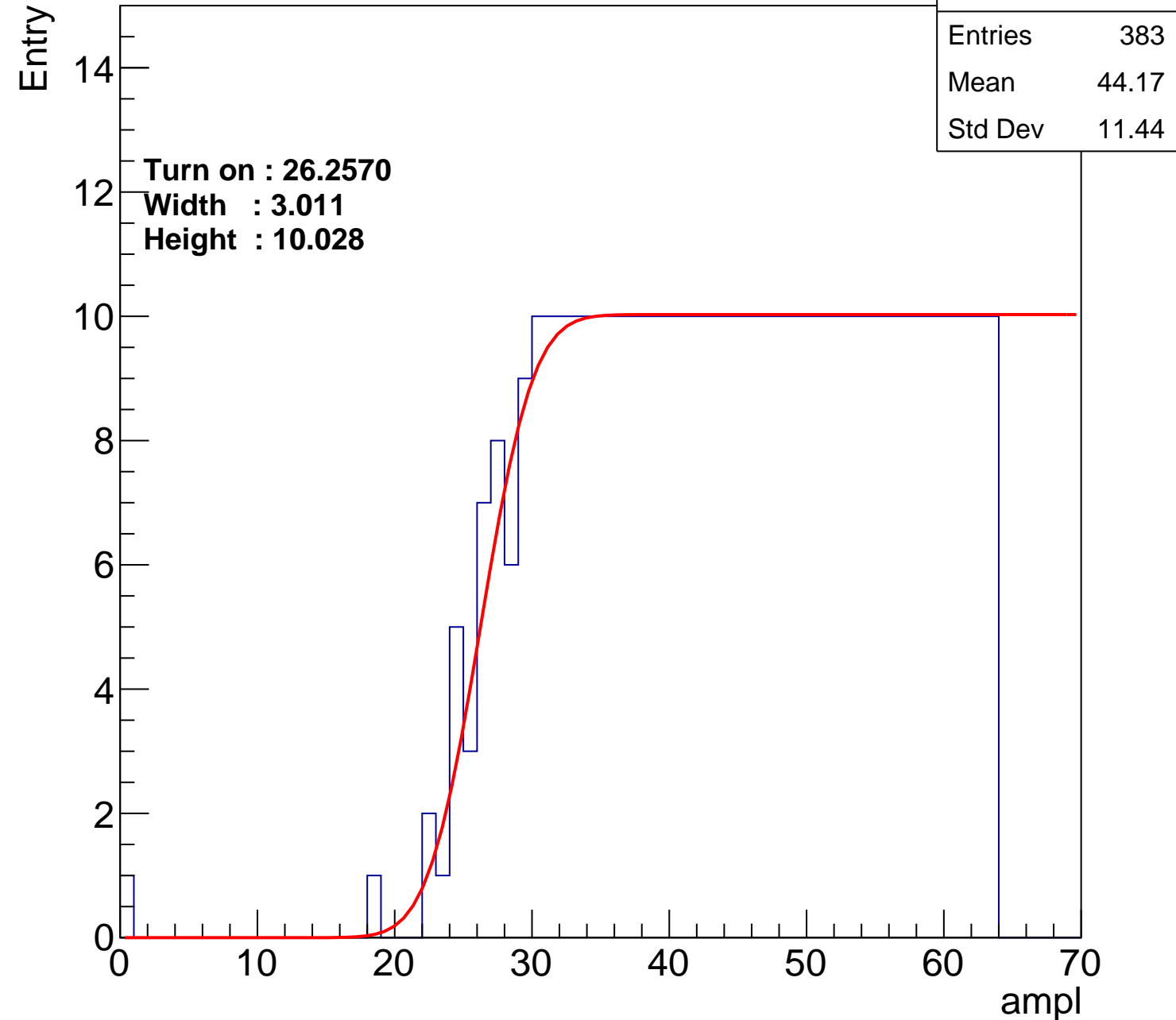
Width : 3.011

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 27.1005

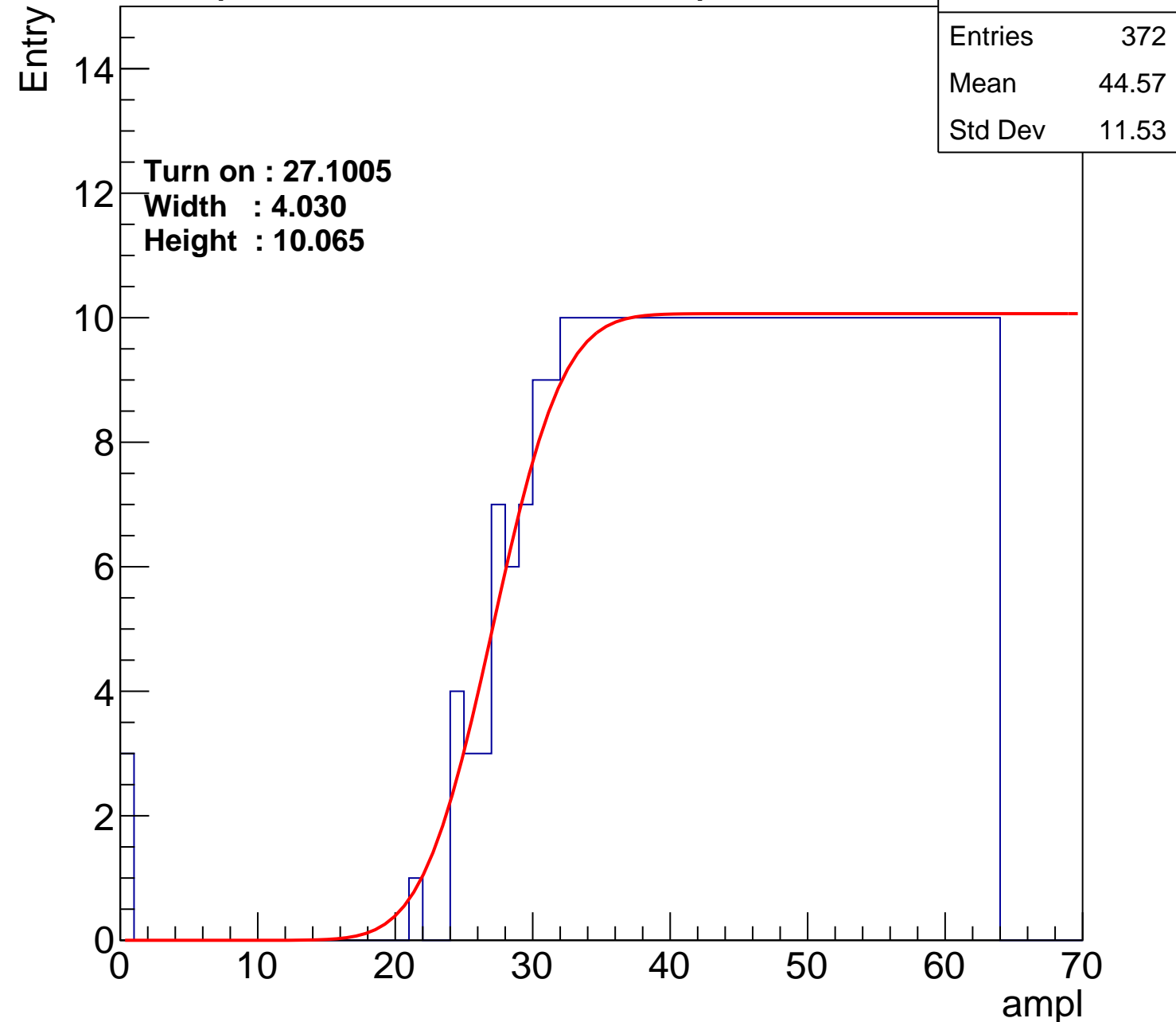
Width : 4.030

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.64
Std Dev	11.79

Turn on : 27.2357

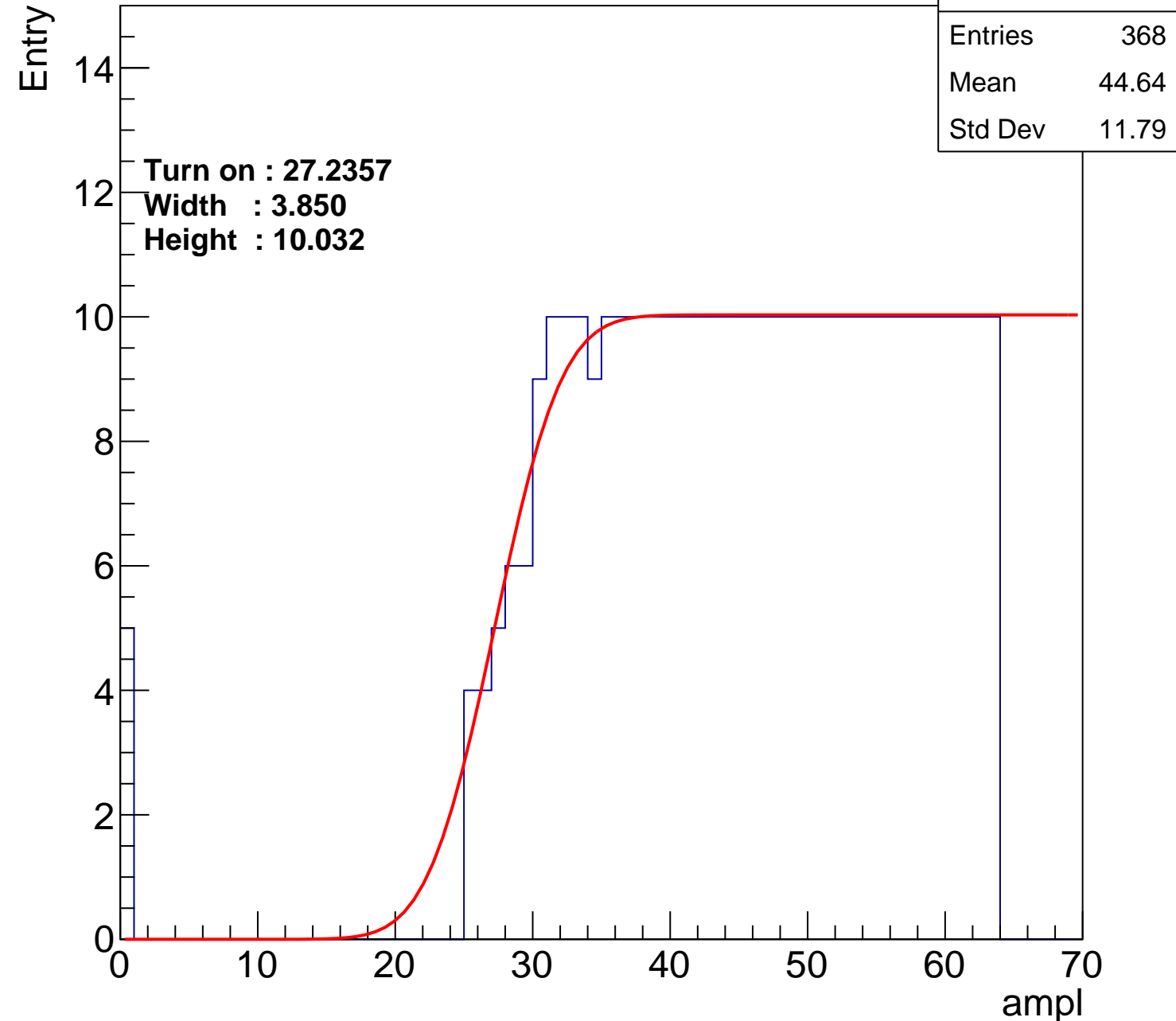
Width : 3.850

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch80

calib_packv5_042523_0143.root, FC#7, port C2

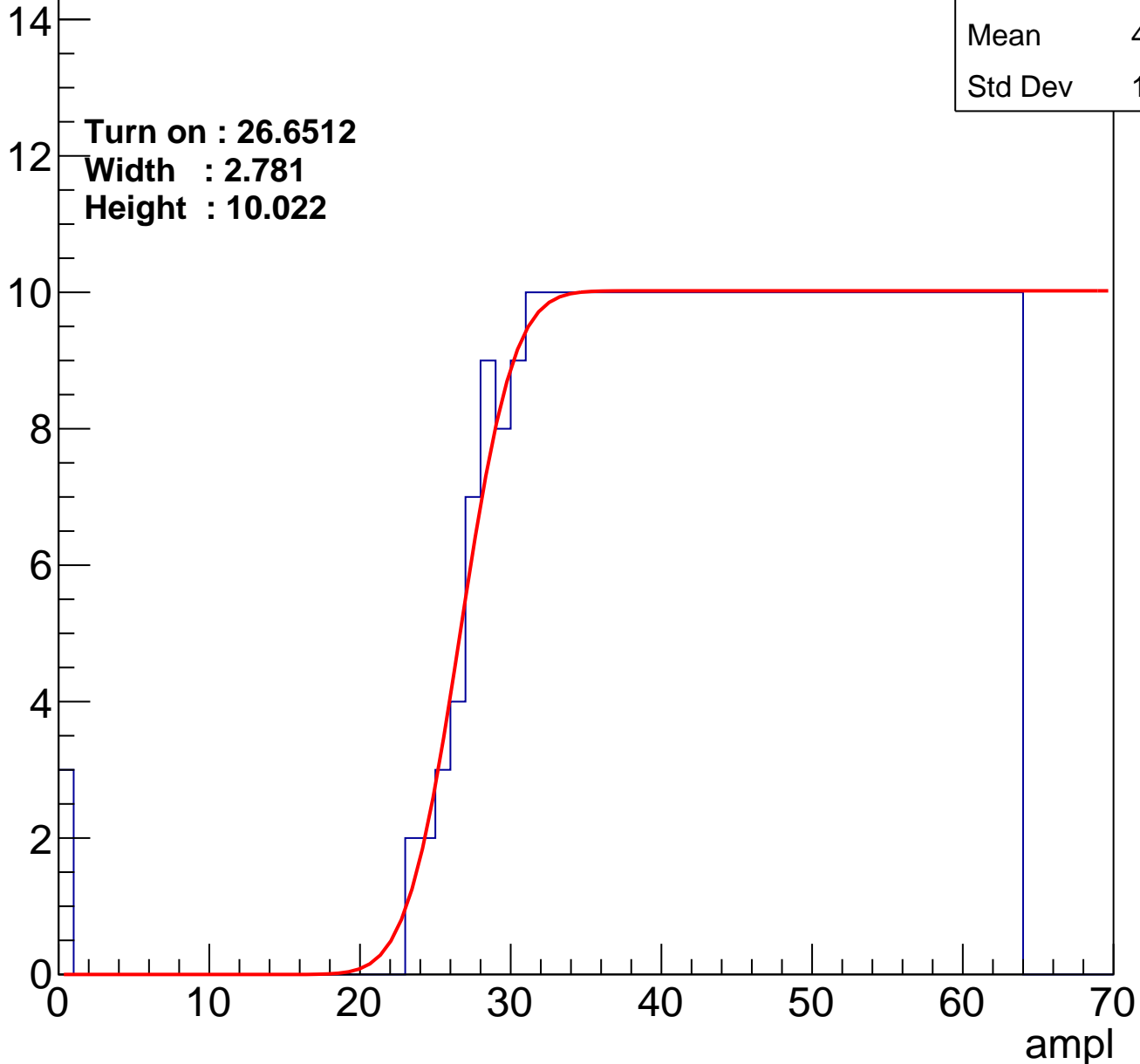
Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.6512

Width : 2.781

Height : 10.022

Entry



B1L103S, U11-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.96
Std Dev	11.35

Turn on : 27.9932

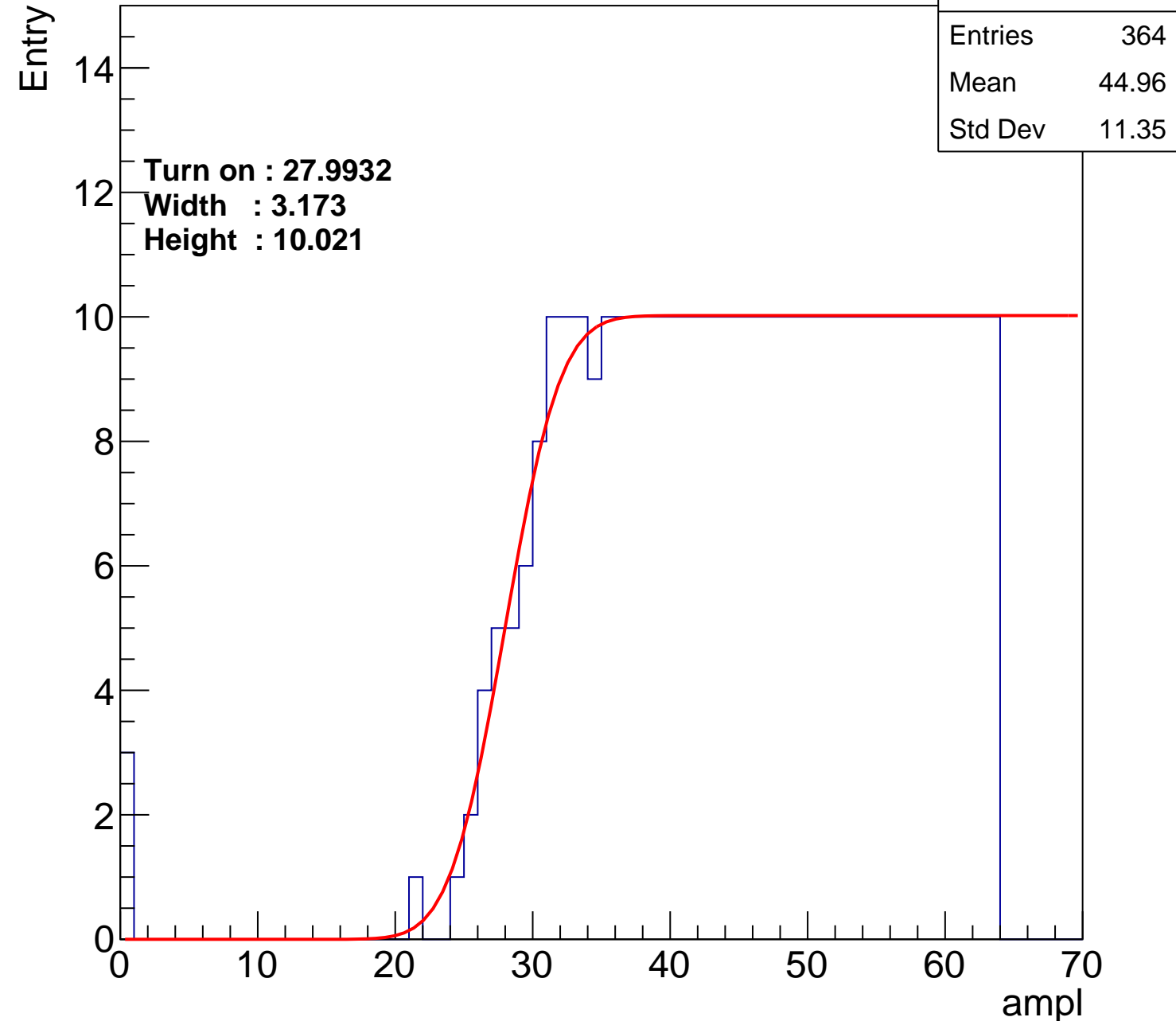
Width : 3.173

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch82

calib_packv5_042523_0143.root, FC#7, port C2

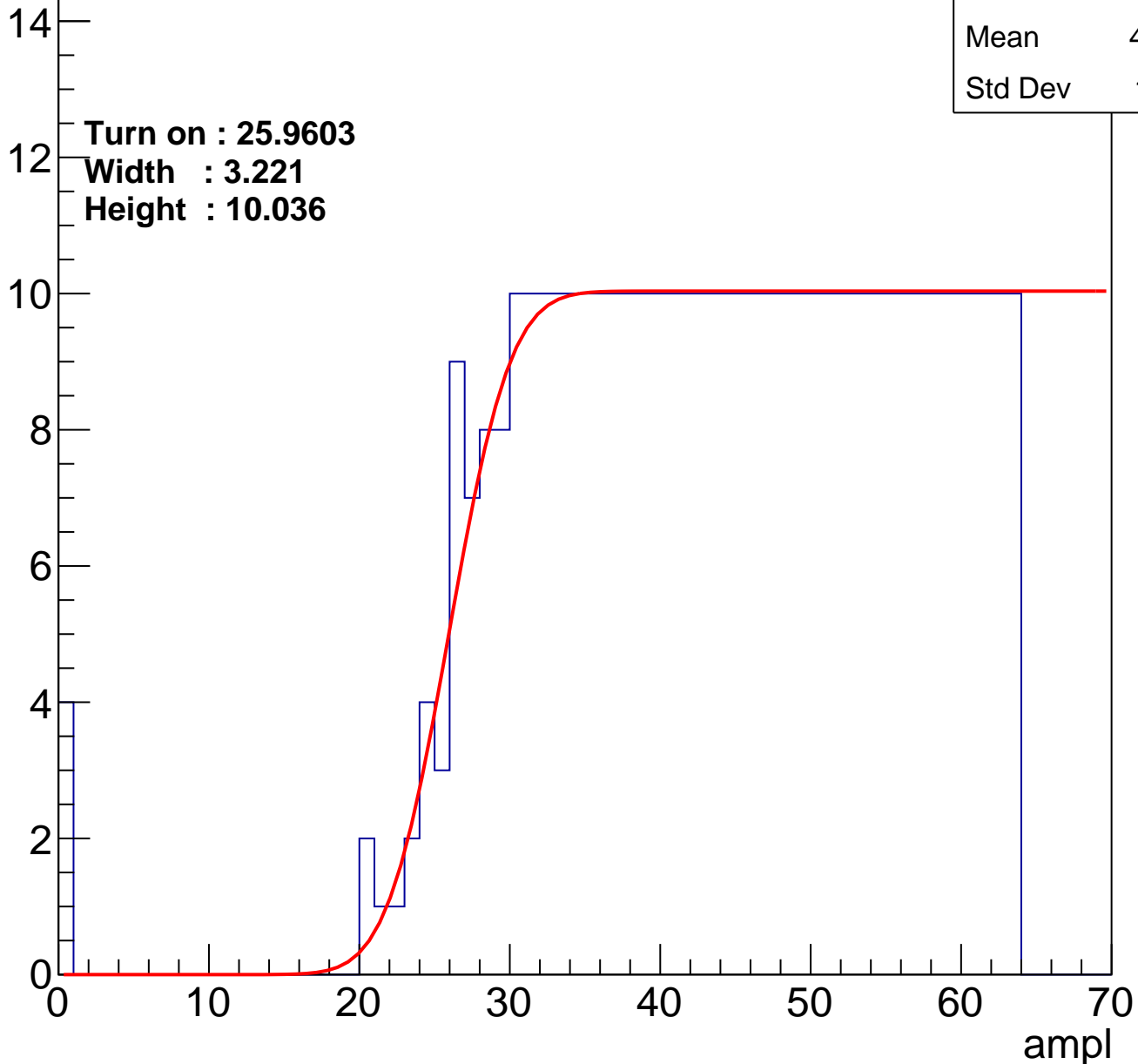
Entries	389
Mean	43.67
Std Dev	12.11

Turn on : 25.9603

Width : 3.221

Height : 10.036

Entry



B1L103S, U11-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.62
Std Dev	11.12

Turn on : 26.6258

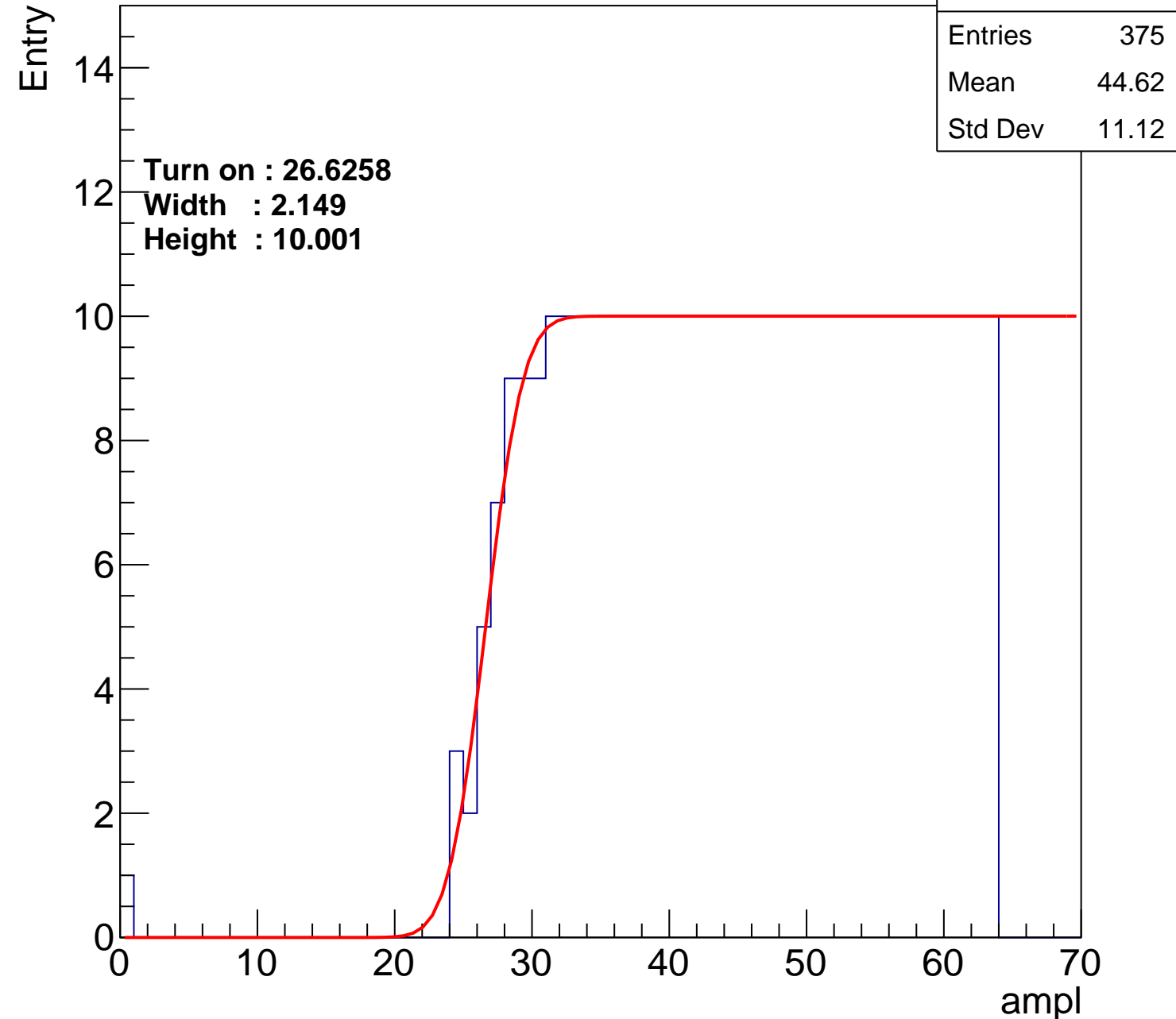
Width : 2.149

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.91
Std Dev	12

Turn on : 26.4407

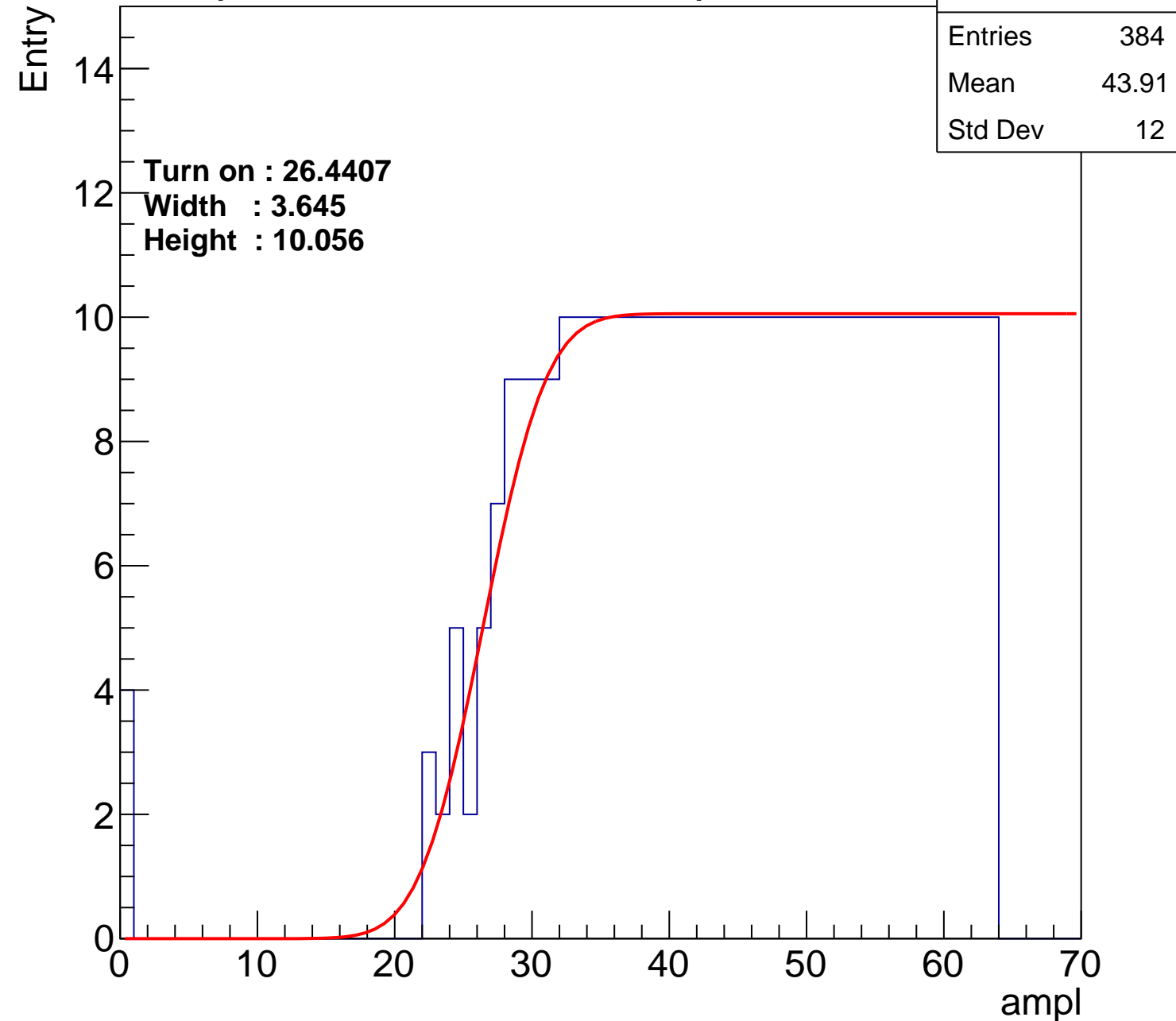
Width : 3.645

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch85

calib_packv5_042523_0143.root, FC#7, port C2

Entries	346
Mean	45.96
Std Dev	10.51

Turn on : 29.6692

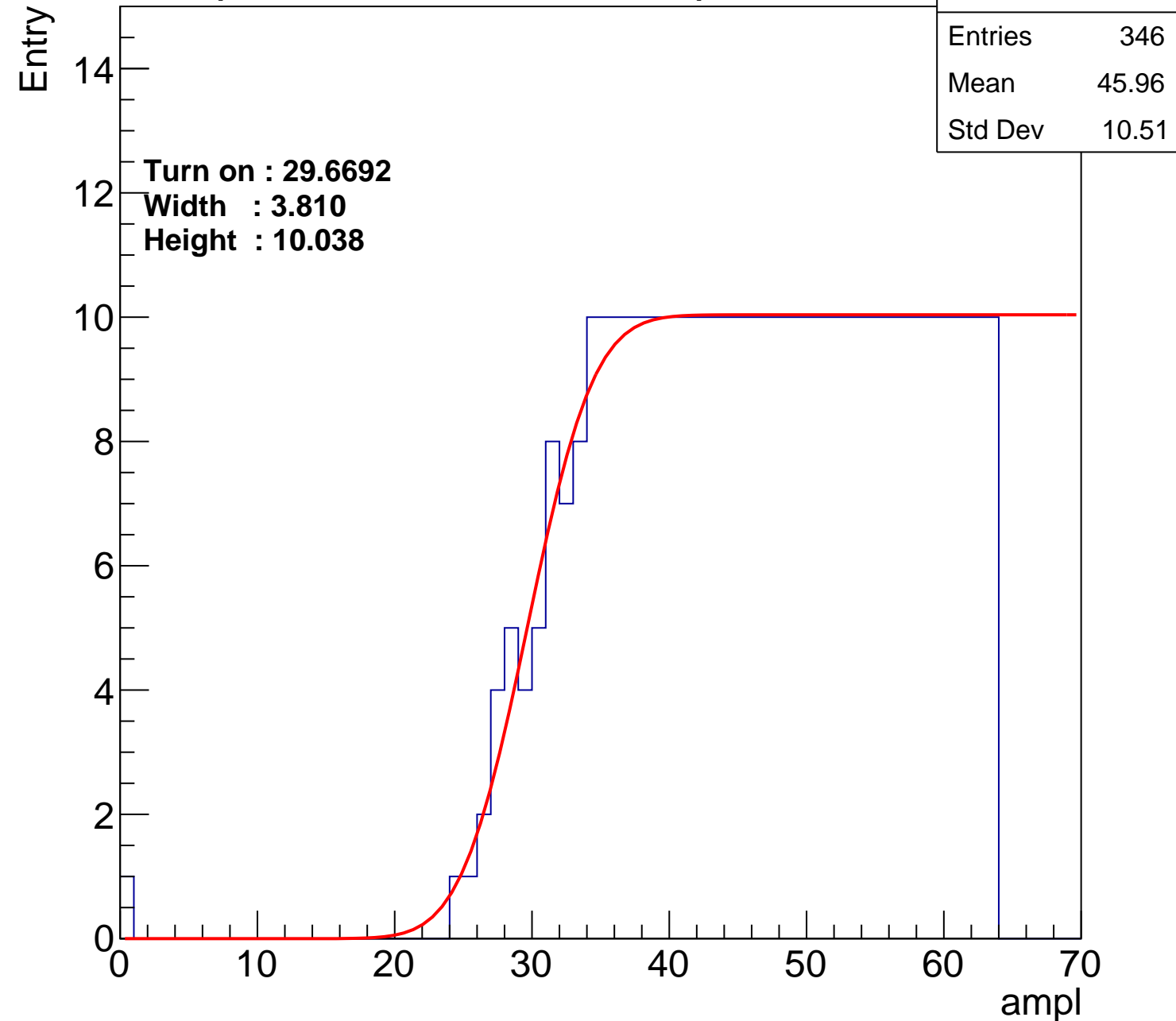
Width : 3.810

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch86

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.26
Std Dev	11.24

Turn on : 29.0234

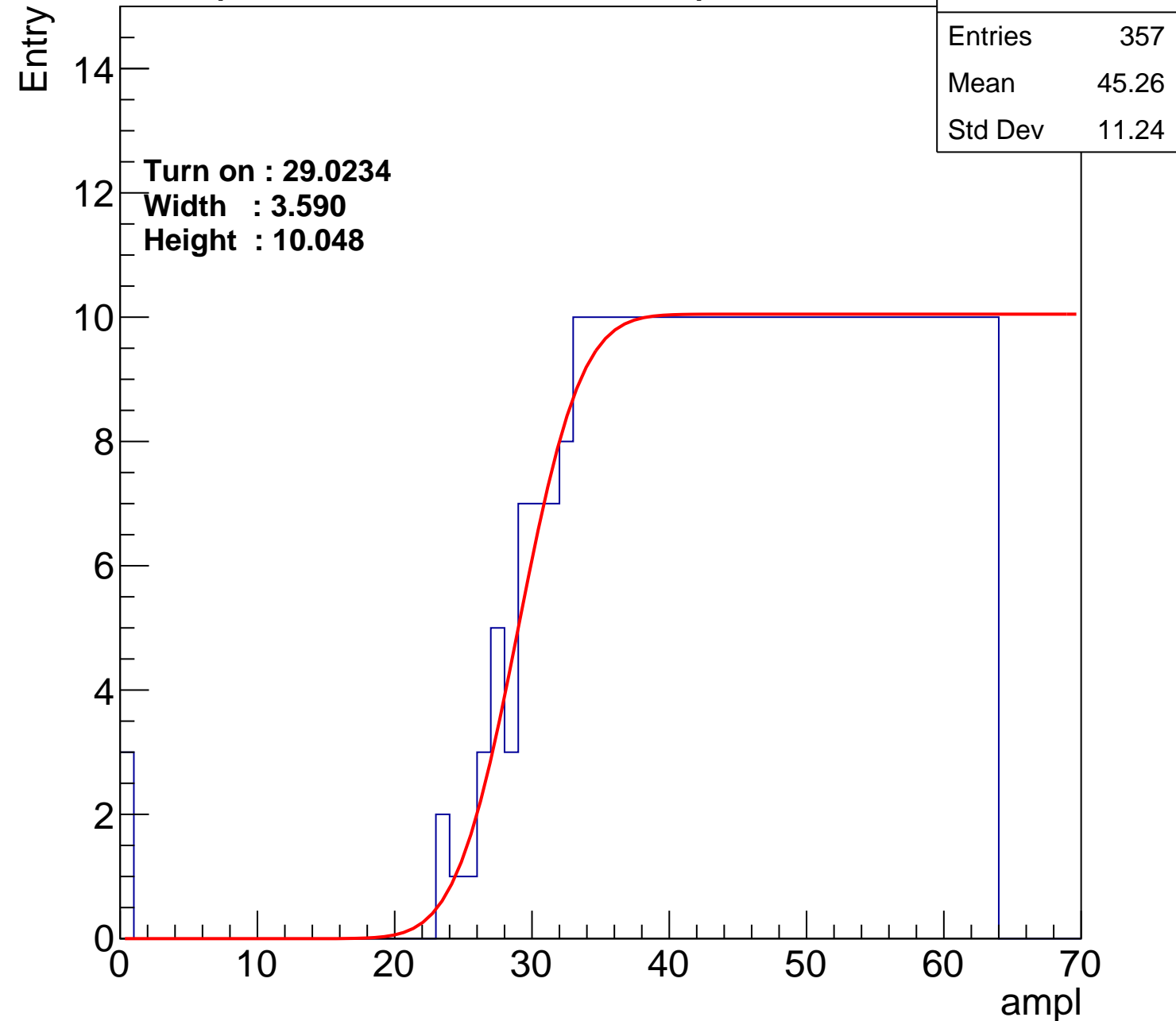
Width : 3.590

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.66
Std Dev	11.68

Turn on : 27.6648

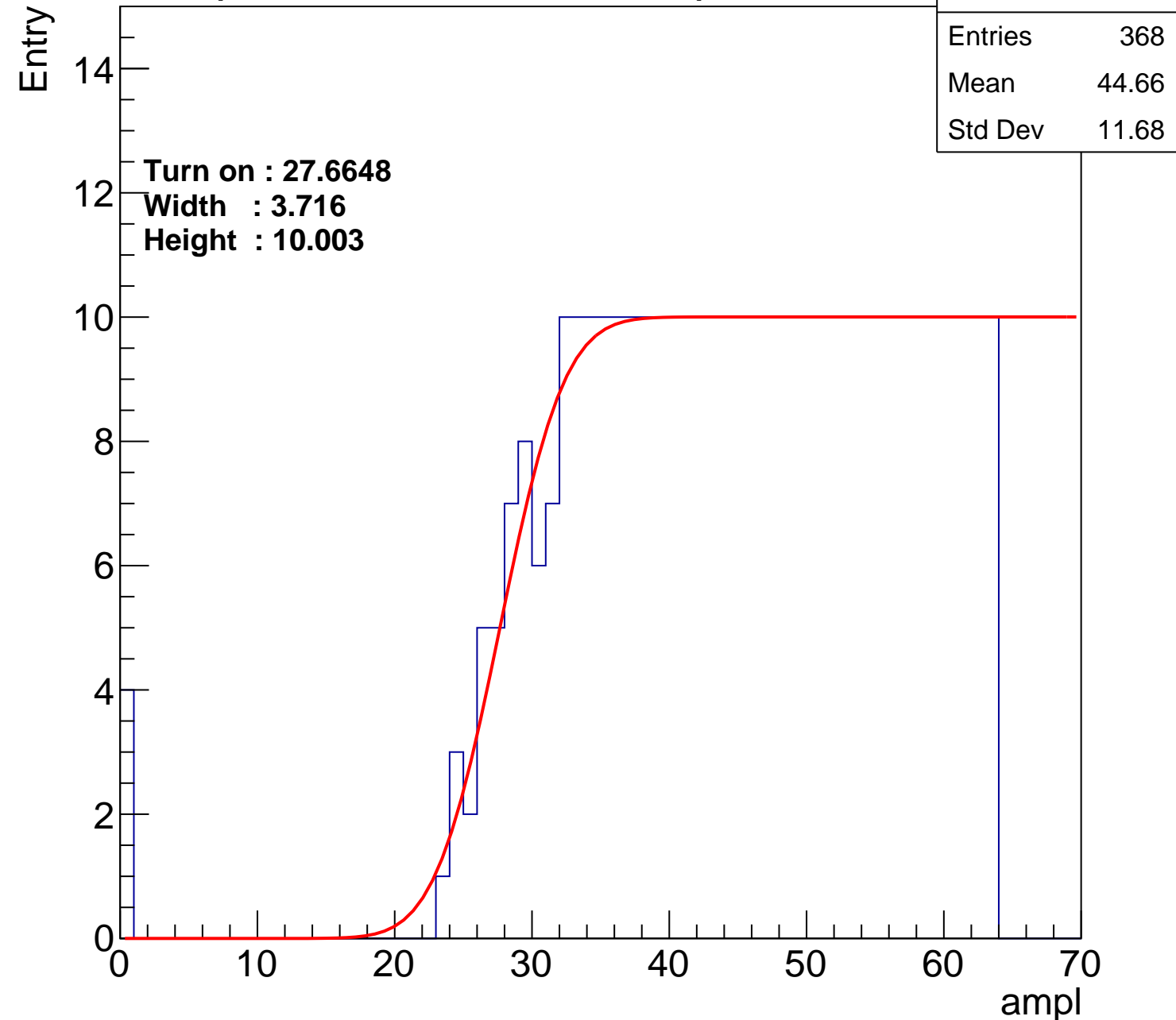
Width : 3.716

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.43
Std Dev	10.98

Turn on : 29.5340

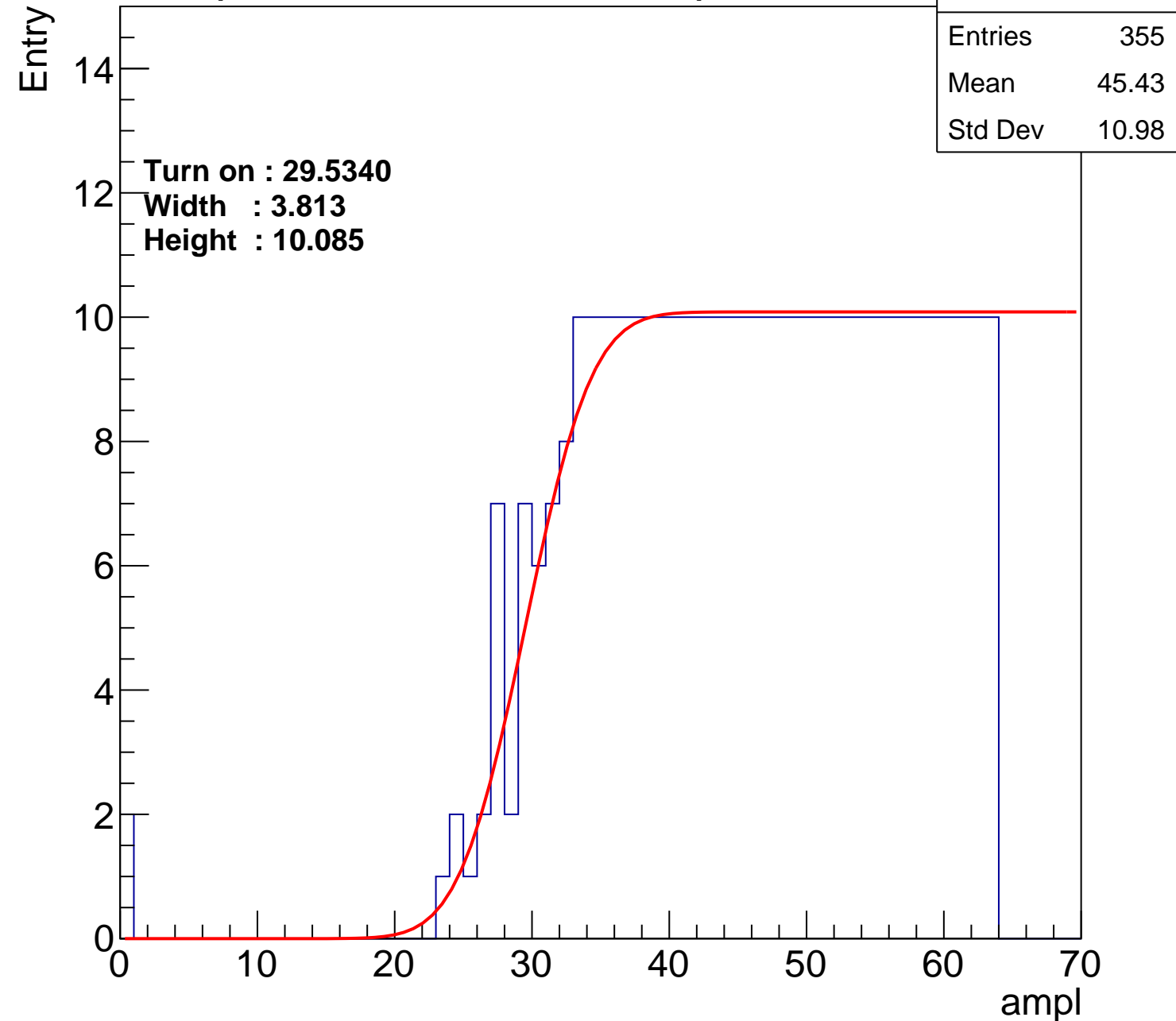
Width : 3.813

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.91
Std Dev	11.37

Turn on : 28.1551

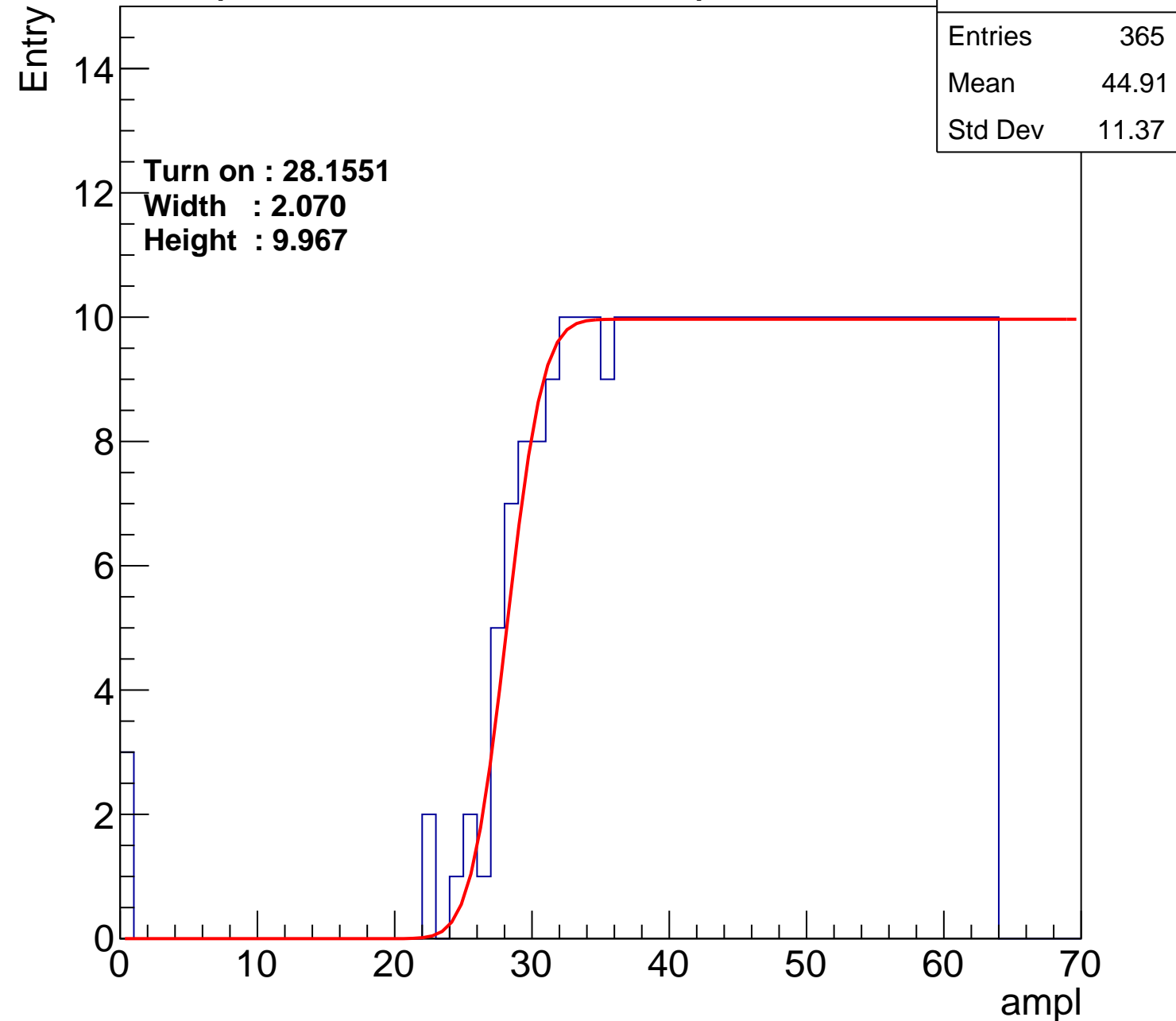
Width : 2.070

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch90

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.6717

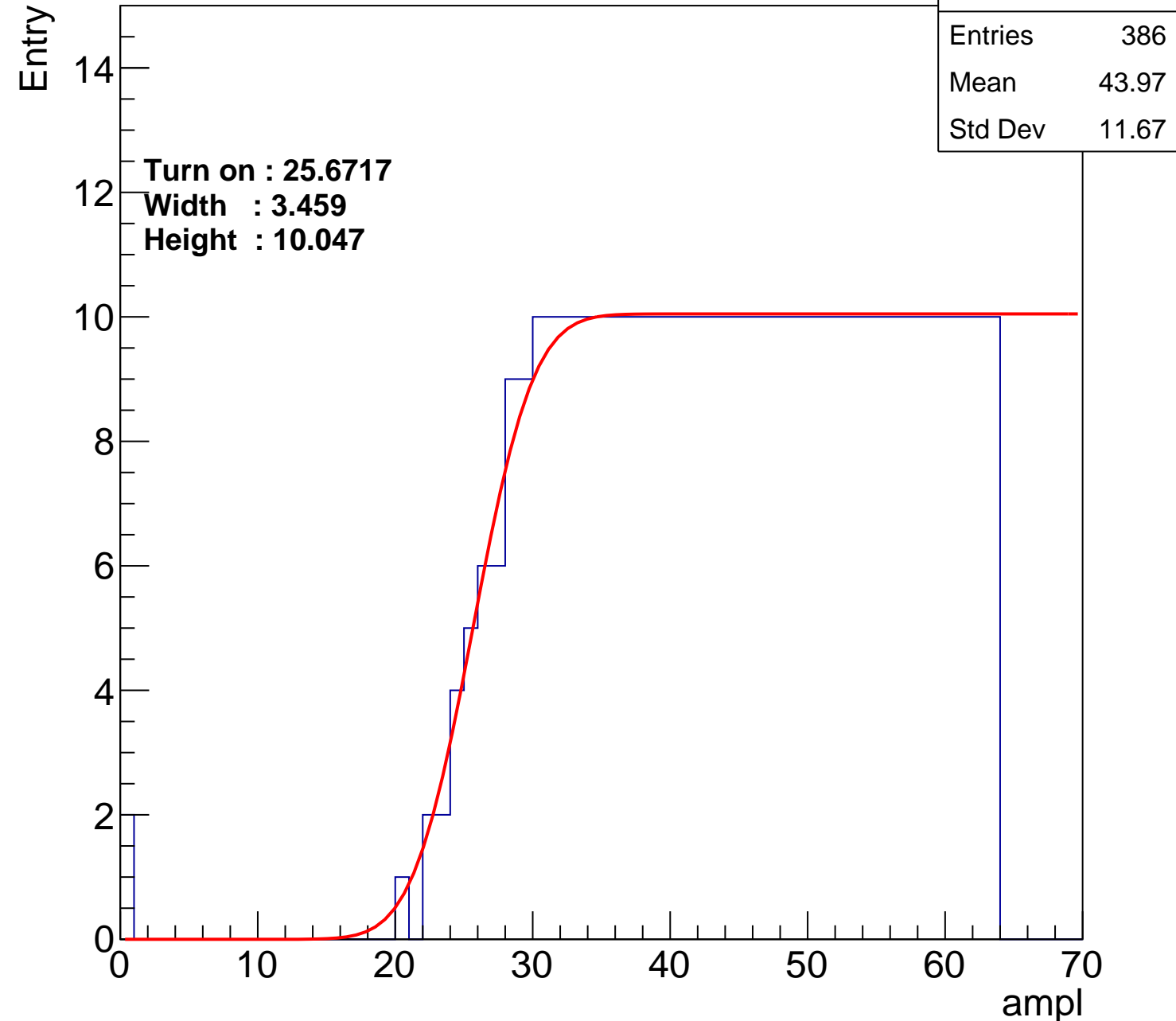
Width : 3.459

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.28
Std Dev	10.86

Turn on : 28.2679

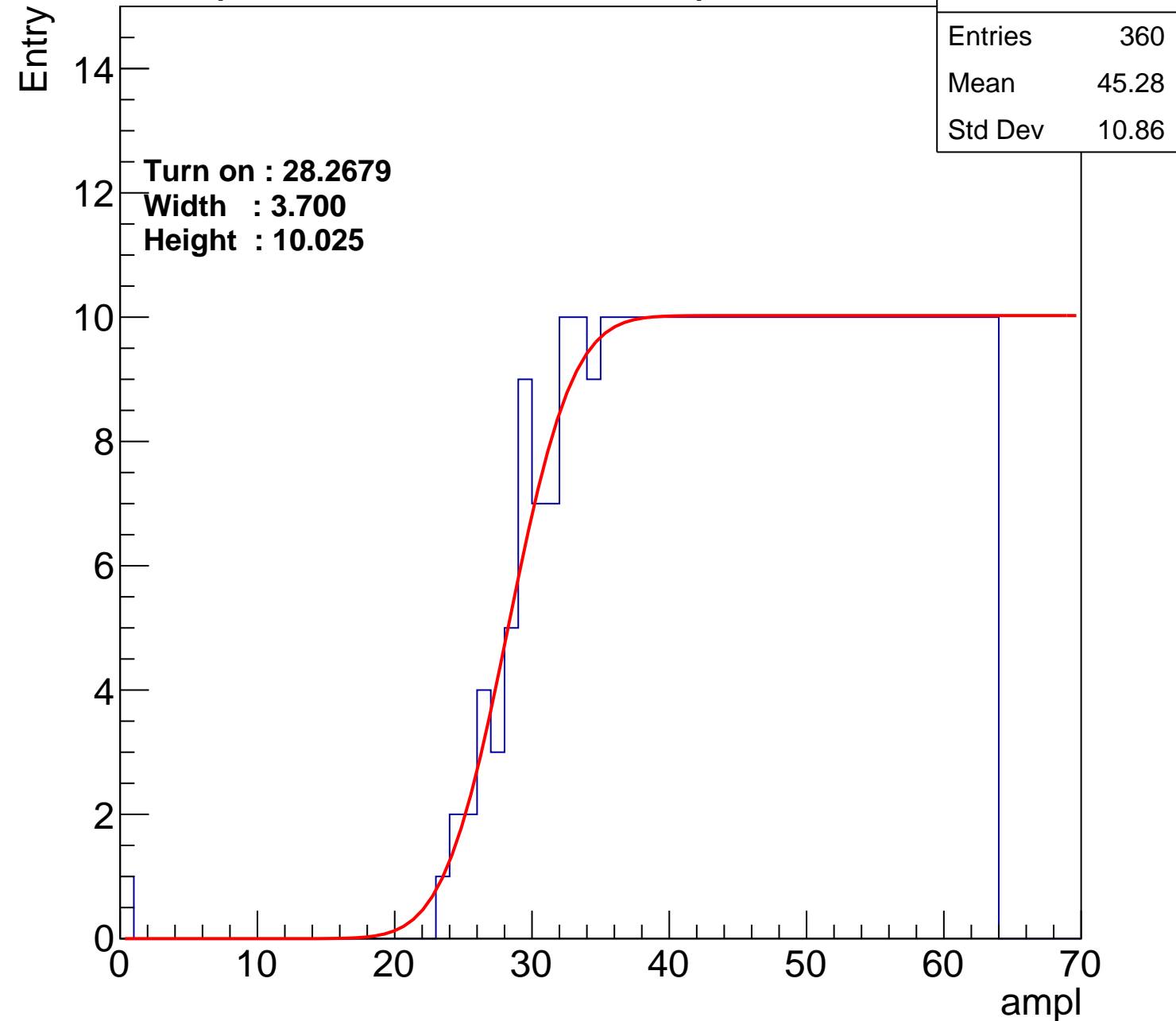
Width : 3.700

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.19
Std Dev	12.35

Turn on : 24.5648

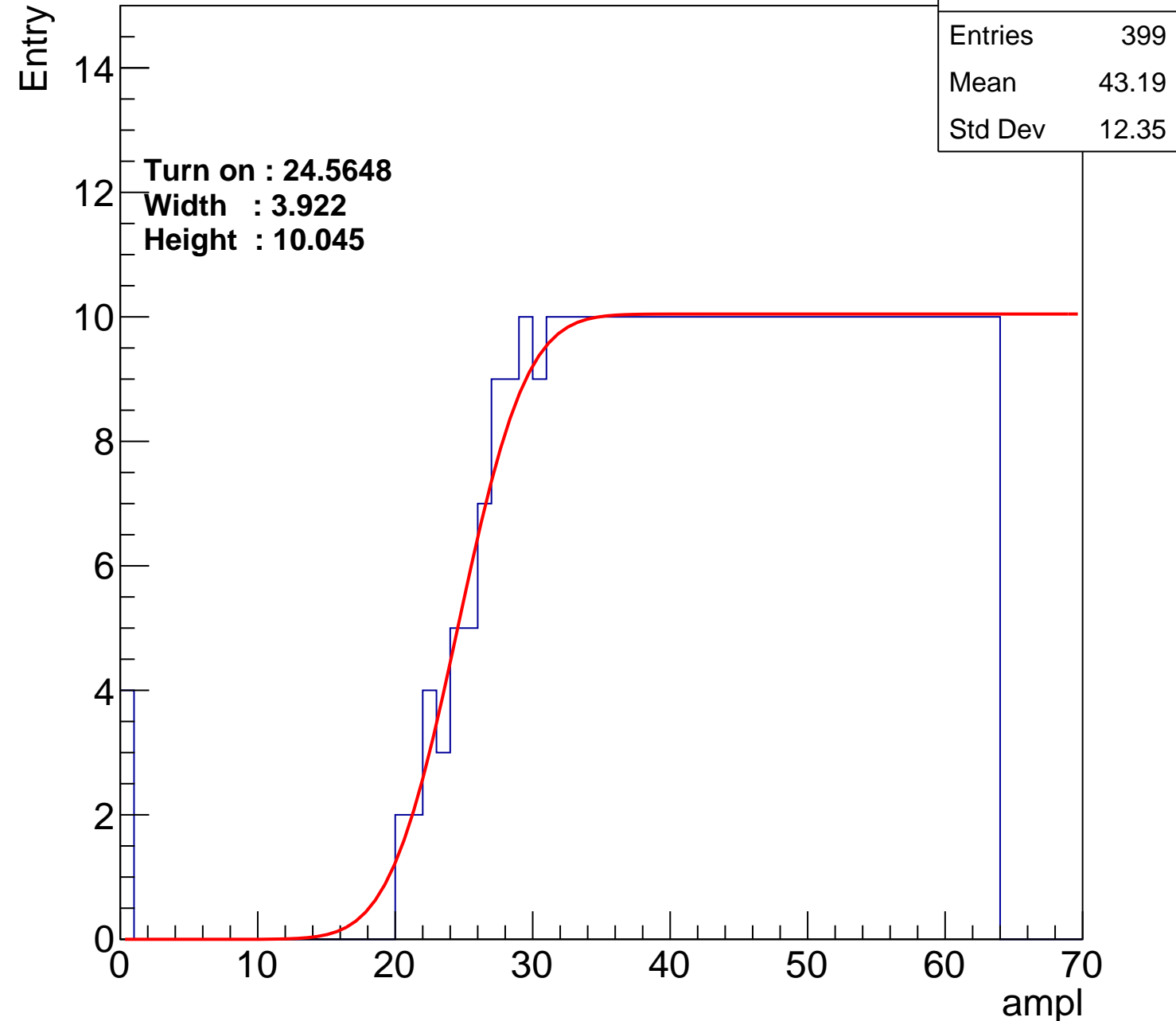
Width : 3.922

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.98
Std Dev	11.11

Turn on : 27.7370

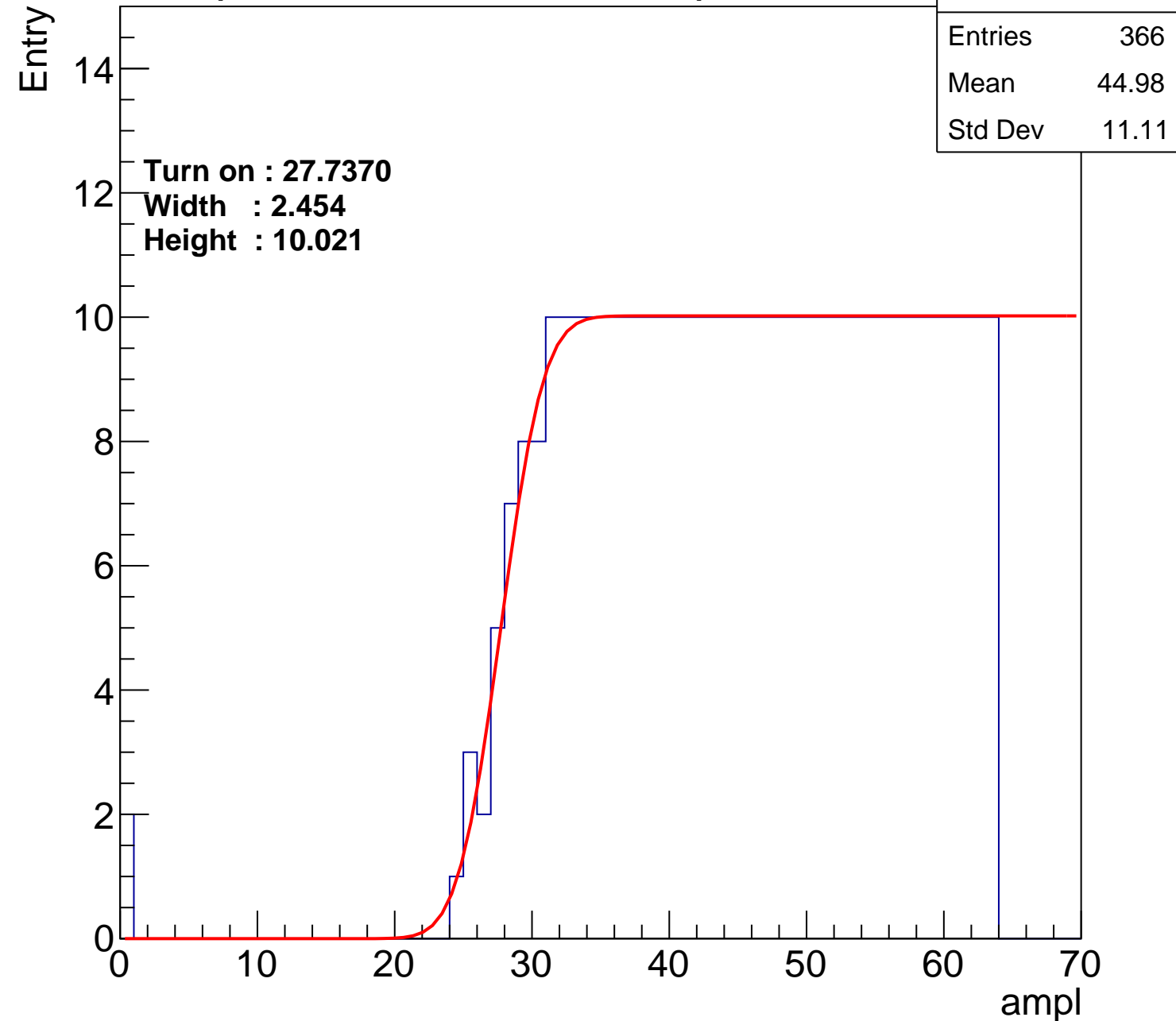
Width : 2.454

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.03
Std Dev	11.79

Turn on : 26.3369

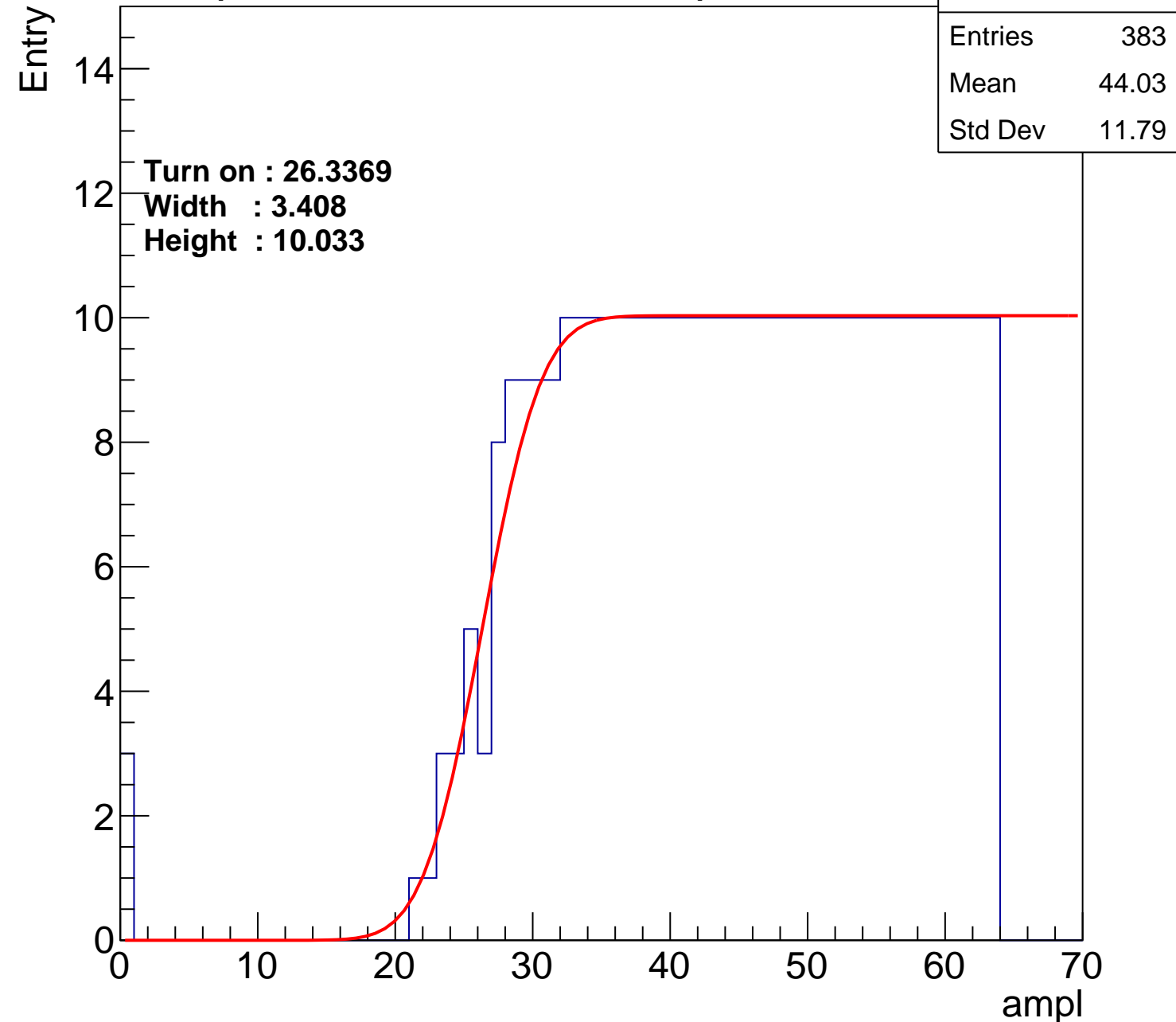
Width : 3.408

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.04
Std Dev	12.09

Turn on : 26.8343

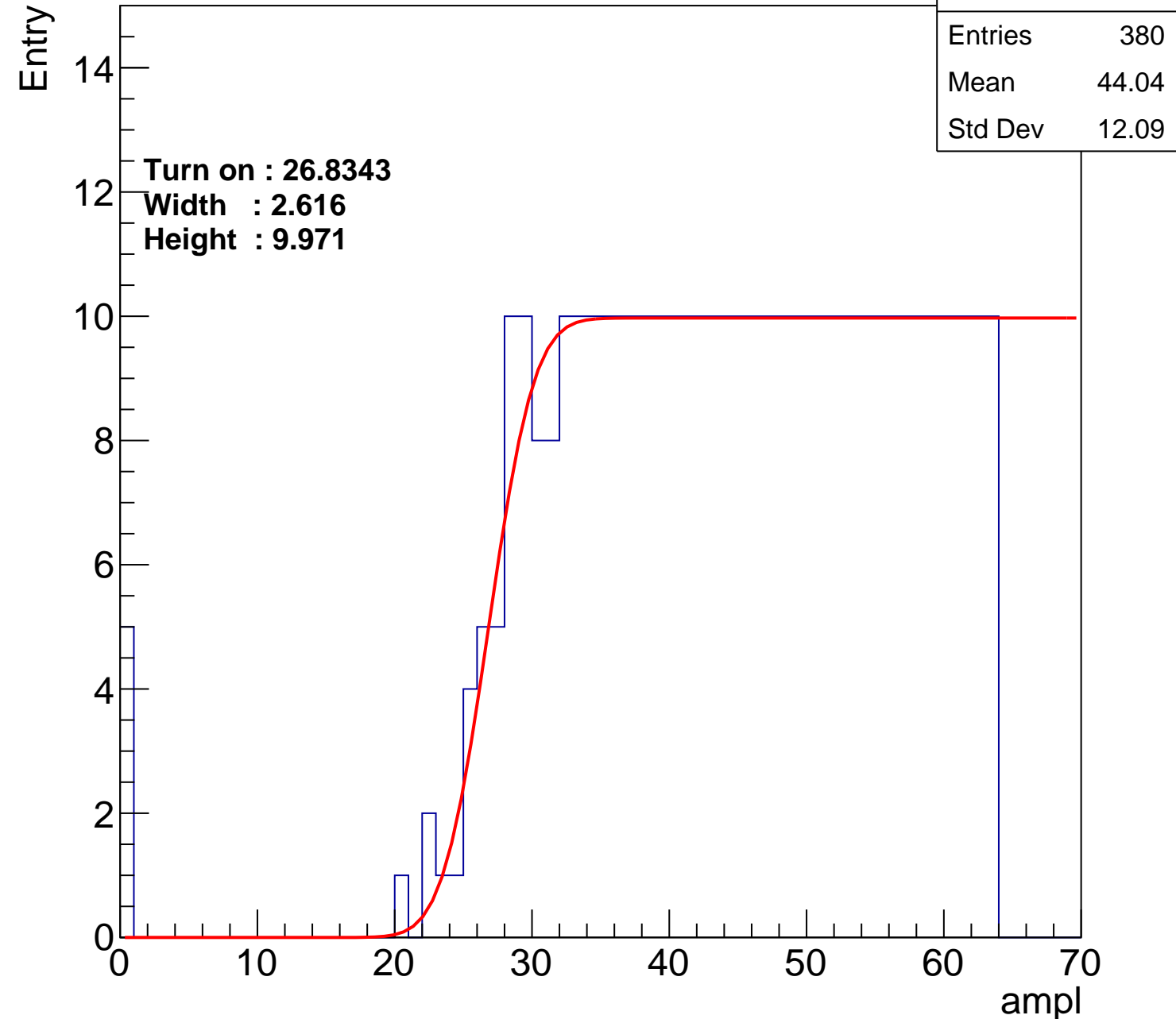
Width : 2.616

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch96

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.44
Std Dev	11.61

Turn on : 27.1604

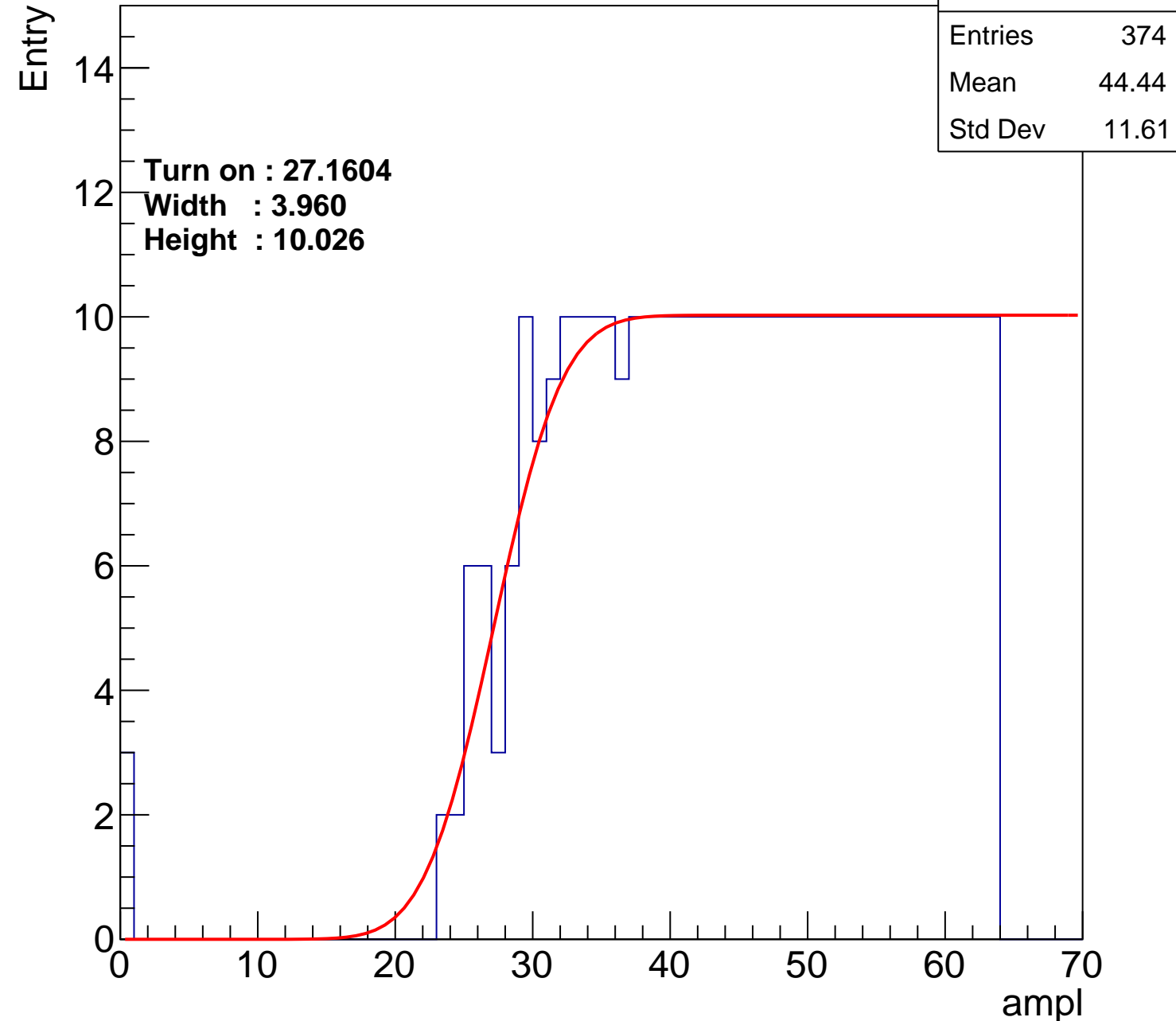
Width : 3.960

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch97

calib_packv5_042523_0143.root, FC#7, port C2

Entries	346
Mean	45.82
Std Dev	10.96

Turn on : 29.2350

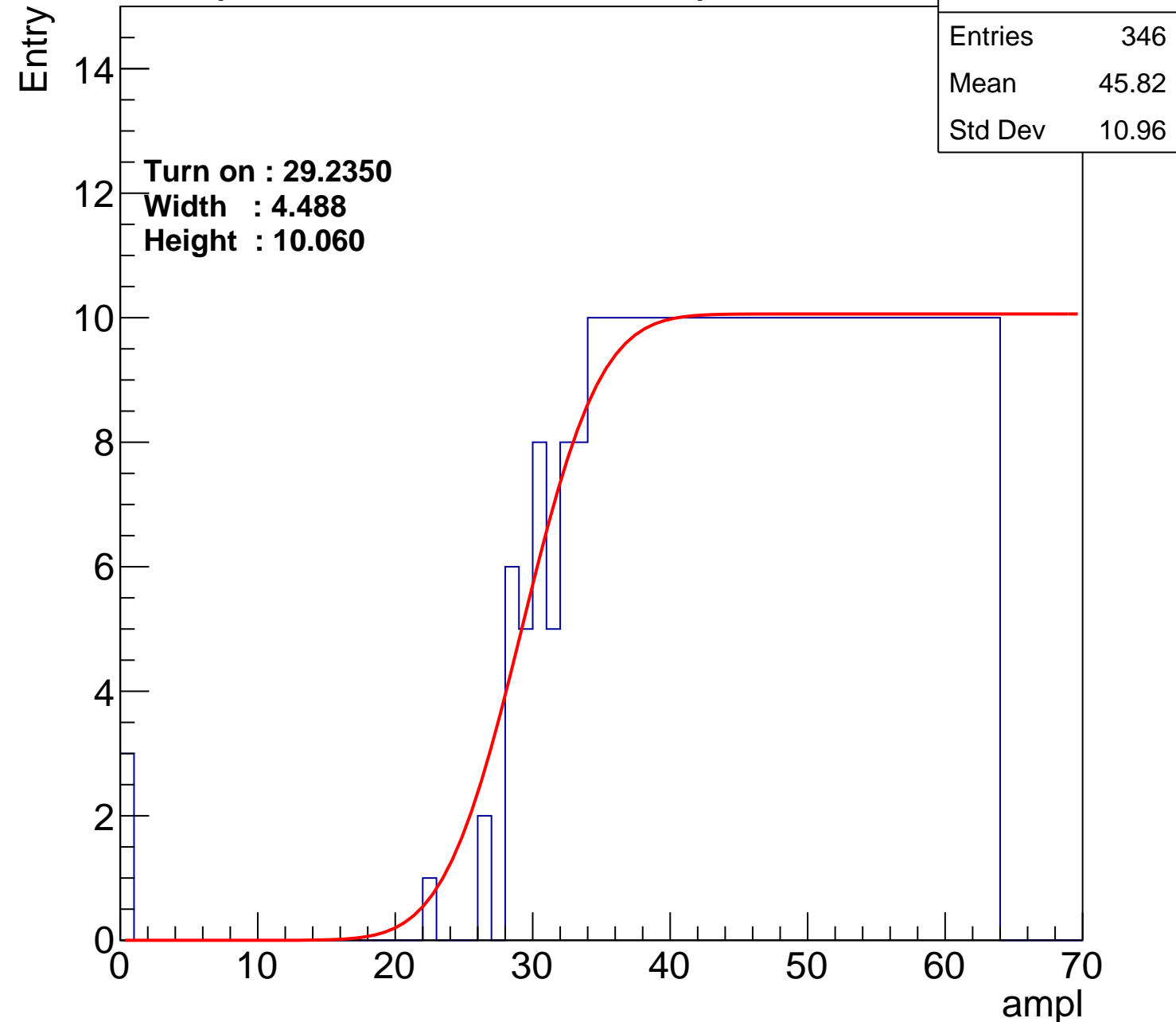
Width : 4.488

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.65
Std Dev	11.81

Turn on : 25.6502

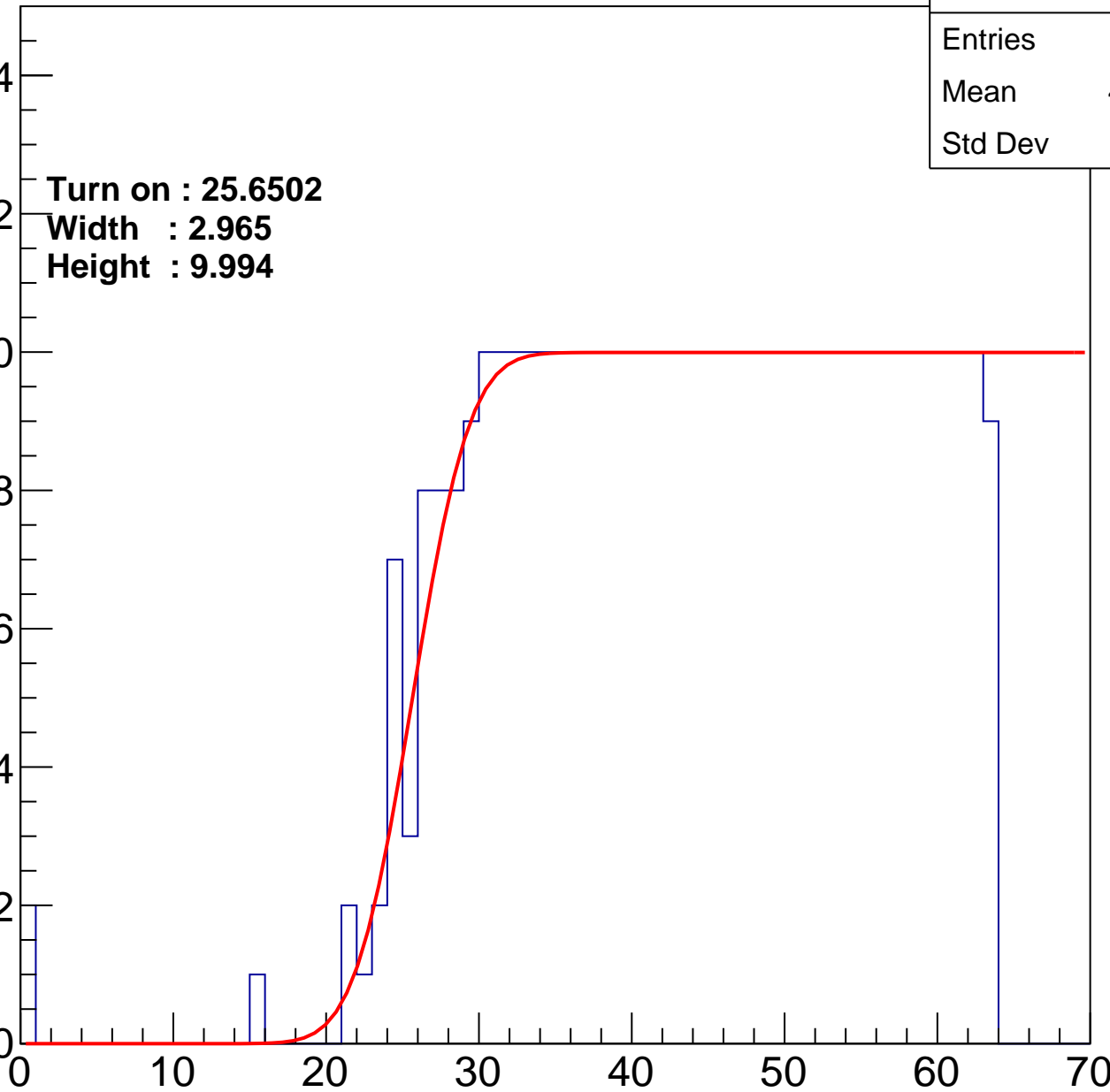
Width : 2.965

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	353
Mean	45.56
Std Dev	10.88

Turn on : 28.5128

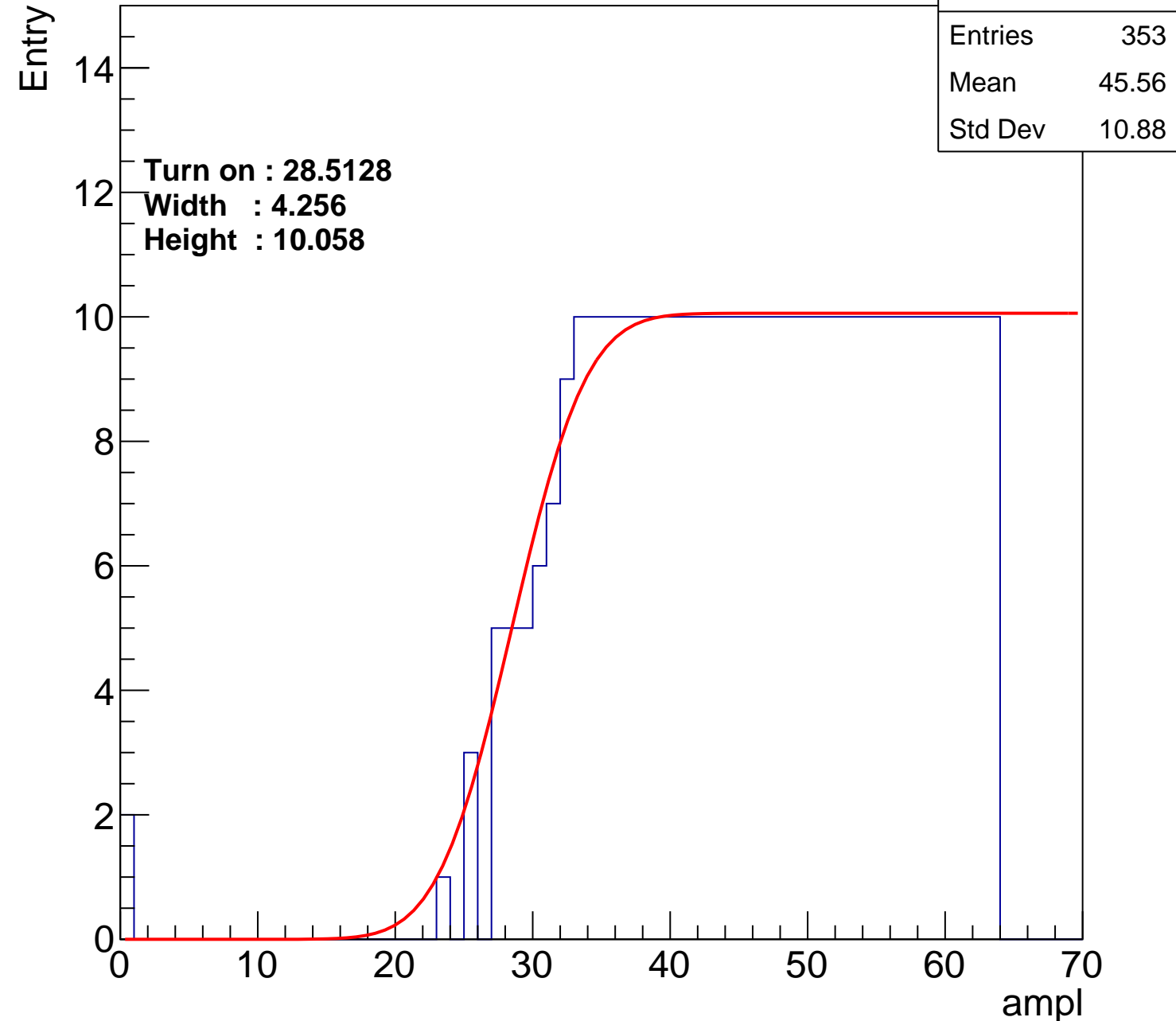
Width : 4.256

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch100

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.37
Std Dev	11.37

Turn on : 26.7033

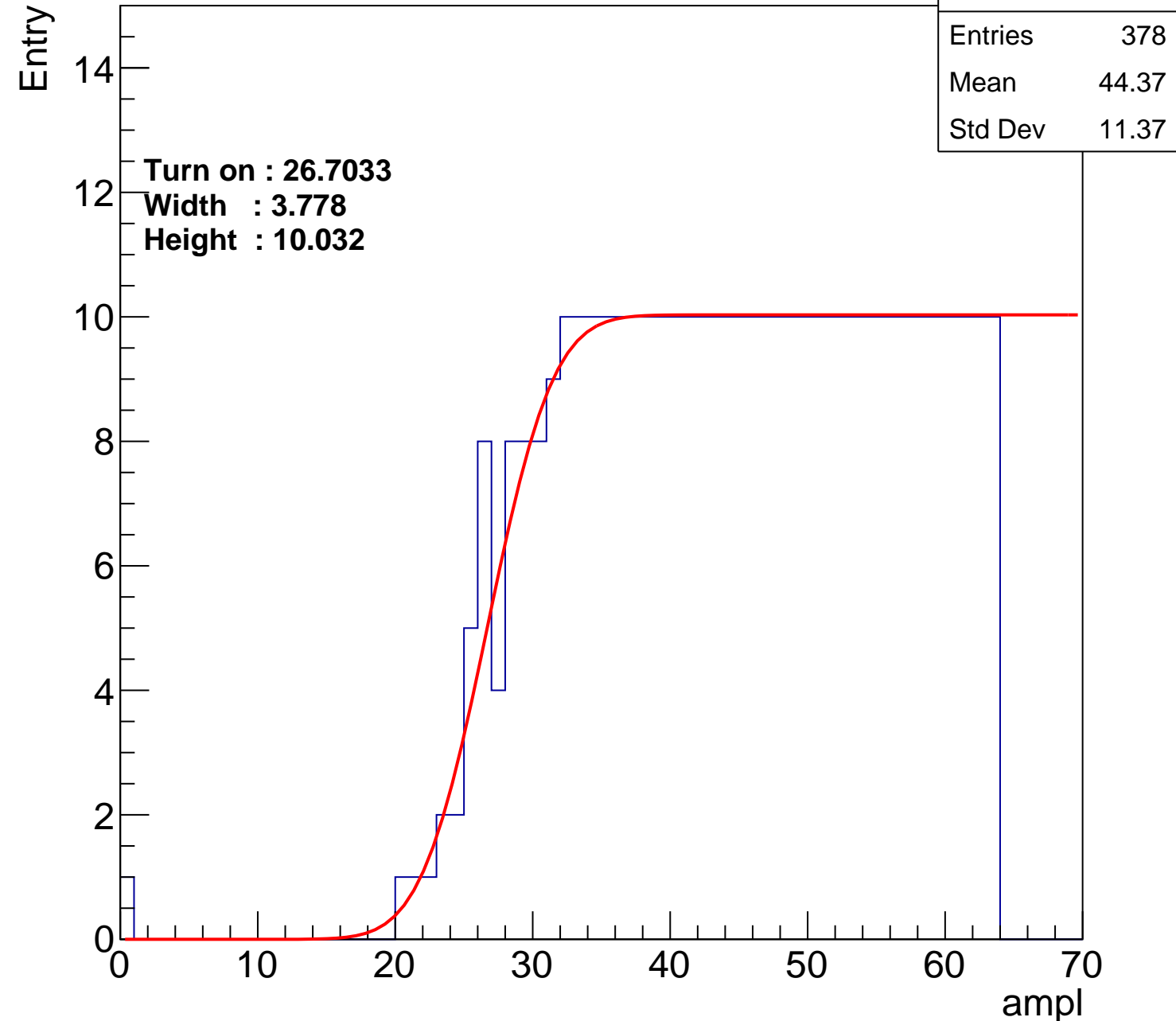
Width : 3.778

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch101

calib_packv5_042523_0143.root, FC#7, port C2

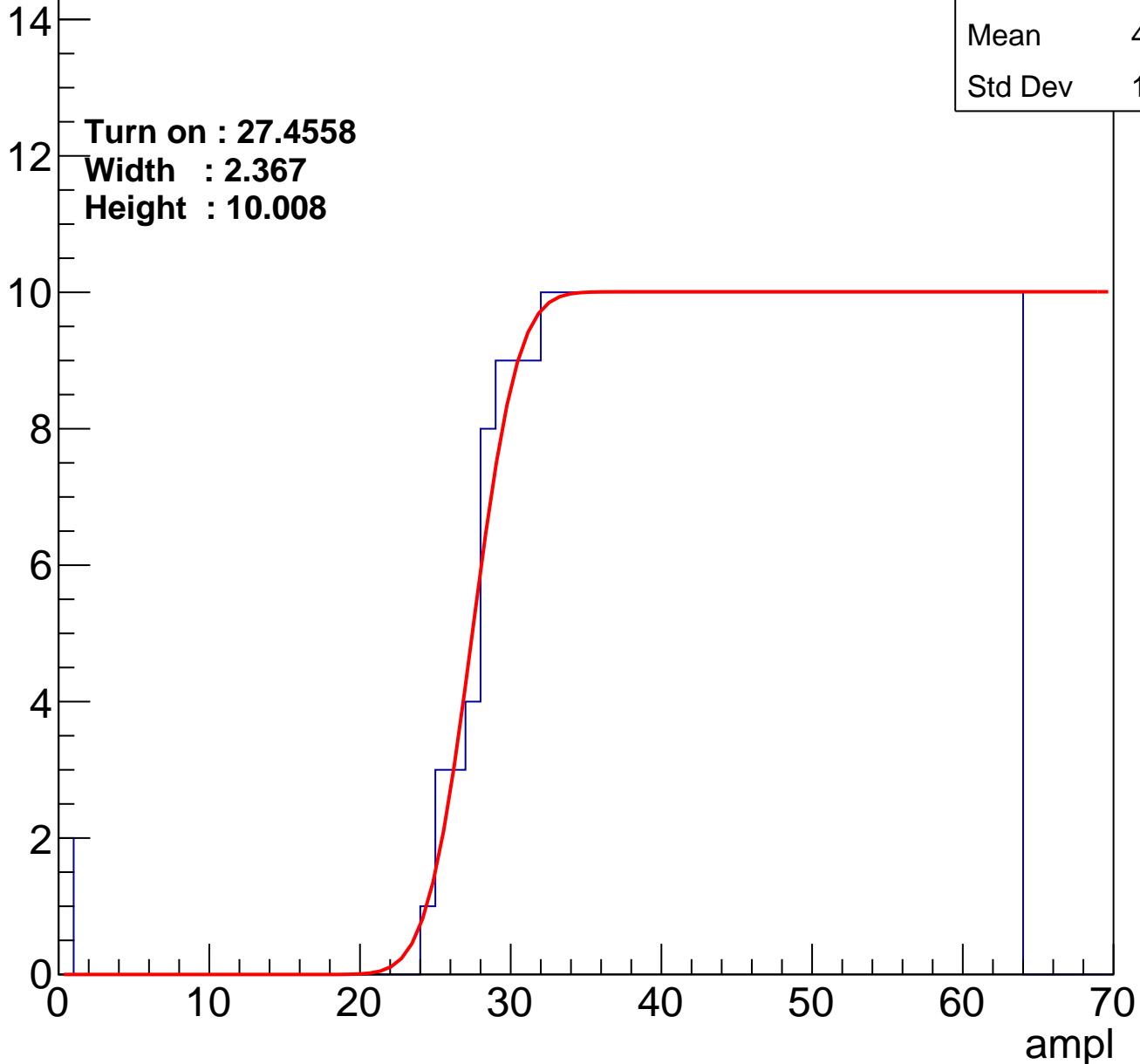
Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.4558

Width : 2.367

Height : 10.008

Entry



B1L103S, U11-ch102

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.18
Std Dev	11.07

Turn on : 28.4205

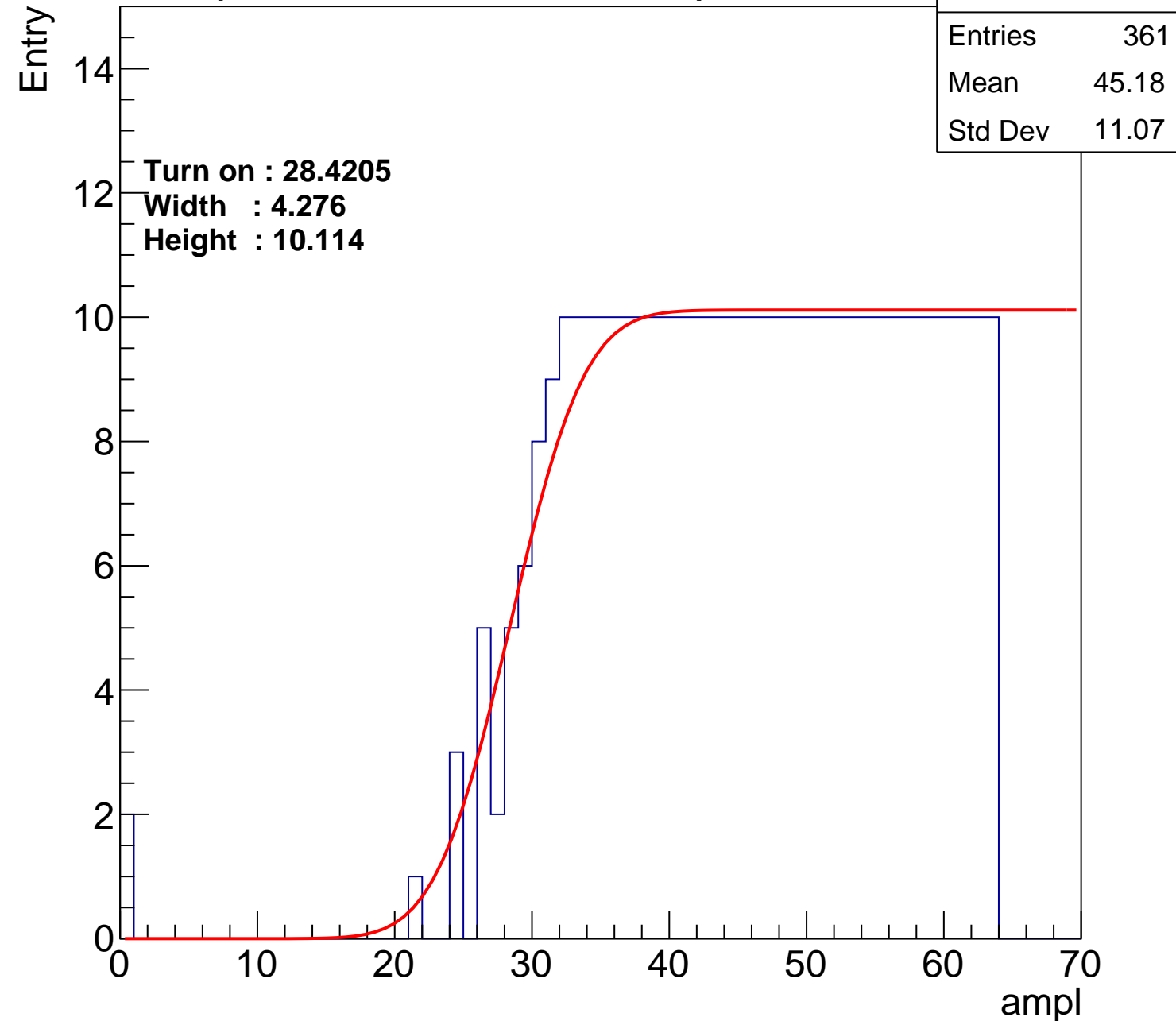
Width : 4.276

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.02
Std Dev	11.2

Turn on : 28.0473

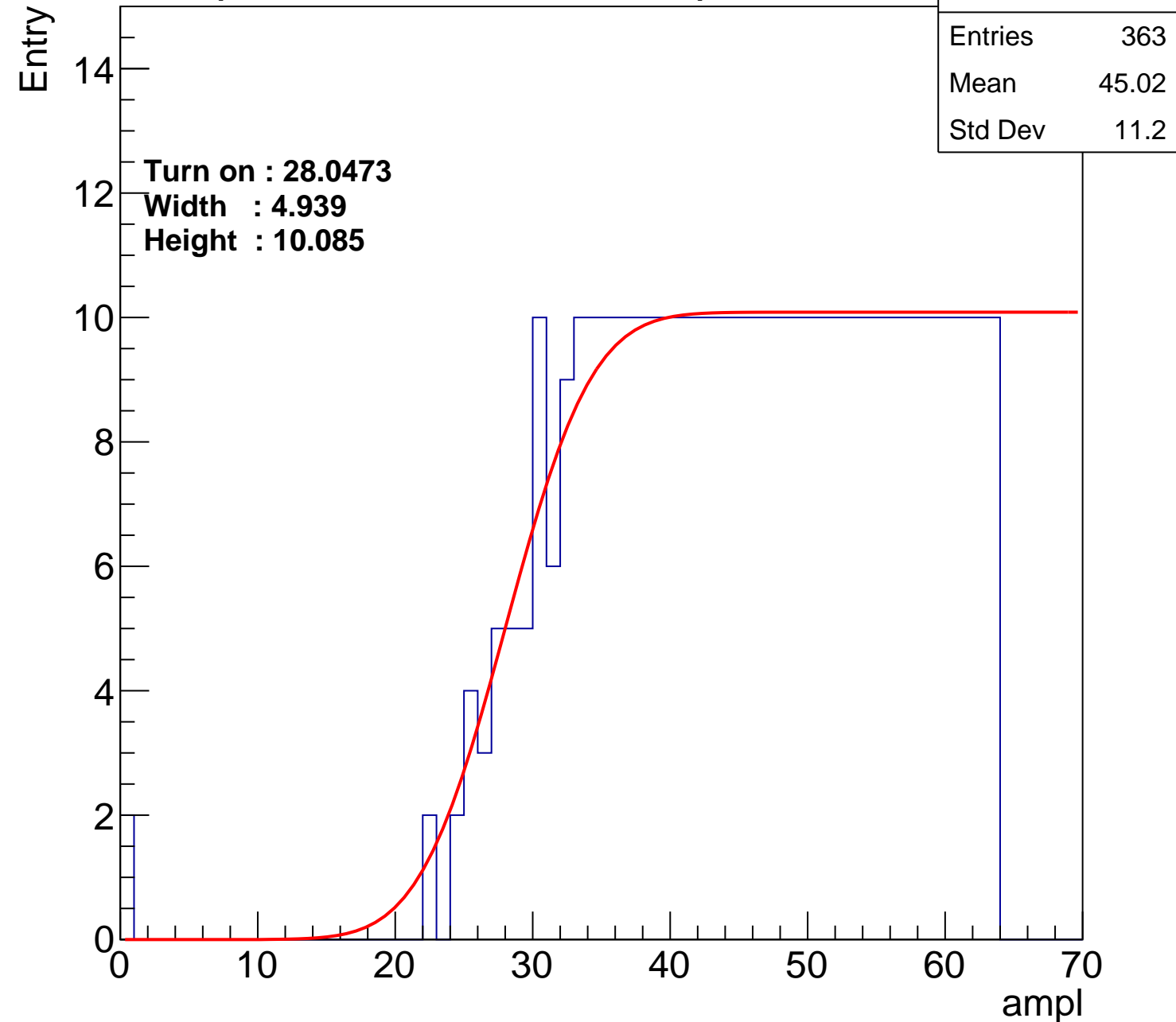
Width : 4.939

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch104

calib_packv5_042523_0143.root, FC#7, port C2

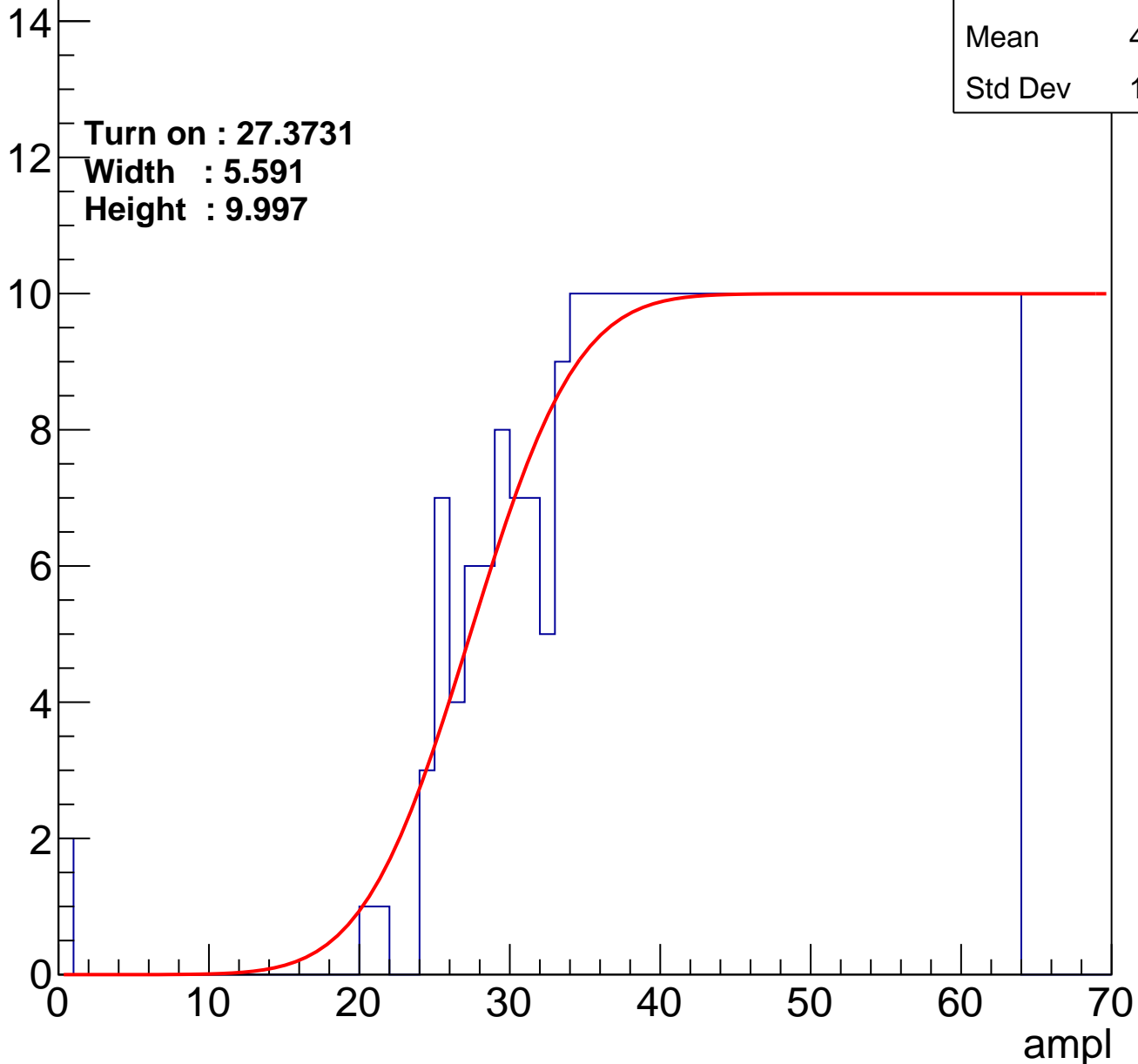
Entries	366
Mean	44.78
Std Dev	11.42

Turn on : 27.3731

Width : 5.591

Height : 9.997

Entry



B1L103S, U11-ch105

calib_packv5_042523_0143.root, FC#7, port C2

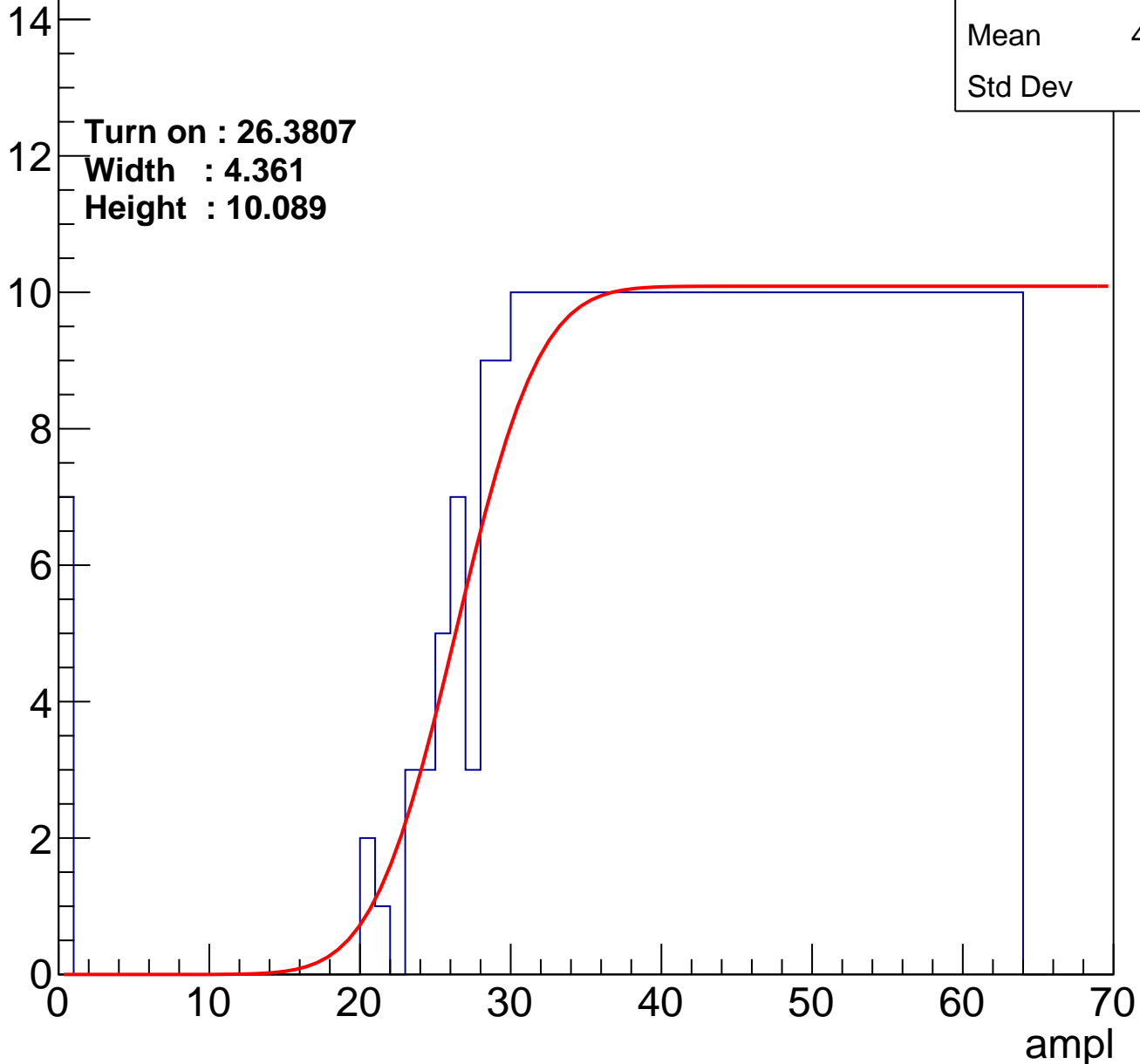
Entries	389
Mean	43.48
Std Dev	12.6

Turn on : 26.3807

Width : 4.361

Height : 10.089

Entry



B1L103S, U11-ch106

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.32
Std Dev	11.01

Turn on : 29.0857

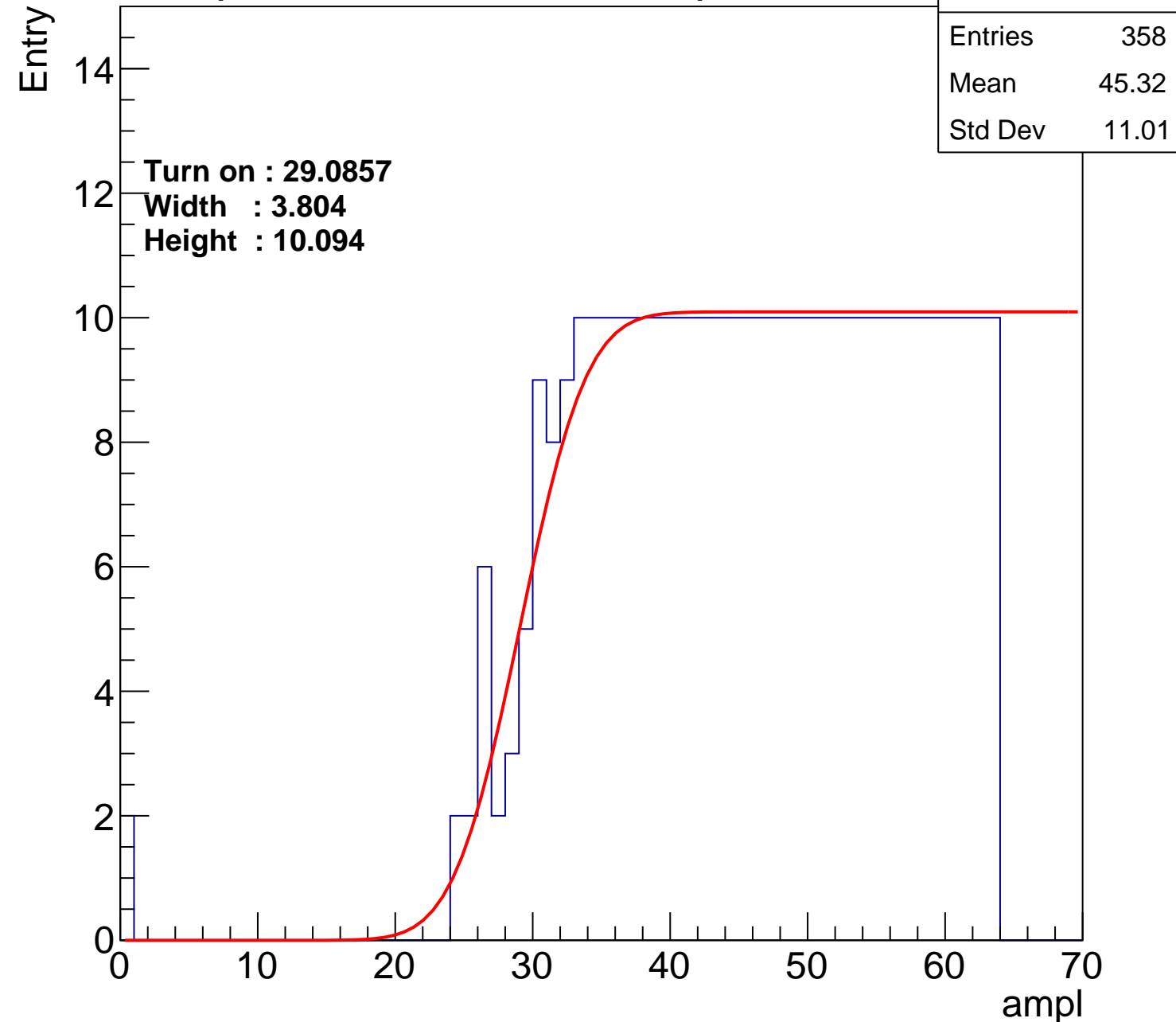
Width : 3.804

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 26.7972

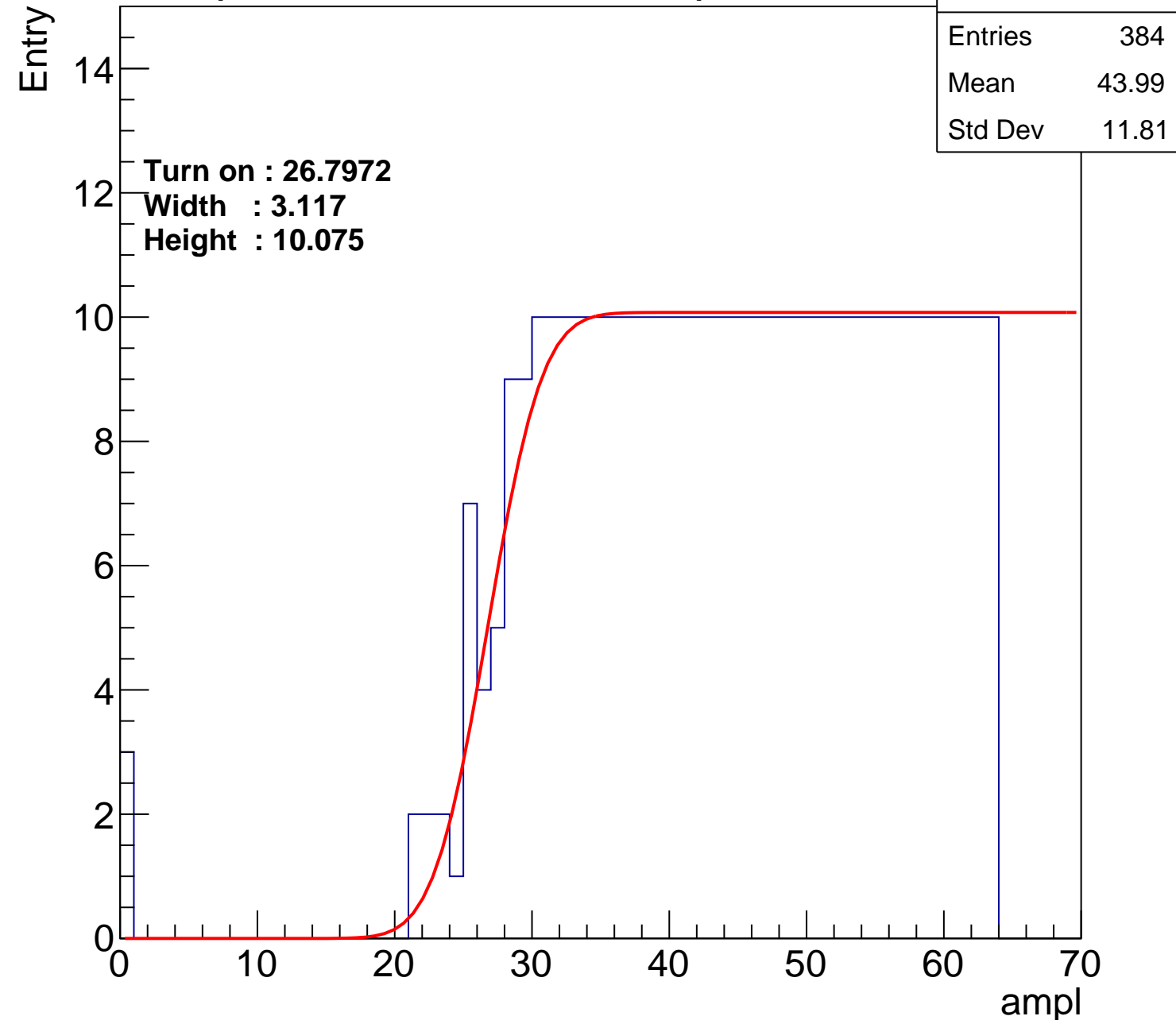
Width : 3.117

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 25.4822

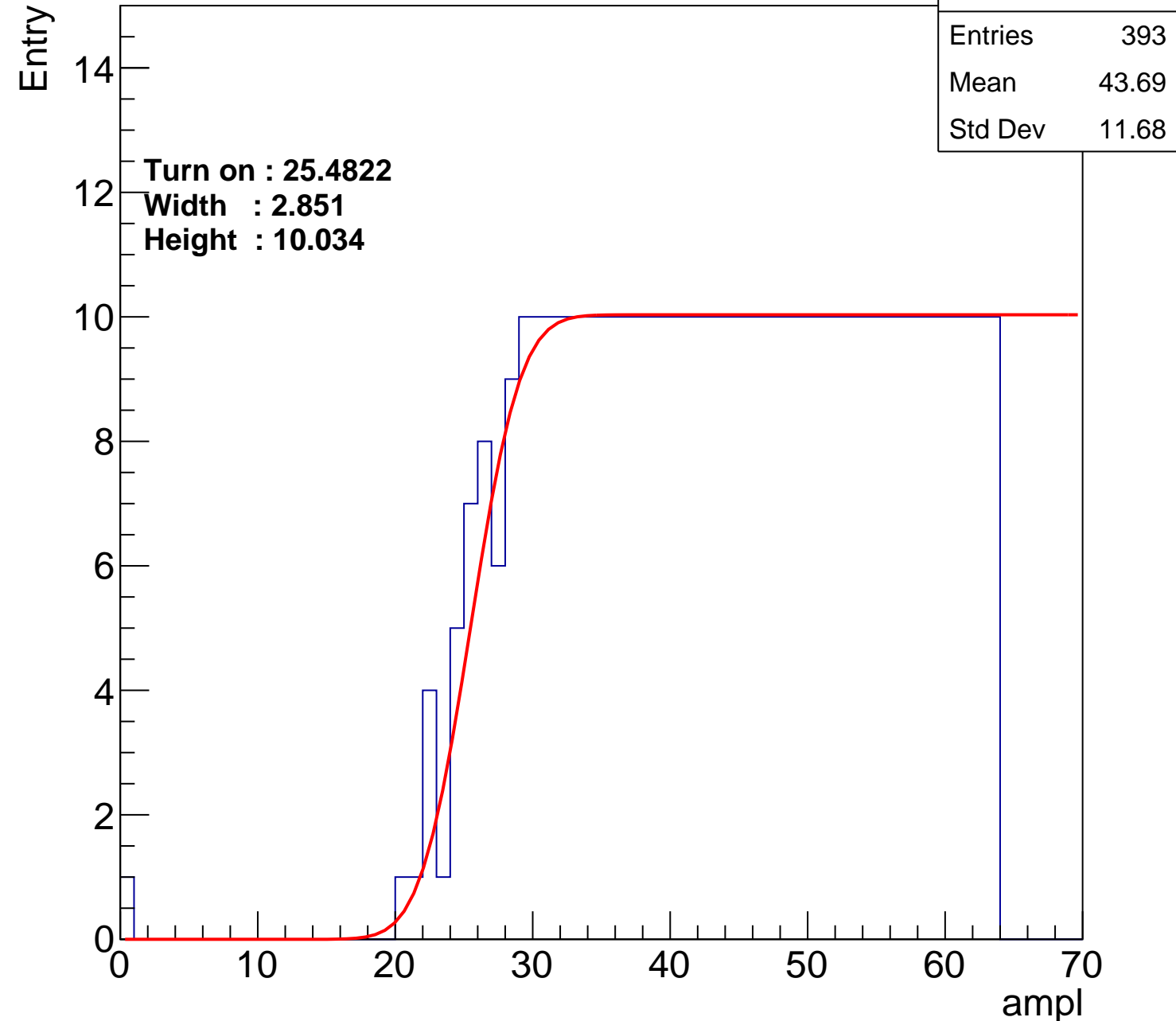
Width : 2.851

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.7871

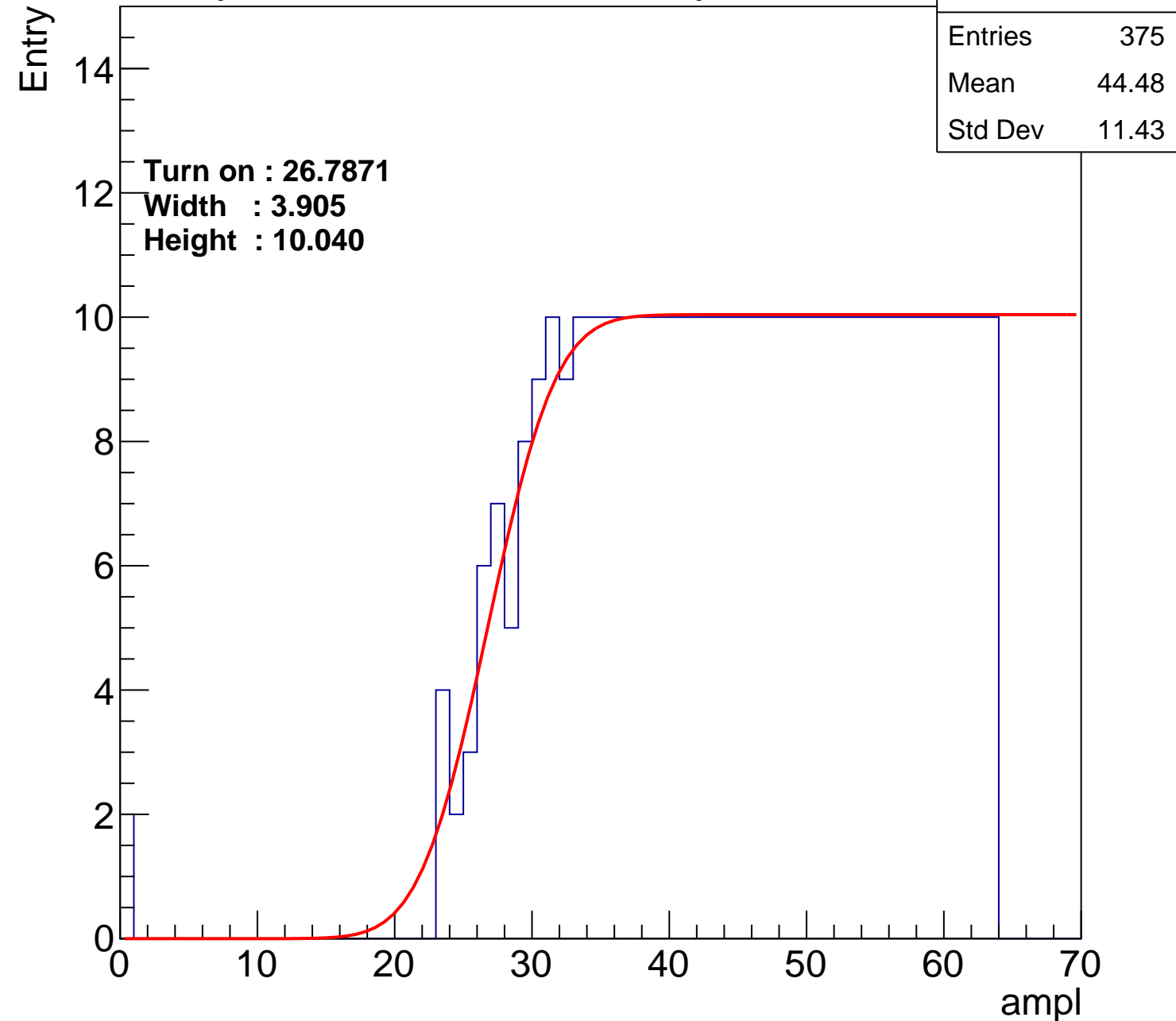
Width : 3.905

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.42
Std Dev	11.67

Turn on : 27.7529

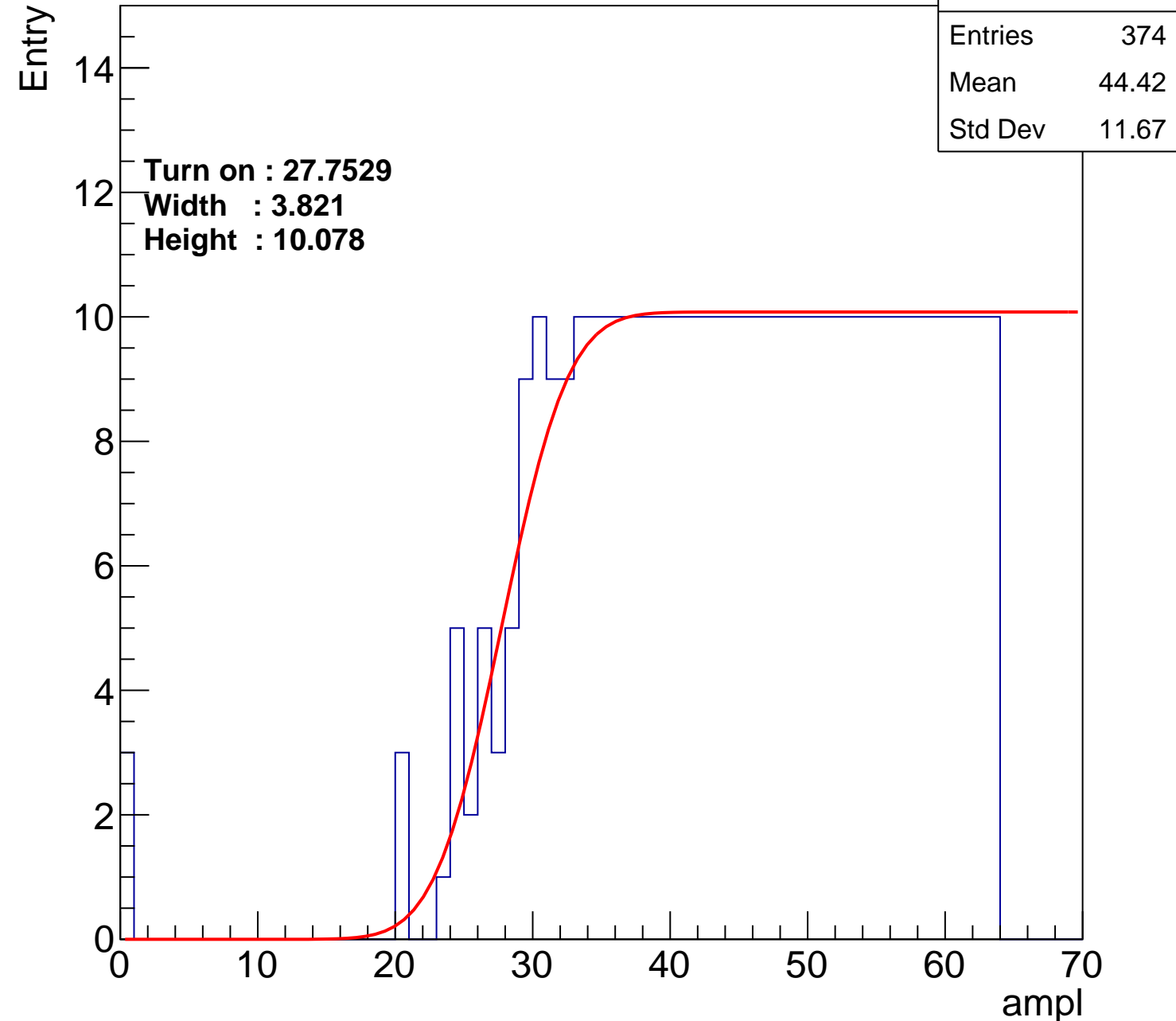
Width : 3.821

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.36
Std Dev	11.14

Turn on : 29.0311

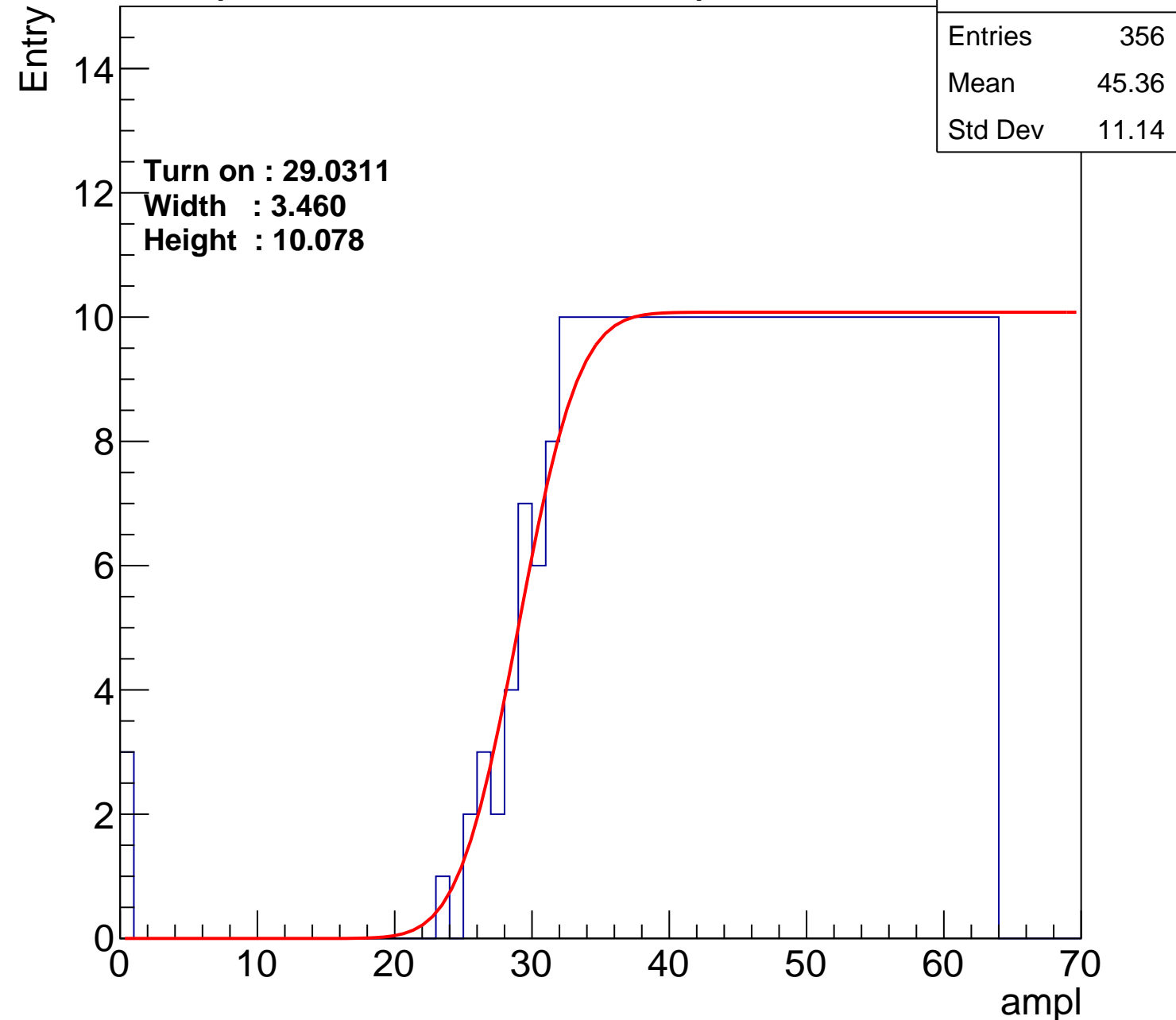
Width : 3.460

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.9
Std Dev	11.25

Turn on : 28.1311

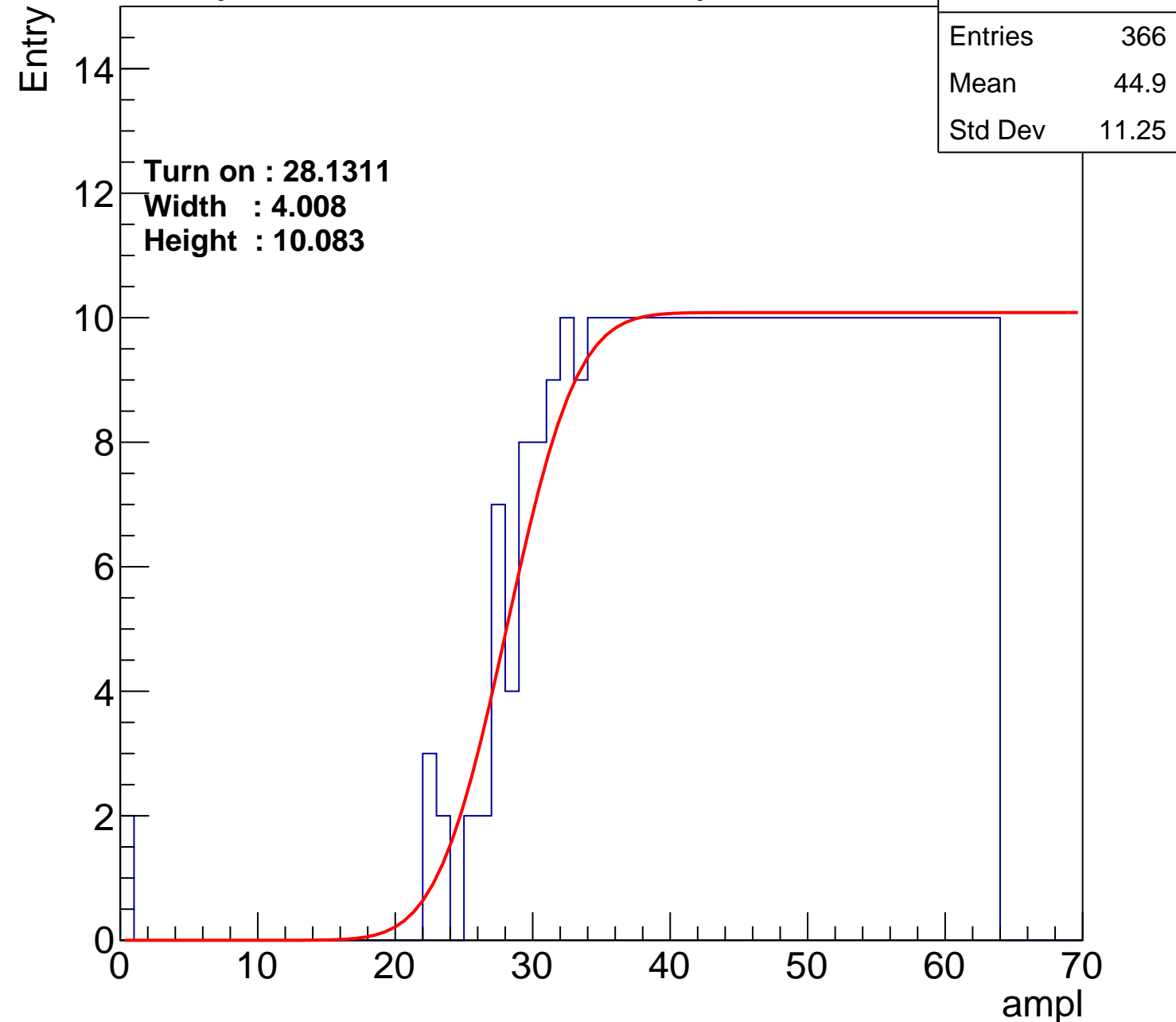
Width : 4.008

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch113

calib_packv5_042523_0143.root, FC#7, port C2

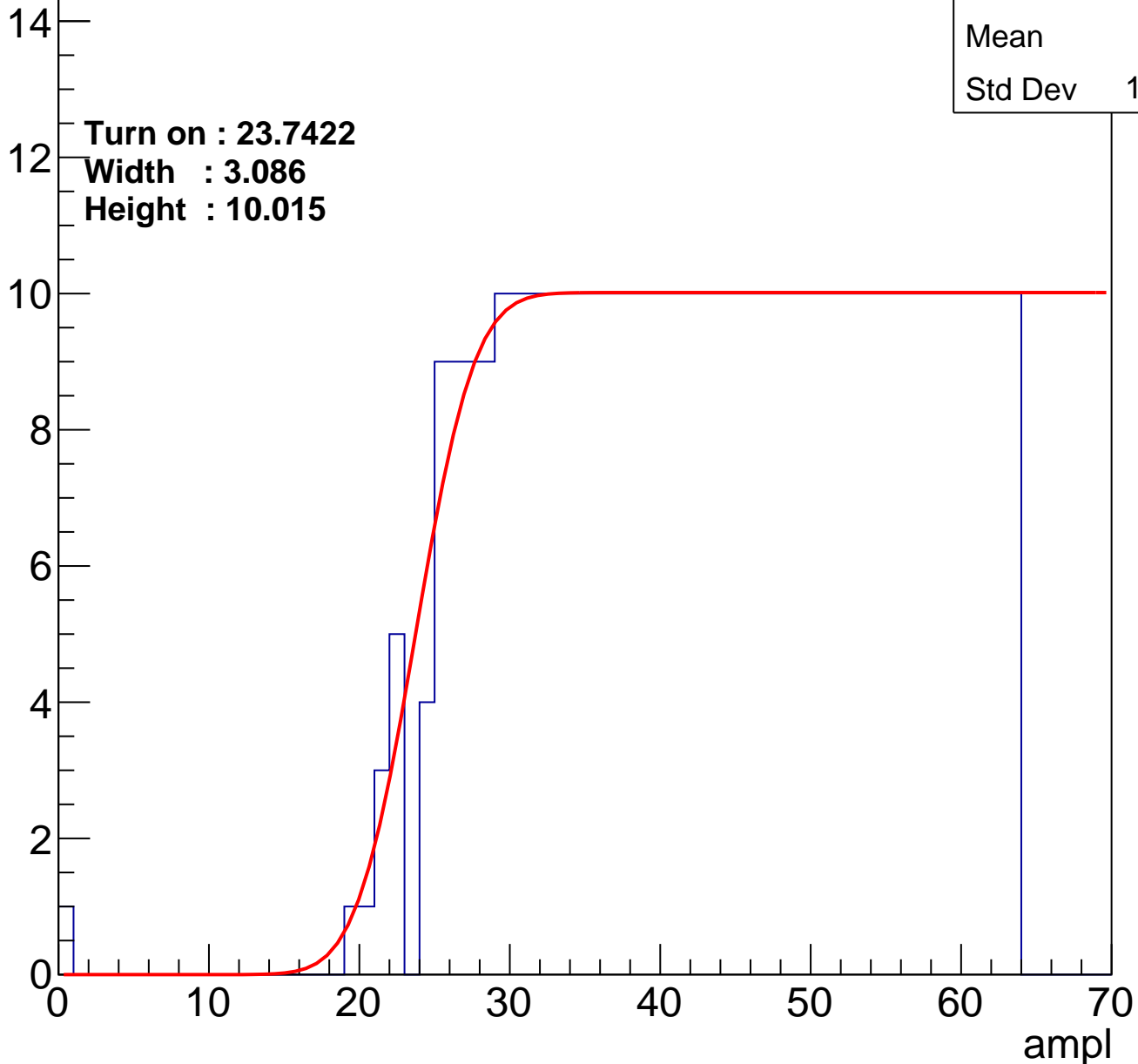
Entries	401
Mean	43.3
Std Dev	11.89

Turn on : 23.7422

Width : 3.086

Height : 10.015

Entry



B1L103S, U11-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.9847

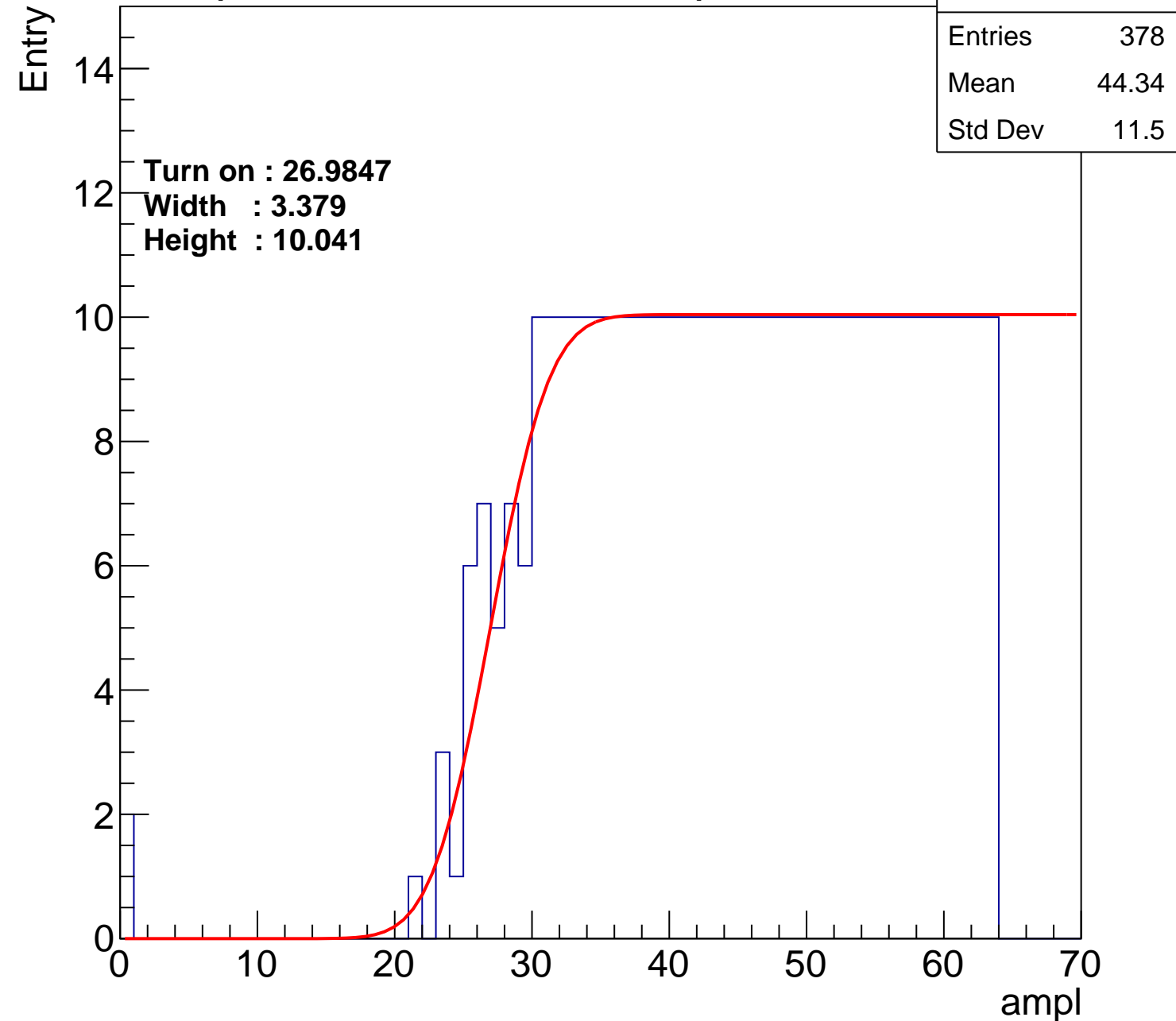
Width : 3.379

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch115

calib_packv5_042523_0143.root, FC#7, port C2

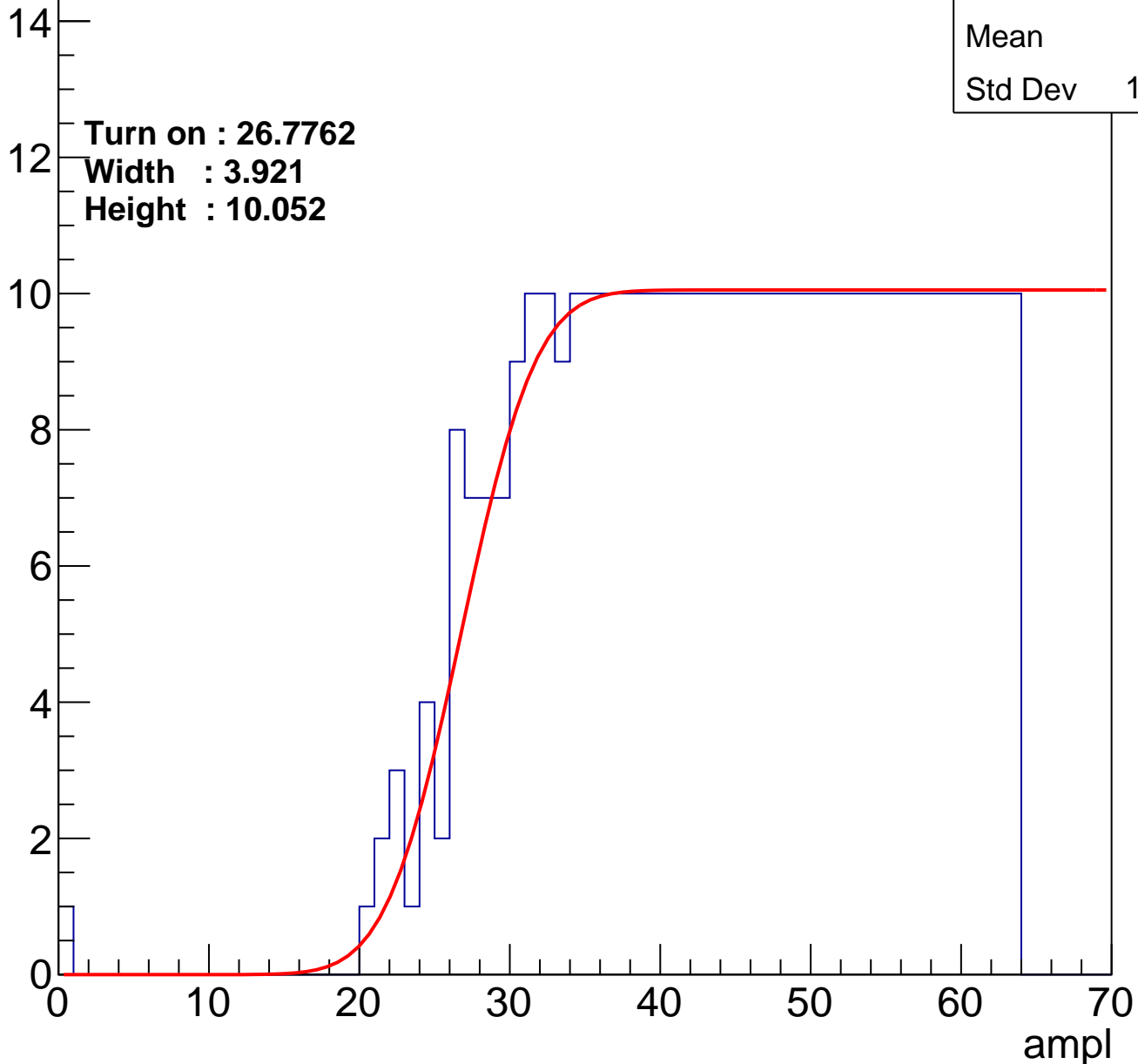
Entries	381
Mean	44.2
Std Dev	11.49

Turn on : 26.7762

Width : 3.921

Height : 10.052

Entry



B1L103S, U11-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.92
Std Dev	12.05

Turn on : 26.2426

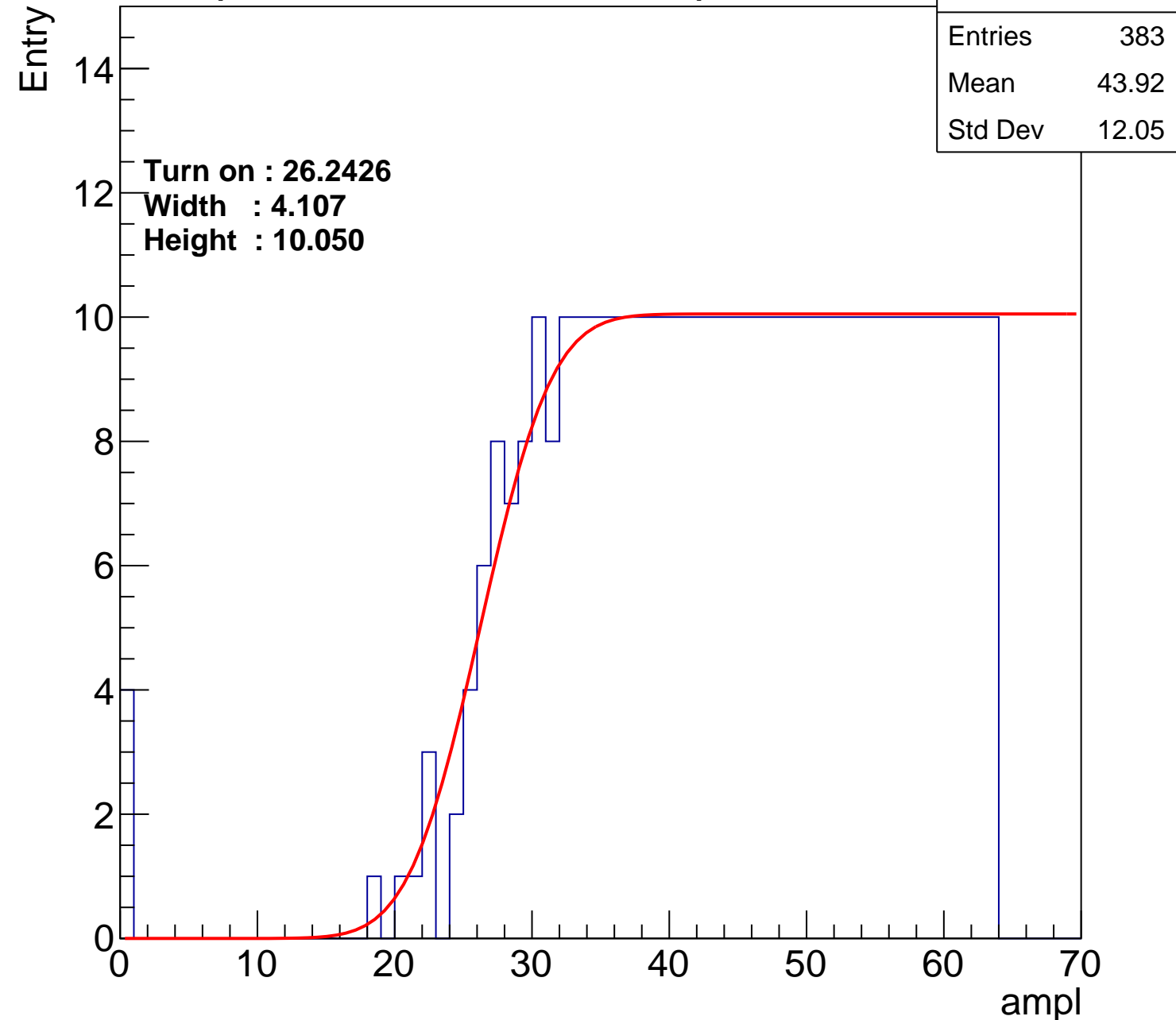
Width : 4.107

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.9506

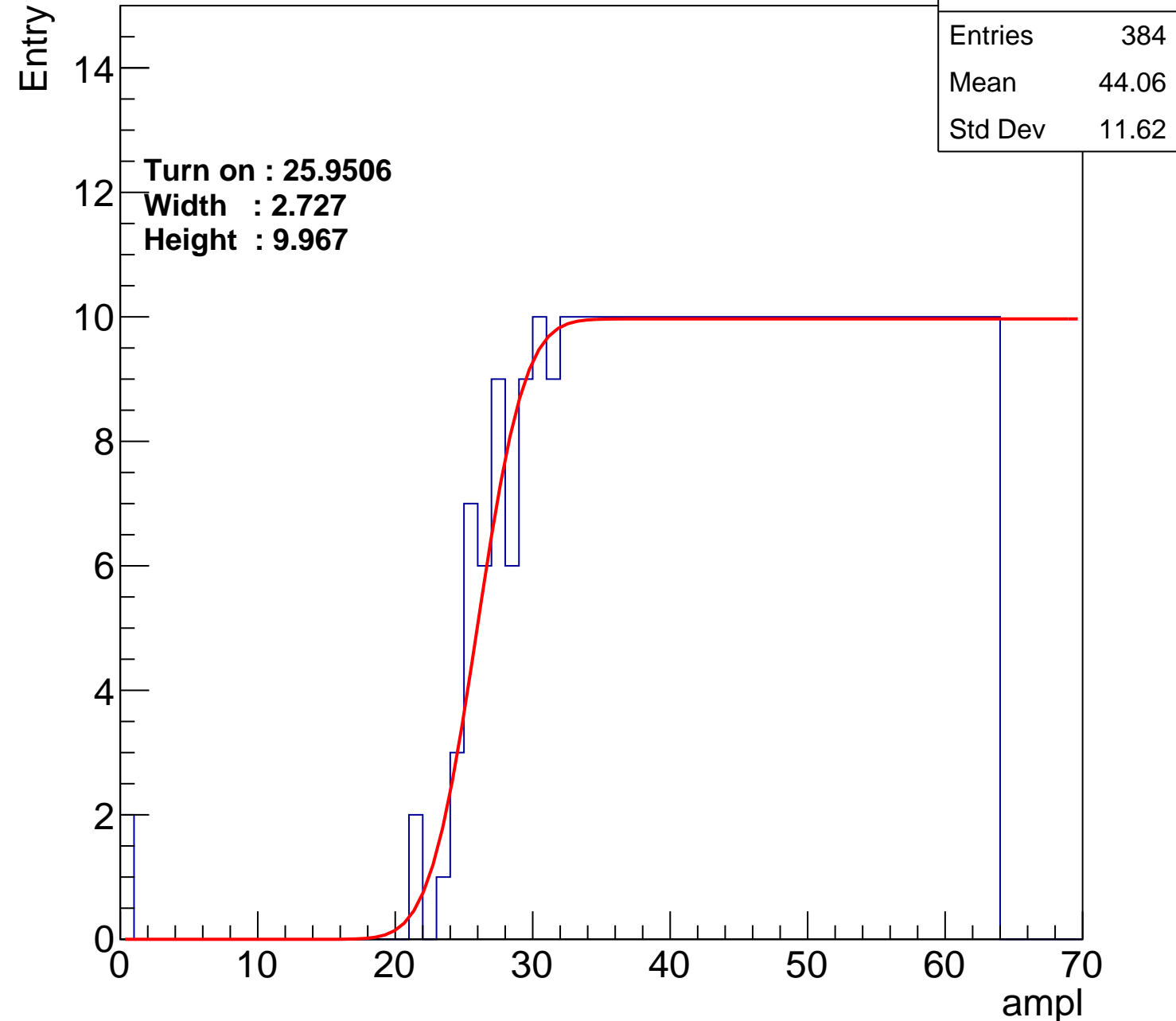
Width : 2.727

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.67
Std Dev	11.6

Turn on : 28.1986

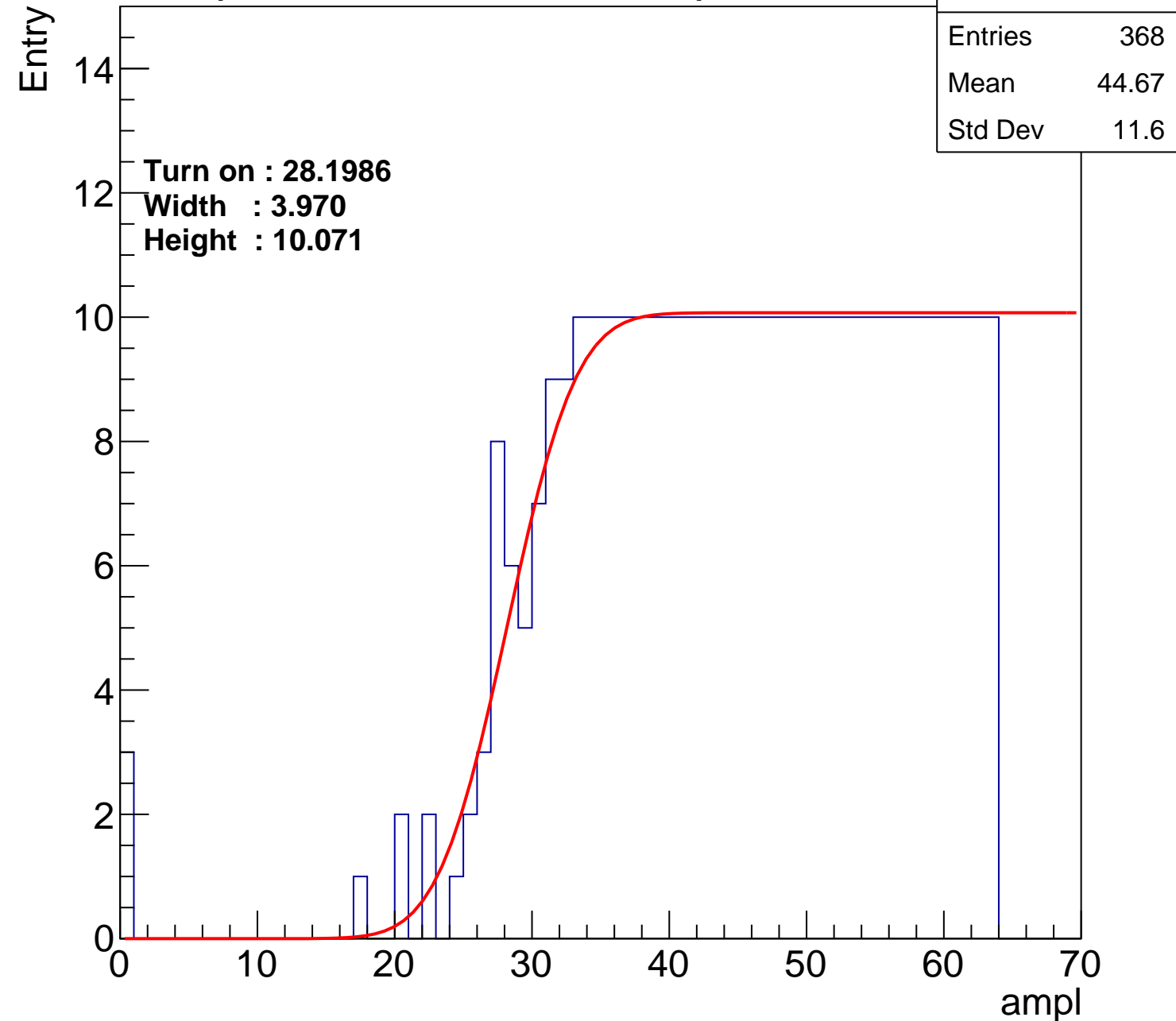
Width : 3.970

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch119

calib_packv5_042523_0143.root, FC#7, port C2

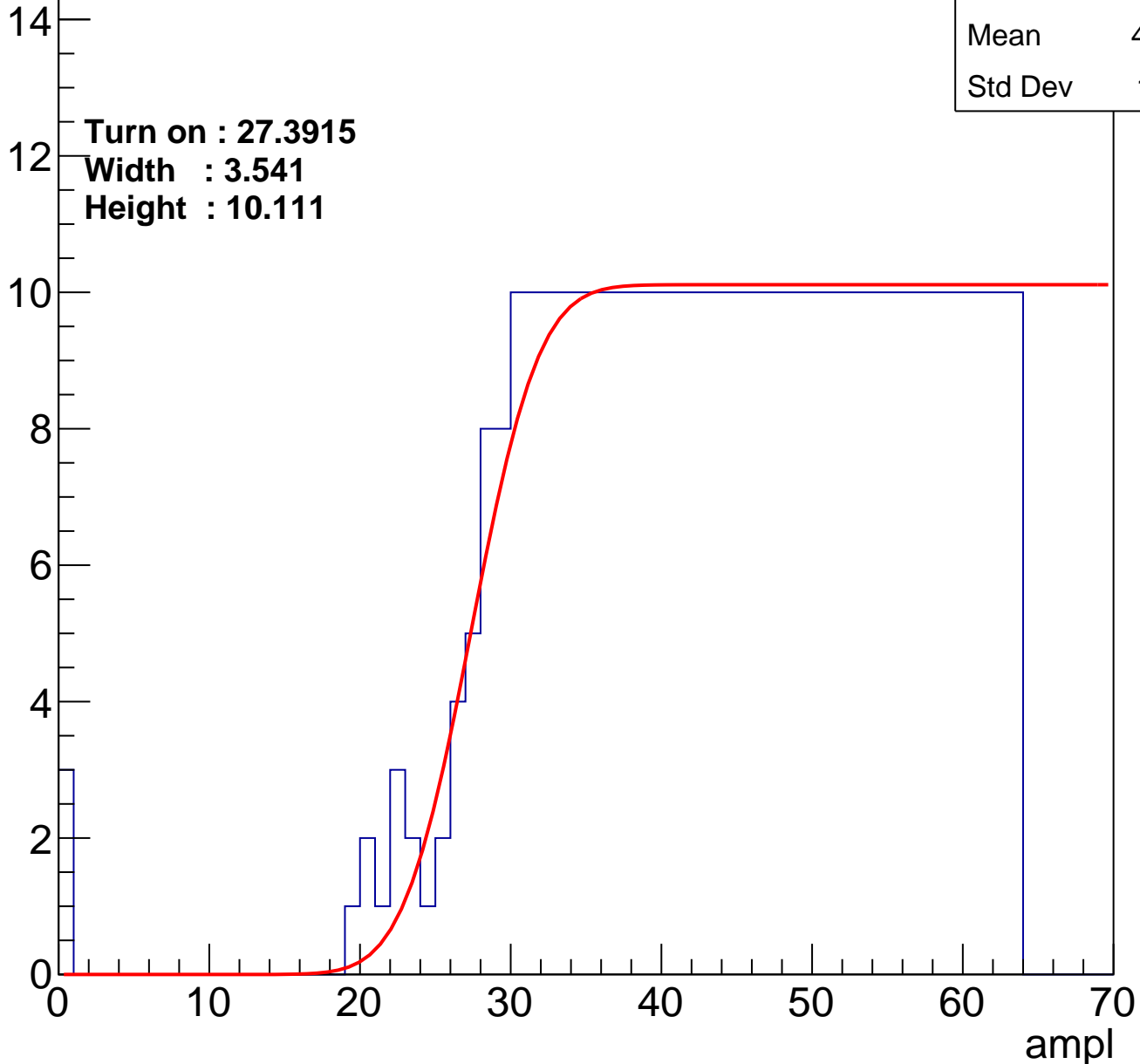
Entries	380
Mean	44.13
Std Dev	11.81

Turn on : 27.3915

Width : 3.541

Height : 10.111

Entry



B1L103S, U11-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.26
Std Dev	11.88

Turn on : 26.9474

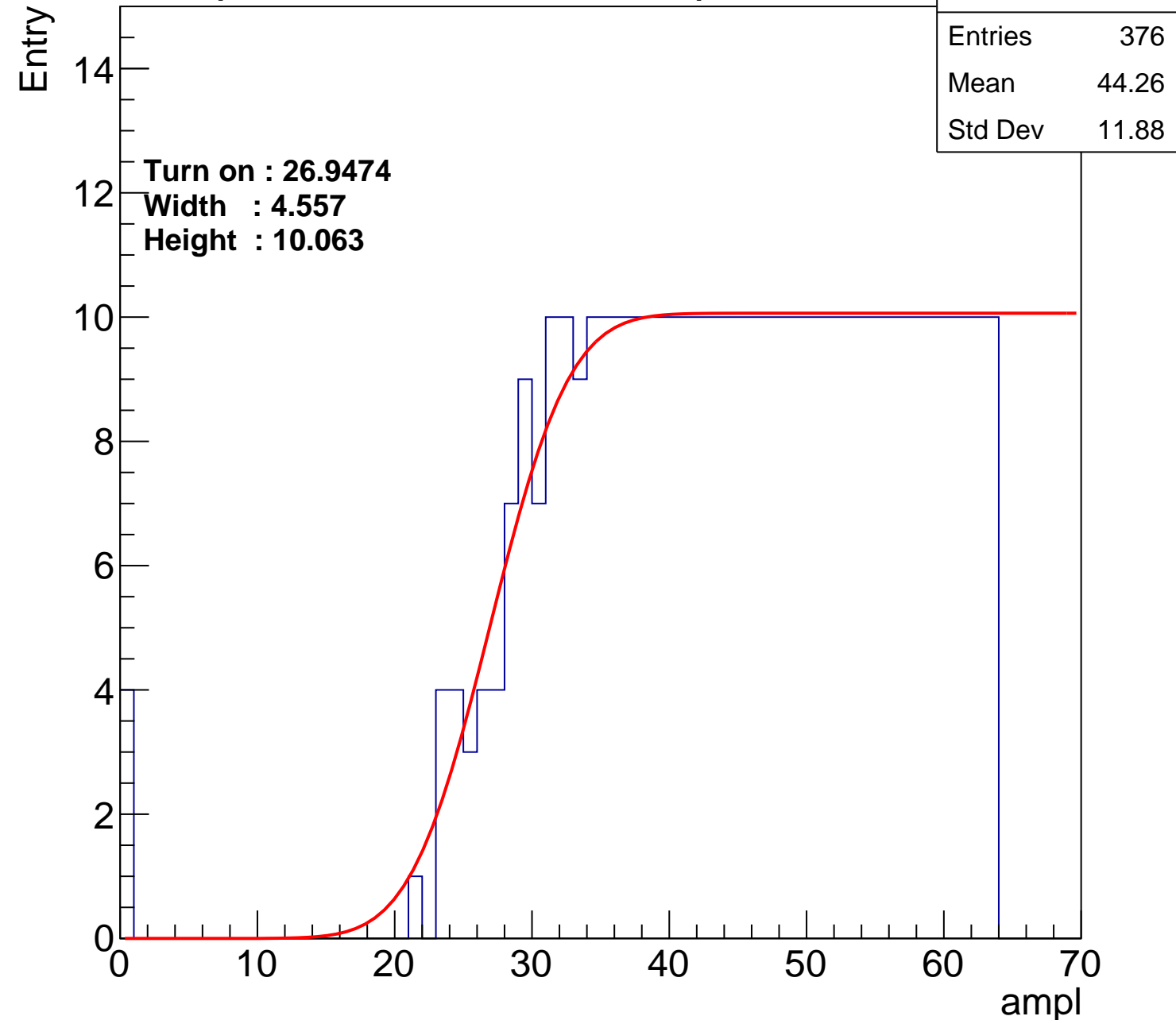
Width : 4.557

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch121

calib_packv5_042523_0143.root, FC#7, port C2

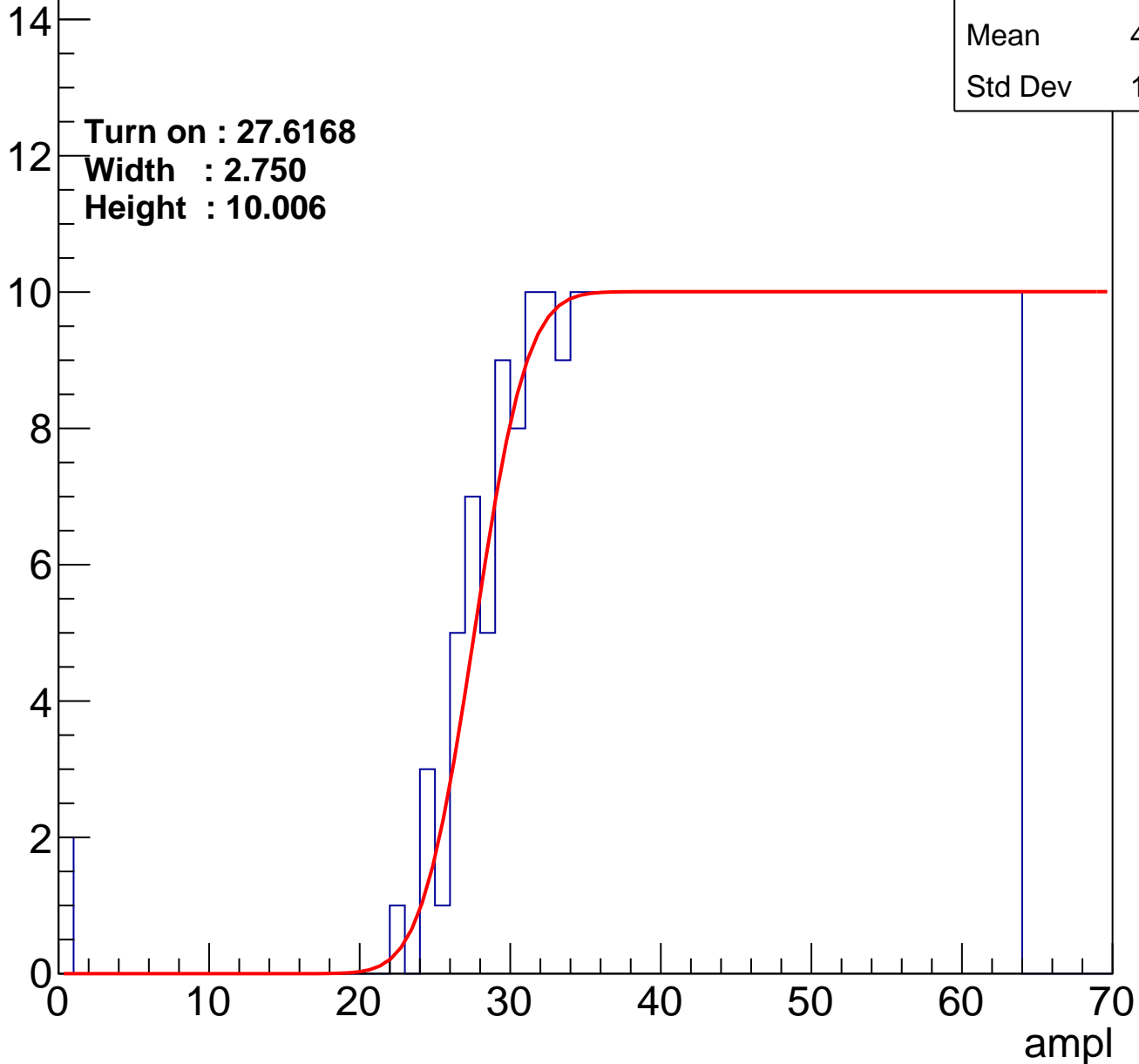
Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.6168

Width : 2.750

Height : 10.006

Entry



B1L103S, U11-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	402
Mean	43.12
Std Dev	12.24

Turn on : 24.8805

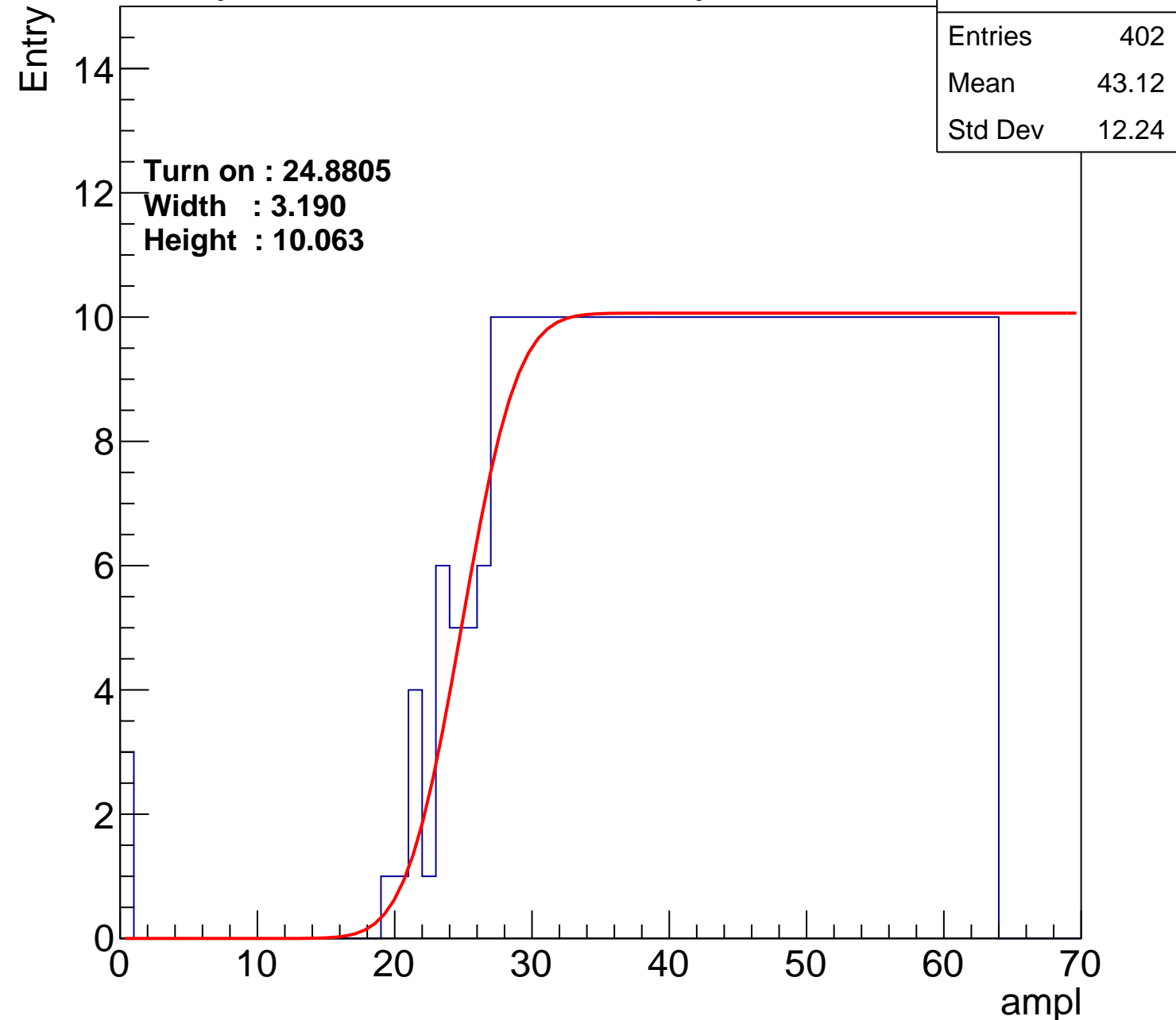
Width : 3.190

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch123

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.73
Std Dev	11.49

Turn on : 27.2074

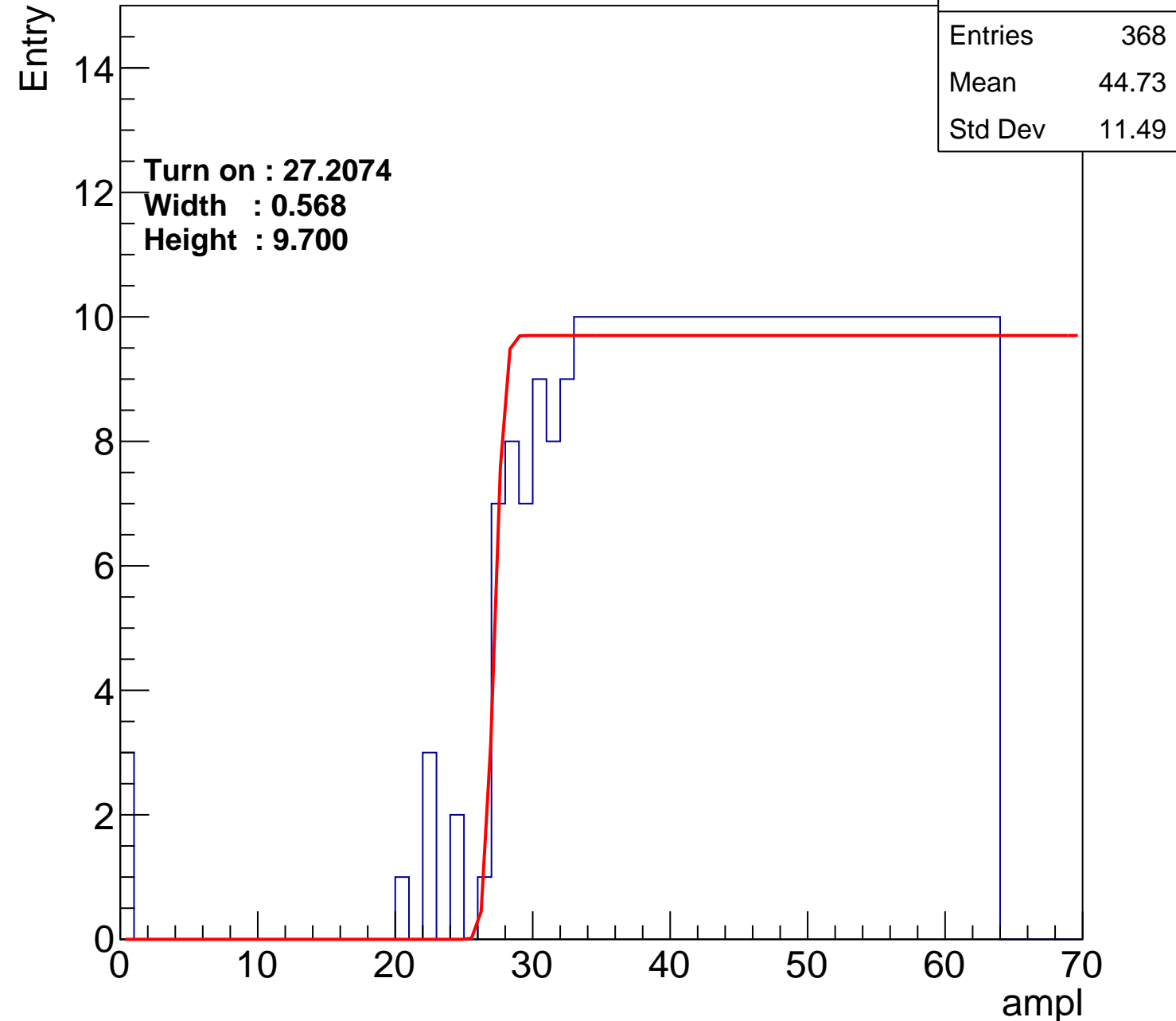
Width : 0.568

Height : 9.700

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.59
Std Dev	12.07

Turn on : 25.5368

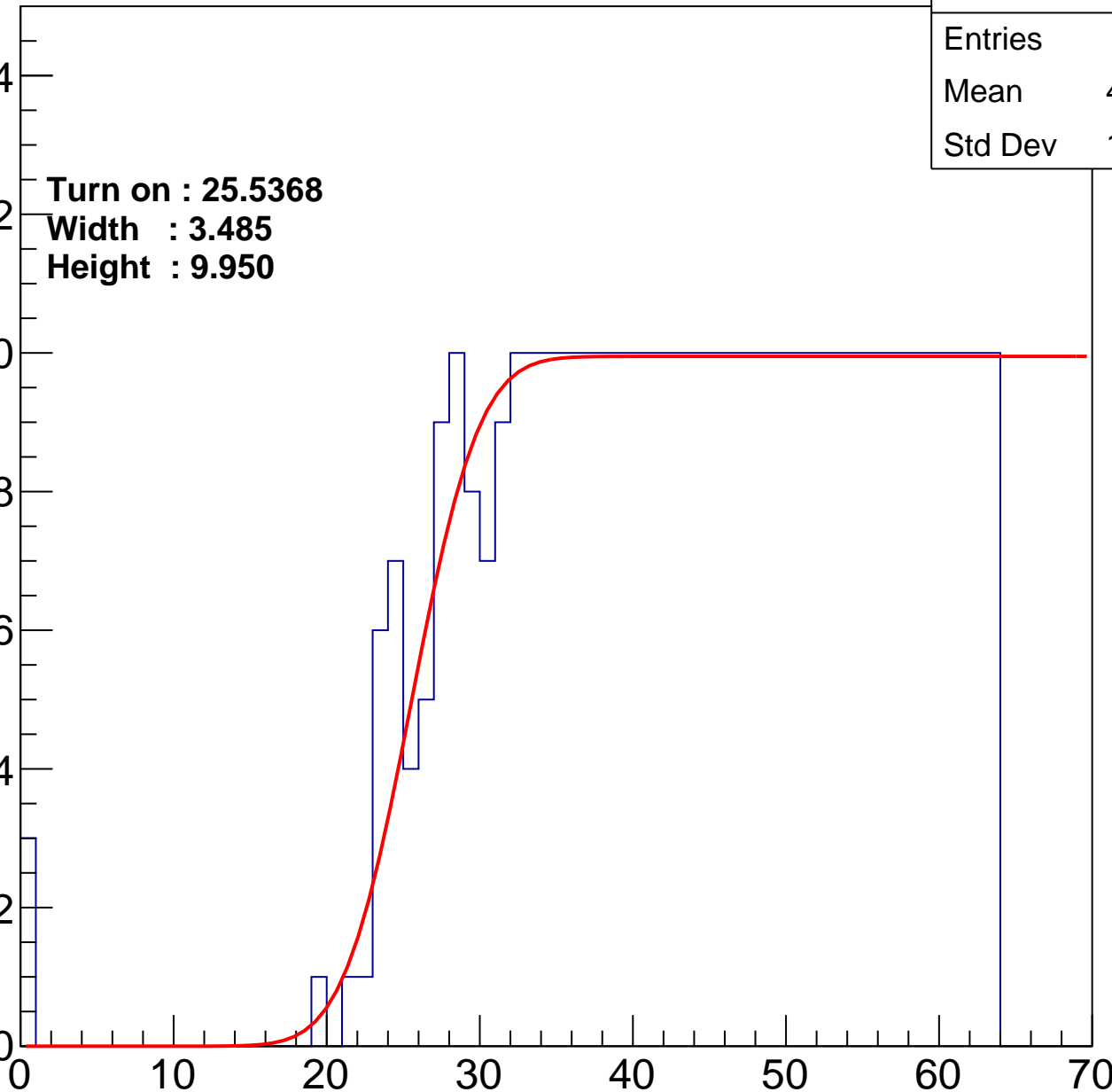
Width : 3.485

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.66
Std Dev	11.37

Turn on : 27.3872

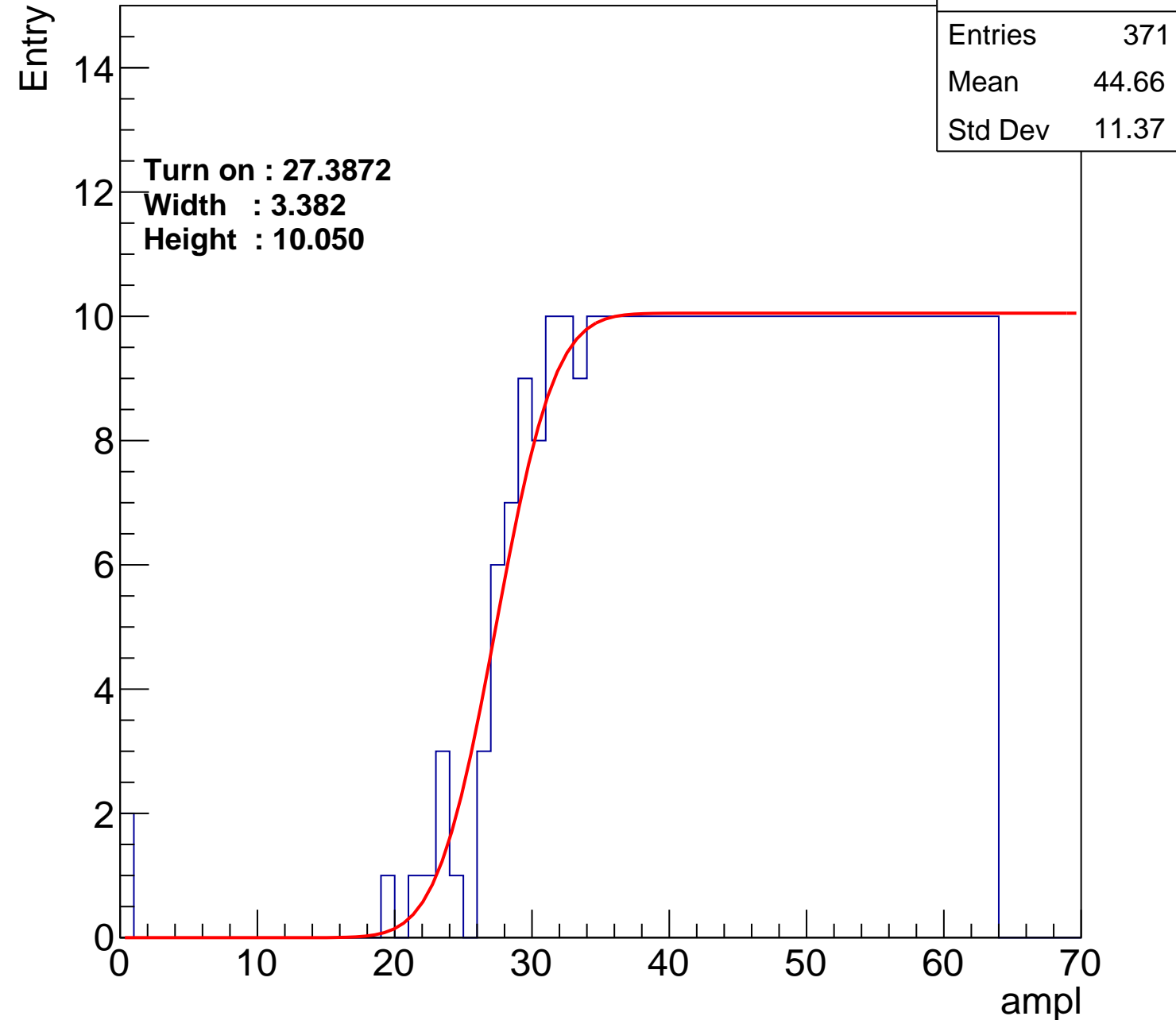
Width : 3.382

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch126

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.64
Std Dev	12.15

Turn on : 25.7677

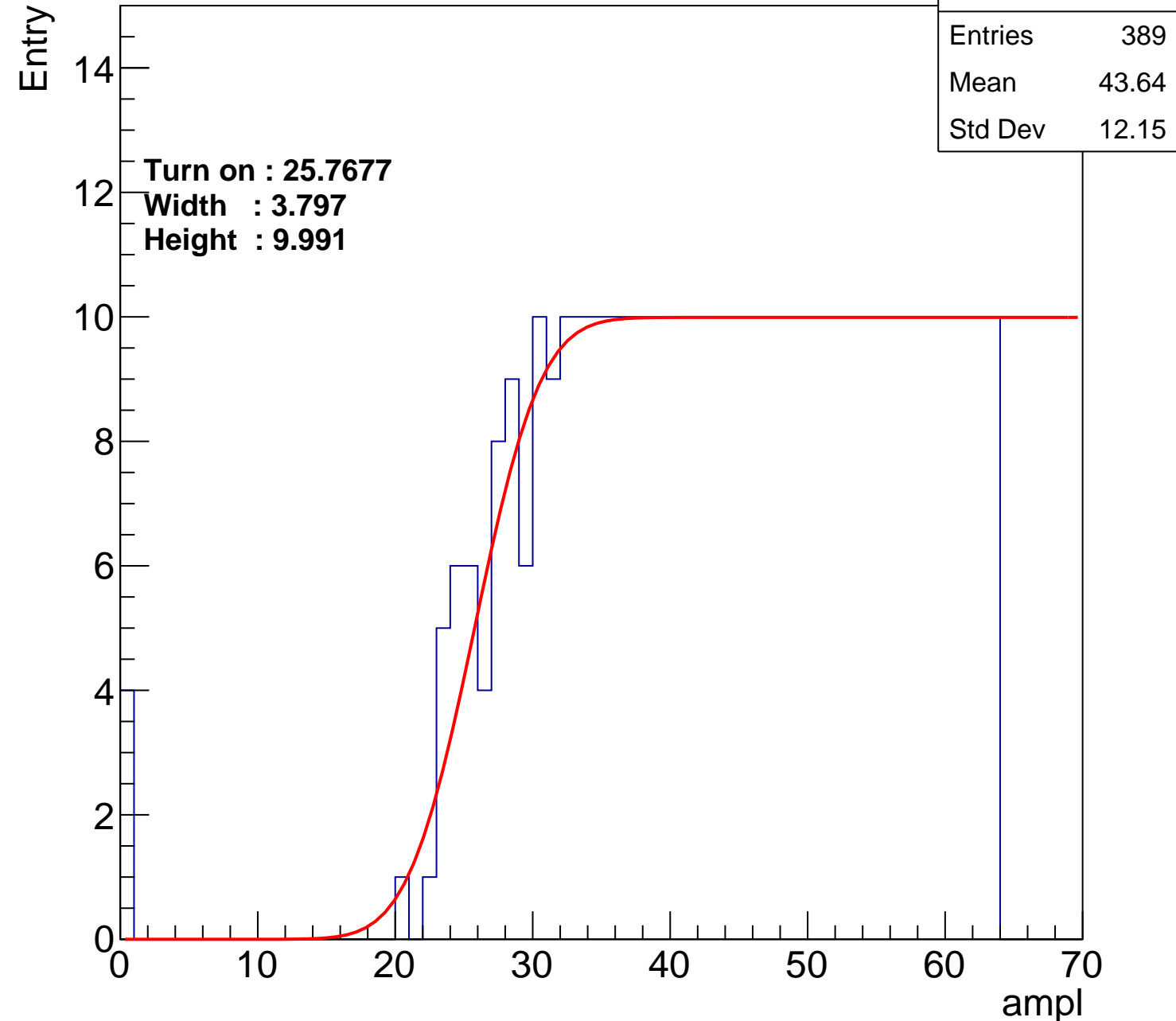
Width : 3.797

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U11-ch127

calib_packv5_042523_0143.root, FC#7, port C2

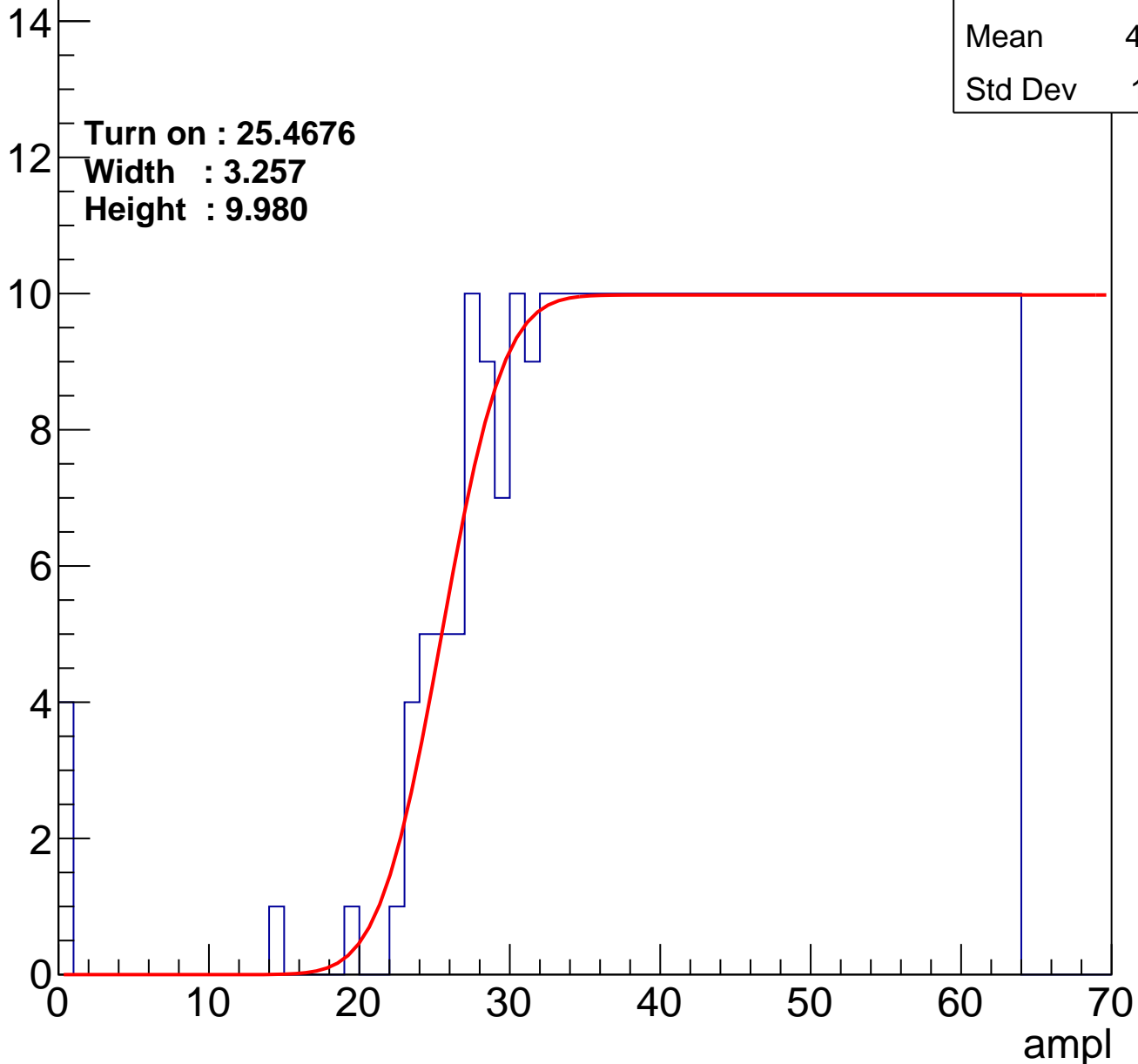
Entries	391
Mean	43.54
Std Dev	12.21

Turn on : 25.4676

Width : 3.257

Height : 9.980

Entry



B1L103S, U11-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.54
Std Dev	12.21

Turn on : 25.4676

Width : 3.257

Height : 9.980

Entry

