

B0L000S, U6-ch0

calib_packv5_042523_0143.root, FC#5, port B1

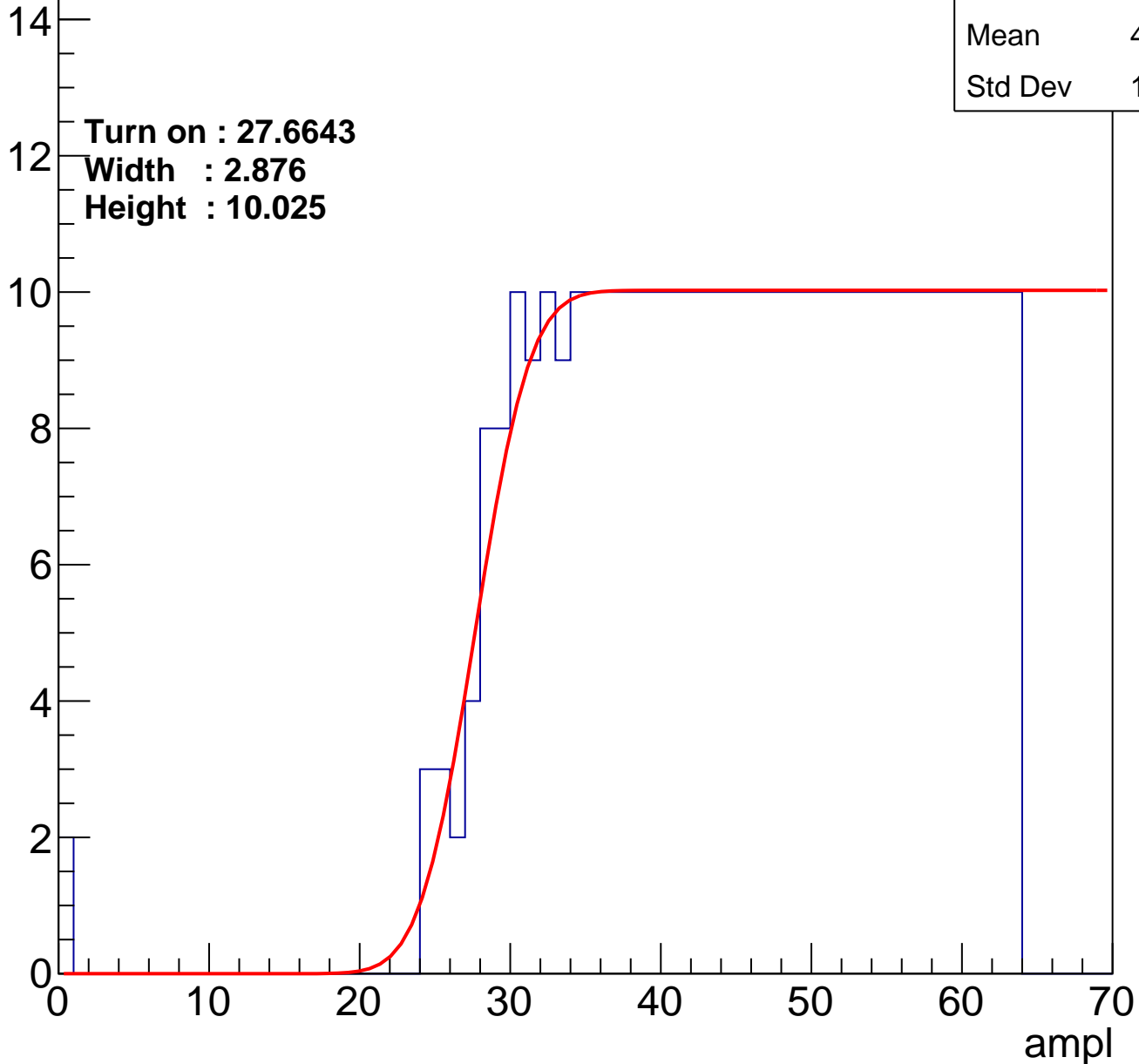
Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.6643

Width : 2.876

Height : 10.025

Entry



B0L000S, U6-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.46
Std Dev	10.89

Turn on : 28.9852

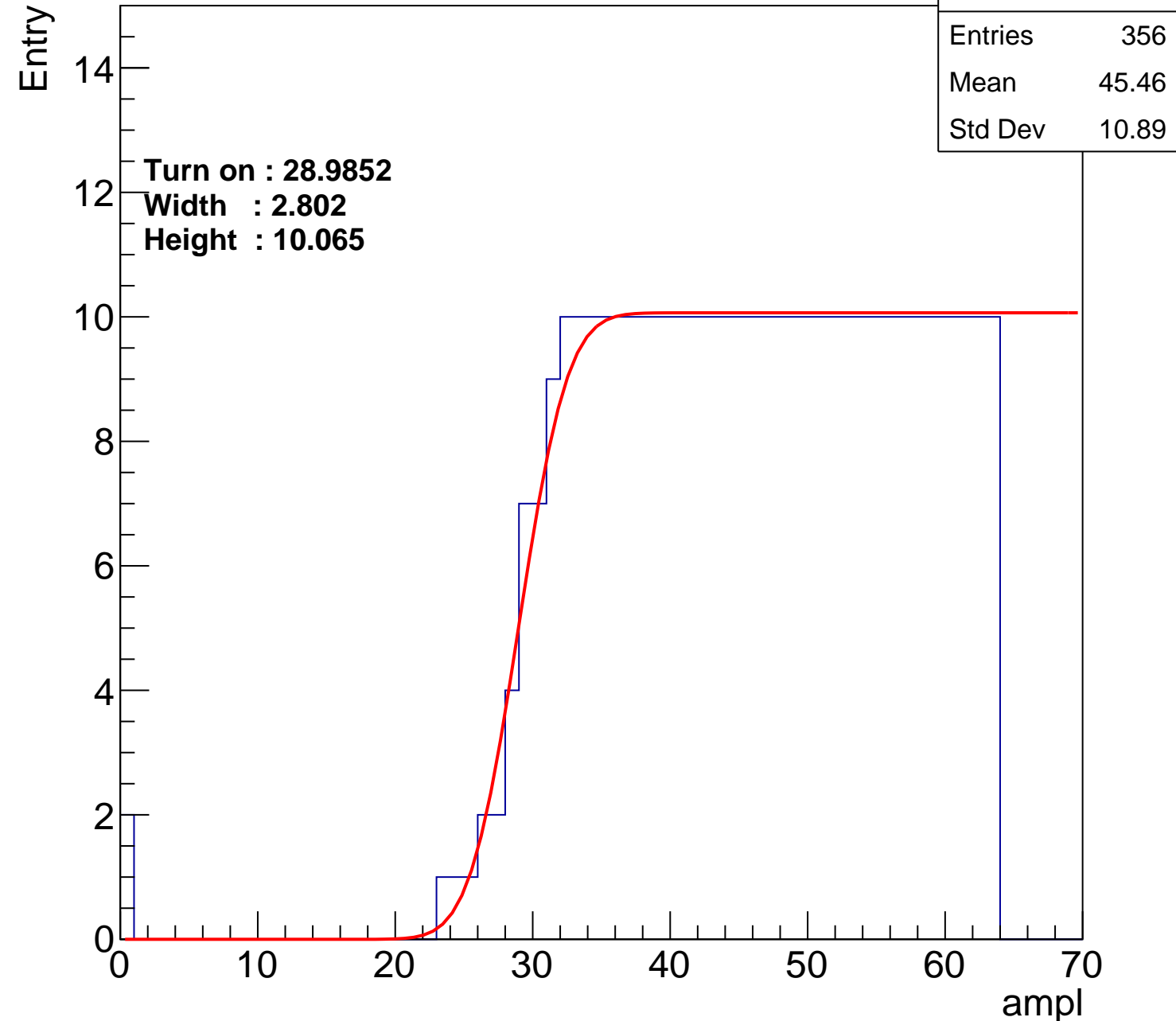
Width : 2.802

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch2

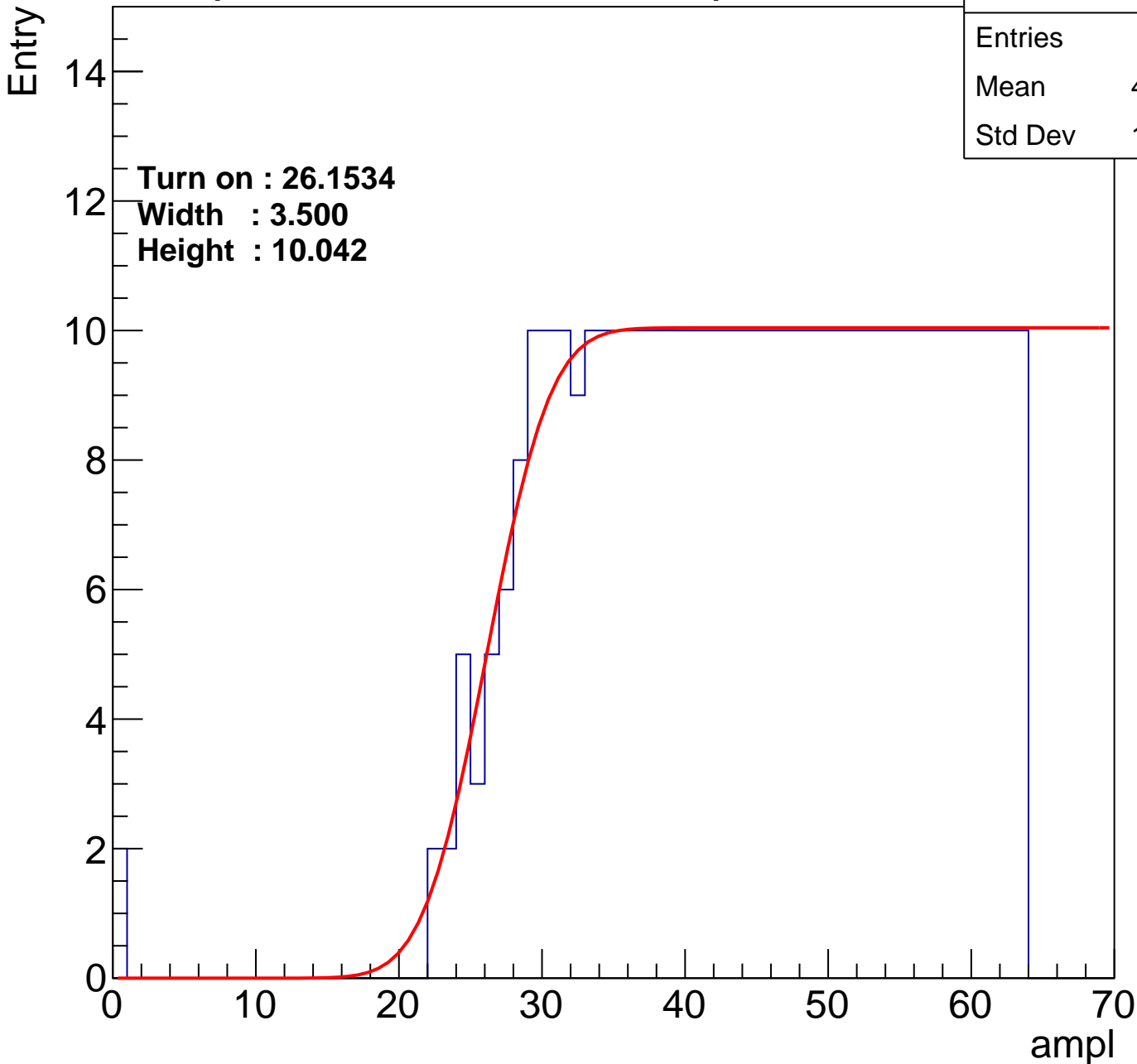
calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1534

Width : 3.500

Height : 10.042



B0L000S, U6-ch3

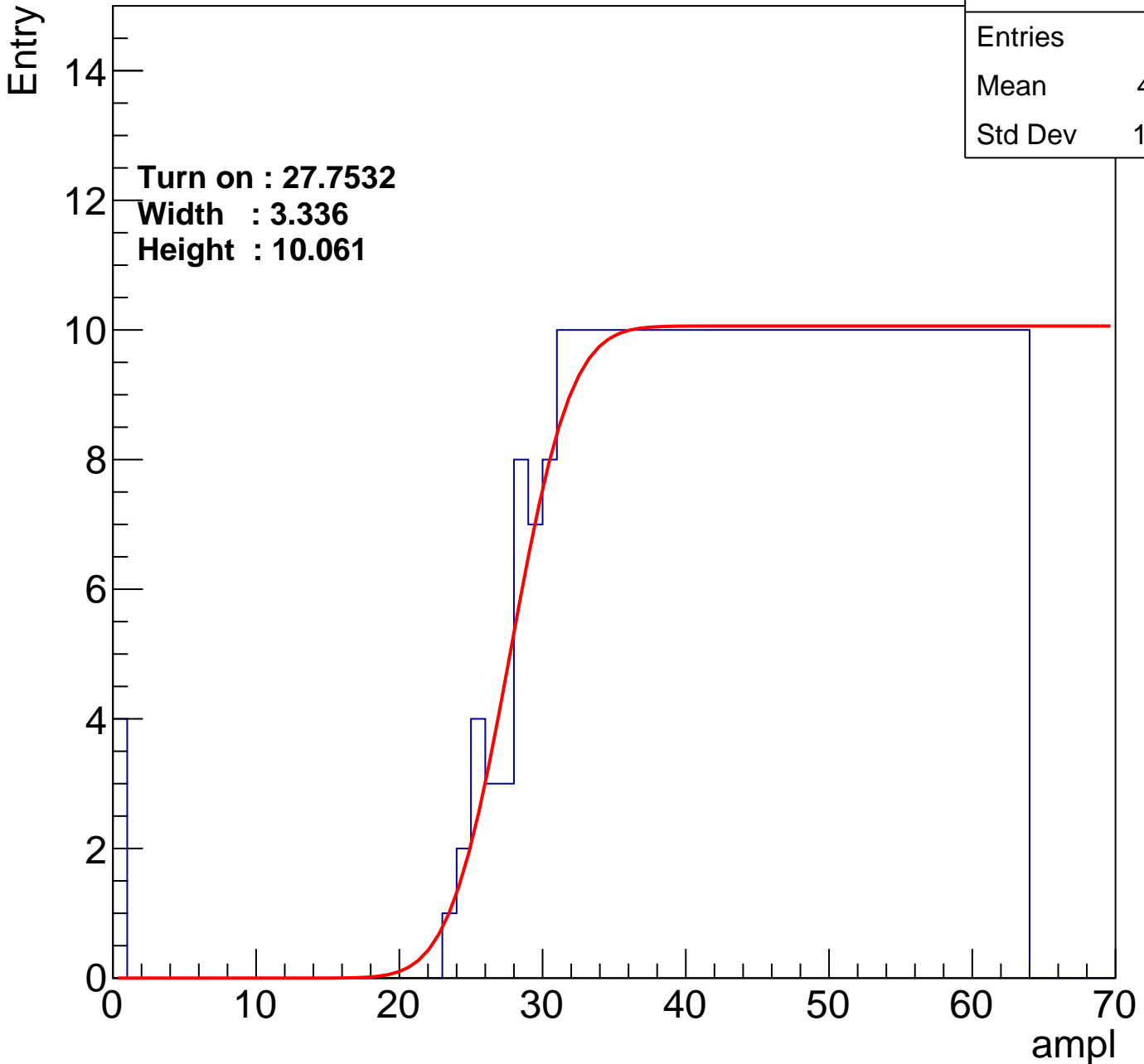
calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.61
Std Dev	11.65

Turn on : 27.7532

Width : 3.336

Height : 10.061



B0L000S, U6-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 26.6619

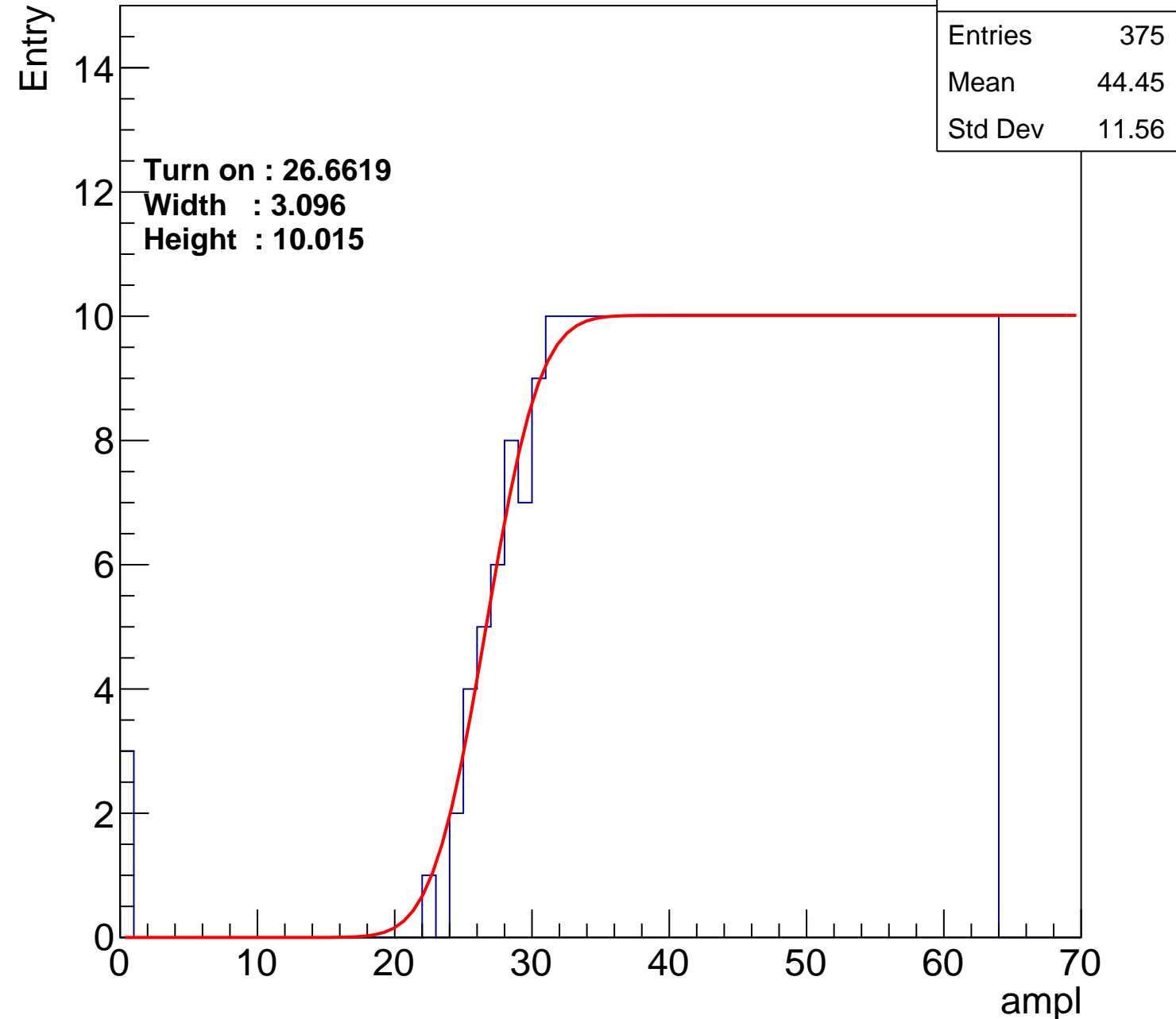
Width : 3.096

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch5

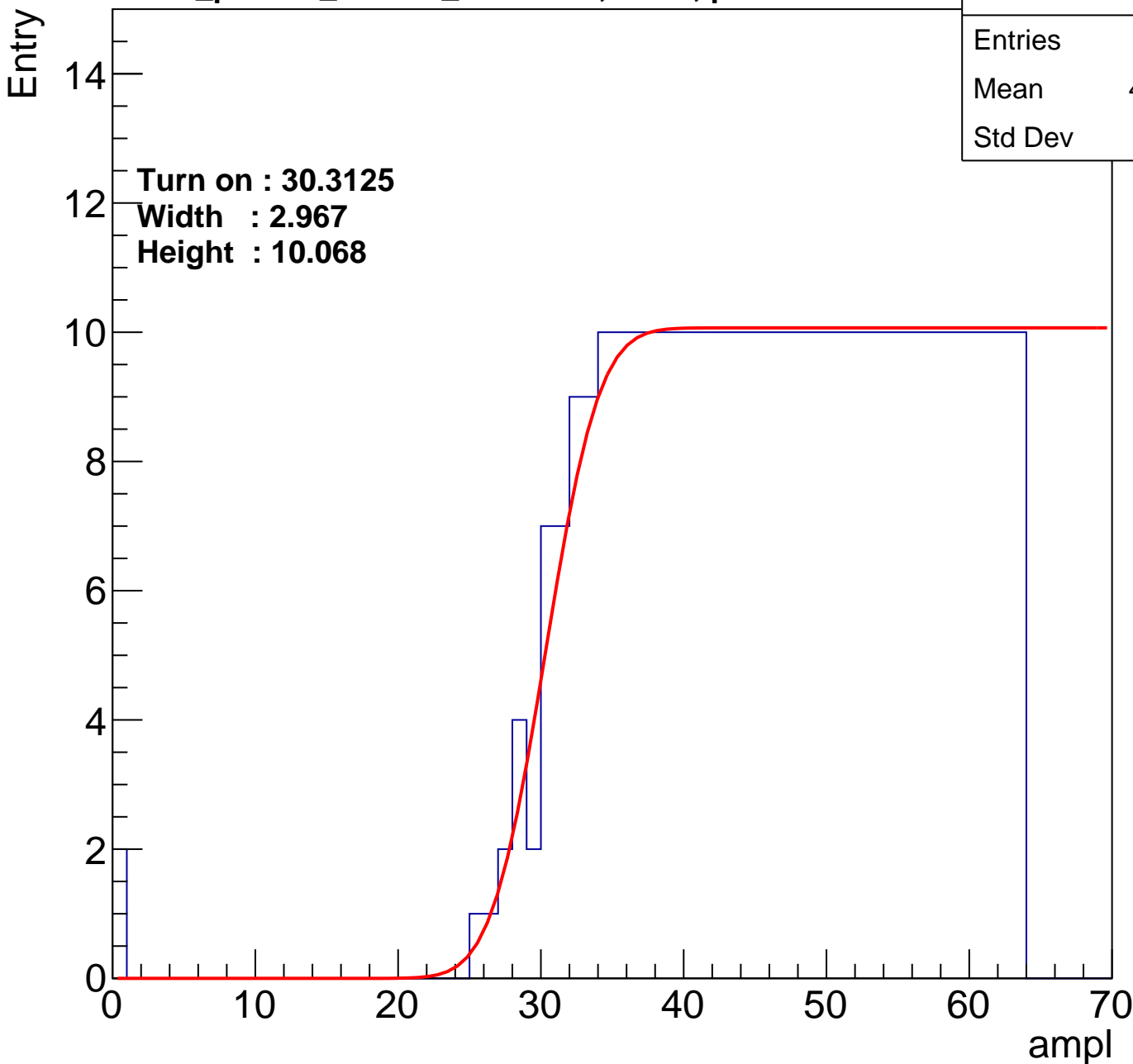
calib_packv5_042523_0143.root, FC#5, port B1

Entries	344
Mean	46.04
Std Dev	10.6

Turn on : 30.3125

Width : 2.967

Height : 10.068



B0L000S, U6-ch6

calib_packv5_042523_0143.root, FC#5, port B1

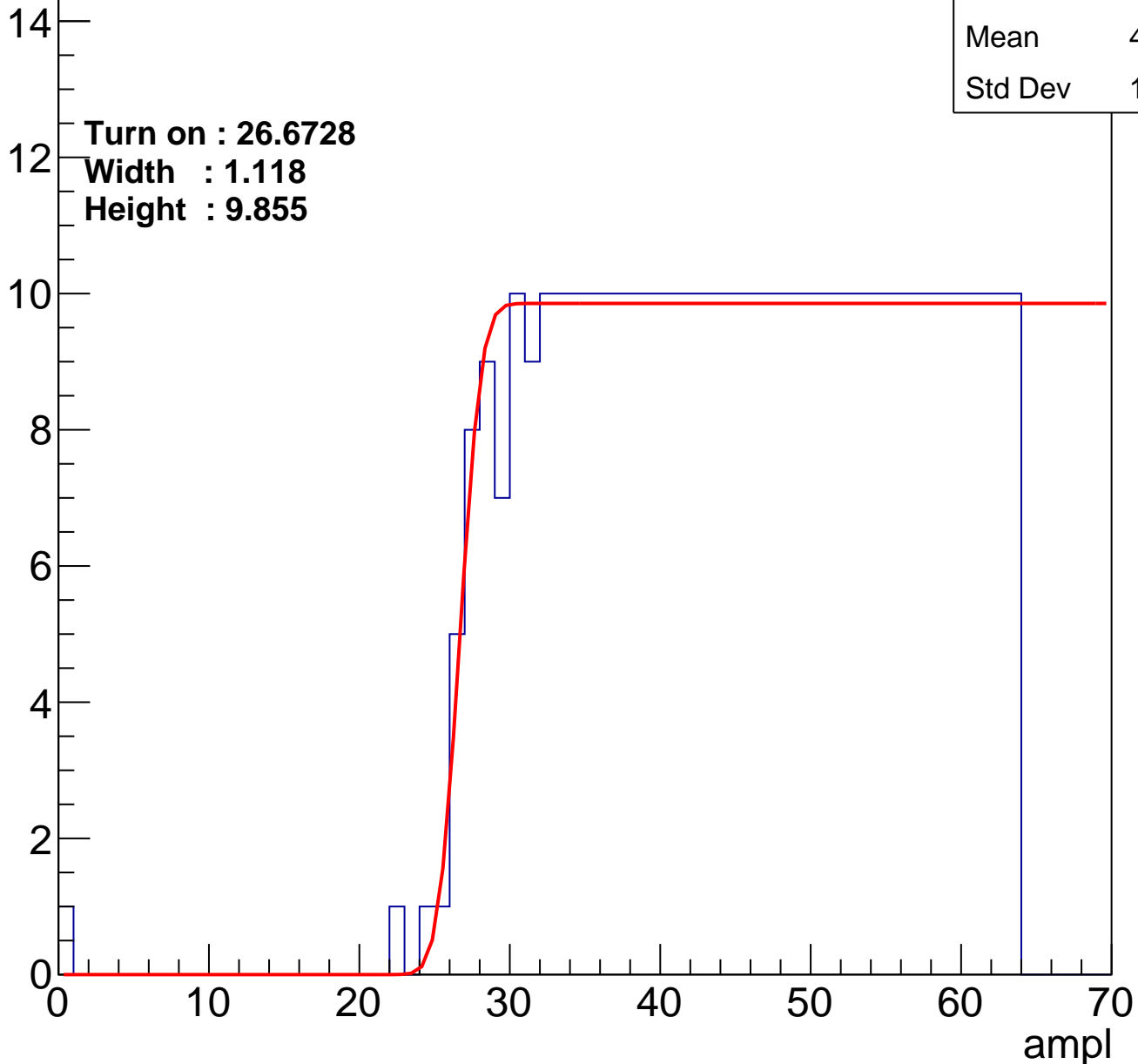
Entries	372
Mean	44.76
Std Dev	11.05

Turn on : 26.6728

Width : 1.118

Height : 9.855

Entry



B0L000S, U6-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.88
Std Dev	11.18

Turn on : 27.4573

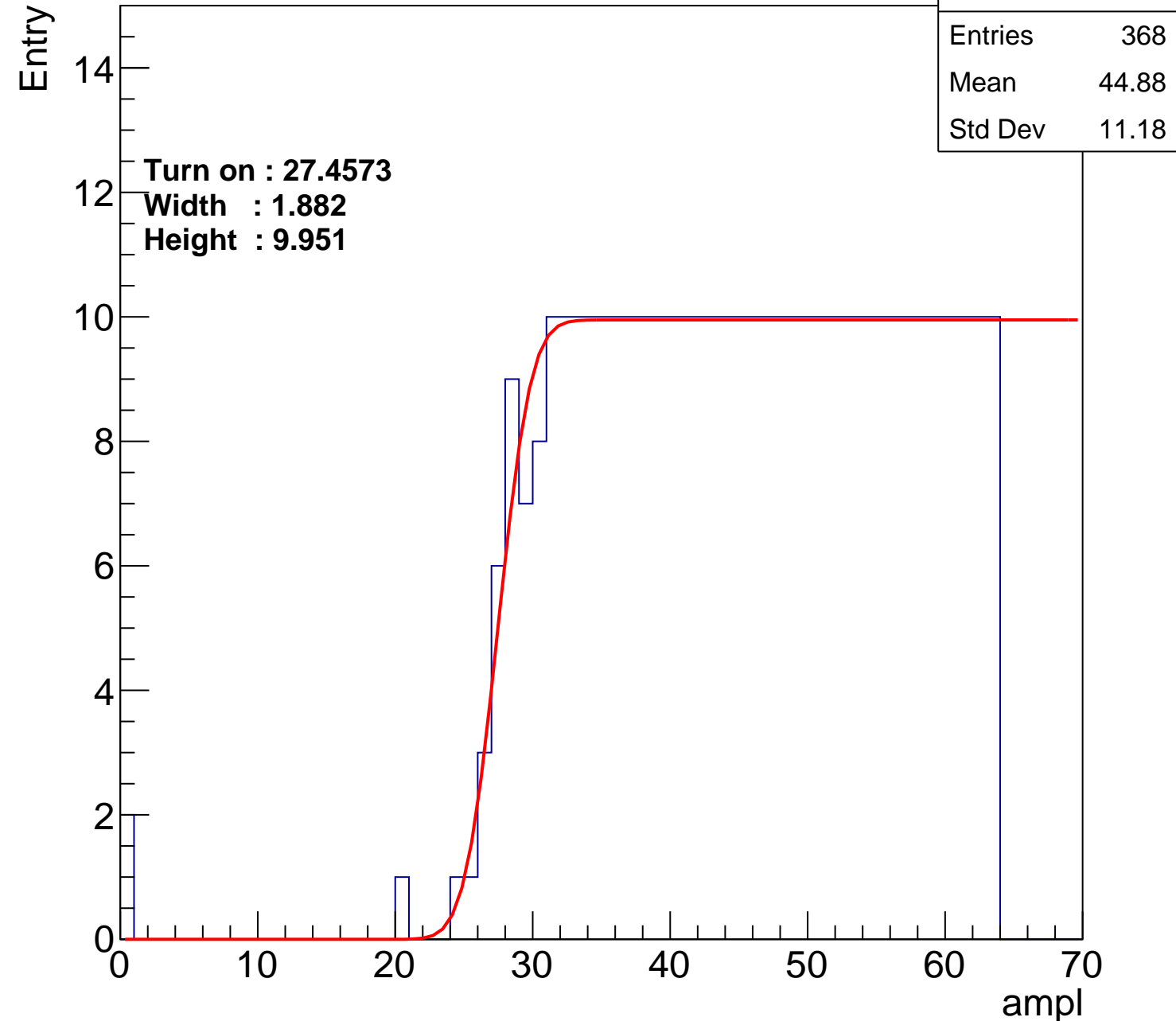
Width : 1.882

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch8

calib_packv5_042523_0143.root, FC#5, port B1

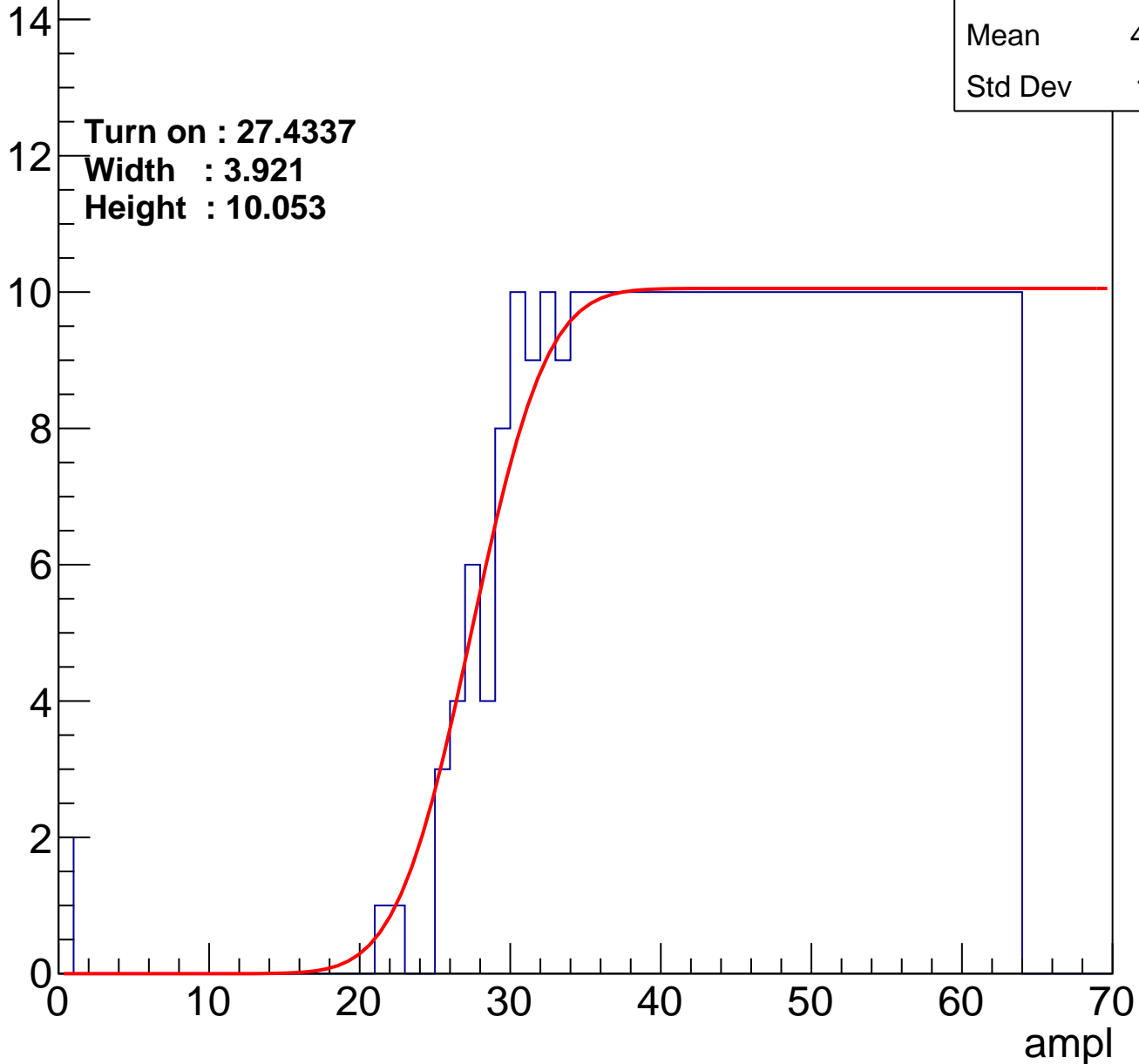
Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.4337

Width : 3.921

Height : 10.053

Entry



B0L000S, U6-ch9

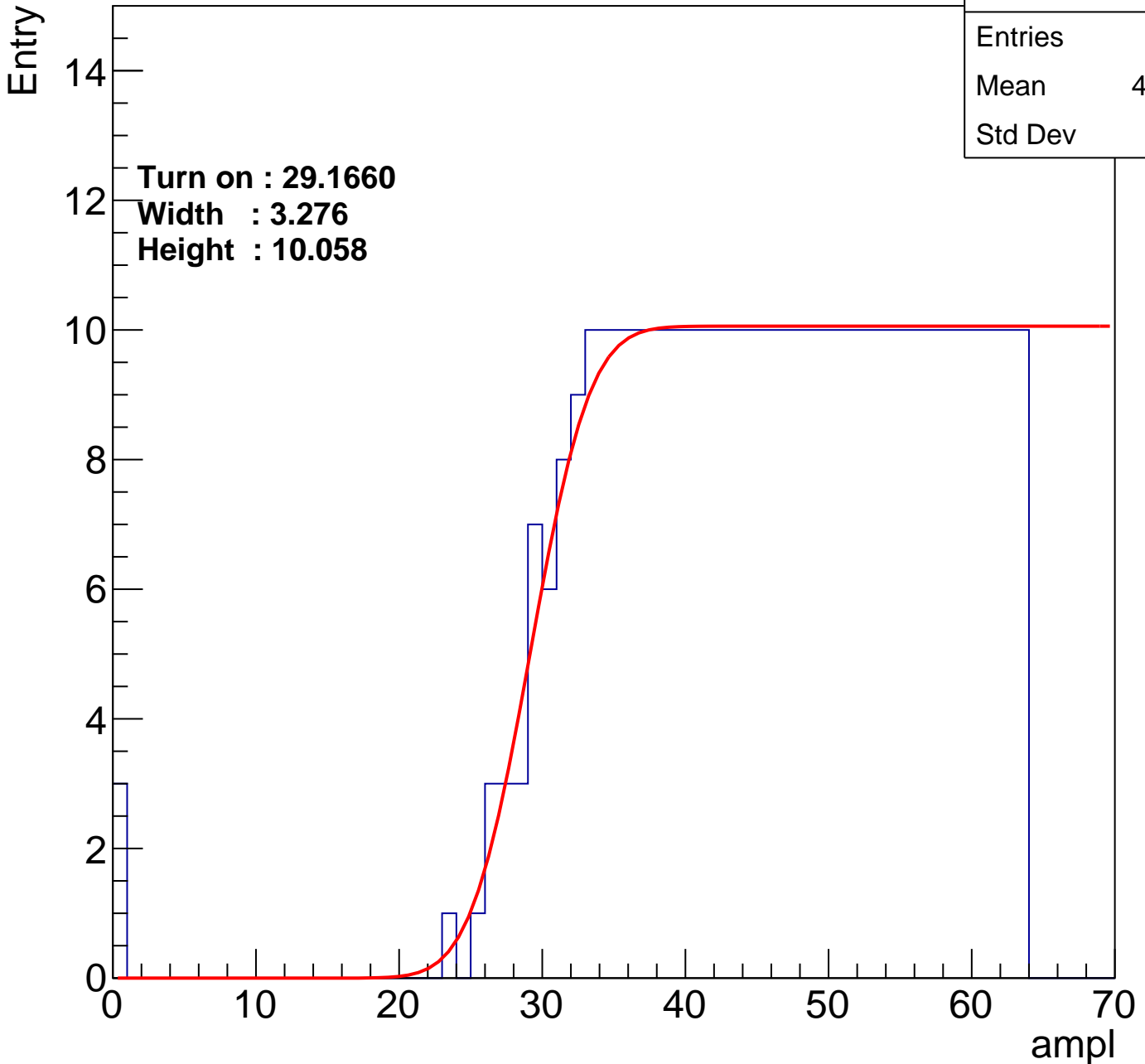
calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.45
Std Dev	11.1

Turn on : 29.1660

Width : 3.276

Height : 10.058



B0L000S, U6-ch10

calib_packv5_042523_0143.root, FC#5, port B1

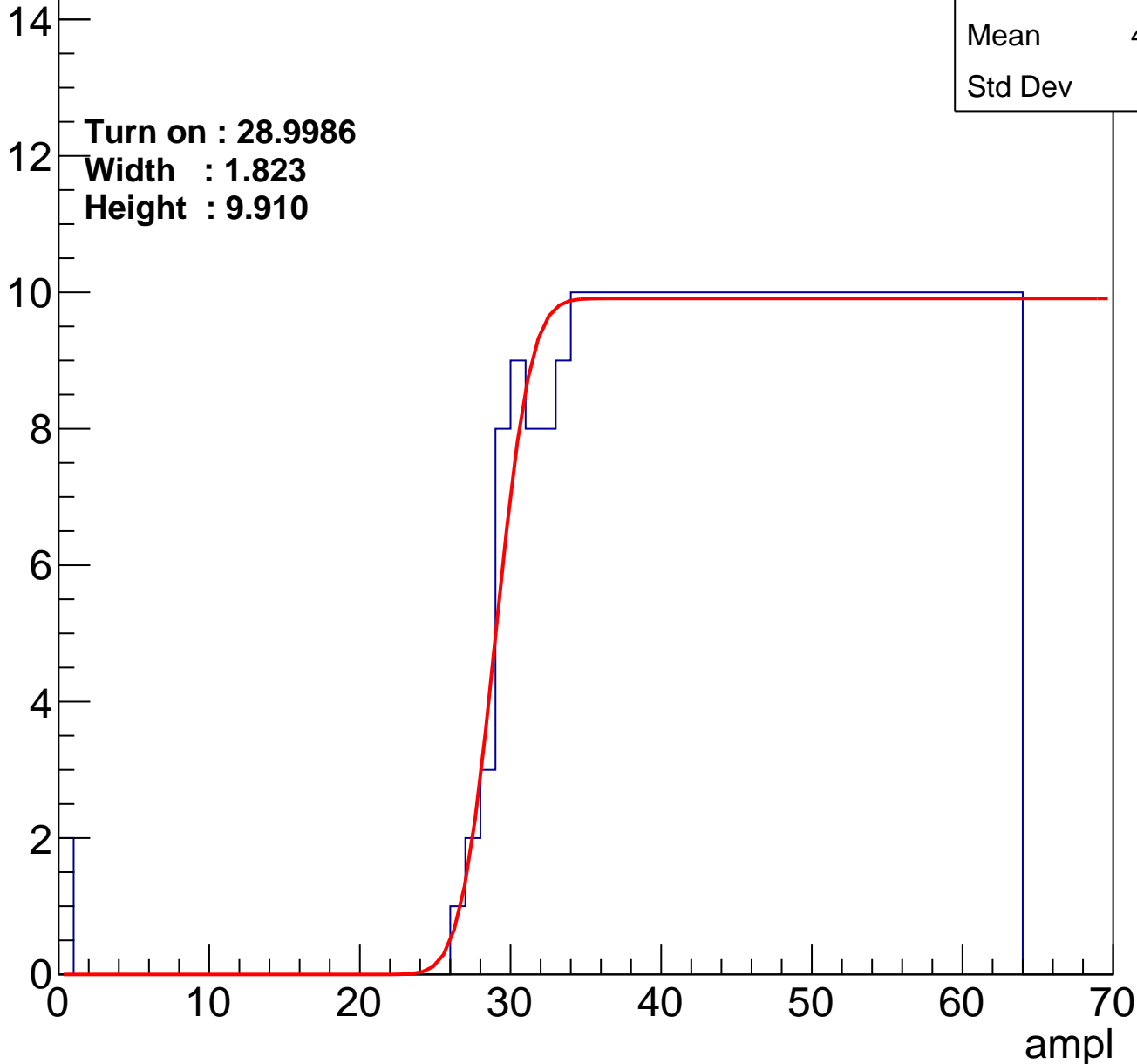
Entry

Entries	350
Mean	45.76
Std Dev	10.71

Turn on : 28.9986

Width : 1.823

Height : 9.910



B0L000S, U6-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.72
Std Dev	10.95

Turn on : 29.6133

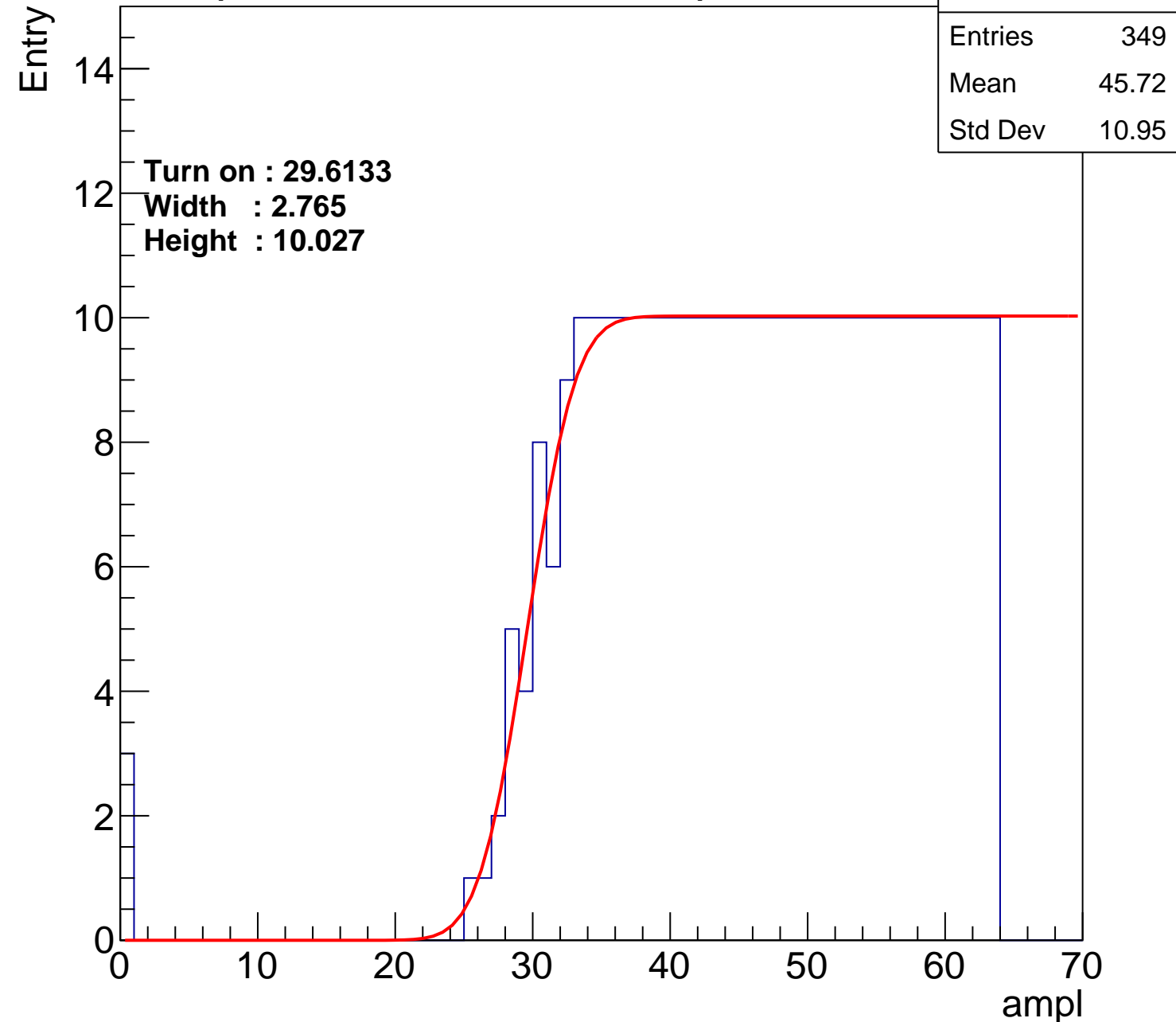
Width : 2.765

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.77
Std Dev	10.75

Turn on : 29.5323

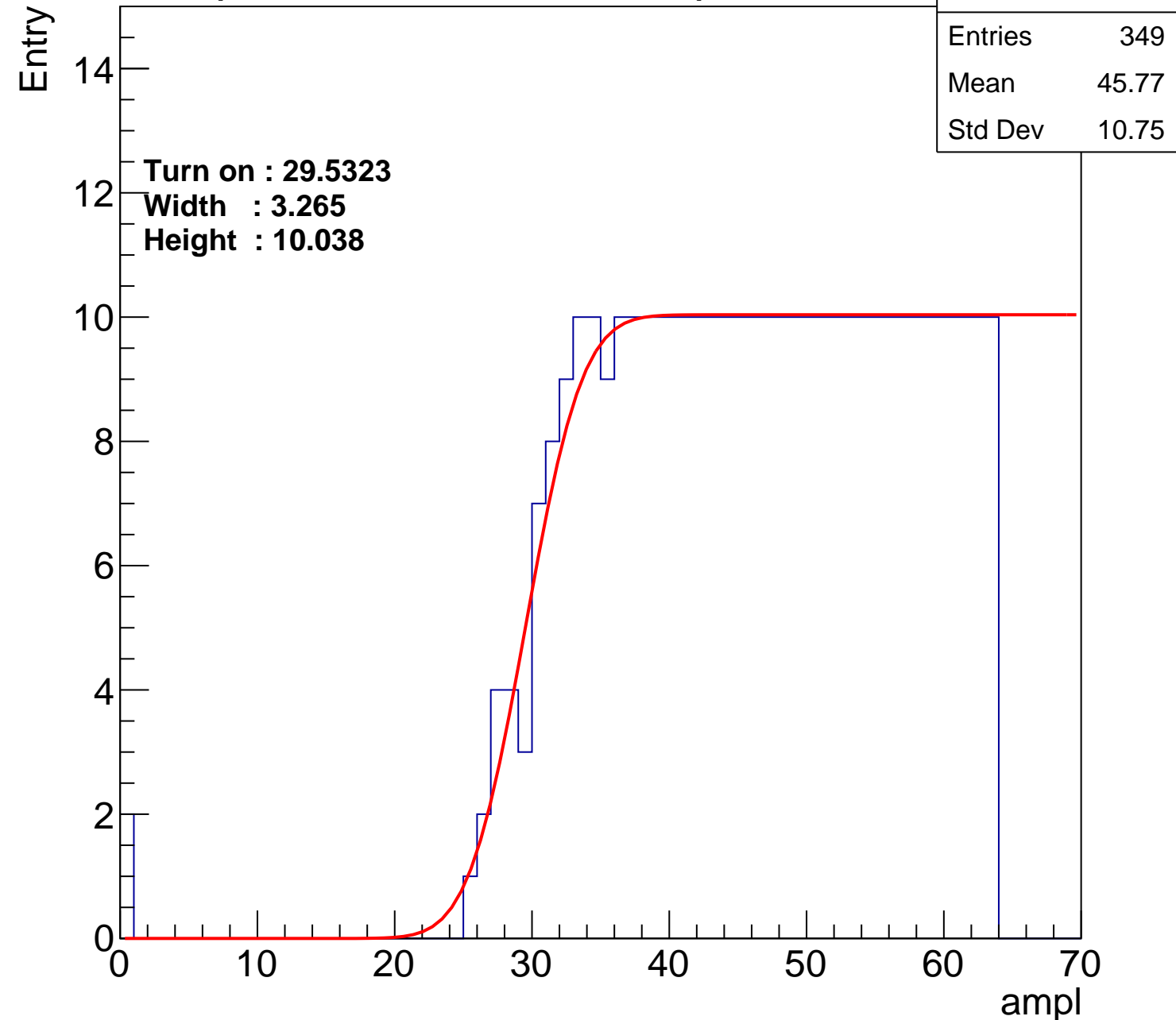
Width : 3.265

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch13

calib_packv5_042523_0143.root, FC#5, port B1

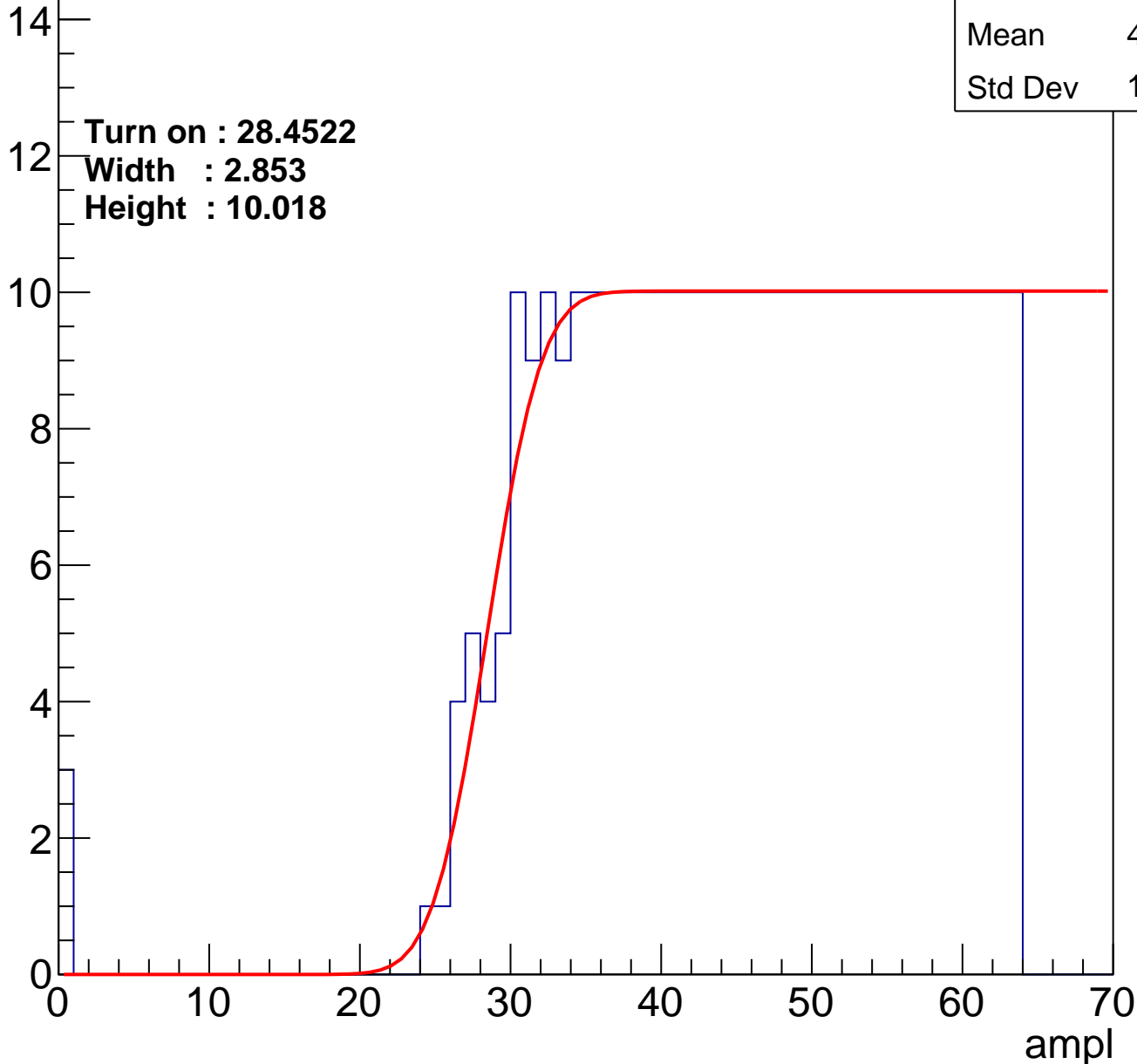
Entry

Entries	361
Mean	45.13
Std Dev	11.23

Turn on : 28.4522

Width : 2.853

Height : 10.018



B0L000S, U6-ch14

calib_packv5_042523_0143.root, FC#5, port B1

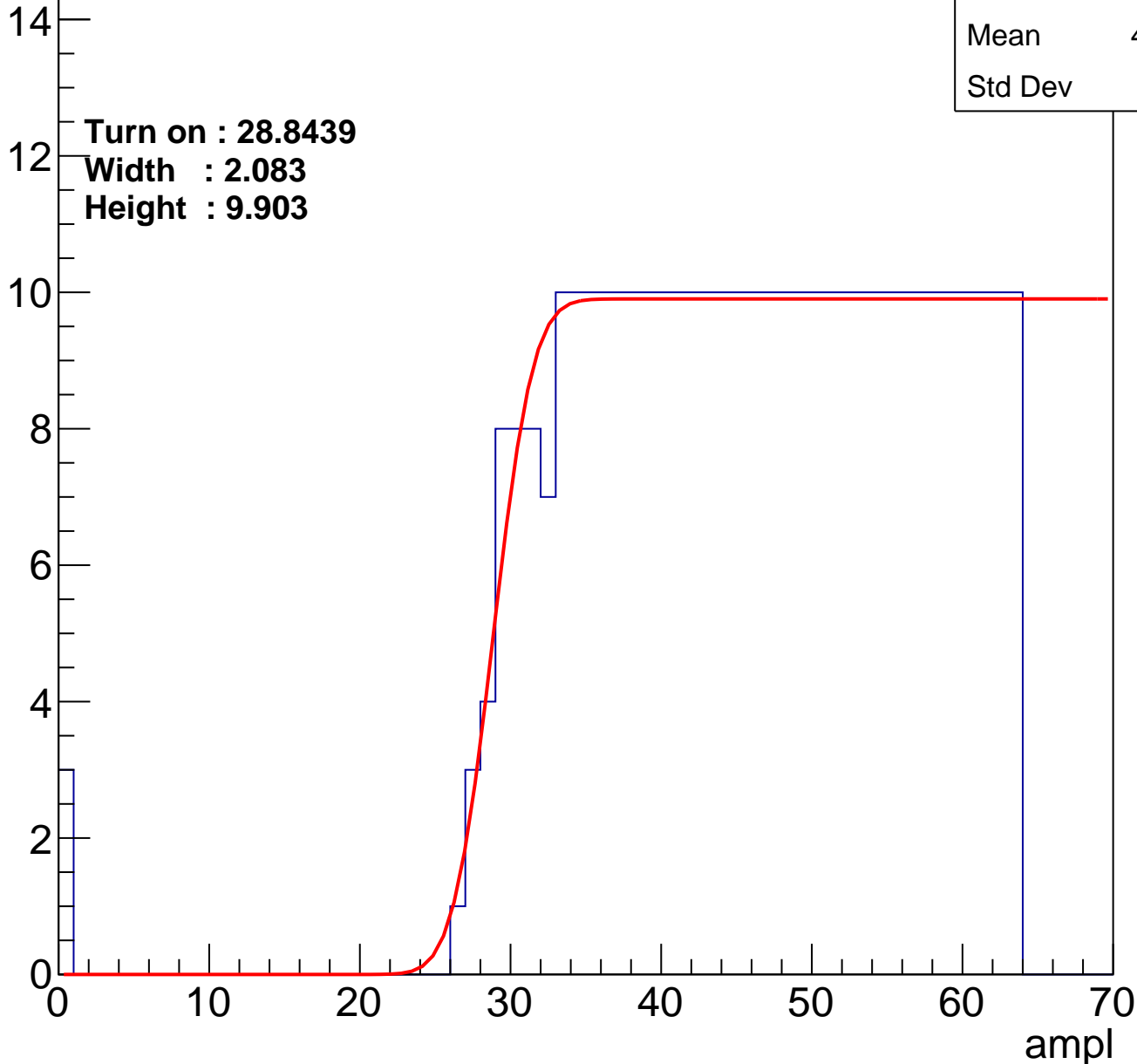
Entry

Entries	352
Mean	45.58
Std Dev	11.01

Turn on : 28.8439

Width : 2.083

Height : 9.903



B0L000S, U6-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9065

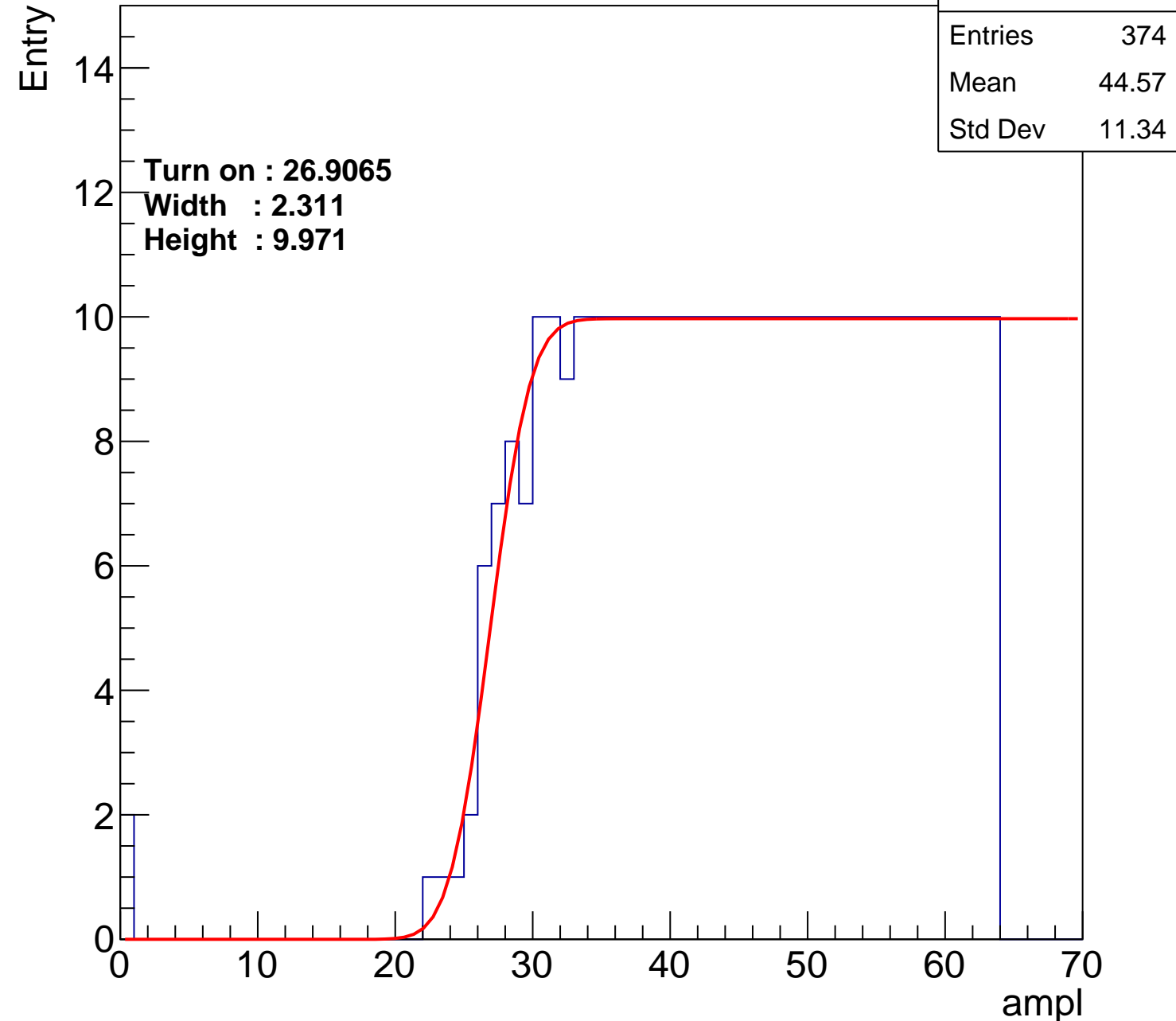
Width : 2.311

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.43
Std Dev	10.93

Turn on : 29.0626

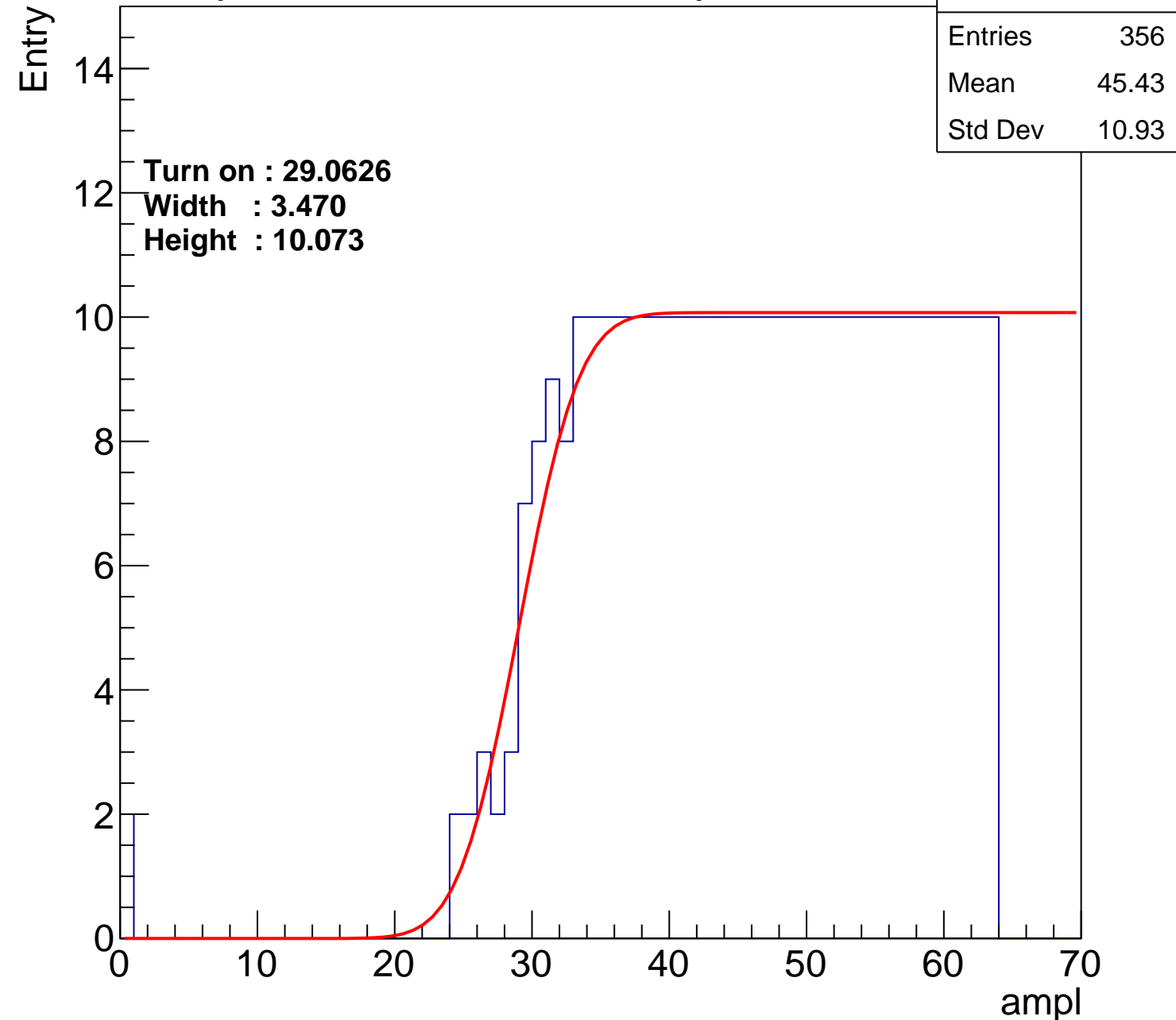
Width : 3.470

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.66
Std Dev	10.62

Turn on : 29.4455

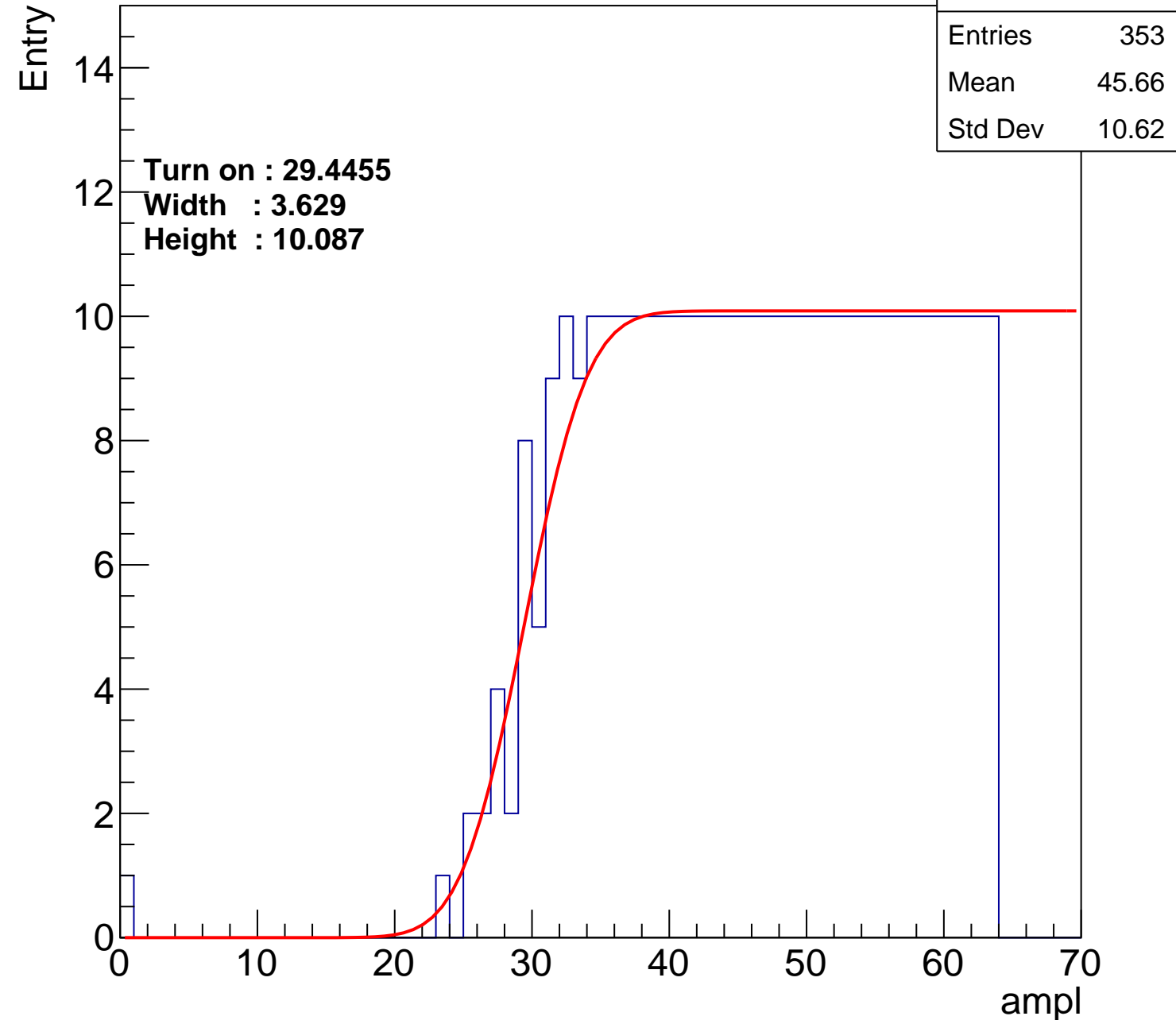
Width : 3.629

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch18

calib_packv5_042523_0143.root, FC#5, port B1

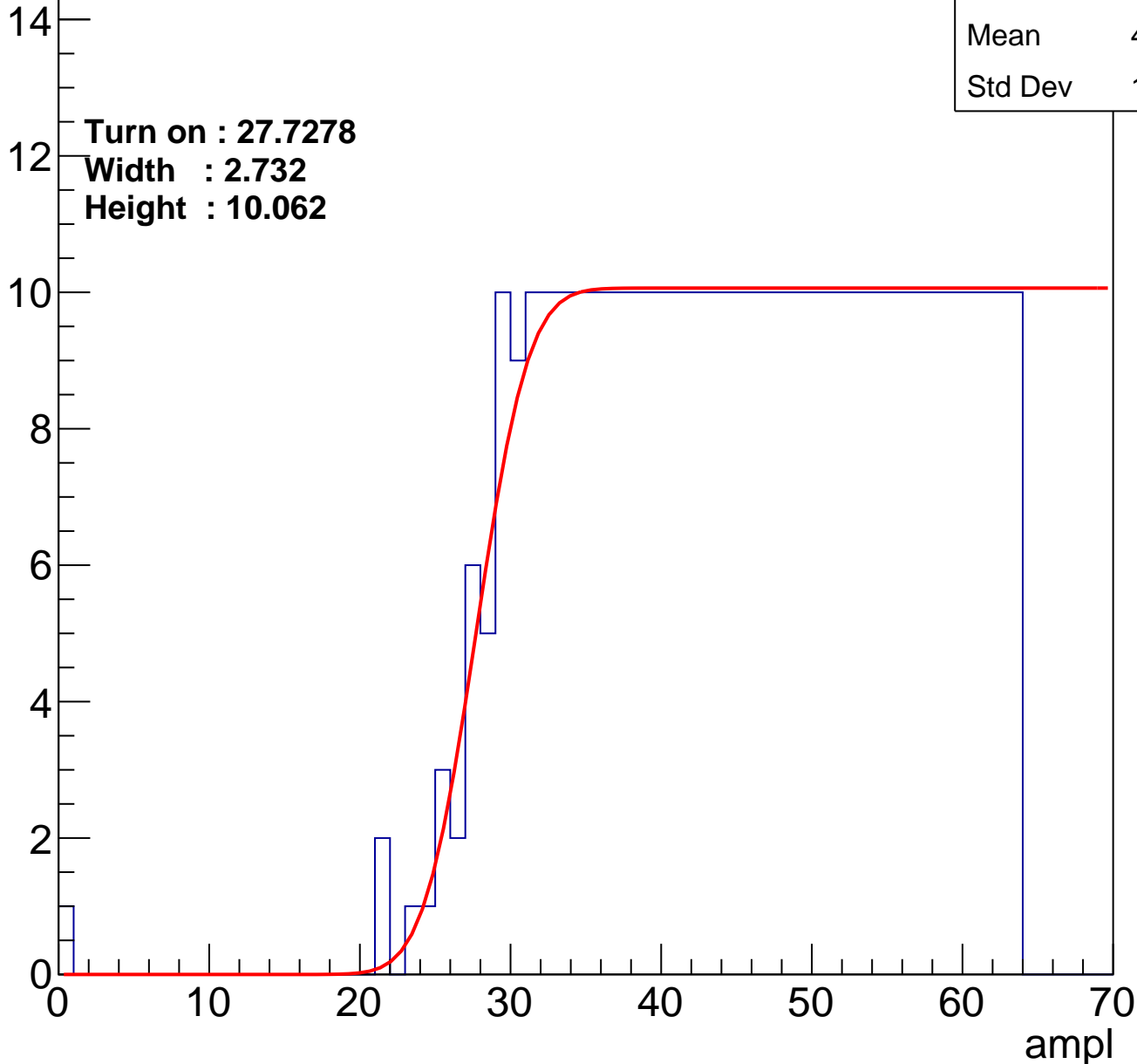
Entry

Entries	370
Mean	44.83
Std Dev	11.06

Turn on : 27.7278

Width : 2.732

Height : 10.062



B0L000S, U6-ch19

calib_packv5_042523_0143.root, FC#5, port B1

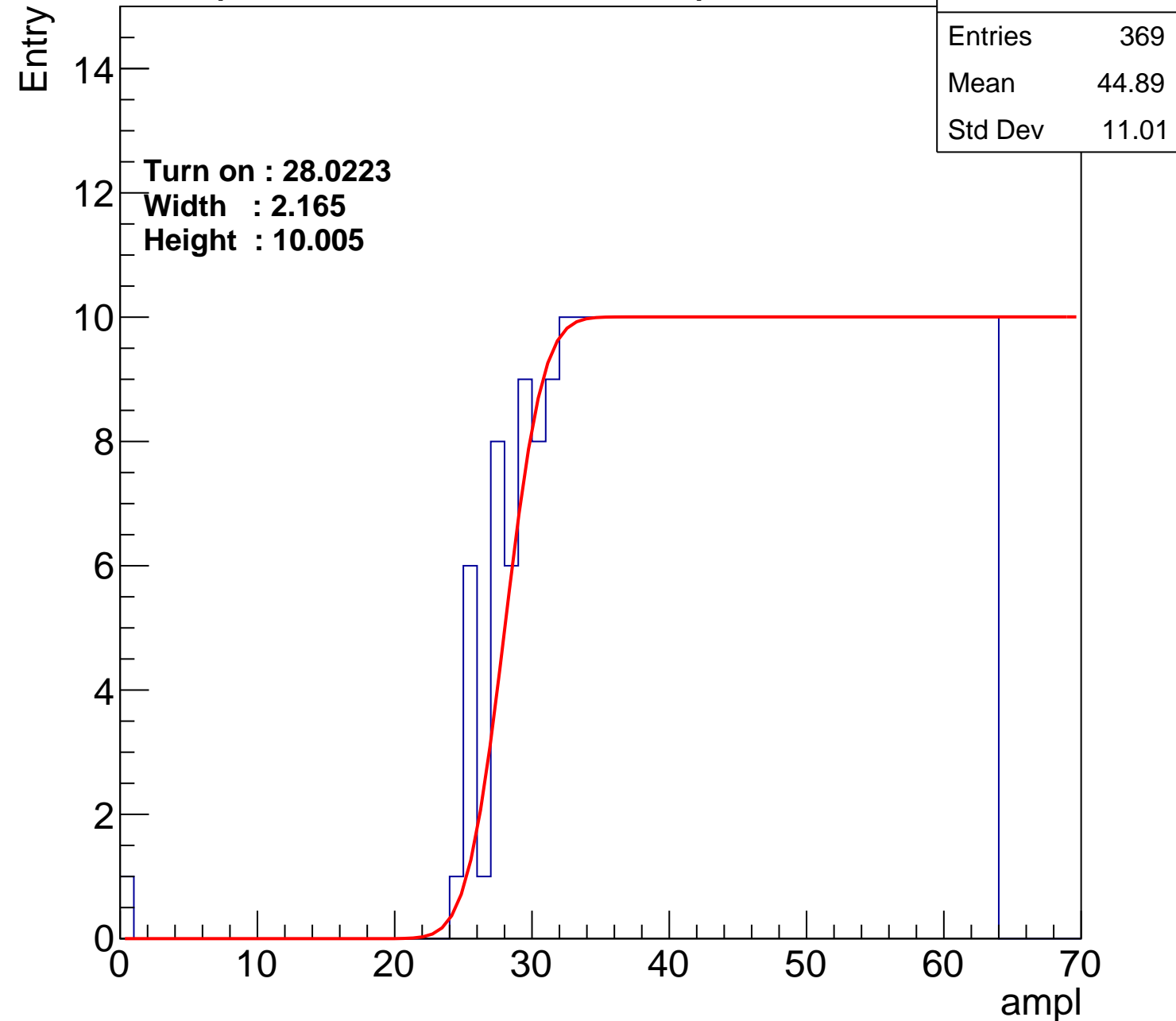
Entry

14
12
10
8
6
4
2
0

Turn on : 28.0223
Width : 2.165
Height : 10.005

Entries	369
Mean	44.89
Std Dev	11.01

ampl



B0L000S, U6-ch20

calib_packv5_042523_0143.root, FC#5, port B1

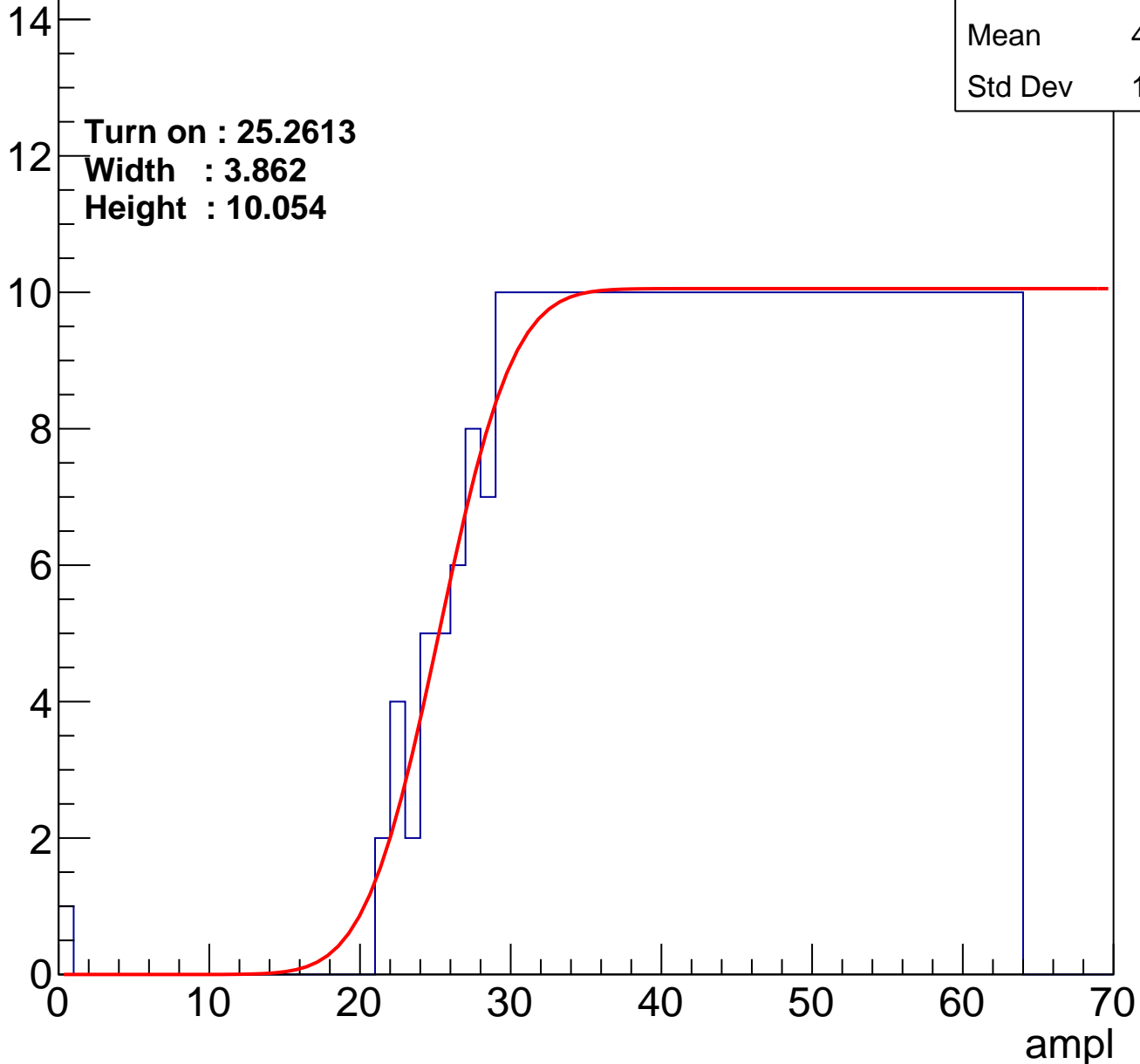
Entries	390
Mean	43.82
Std Dev	11.63

Turn on : 25.2613

Width : 3.862

Height : 10.054

Entry



B0L000S, U6-ch21

calib_packv5_042523_0143.root, FC#5, port B1

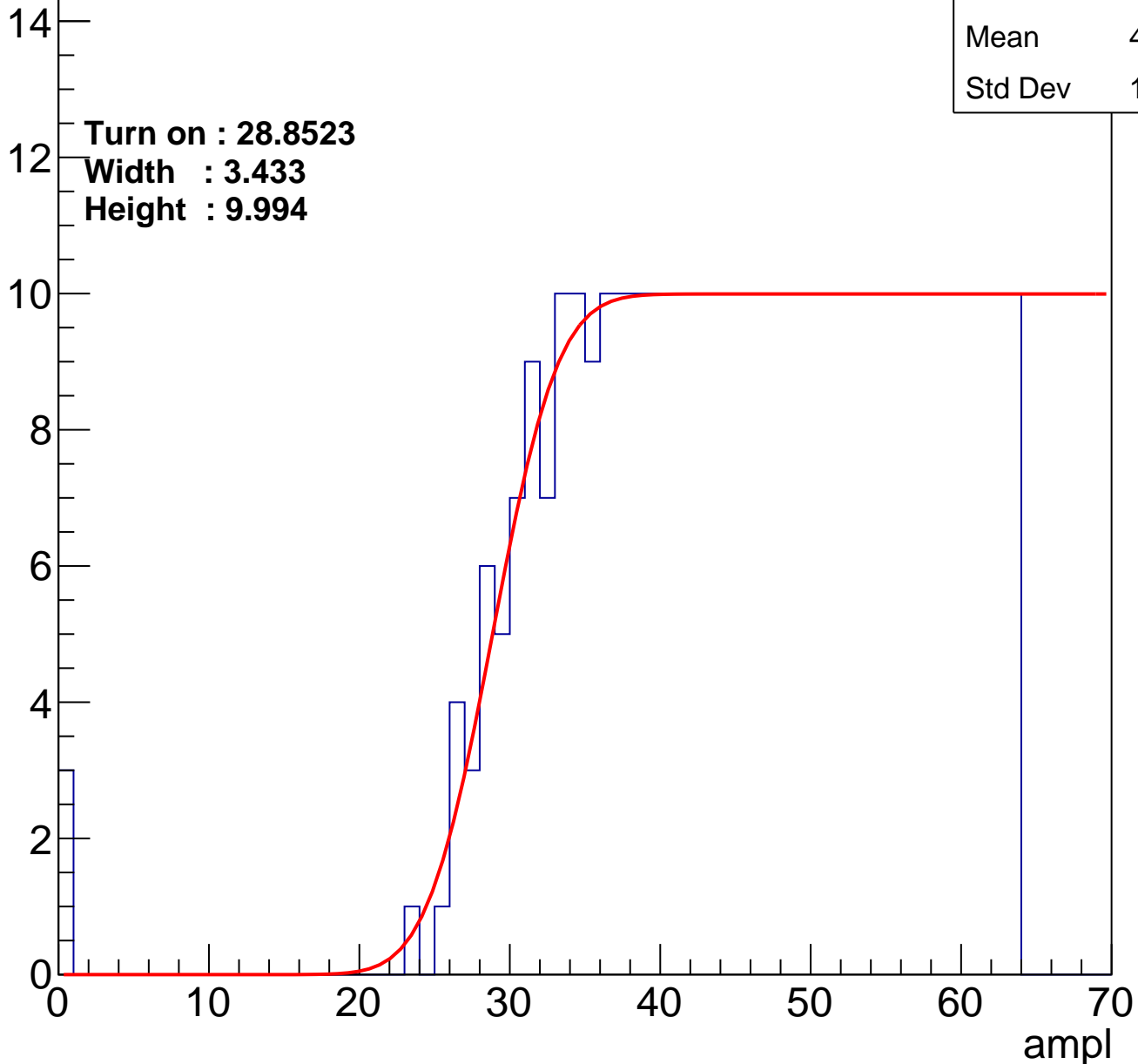
Entries	355
Mean	45.36
Std Dev	11.18

Turn on : 28.8523

Width : 3.433

Height : 9.994

Entry



B0L000S, U6-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.35
Std Dev	11.16

Turn on : 29.4914

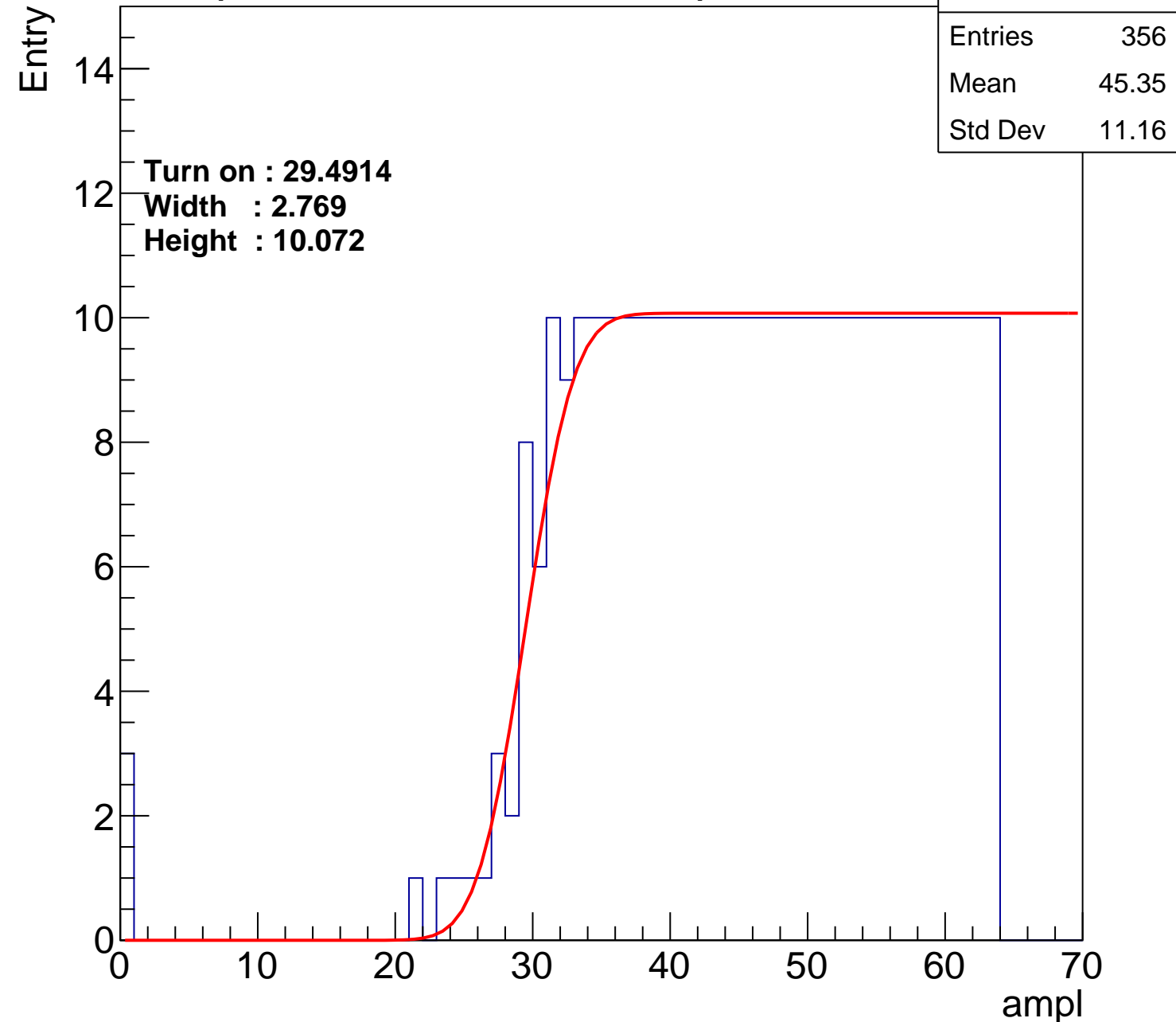
Width : 2.769

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch23

calib_packv5_042523_0143.root, FC#5, port B1

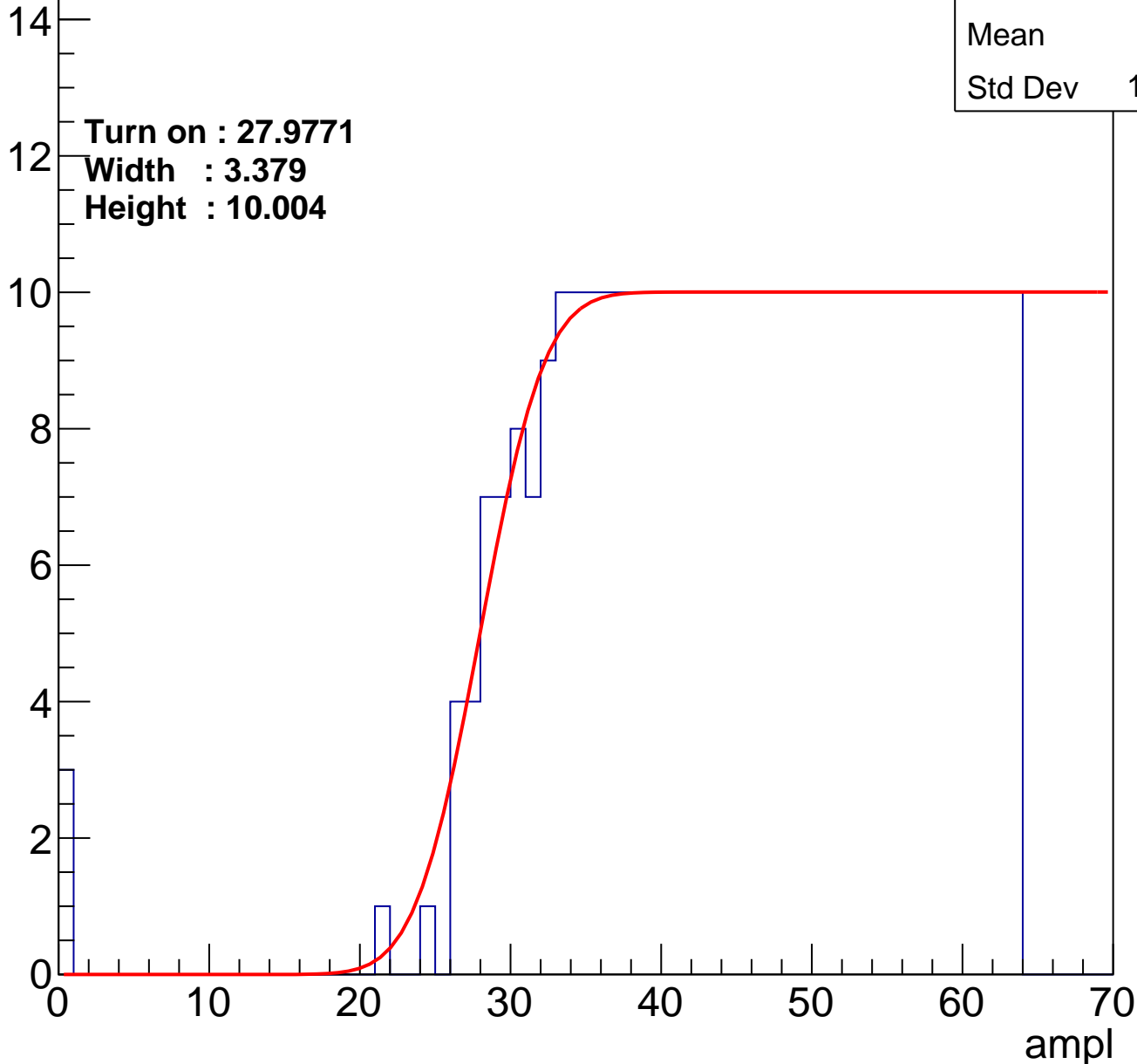
Entry

Entries	361
Mean	45.1
Std Dev	11.28

Turn on : 27.9771

Width : 3.379

Height : 10.004



B0L000S, U6-ch24

calib_packv5_042523_0143.root, FC#5, port B1

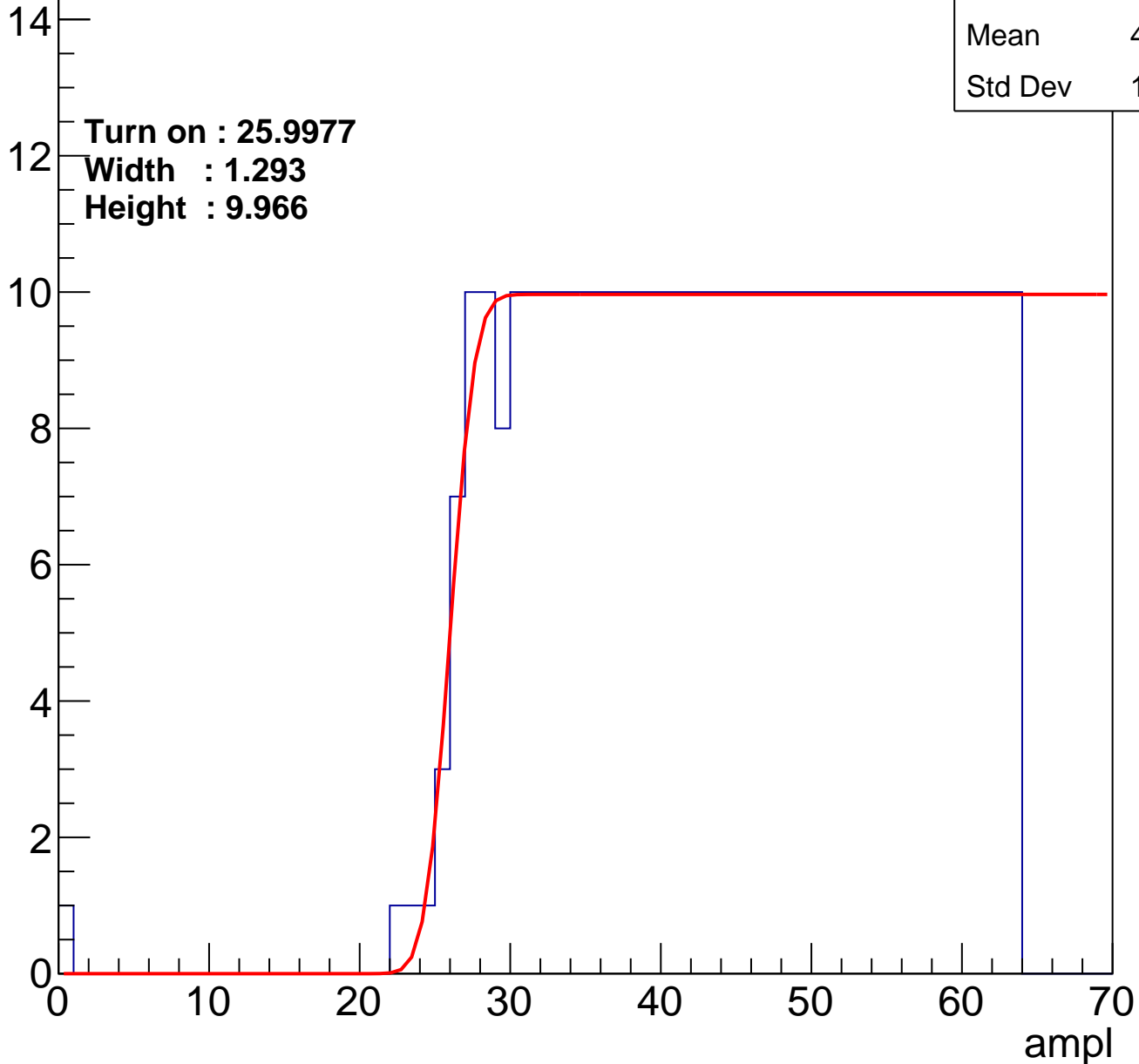
Entry

Entries	382
Mean	44.29
Std Dev	11.29

Turn on : 25.9977

Width : 1.293

Height : 9.966



B0L000S, U6-ch25

calib_packv5_042523_0143.root, FC#5, port B1

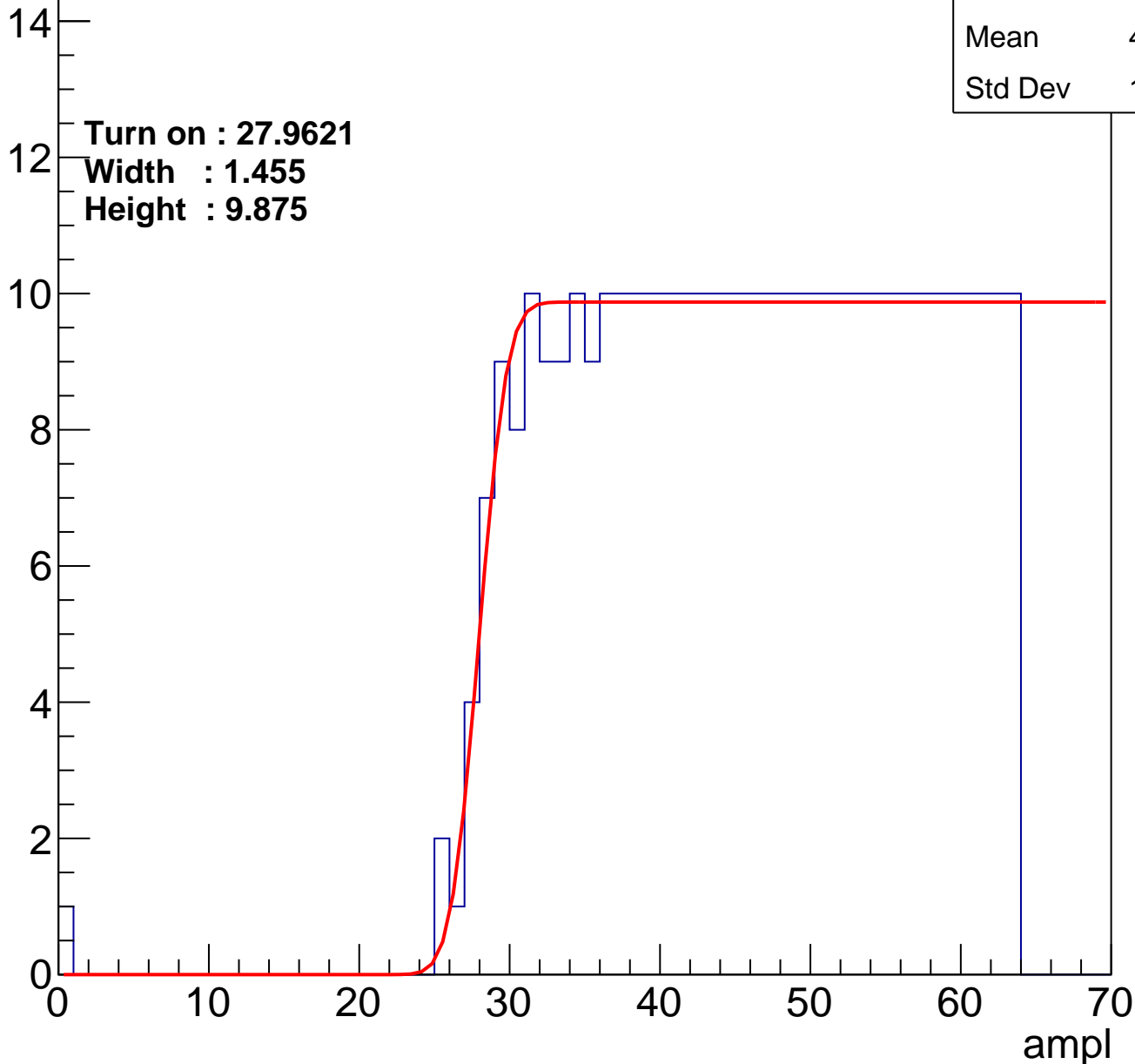
Entry

Entries	359
Mean	45.38
Std Dev	10.74

Turn on : 27.9621

Width : 1.455

Height : 9.875



B0L000S, U6-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.05
Std Dev	11.31

Turn on : 28.6625

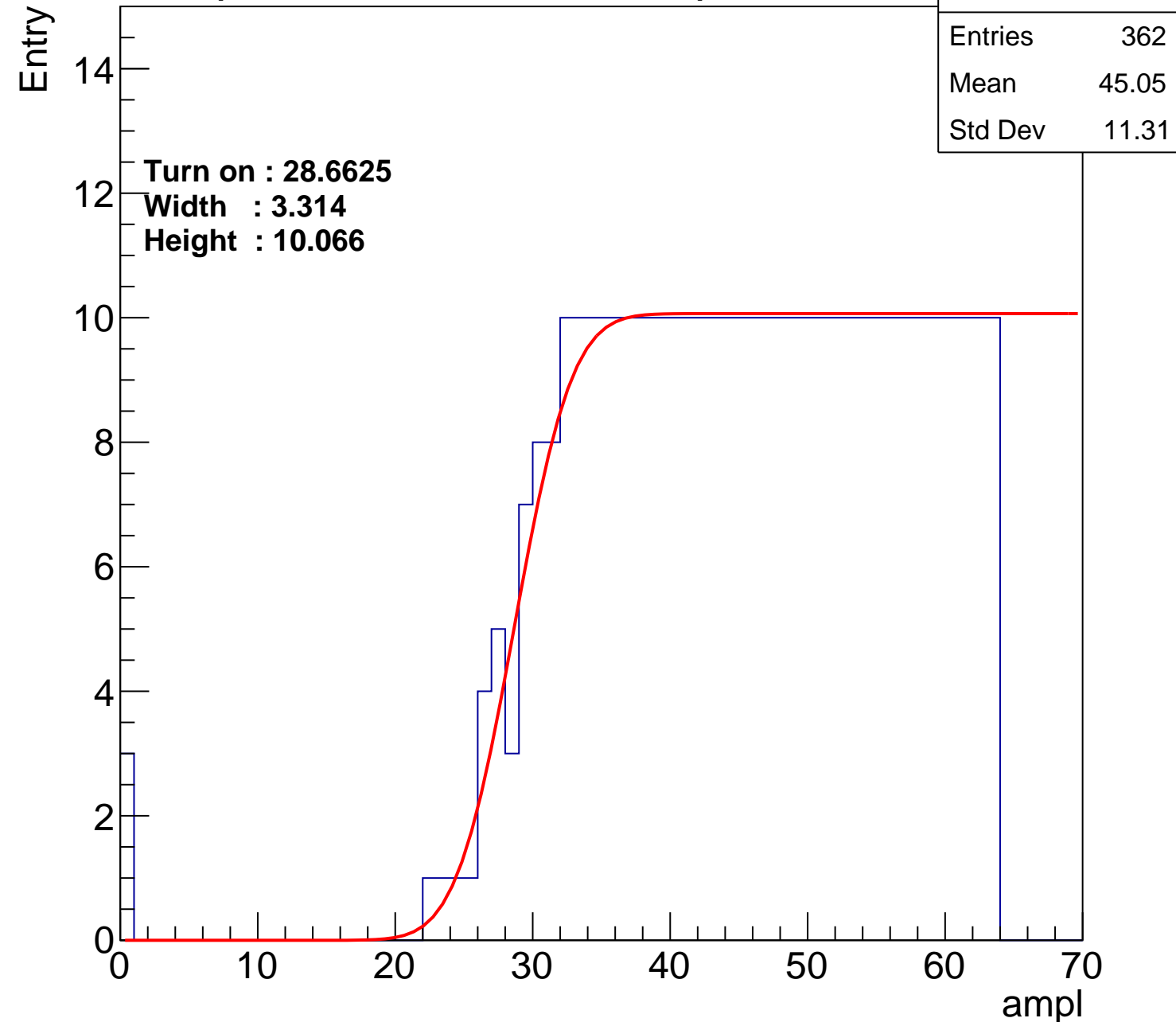
Width : 3.314

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.14
Std Dev	11.22

Turn on : 28.0049

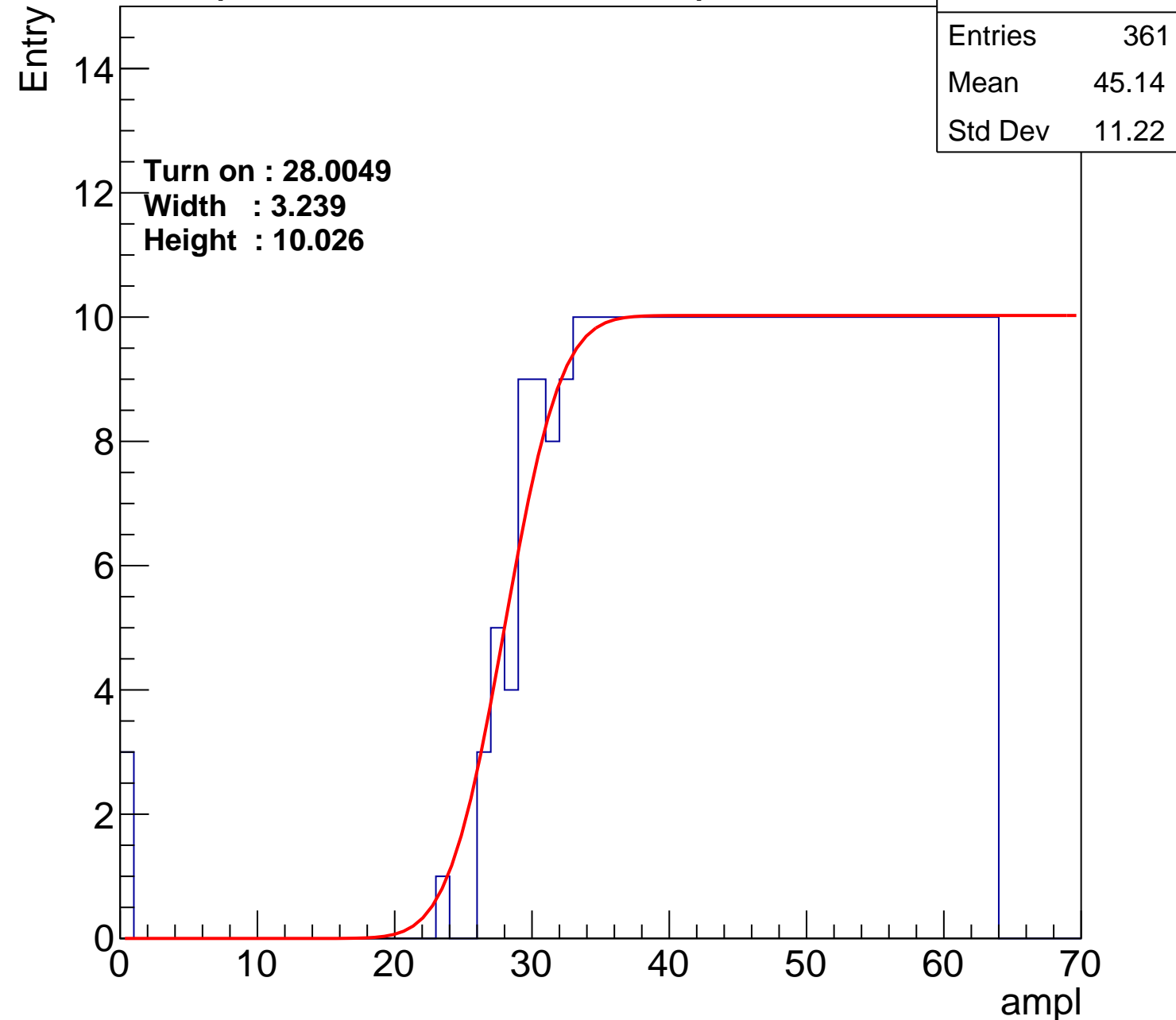
Width : 3.239

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch28

calib_packv5_042523_0143.root, FC#5, port B1

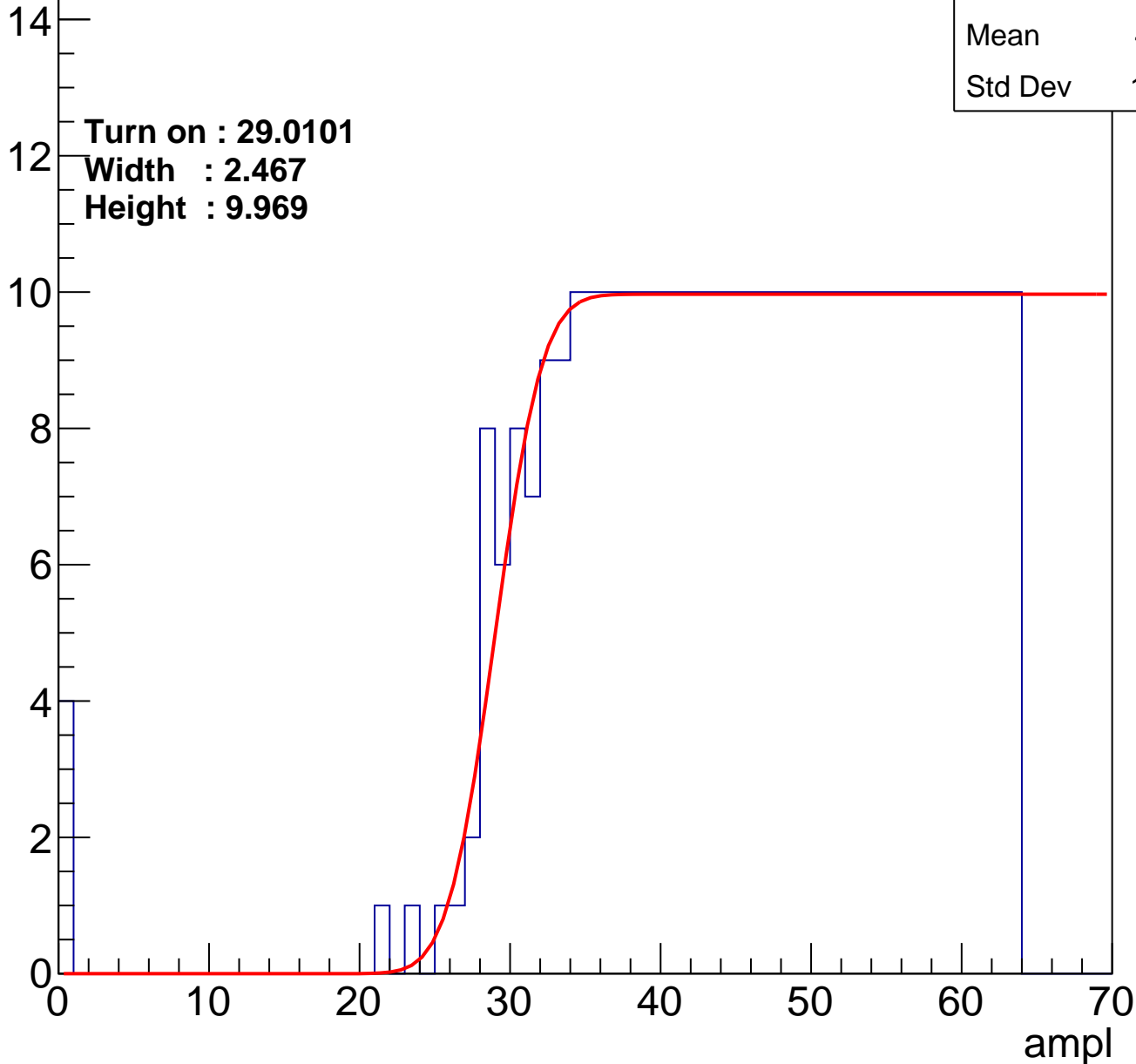
Entry

Entries	357
Mean	45.21
Std Dev	11.42

Turn on : 29.0101

Width : 2.467

Height : 9.969



B0L000S, U6-ch29

calib_packv5_042523_0143.root, FC#5, port B1

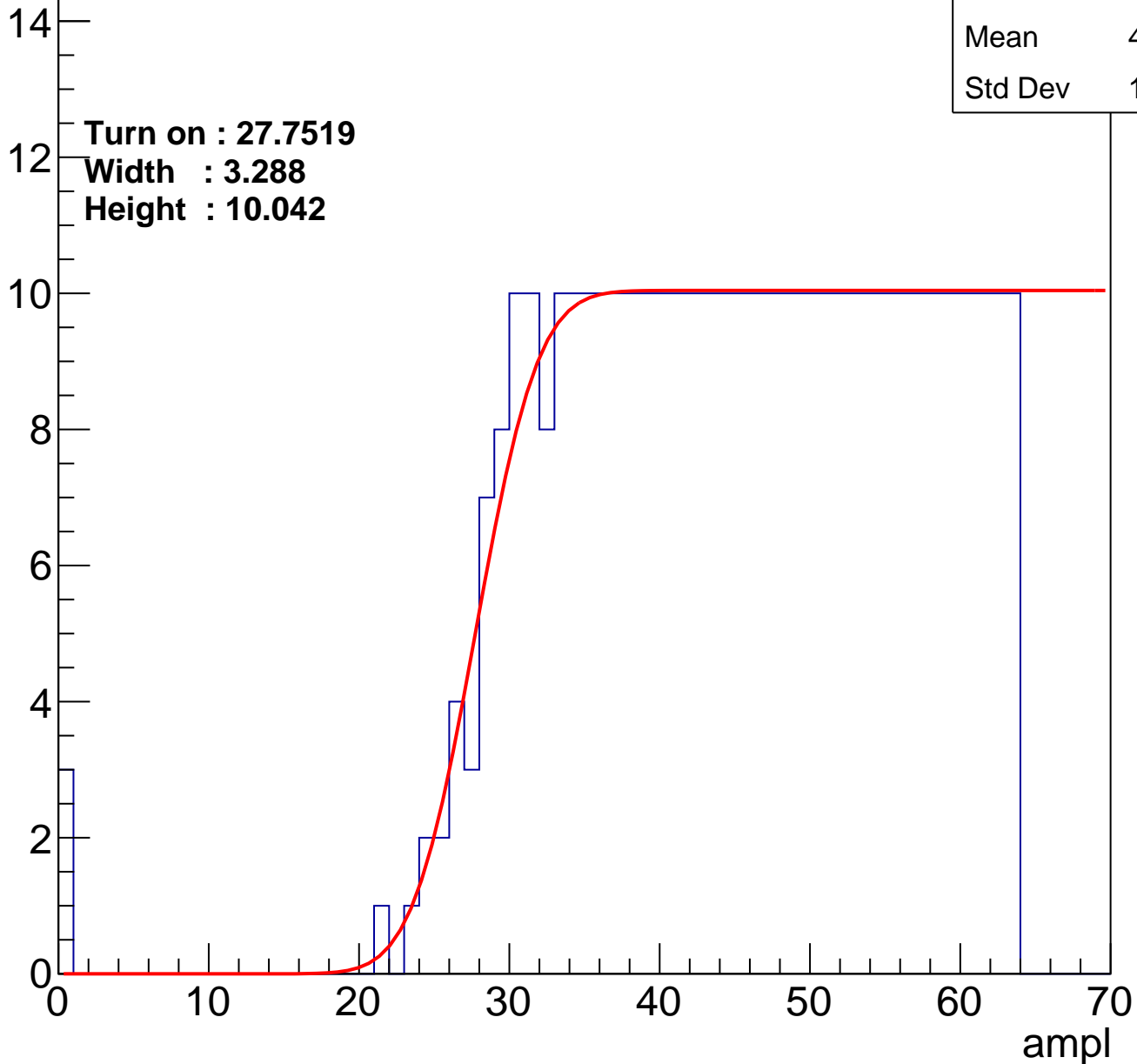
Entry

Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 27.7519

Width : 3.288

Height : 10.042



B0L000S, U6-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.53
Std Dev	11.48

Turn on : 27.1501

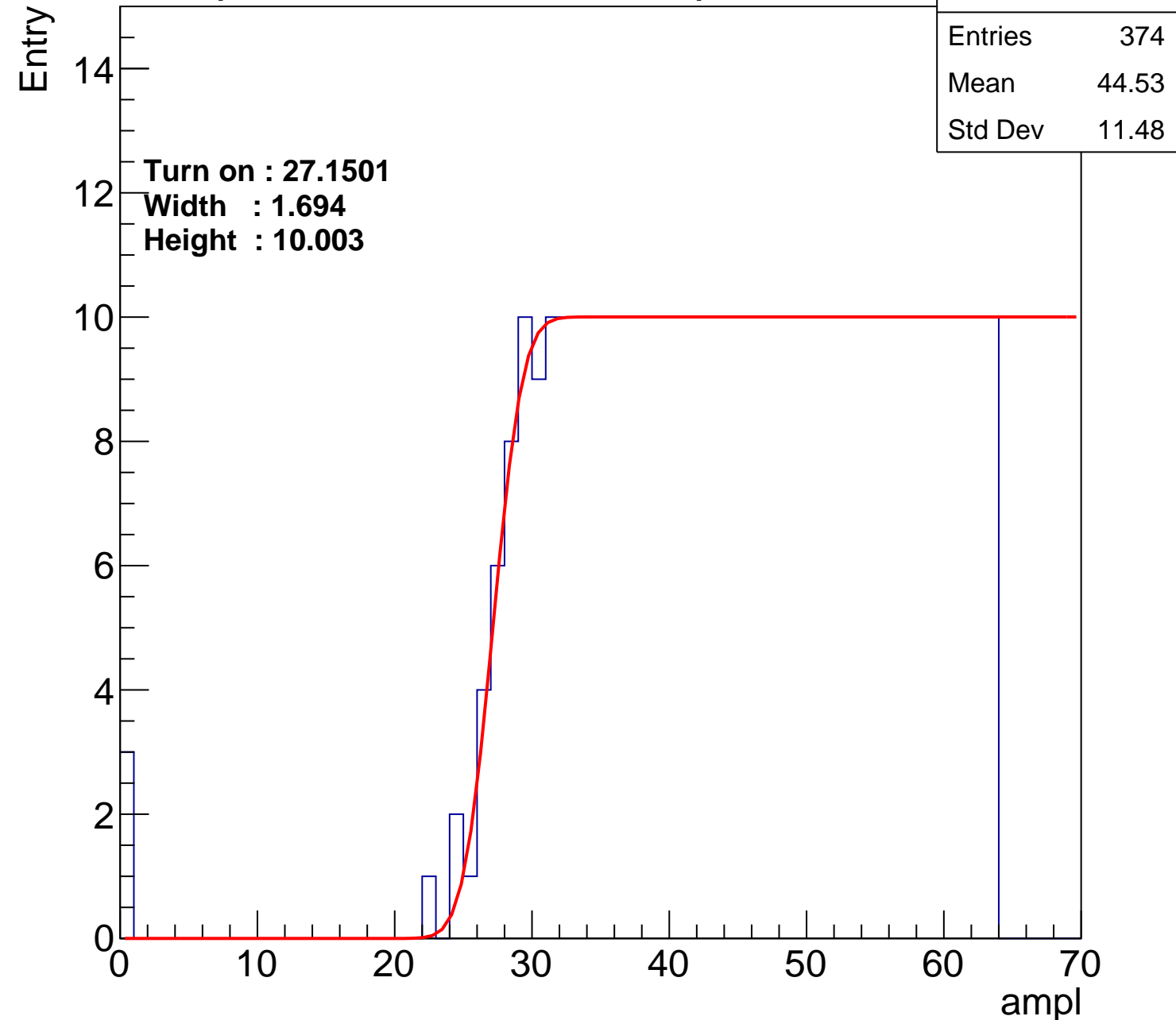
Width : 1.694

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch31

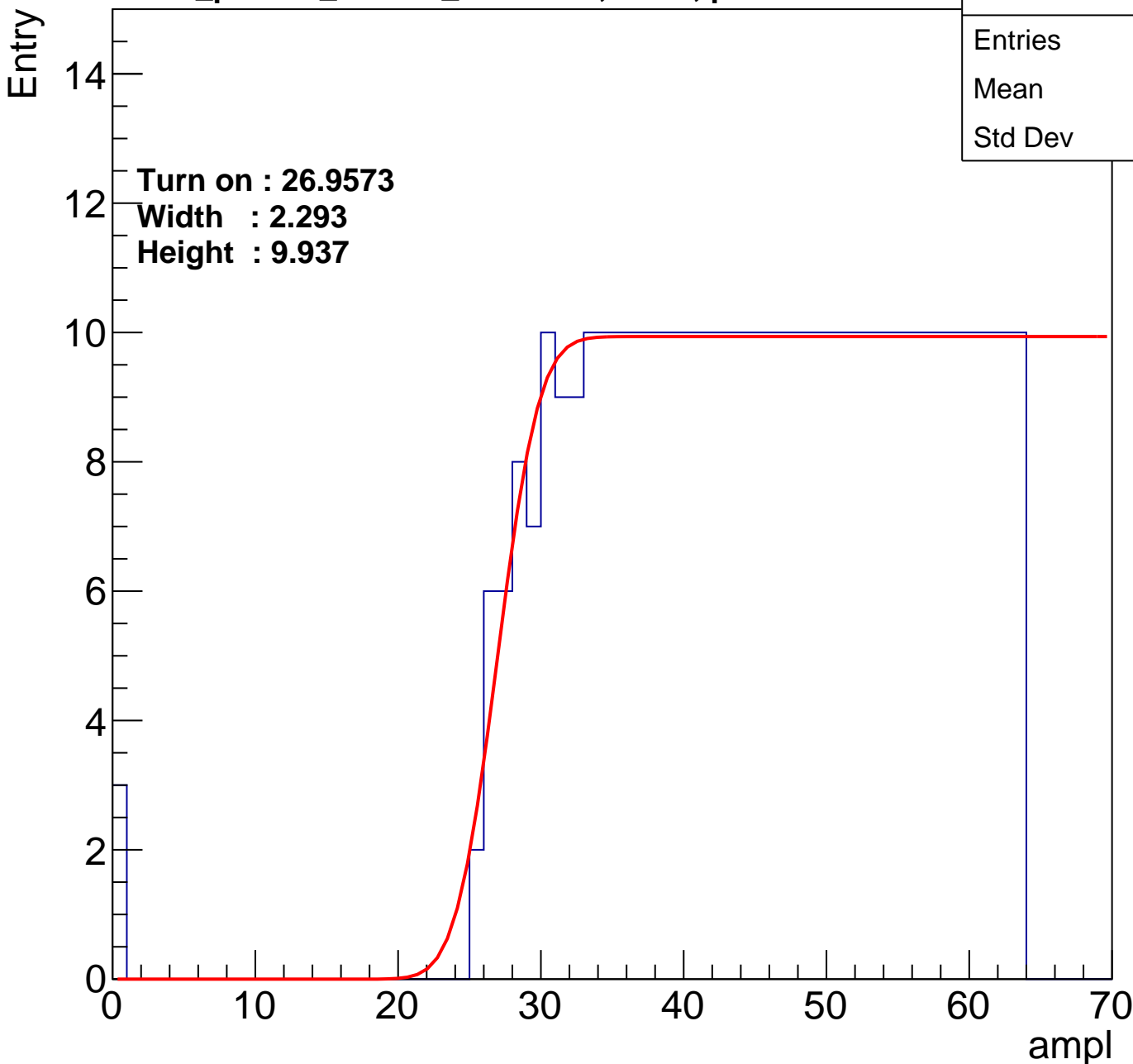
calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.71
Std Dev	11.41

Turn on : 26.9573

Width : 2.293

Height : 9.937



B0L000S, U6-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.26
Std Dev	11.14

Turn on : 28.4556

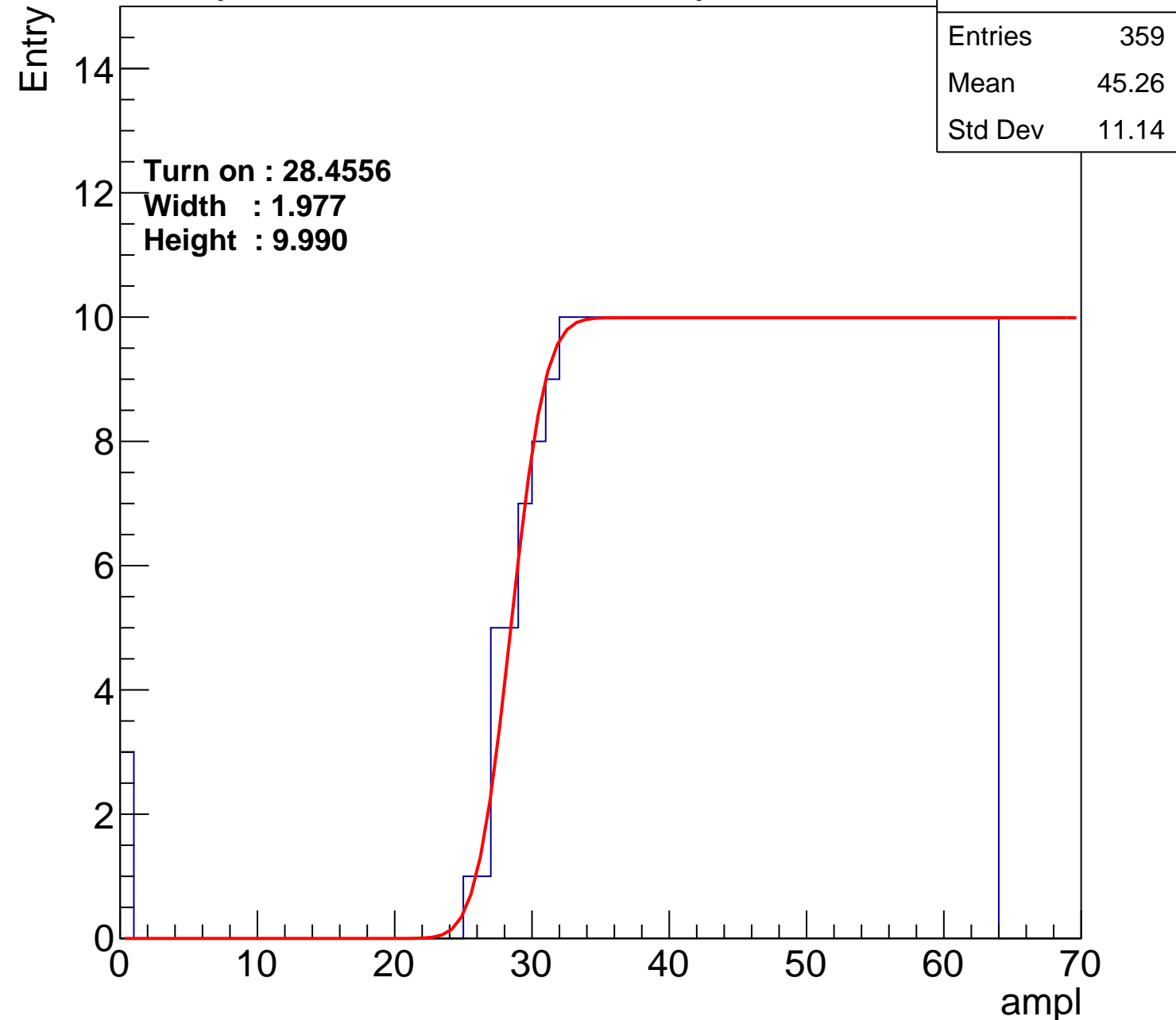
Width : 1.977

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch33

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.42
Std Dev	11.24

Turn on : 28.6116

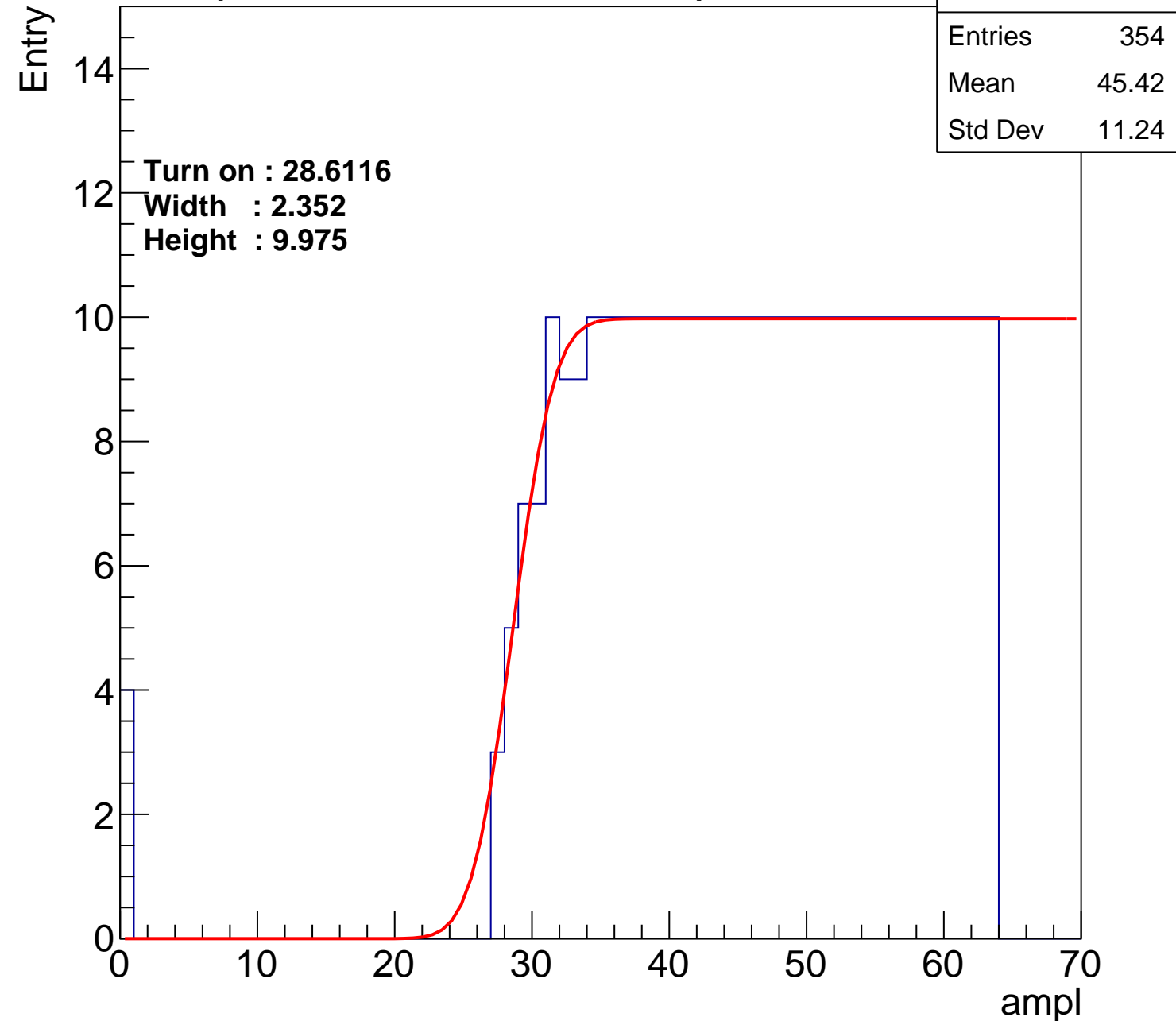
Width : 2.352

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.7290

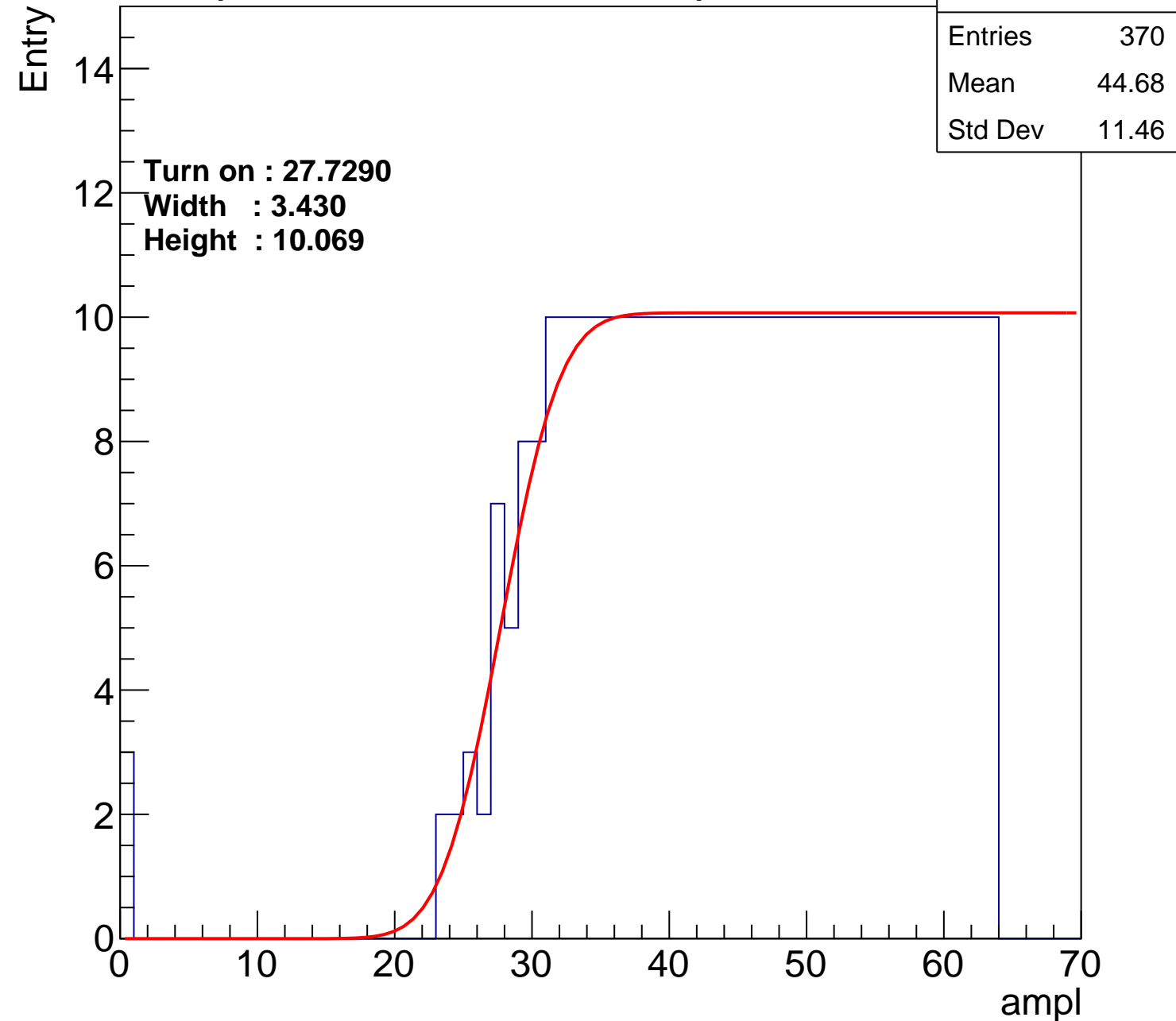
Width : 3.430

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch35

calib_packv5_042523_0143.root, FC#5, port B1

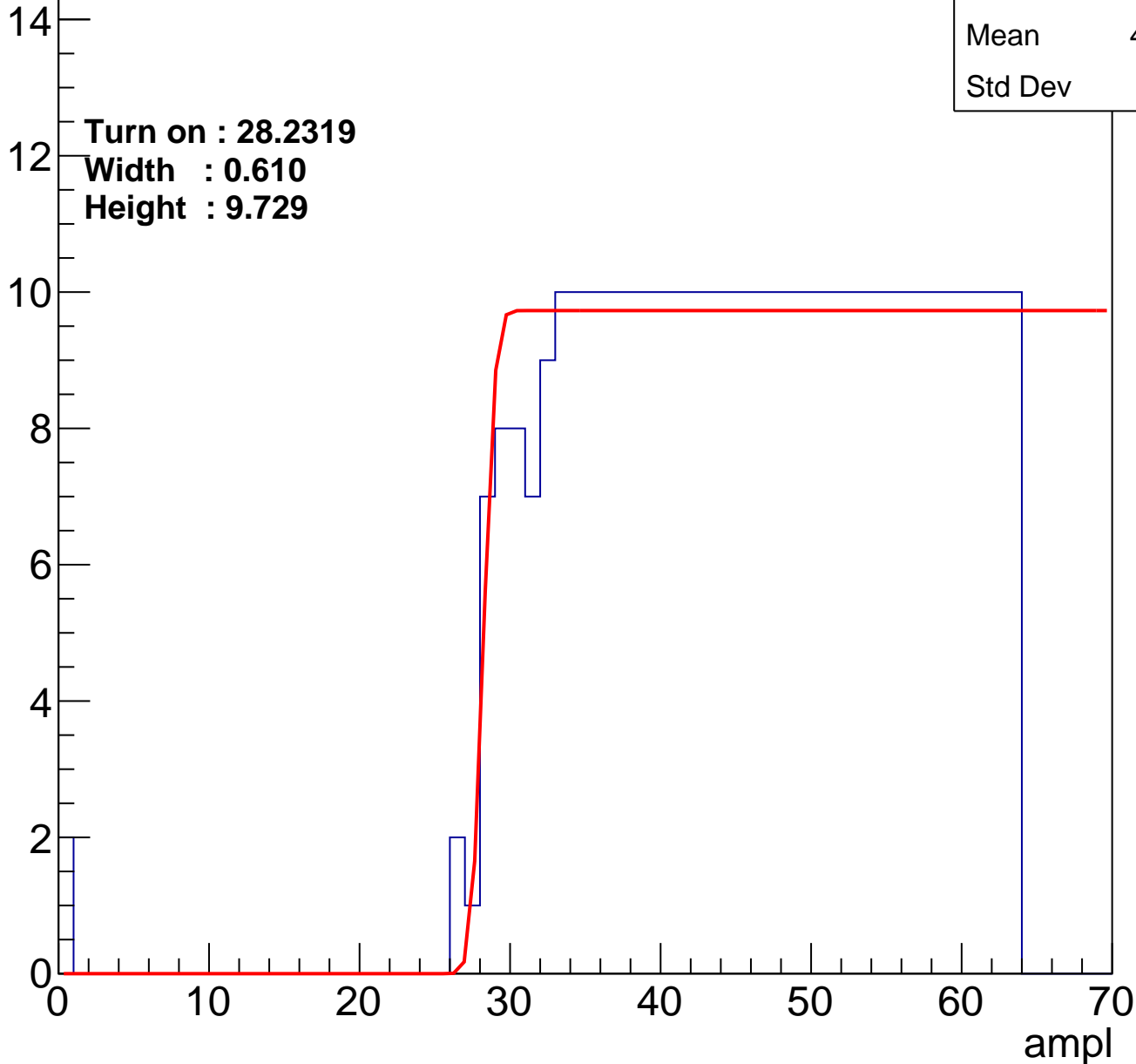
Entry

Entries	354
Mean	45.57
Std Dev	10.81

Turn on : 28.2319

Width : 0.610

Height : 9.729



B0L000S, U6-ch36

calib_packv5_042523_0143.root, FC#5, port B1

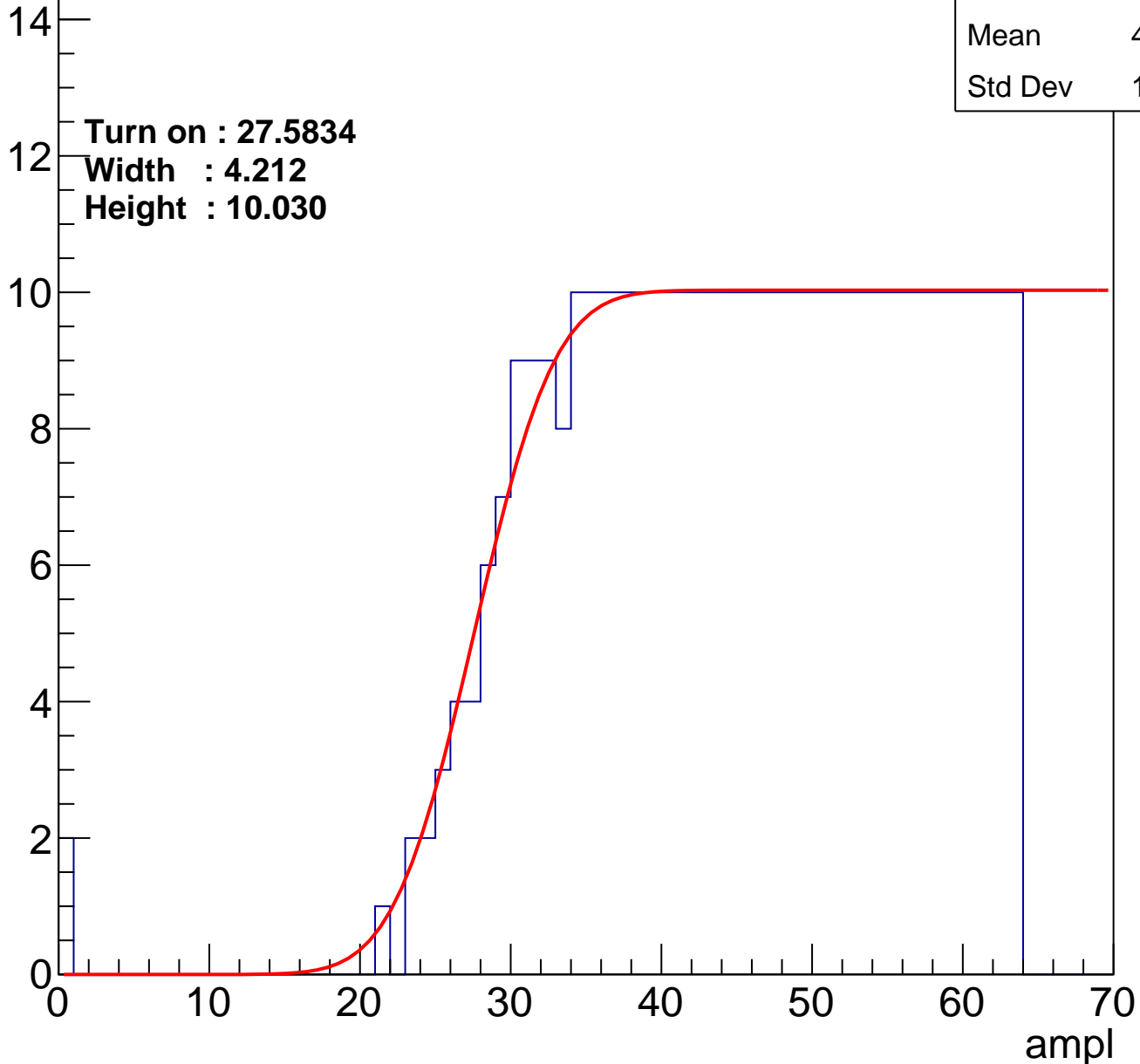
Entries	366
Mean	44.87
Std Dev	11.28

Turn on : 27.5834

Width : 4.212

Height : 10.030

Entry



B0L000S, U6-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.0893

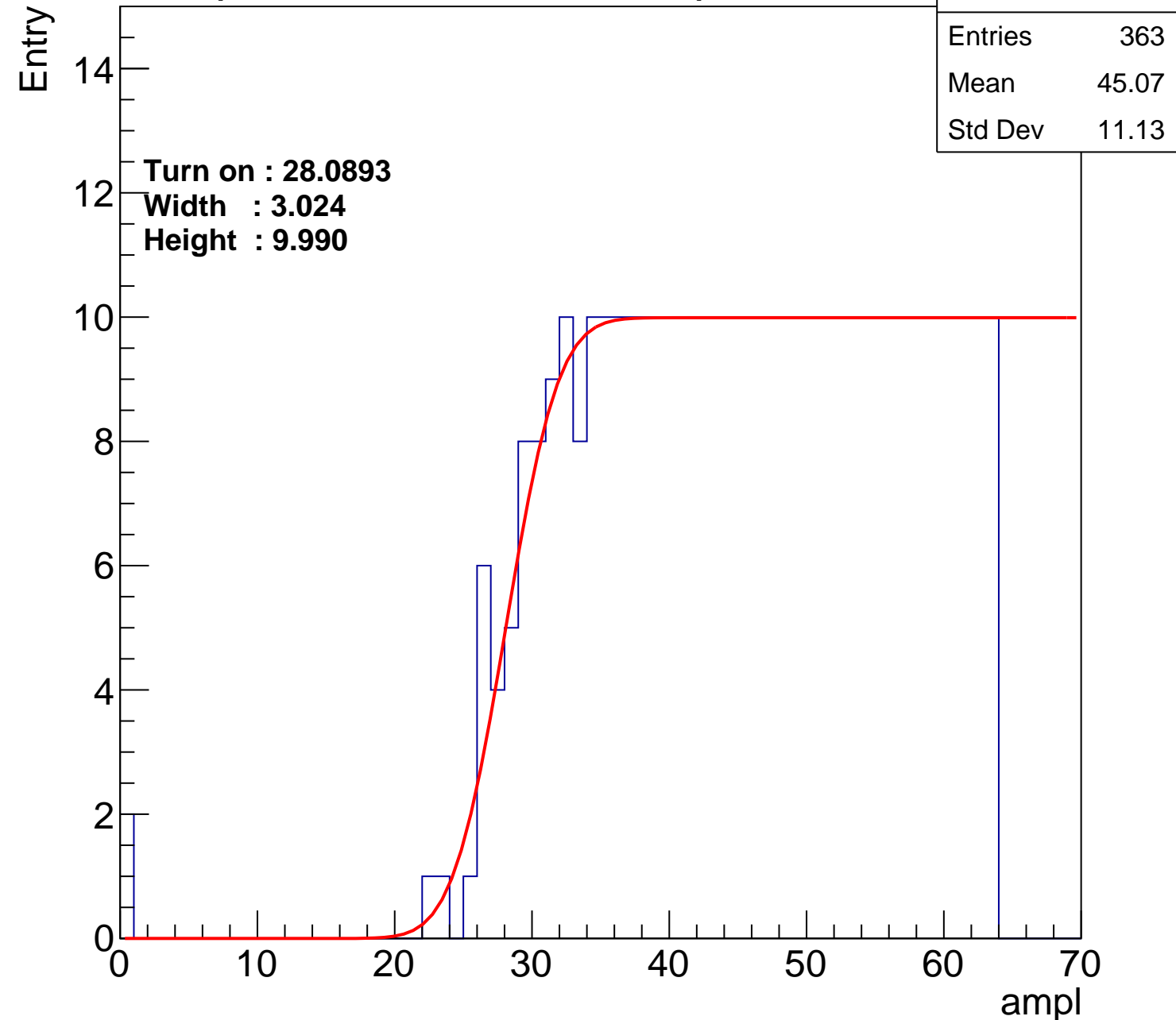
Width : 3.024

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch38

calib_packv5_042523_0143.root, FC#5, port B1

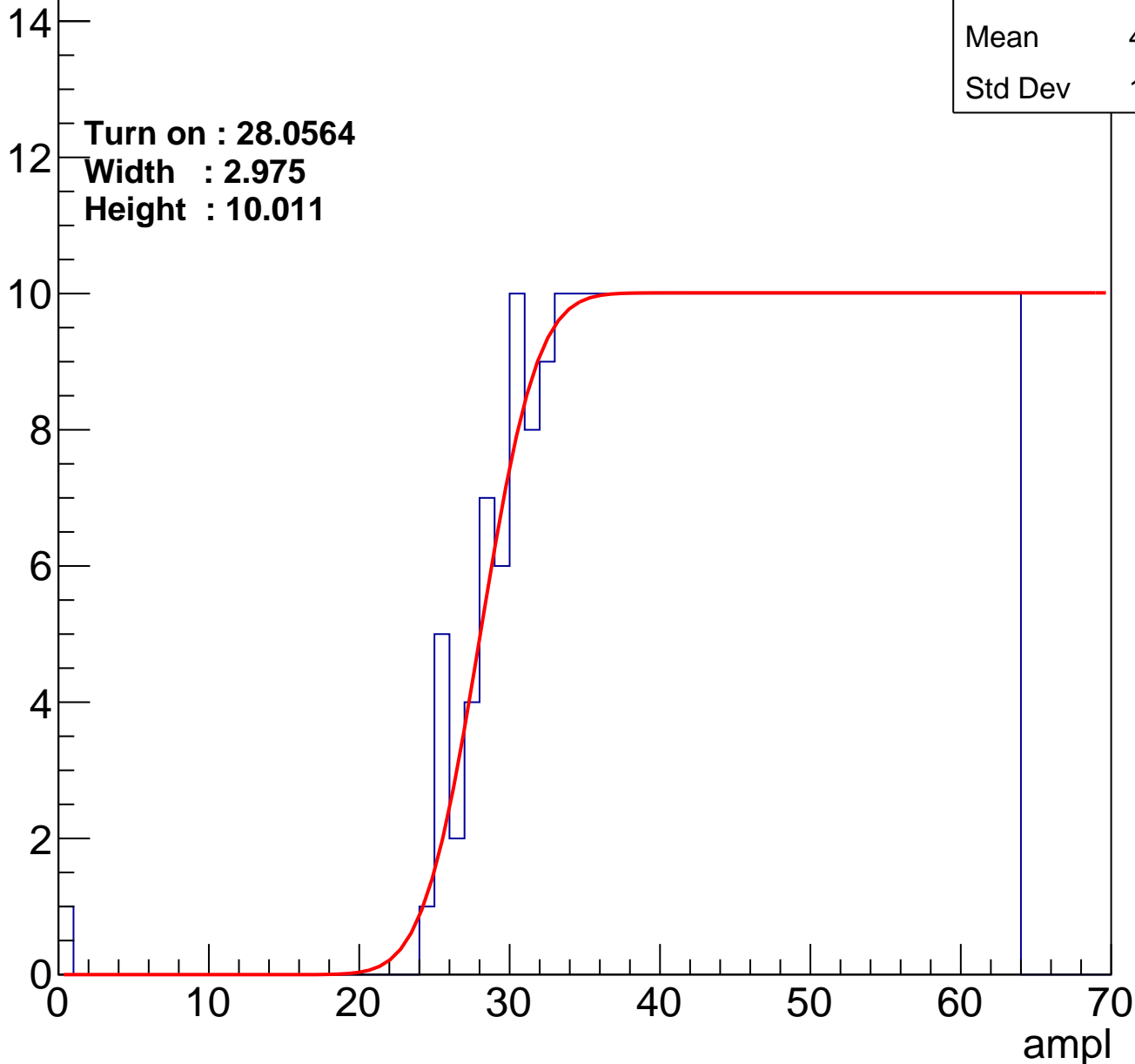
Entry

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 28.0564

Width : 2.975

Height : 10.011



B0L000S, U6-ch39

calib_packv5_042523_0143.root, FC#5, port B1

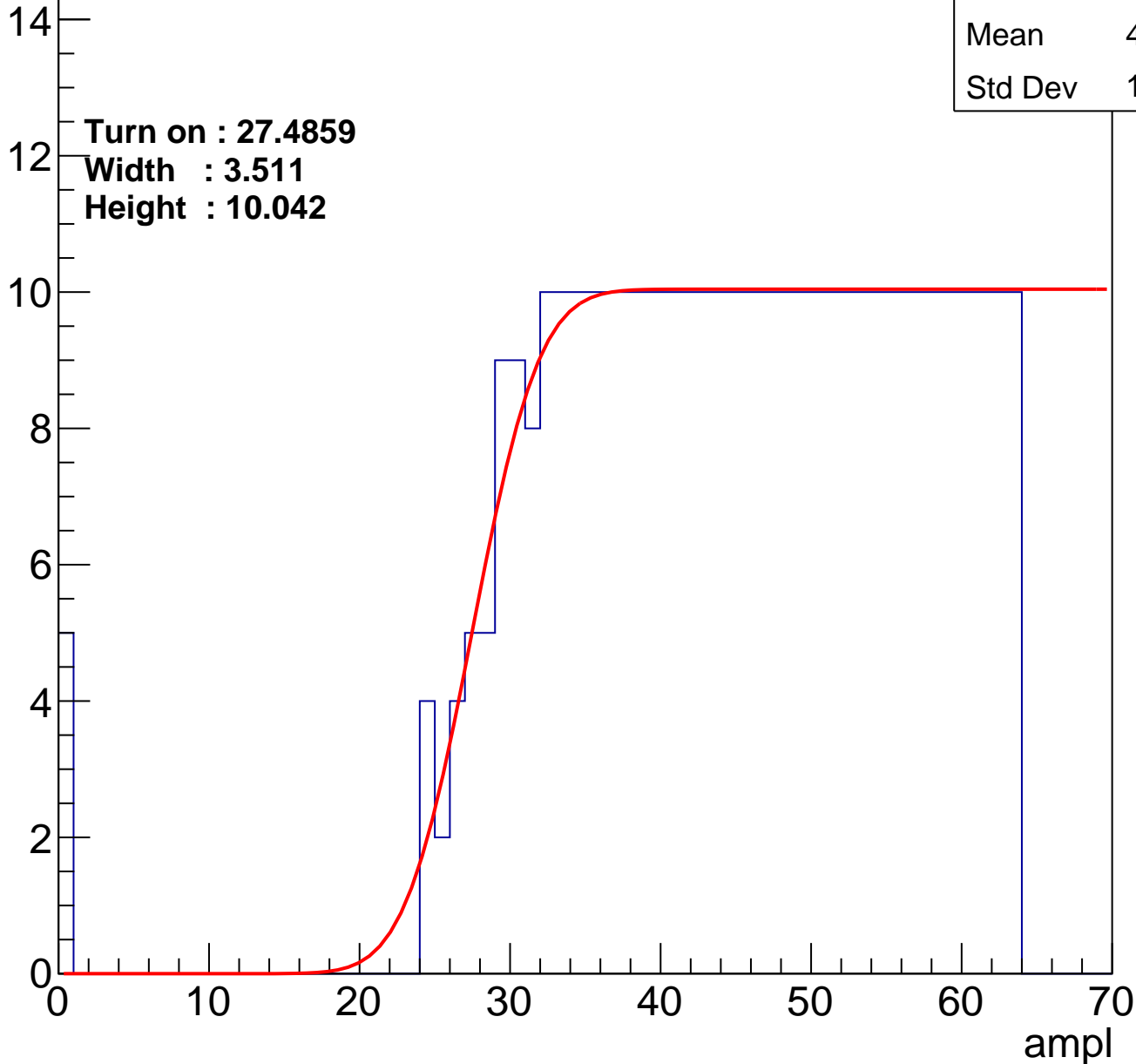
Entry

Entries	371
Mean	44.49
Std Dev	11.87

Turn on : 27.4859

Width : 3.511

Height : 10.042



B0L000S, U6-ch40

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 24.8795

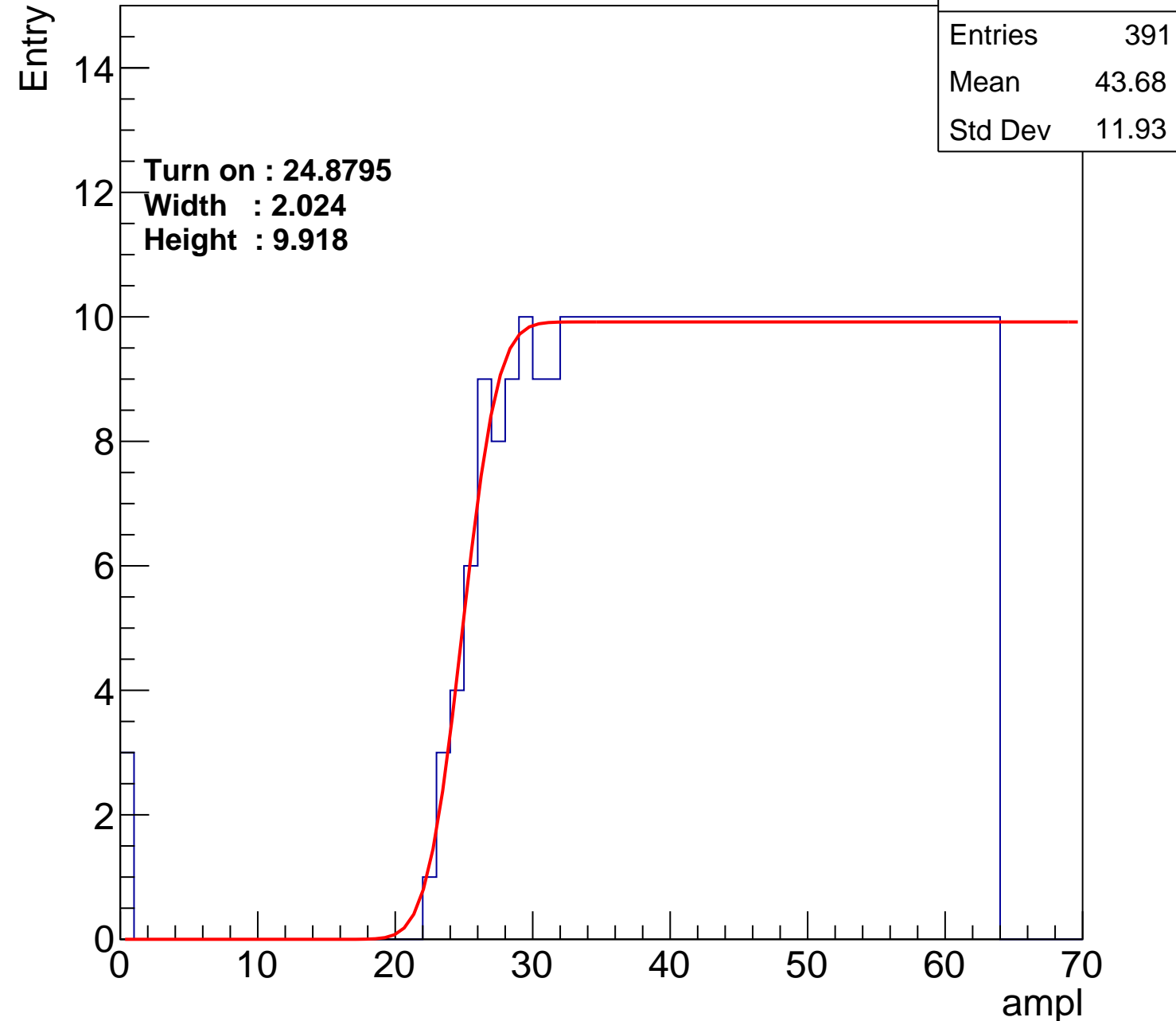
Width : 2.024

Height : 9.918

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 26.9606

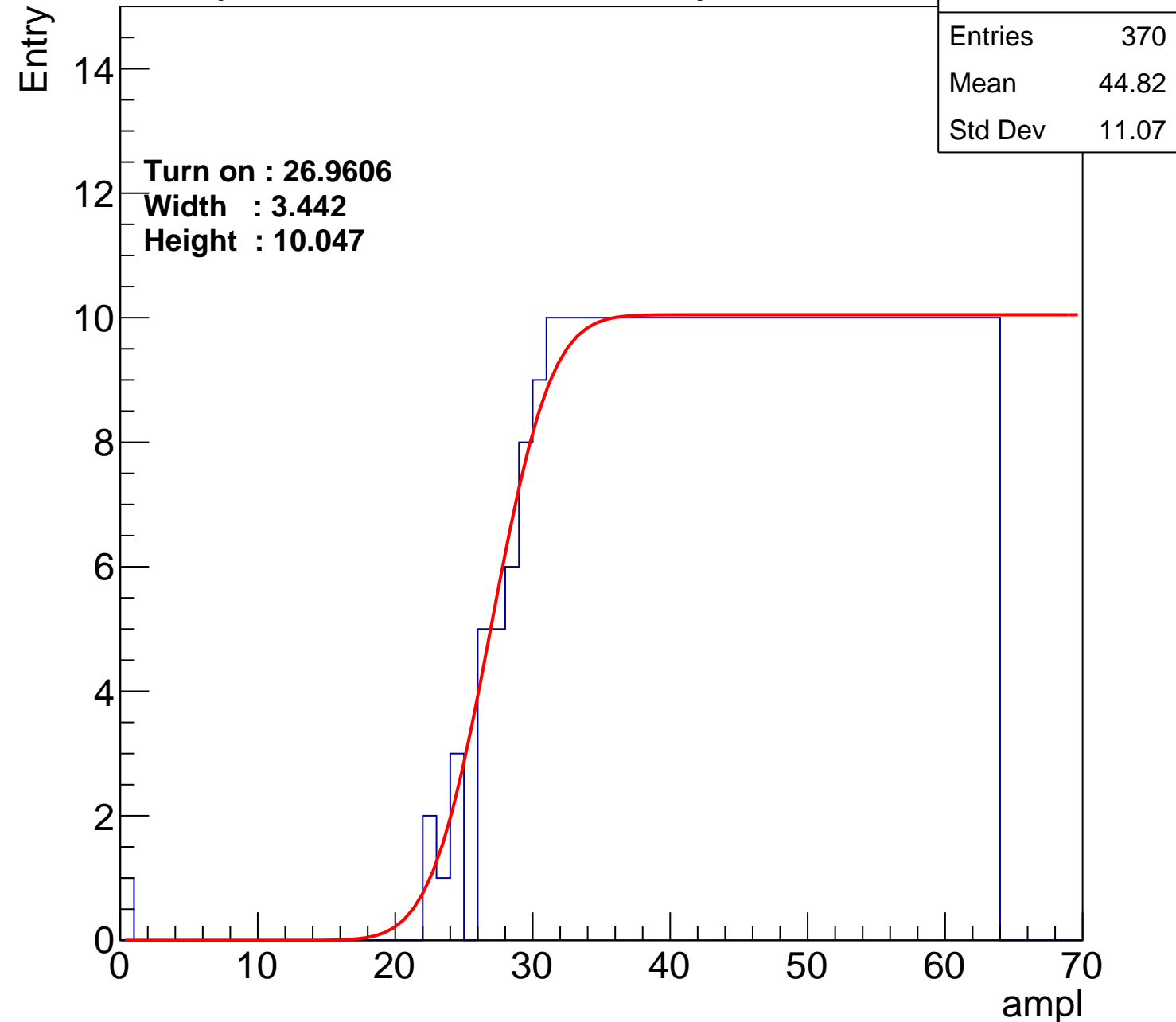
Width : 3.442

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch42

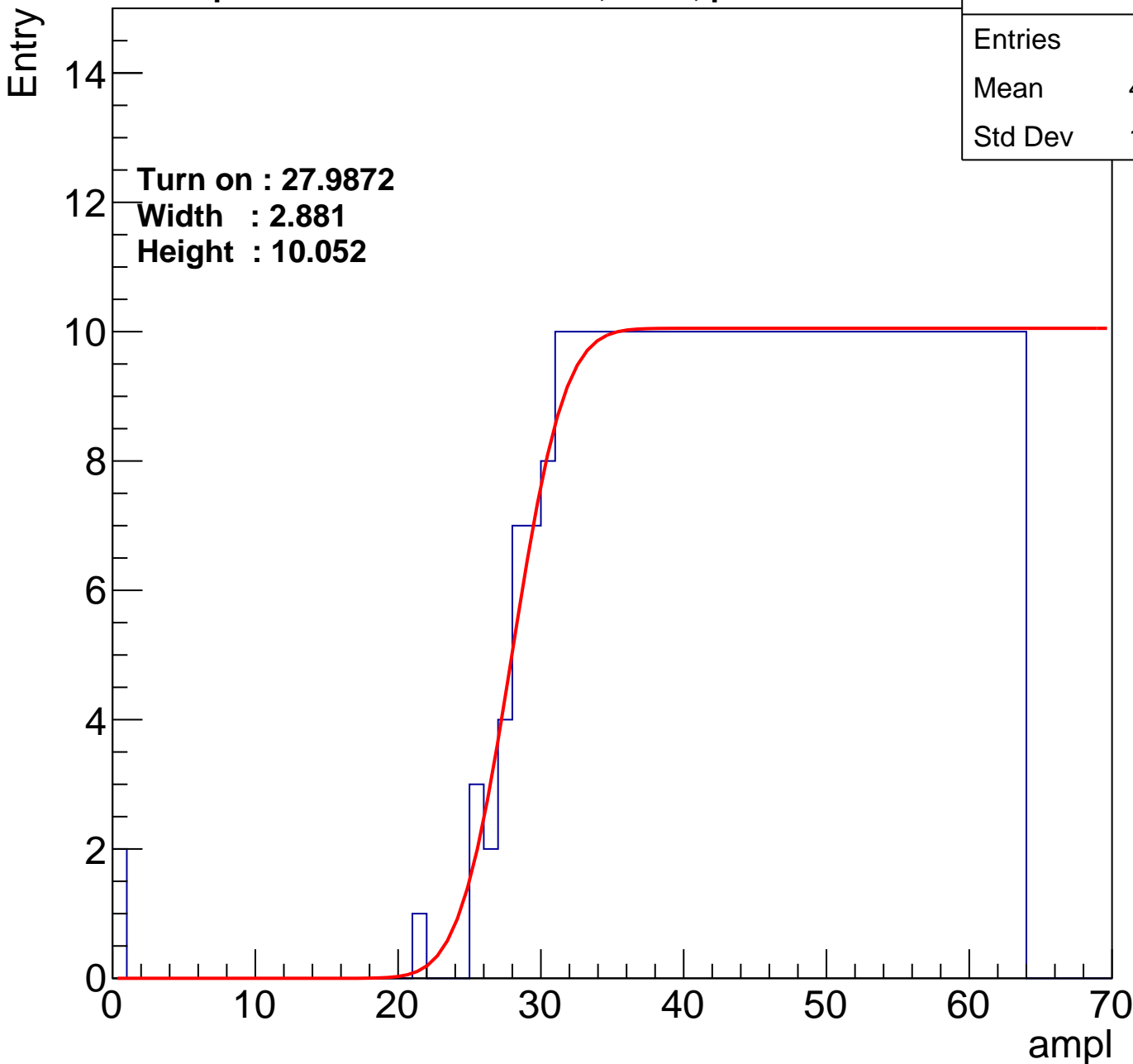
calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.9872

Width : 2.881

Height : 10.052



B0L000S, U6-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.51
Std Dev	11.19

Turn on : 26.4809

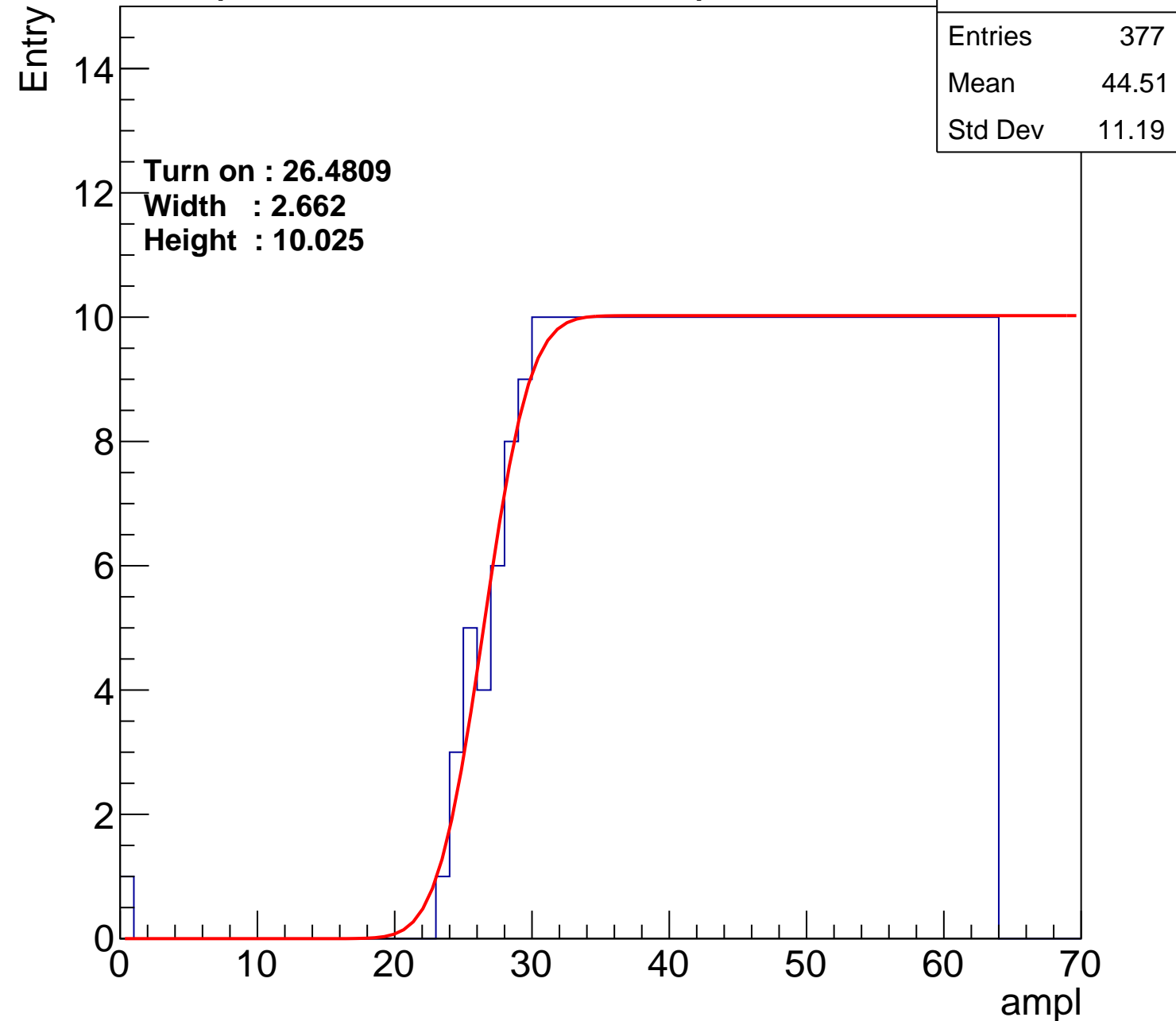
Width : 2.662

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch44

calib_packv5_042523_0143.root, FC#5, port B1

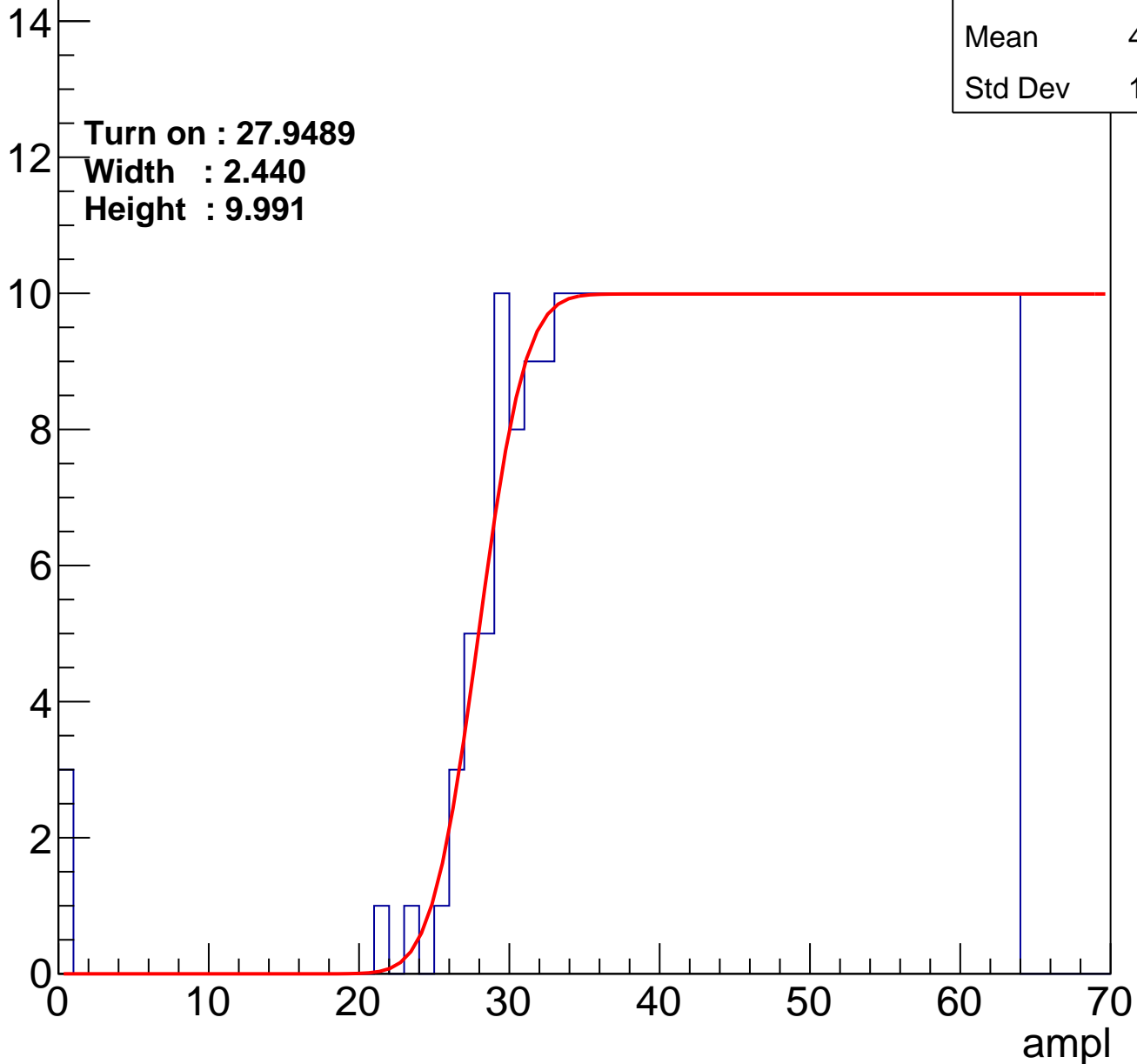
Entry

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 27.9489

Width : 2.440

Height : 9.991



B0L000S, U6-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.41
Std Dev	11.88

Turn on : 28.0043

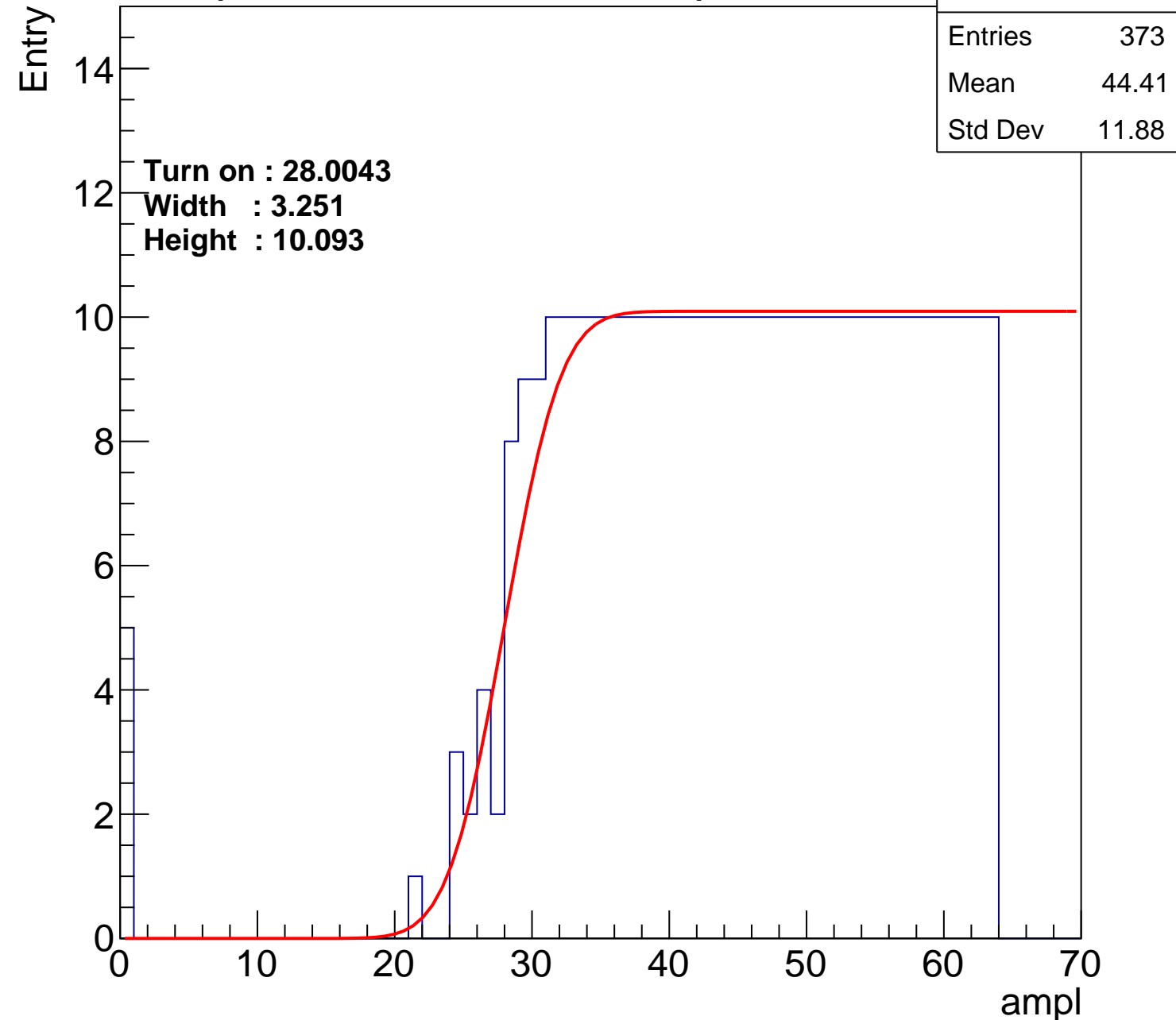
Width : 3.251

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch46

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.28
Std Dev	11.01

Turn on : 28.5208

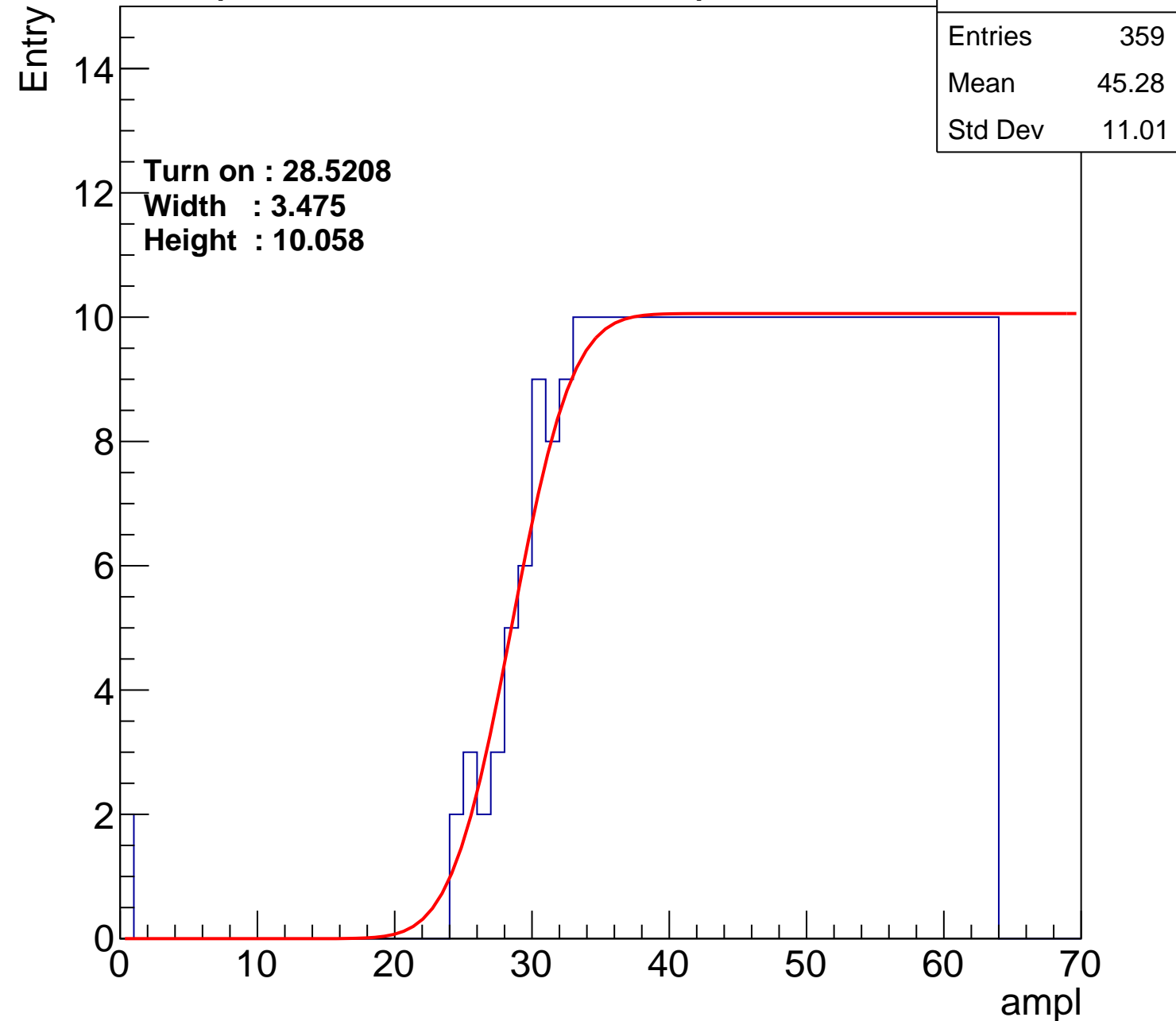
Width : 3.475

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.31
Std Dev	10.86

Turn on : 28.7712

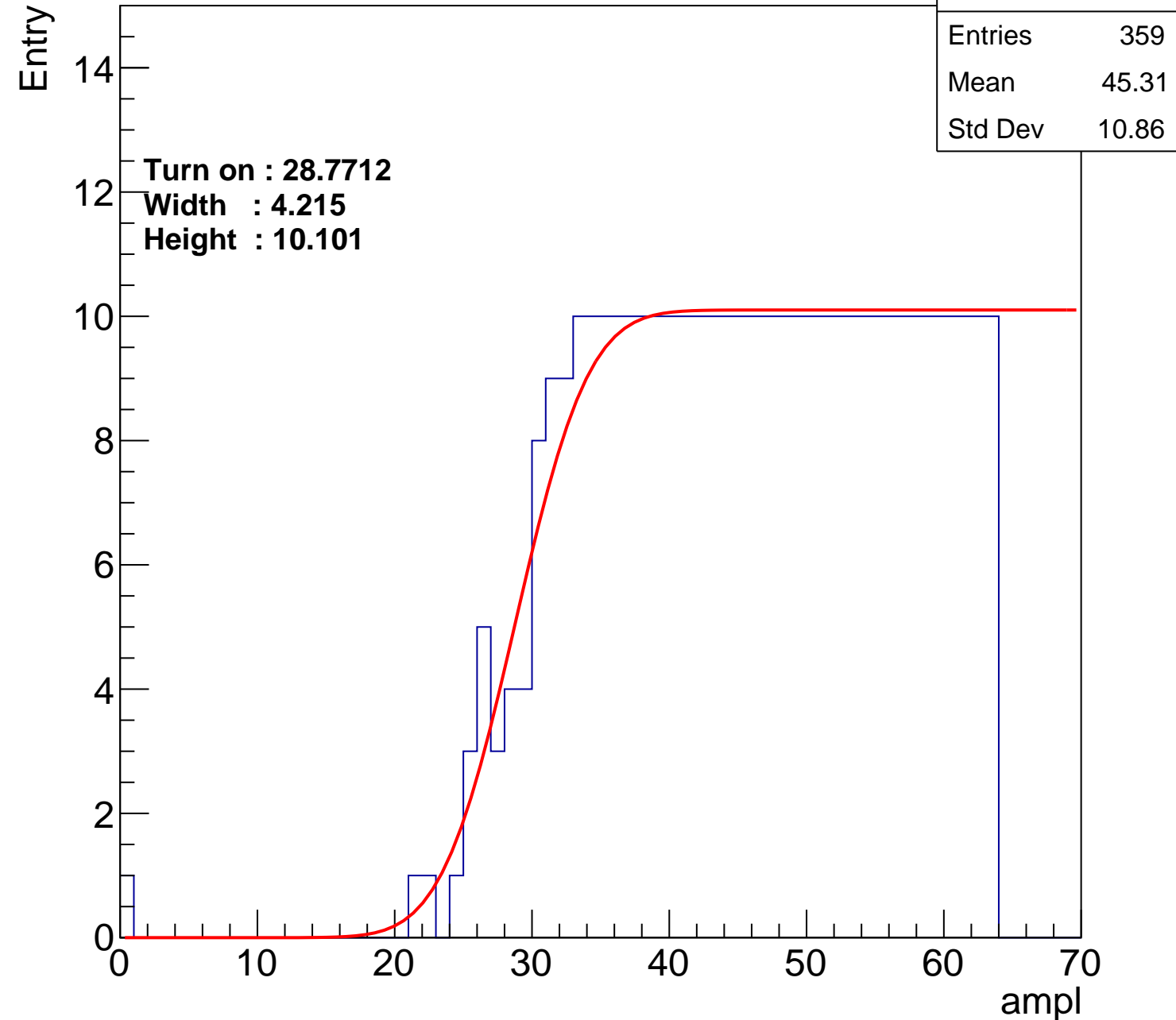
Width : 4.215

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch48

calib_packv5_042523_0143.root, FC#5, port B1

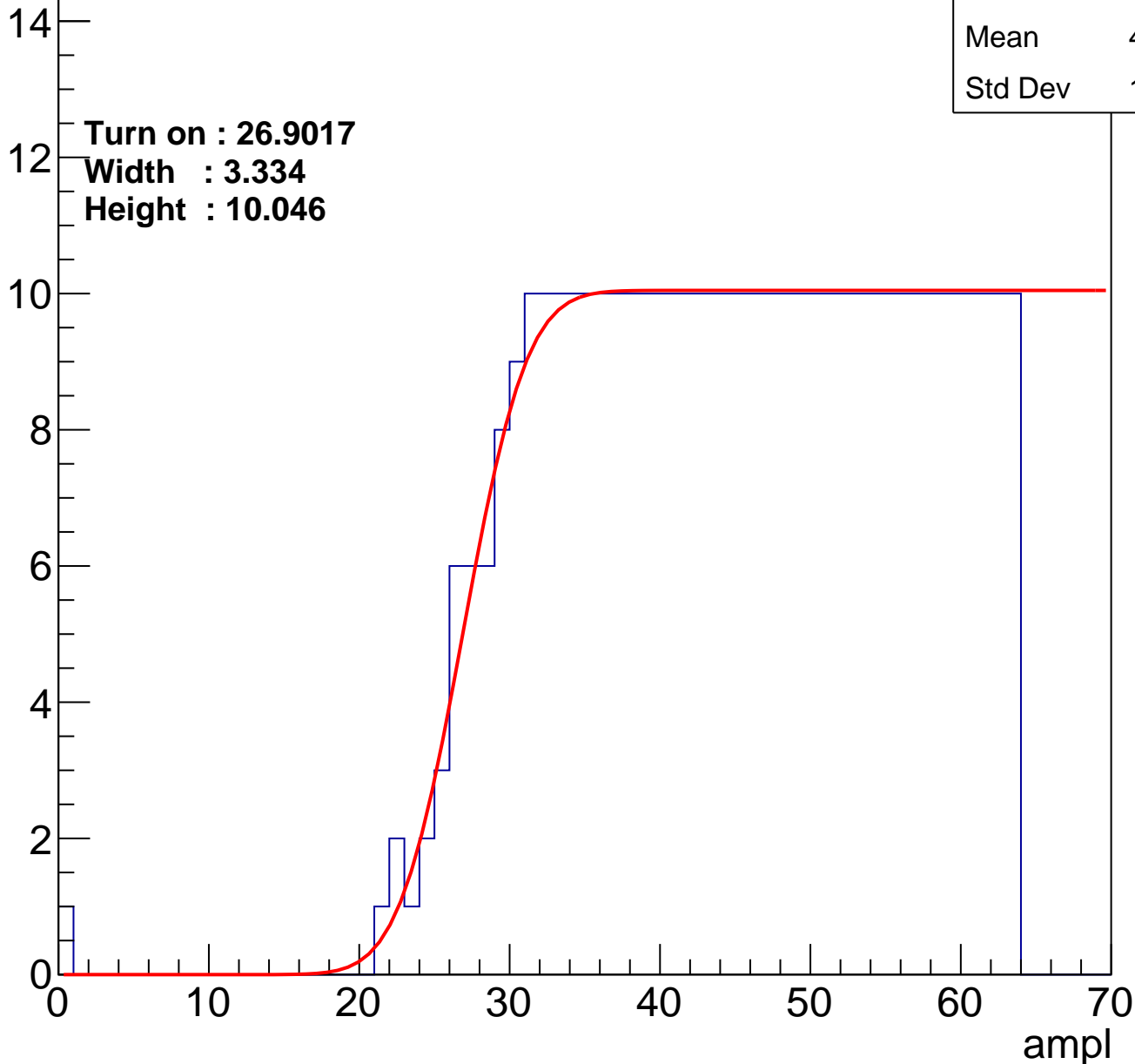
Entry

Entries	375
Mean	44.56
Std Dev	11.23

Turn on : 26.9017

Width : 3.334

Height : 10.046



B0L000S, U6-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.39
Std Dev	10.73

Turn on : 28.3509

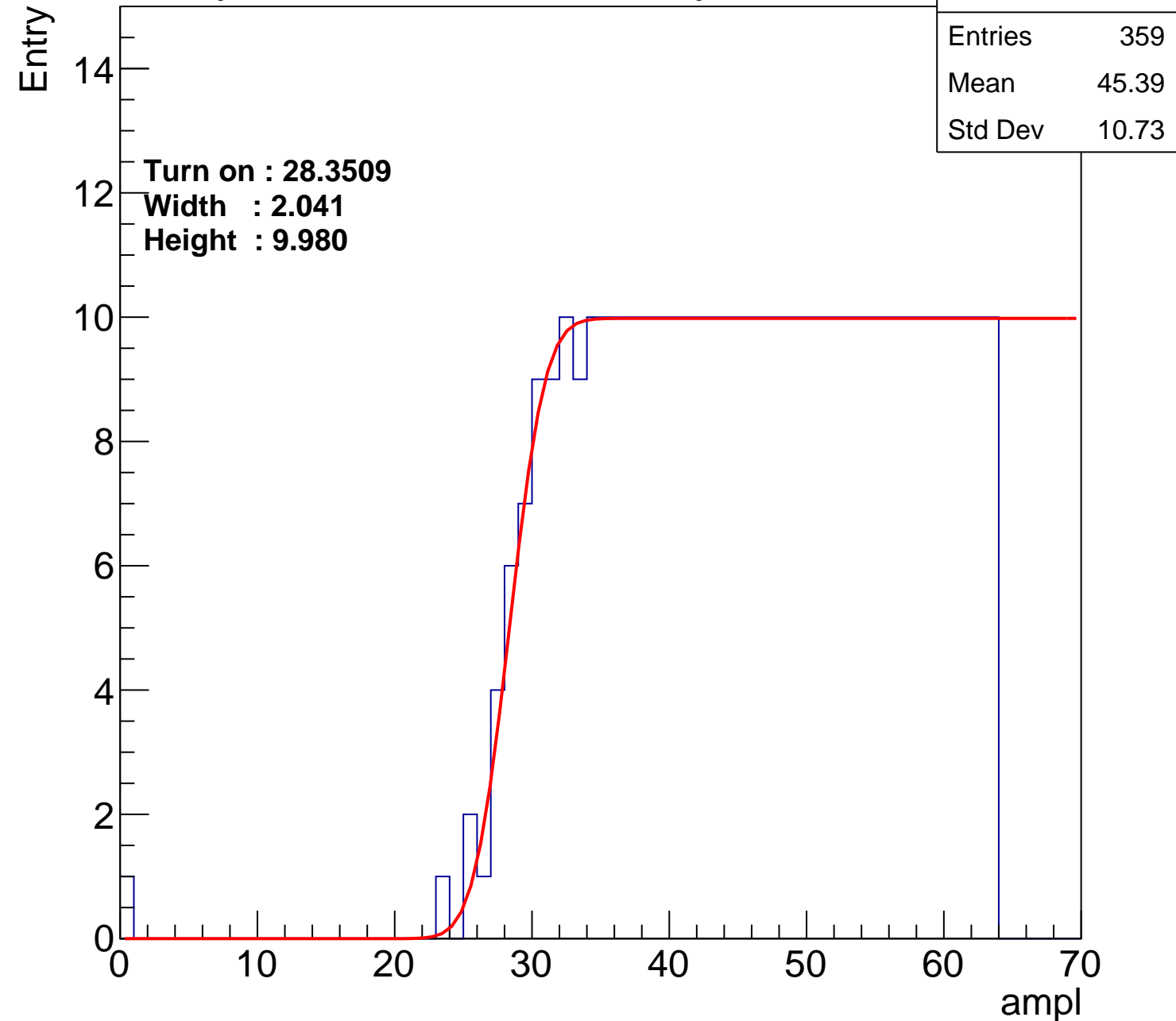
Width : 2.041

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch50

calib_packv5_042523_0143.root, FC#5, port B1

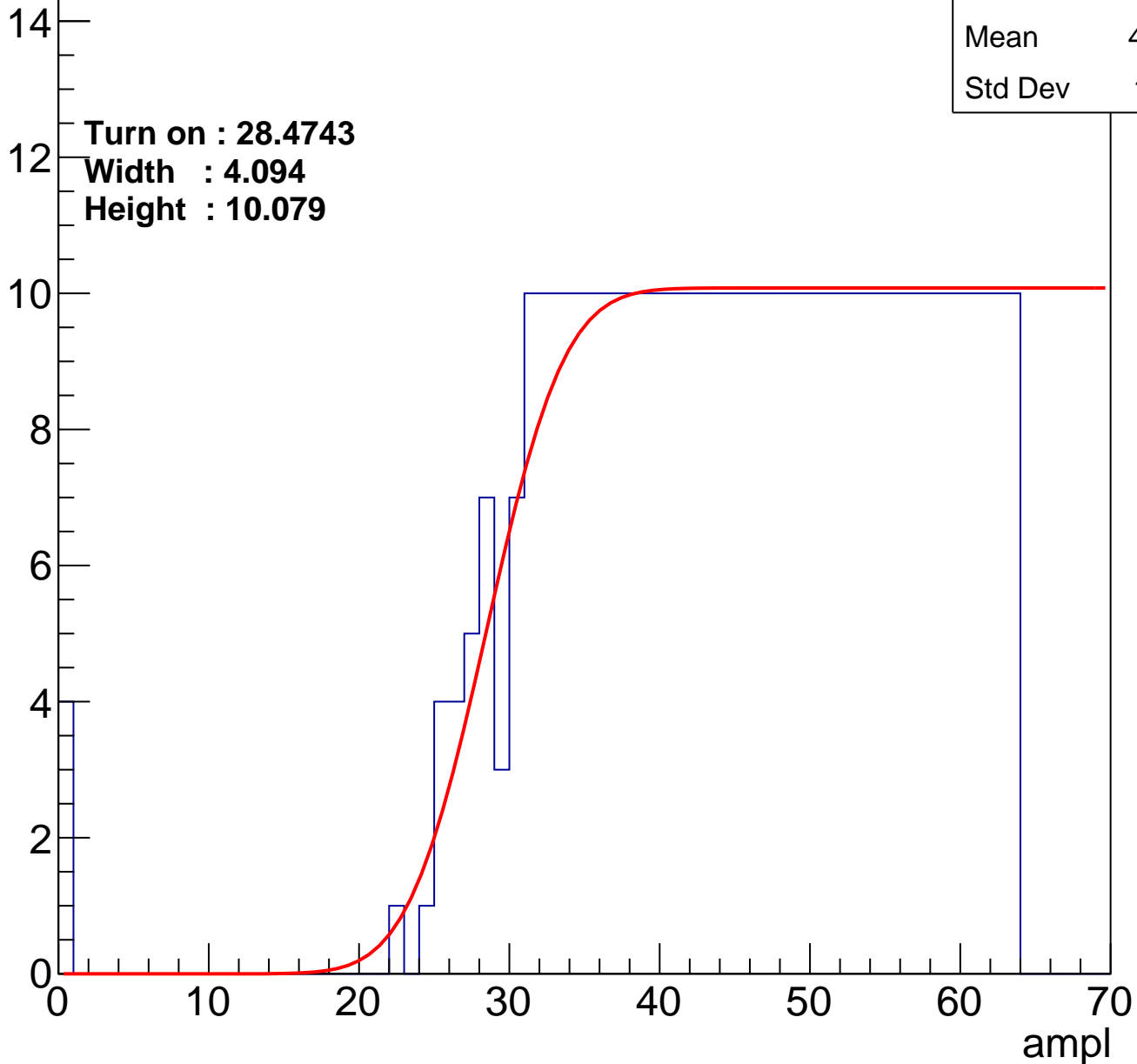
Entries	366
Mean	44.78
Std Dev	11.61

Turn on : 28.4743

Width : 4.094

Height : 10.079

Entry



B0L000S, U6-ch51

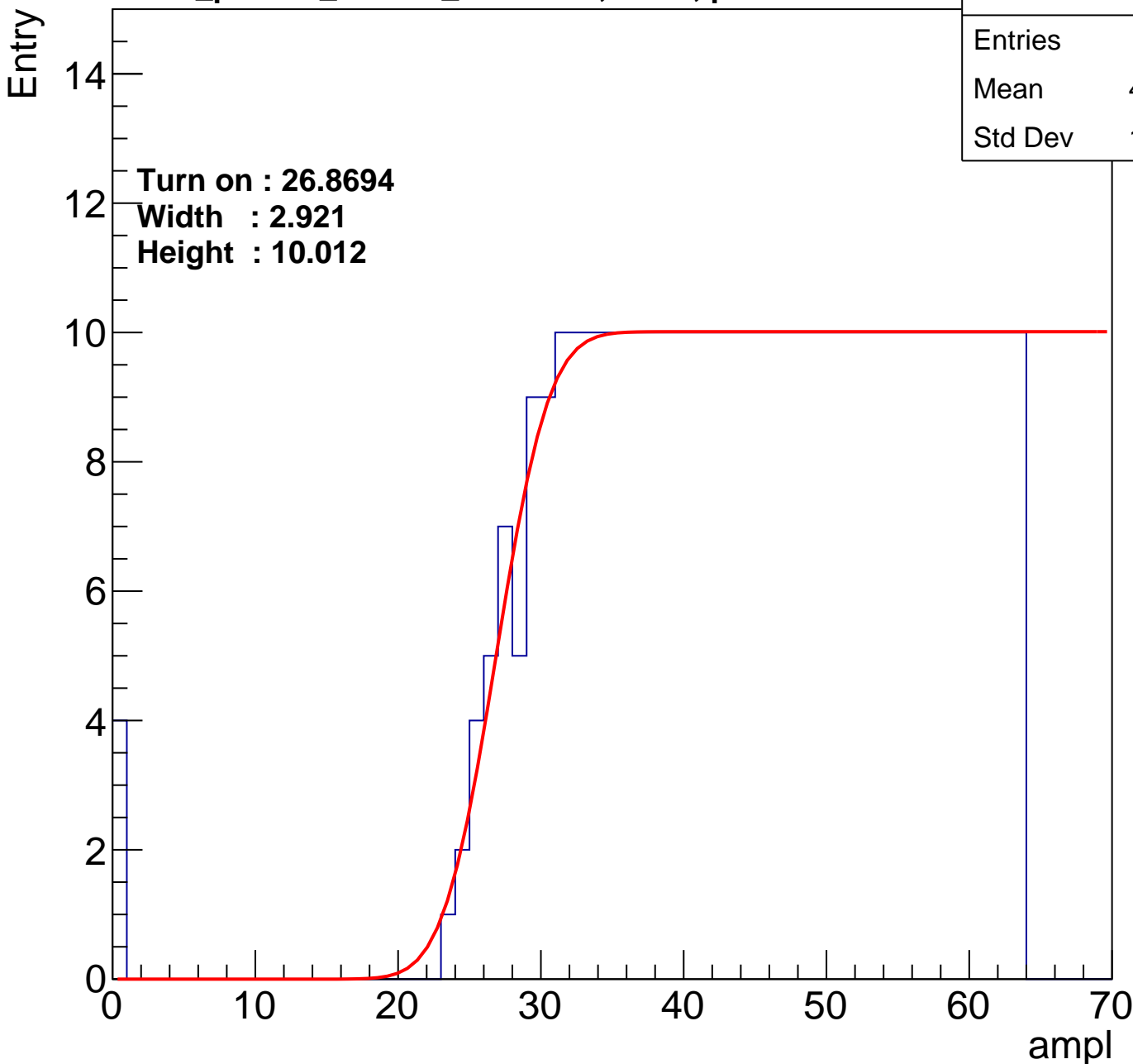
calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.34
Std Dev	11.76

Turn on : 26.8694

Width : 2.921

Height : 10.012



B0L000S, U6-ch52

calib_packv5_042523_0143.root, FC#5, port B1

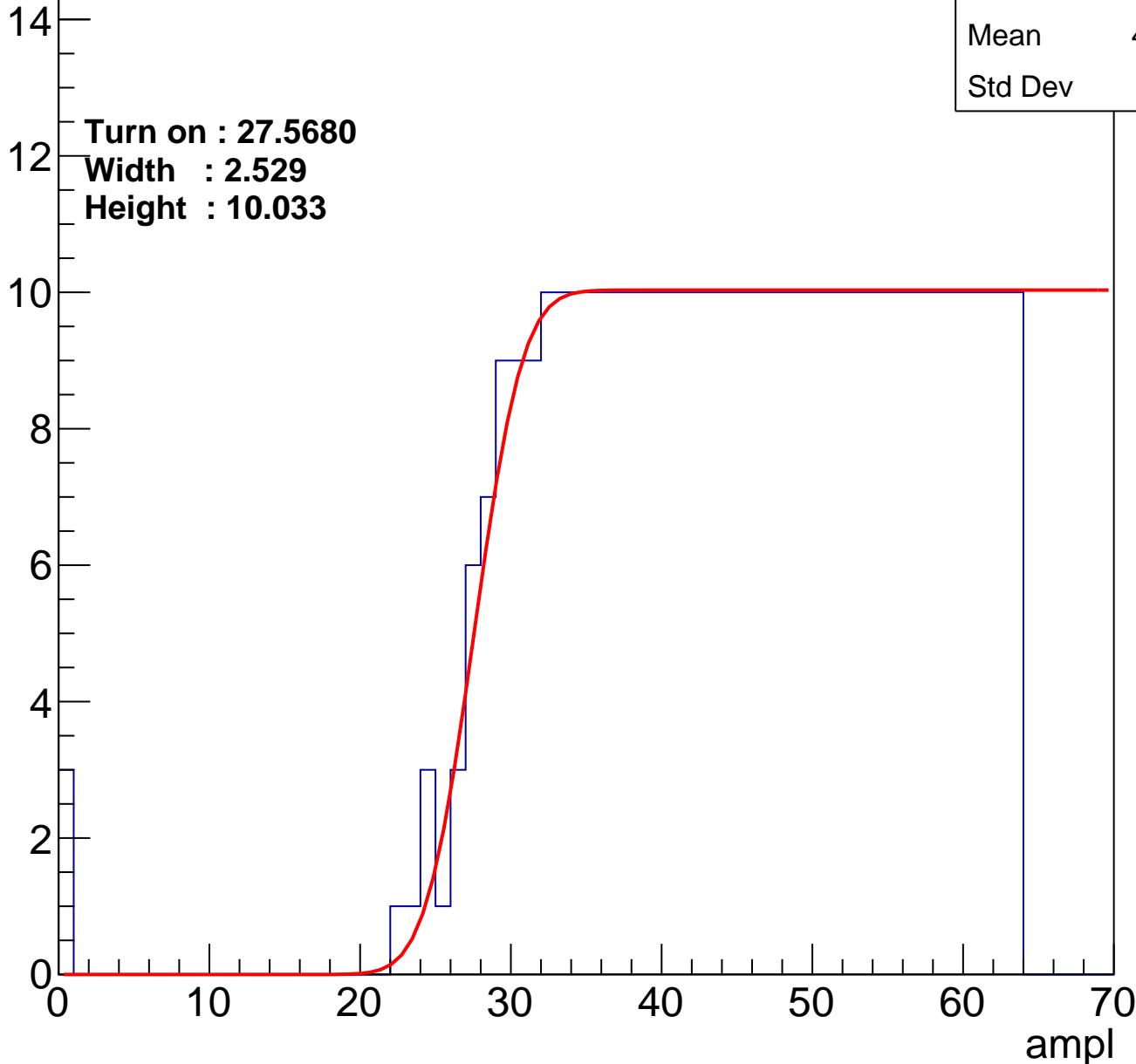
Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 27.5680

Width : 2.529

Height : 10.033

Entry



B0L000S, U6-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.5416

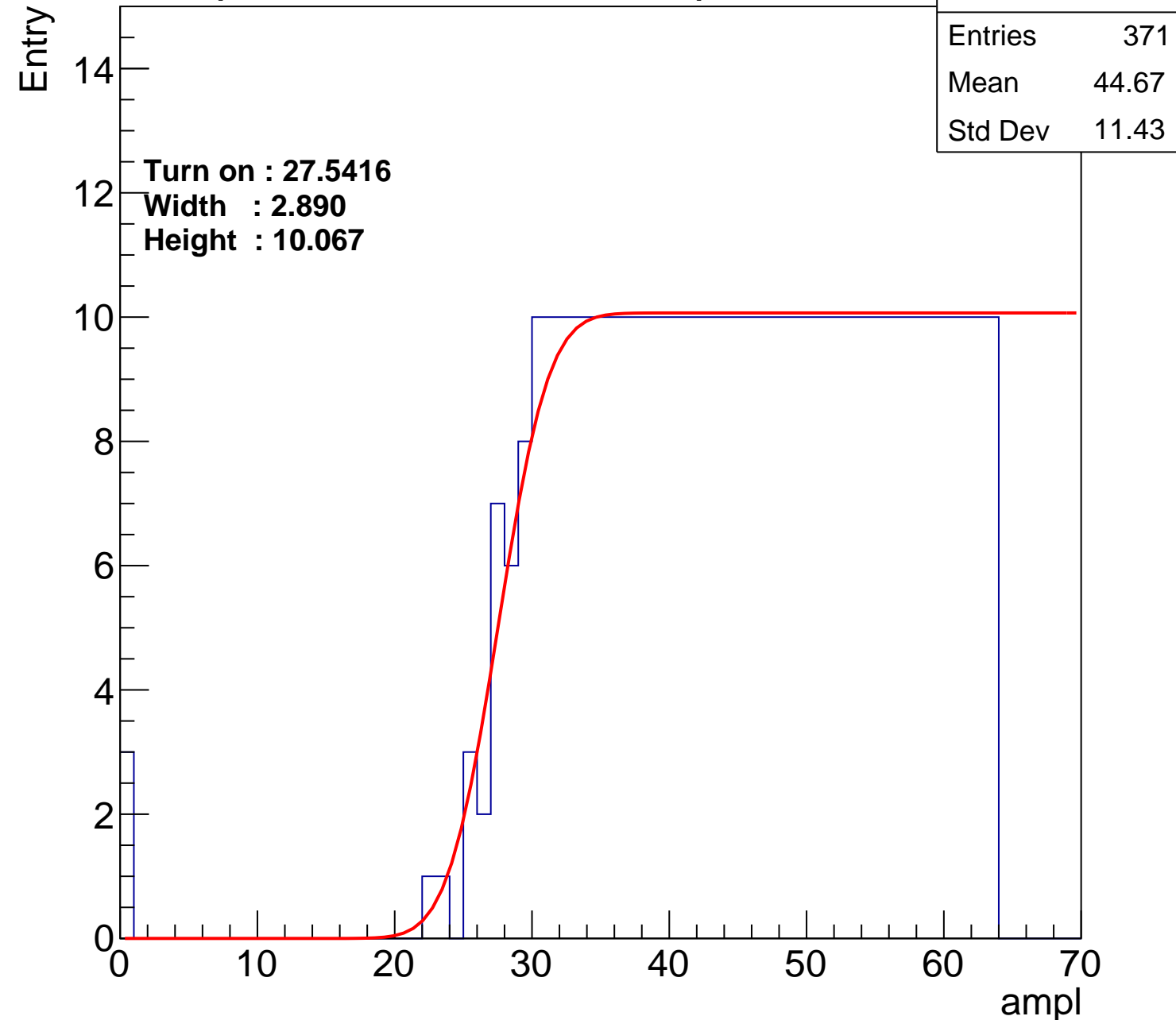
Width : 2.890

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch54

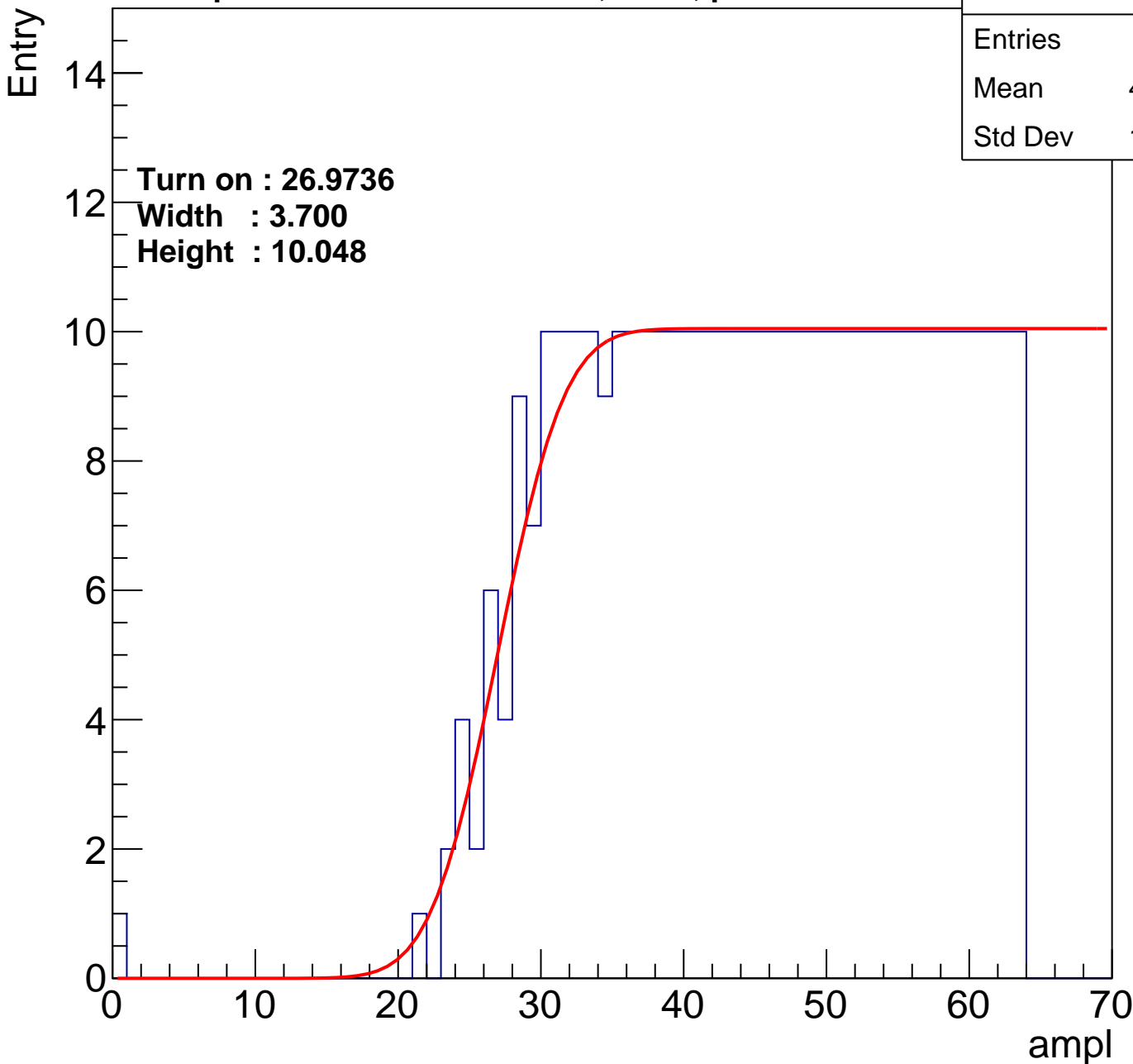
calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.55
Std Dev	11.23

Turn on : 26.9736

Width : 3.700

Height : 10.048



B0L000S, U6-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.61
Std Dev	11.02

Turn on : 29.5953

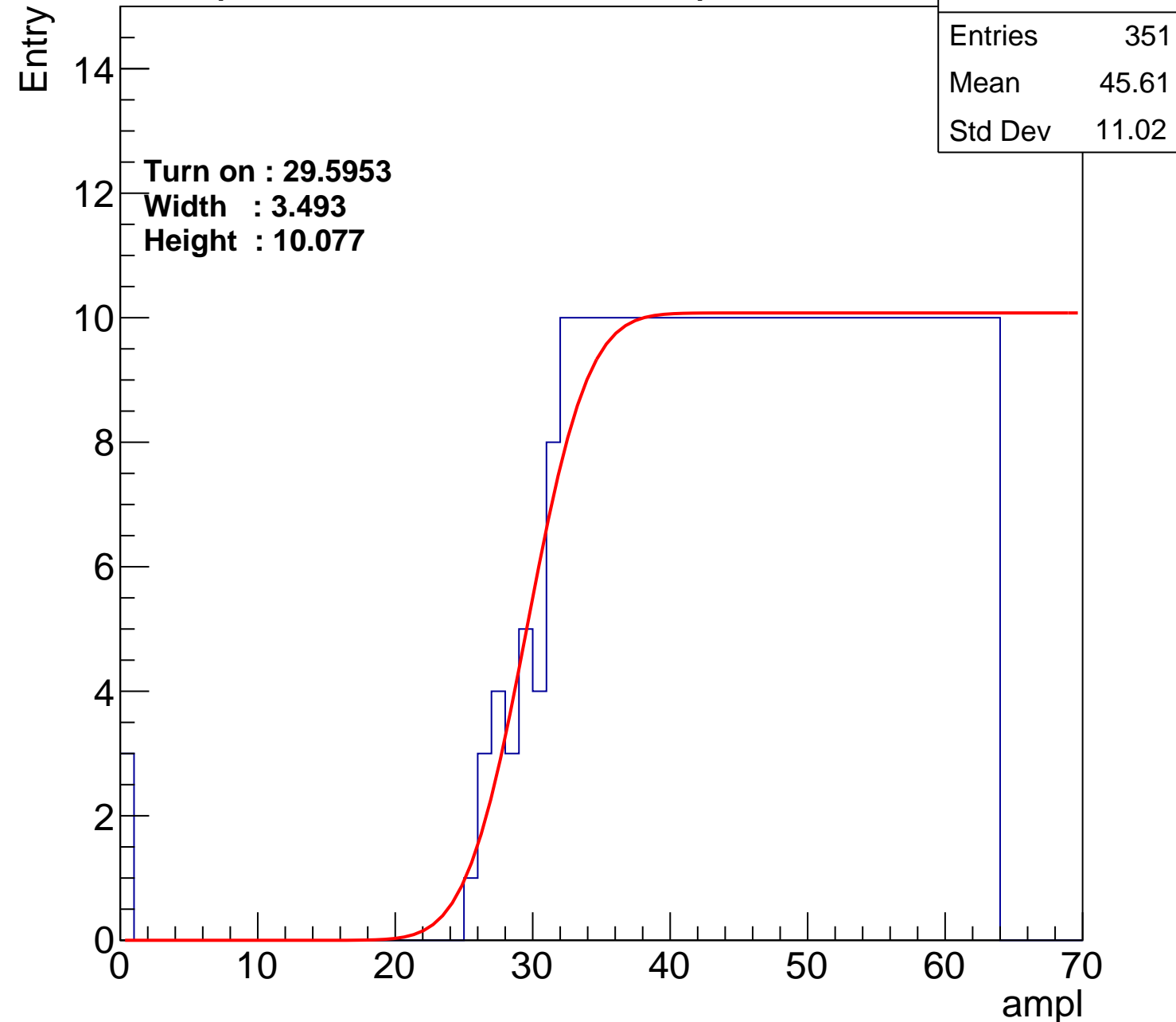
Width : 3.493

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch56

calib_packv5_042523_0143.root, FC#5, port B1

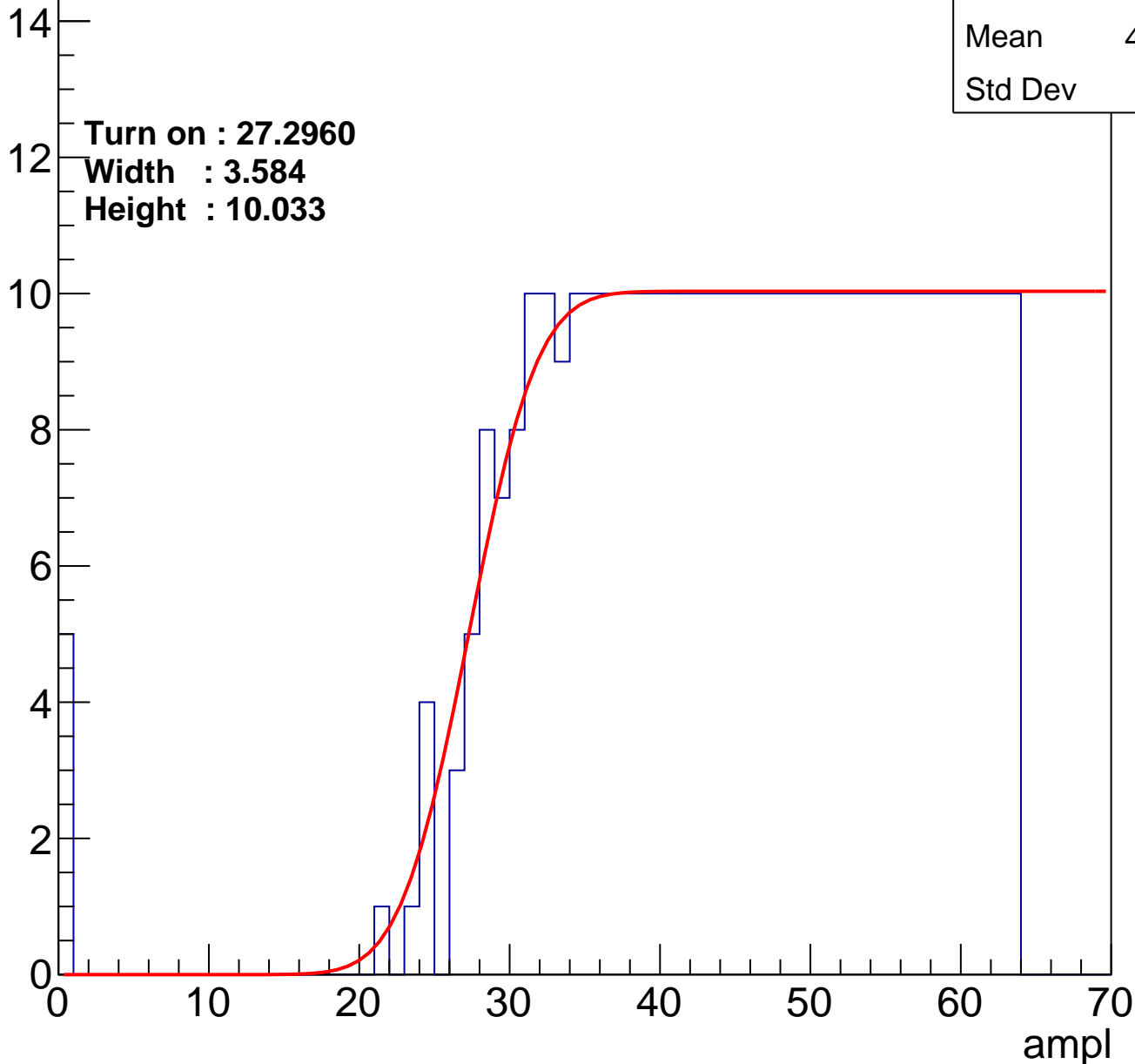
Entry

Entries	371
Mean	44.47
Std Dev	11.9

Turn on : 27.2960

Width : 3.584

Height : 10.033



B0L000S, U6-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.2
Std Dev	11.38

Turn on : 28.6991

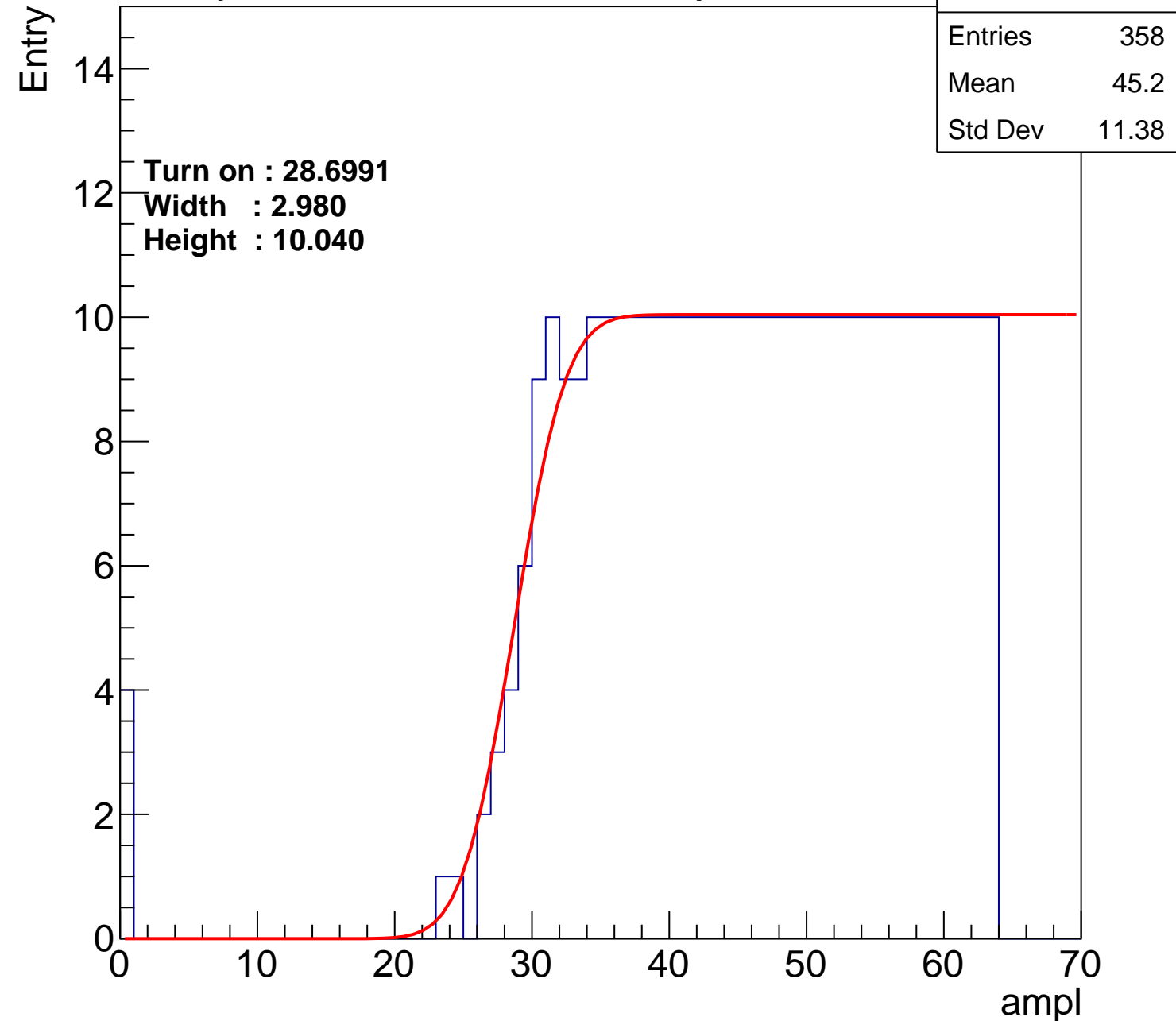
Width : 2.980

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch58

calib_packv5_042523_0143.root, FC#5, port B1

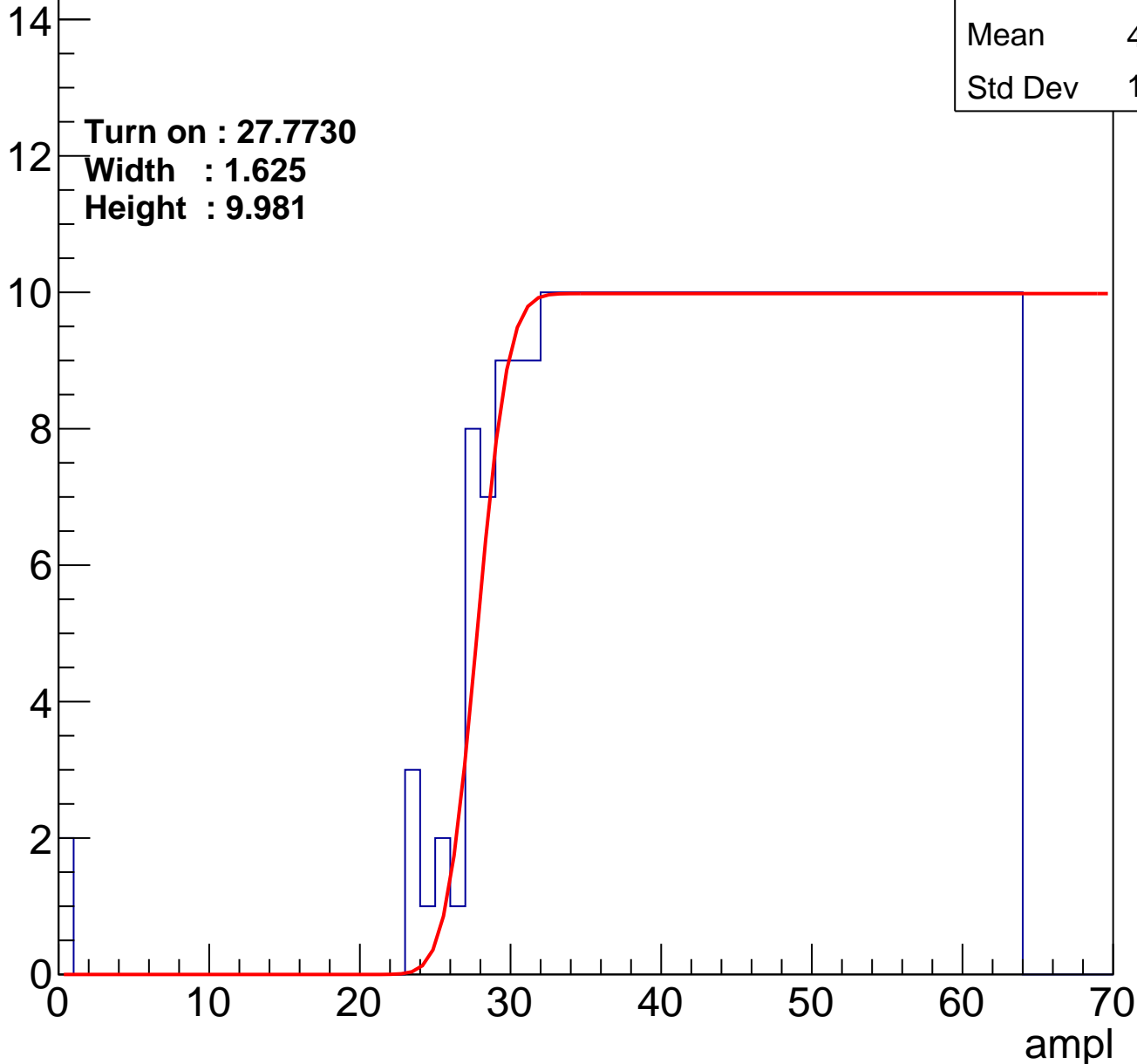
Entry

Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.7730

Width : 1.625

Height : 9.981



B0L000S, U6-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 26.1240

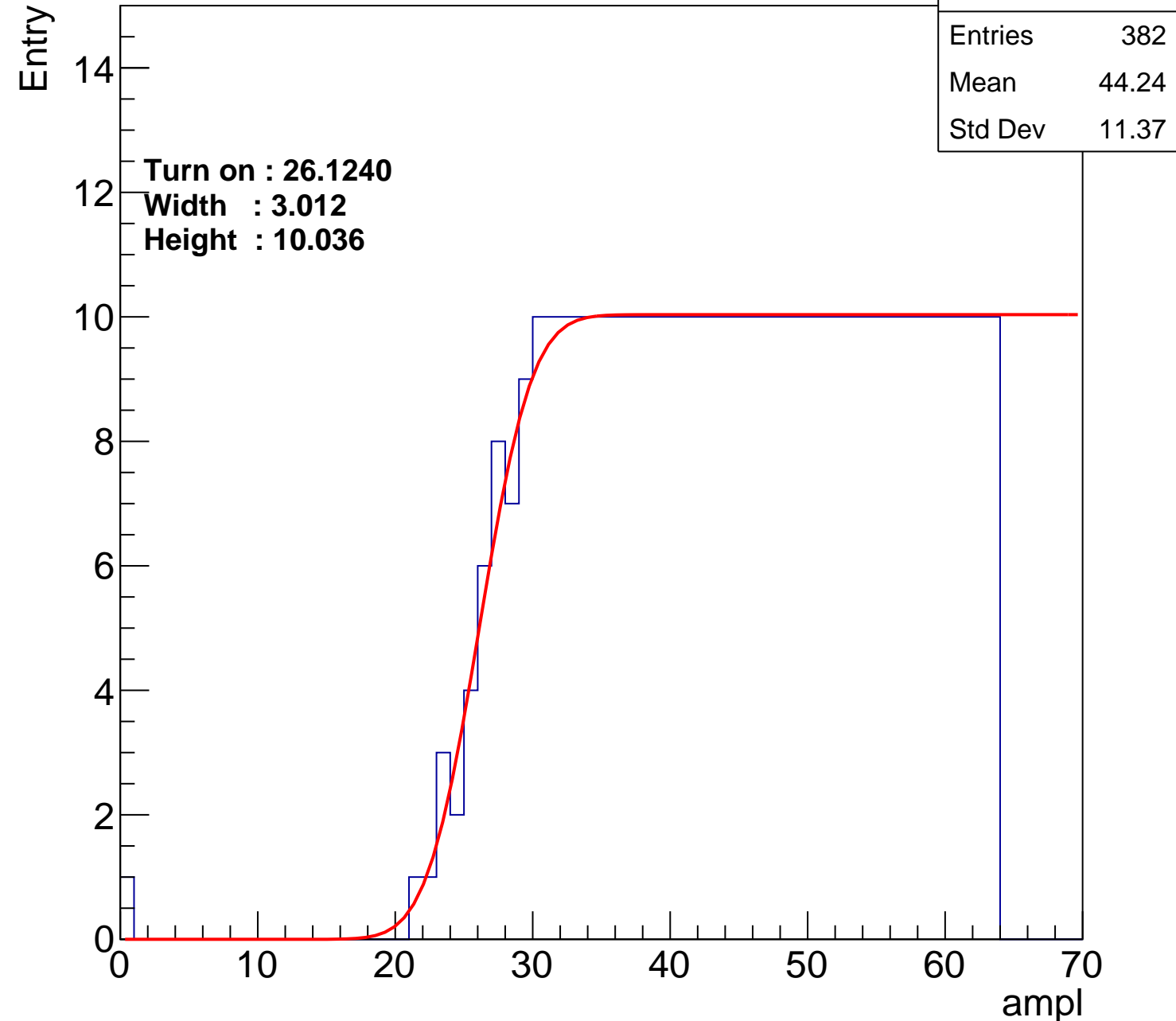
Width : 3.012

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.5946

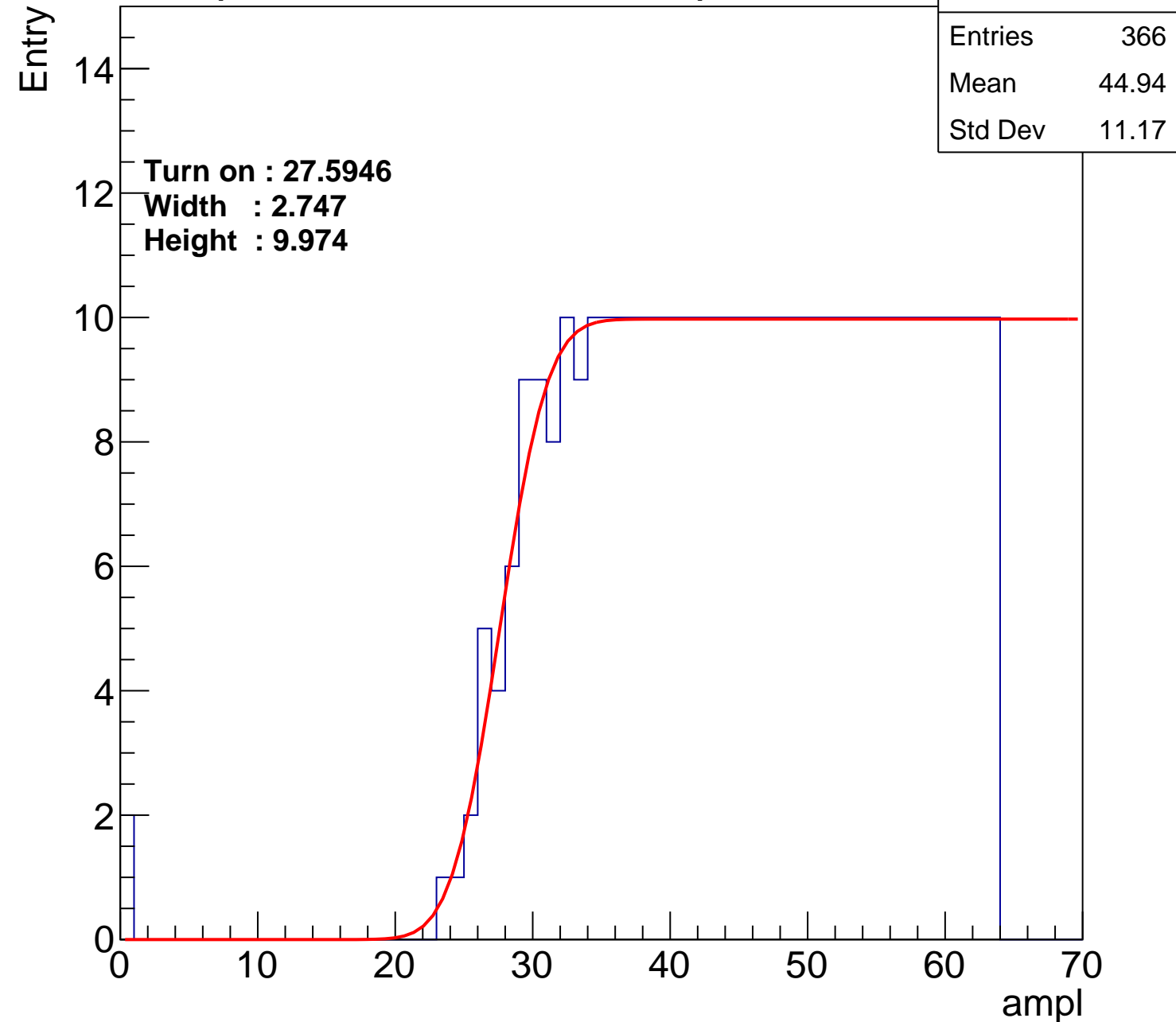
Width : 2.747

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	43.95
Std Dev	12.24

Turn on : 26.9564

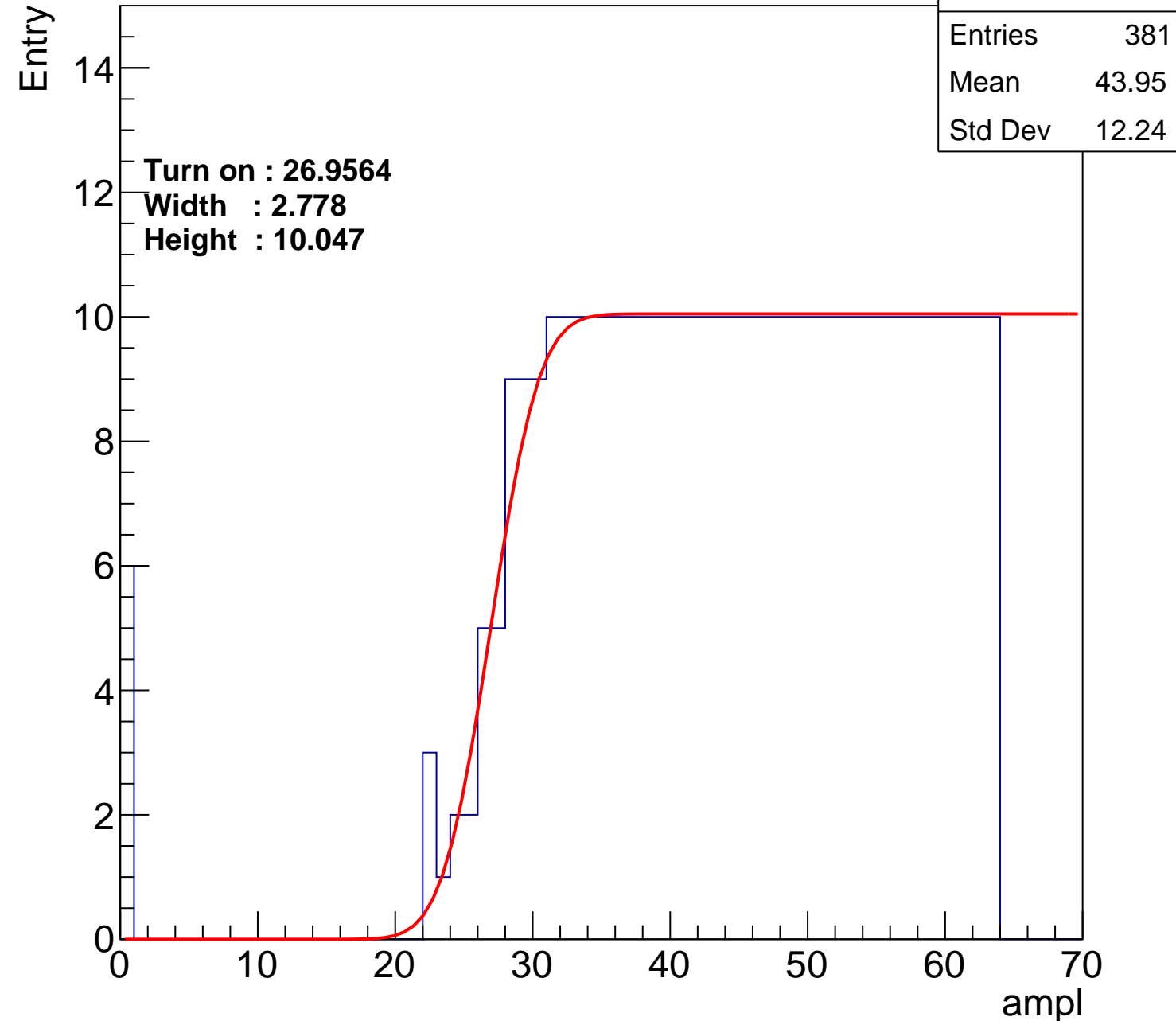
Width : 2.778

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch62

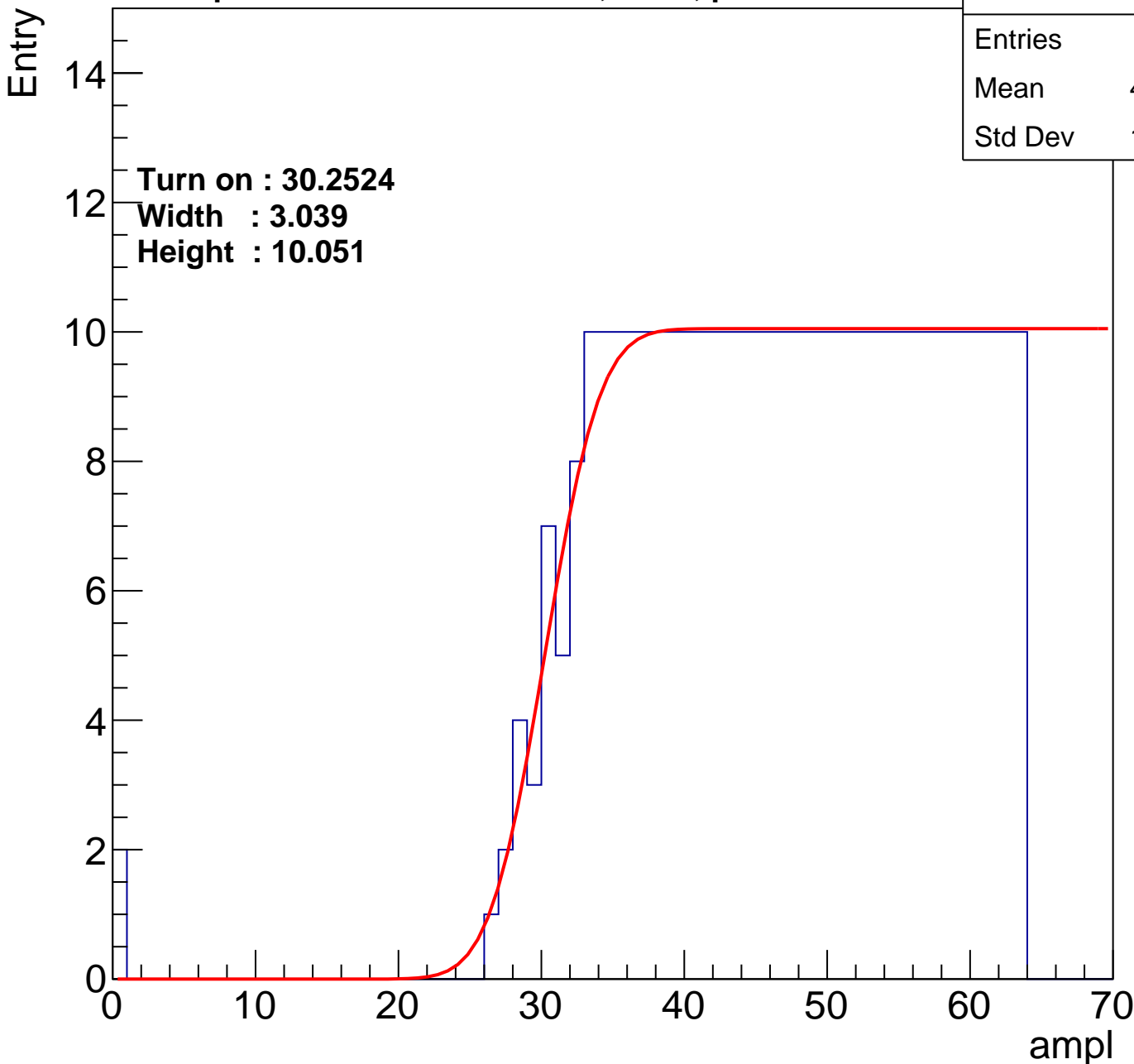
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 30.2524

Width : 3.039

Height : 10.051

Entries	342
Mean	46.14
Std Dev	10.55



B0L000S, U6-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.88
Std Dev	11.28

Turn on : 27.9491

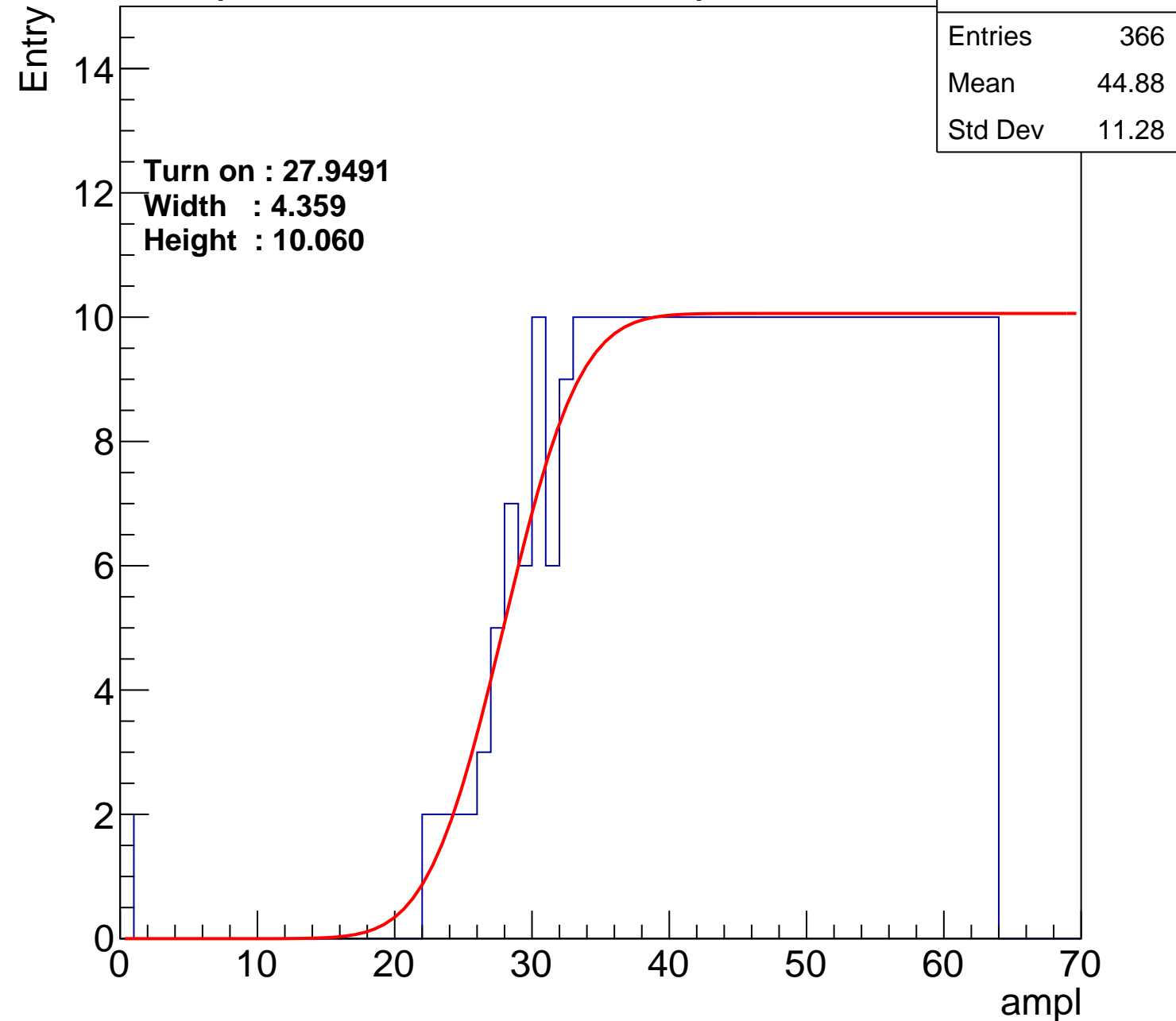
Width : 4.359

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch64

calib_packv5_042523_0143.root, FC#5, port B1

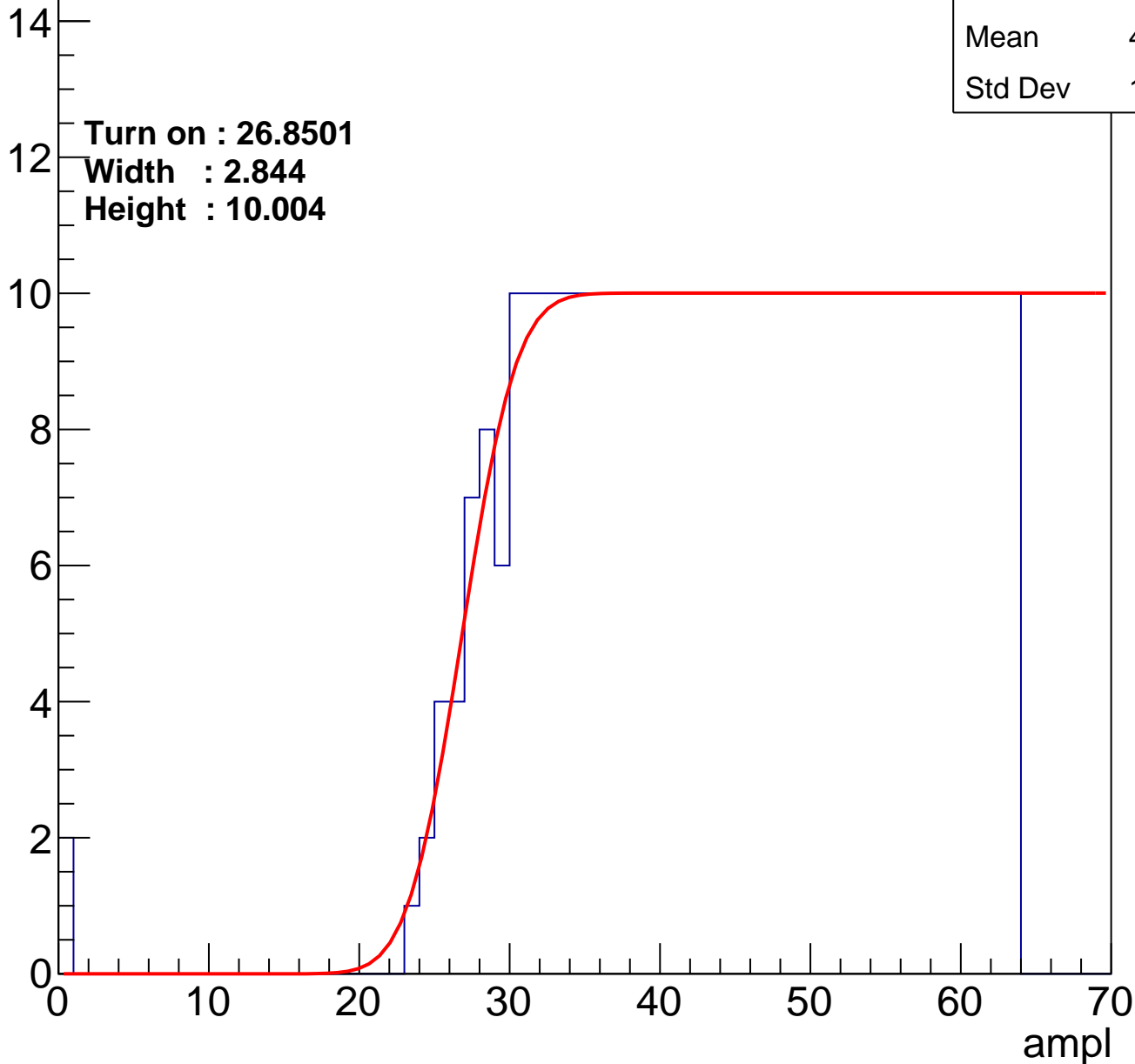
Entry

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 26.8501

Width : 2.844

Height : 10.004



B0L000S, U6-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.62
Std Dev	11.64

Turn on : 27.4484

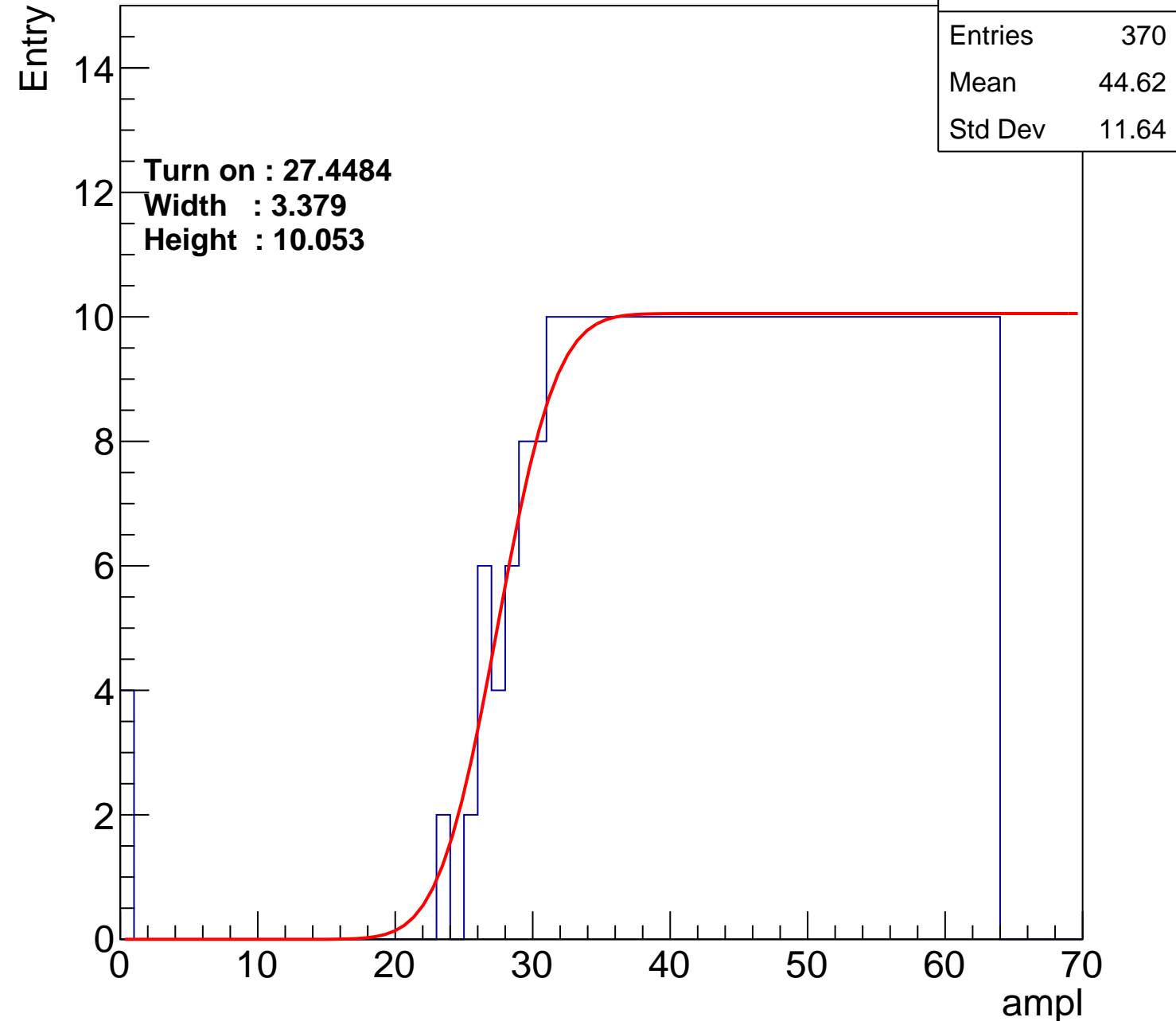
Width : 3.379

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch66

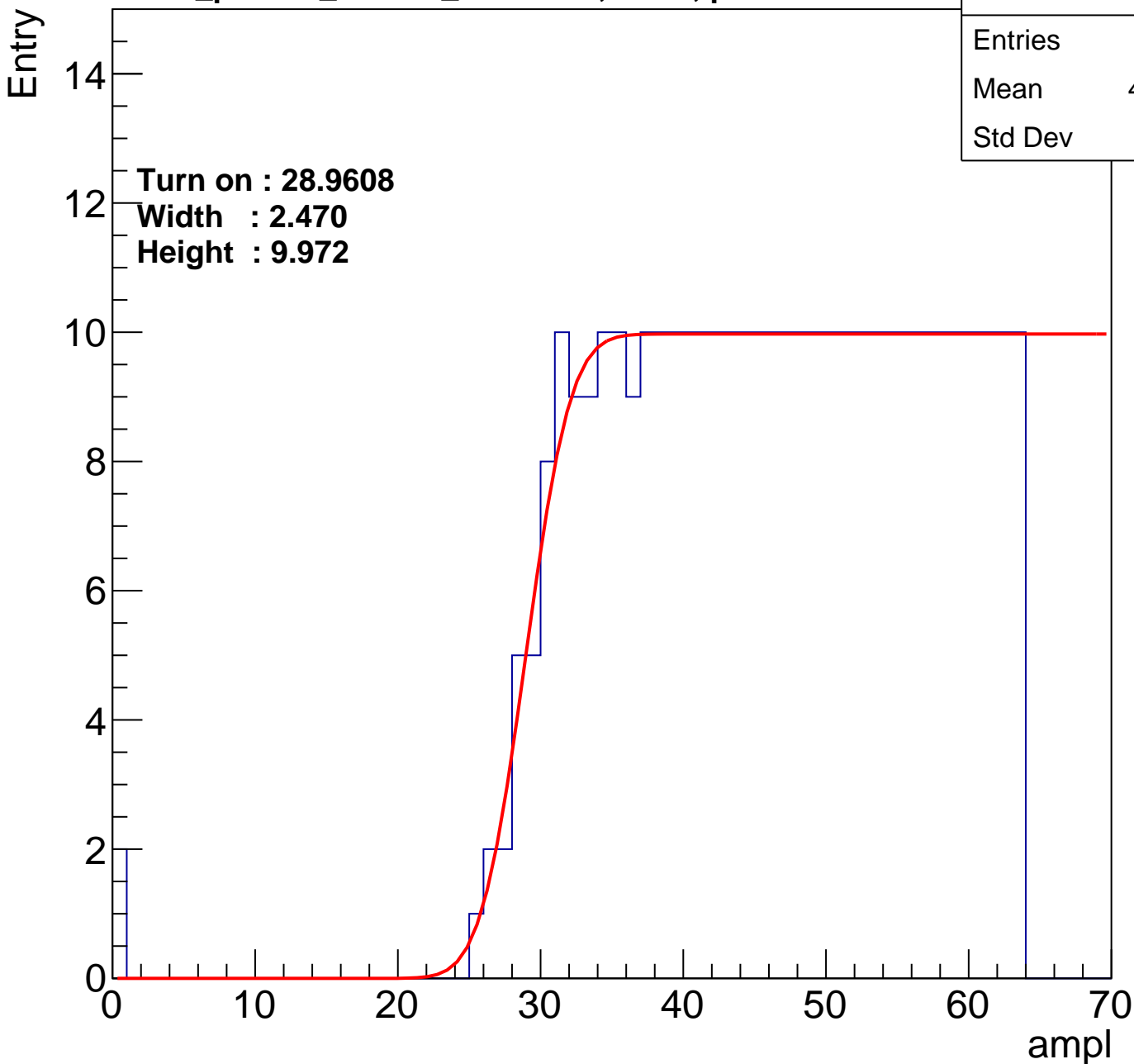
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 28.9608

Width : 2.470

Height : 9.972

Entries	352
Mean	45.64
Std Dev	10.8



B0L000S, U6-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.45
Std Dev	11.42

Turn on : 27.0136

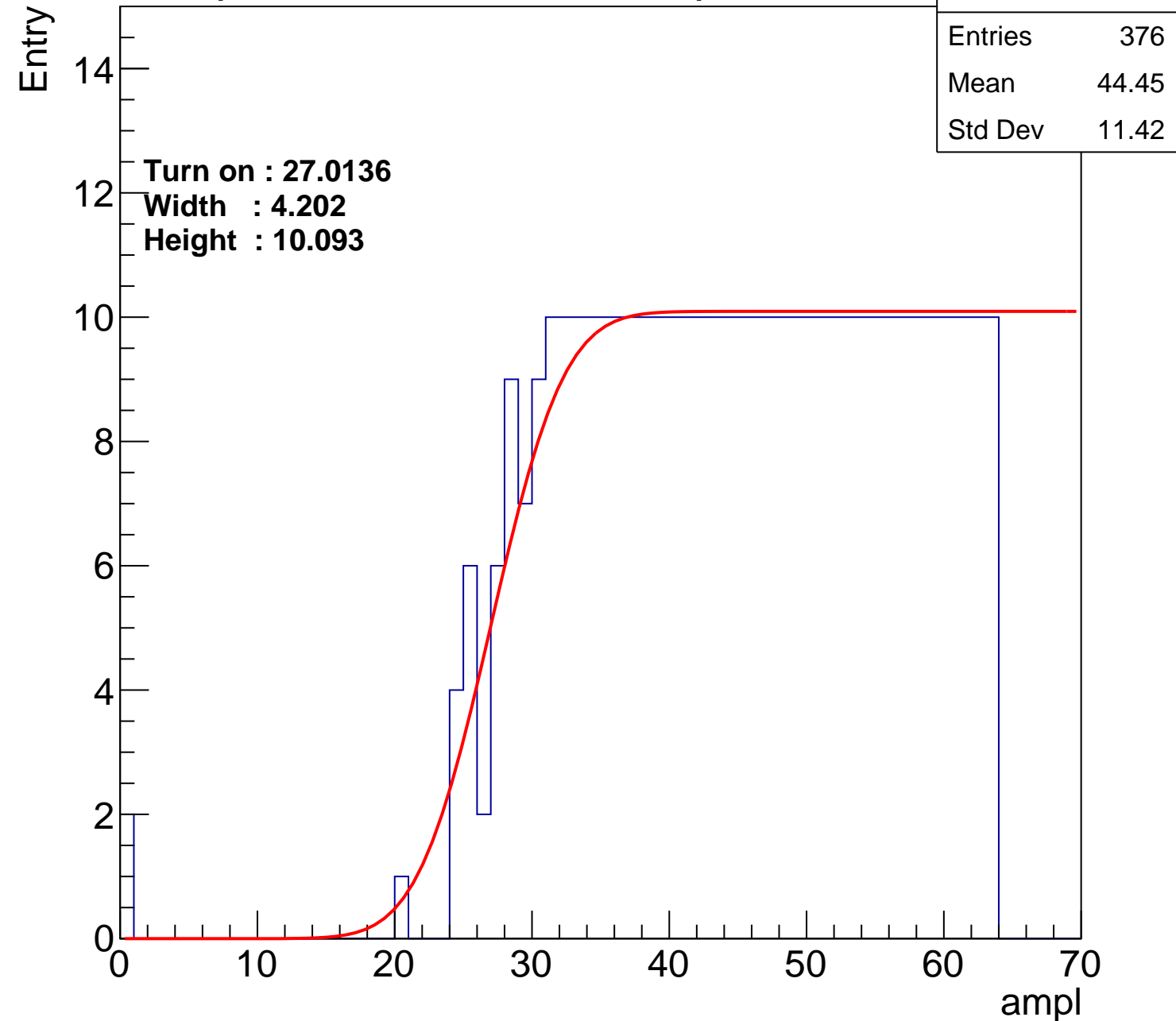
Width : 4.202

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.13
Std Dev	11.23

Turn on : 28.6561

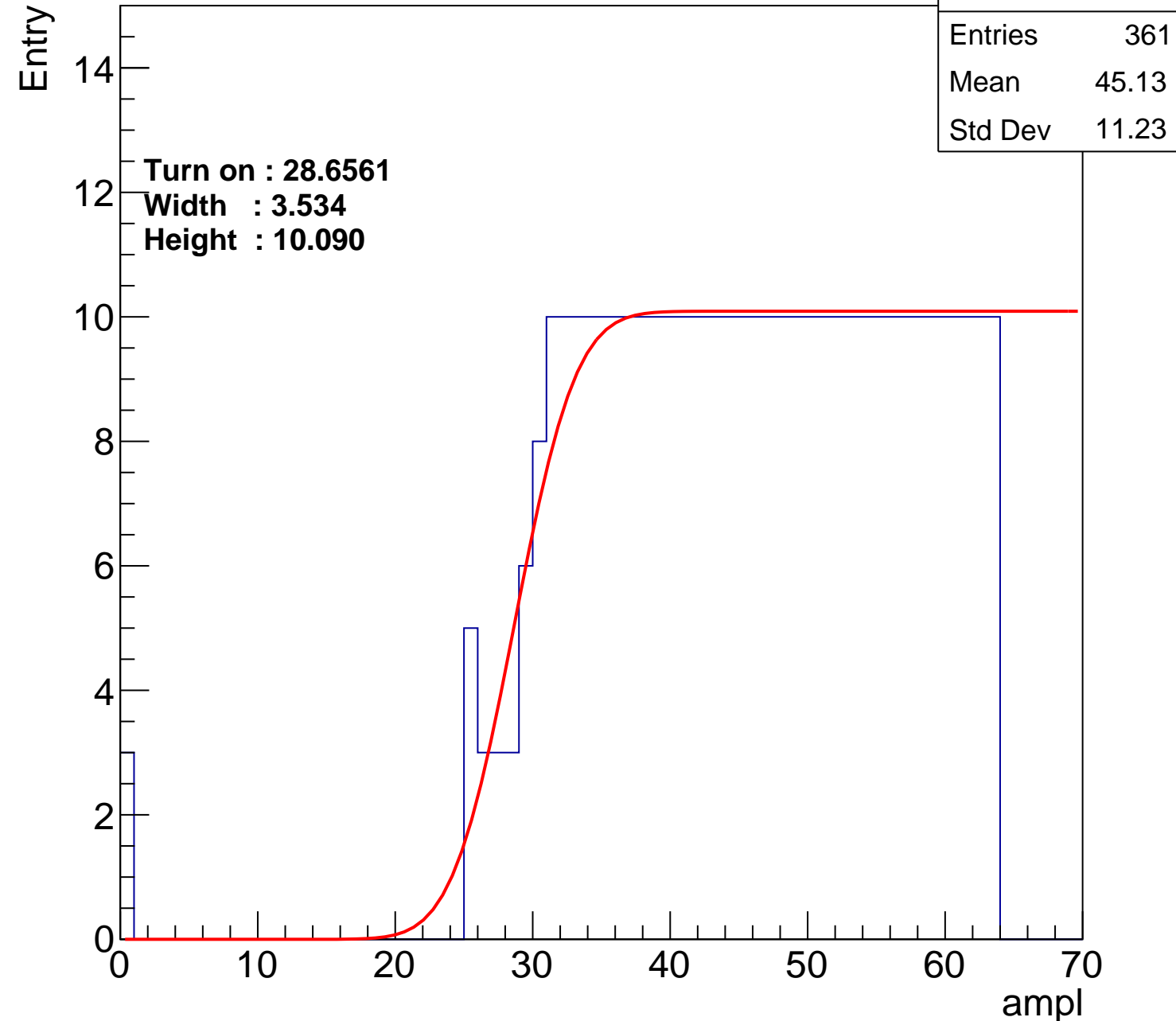
Width : 3.534

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.69
Std Dev	10.66

Turn on : 28.7350

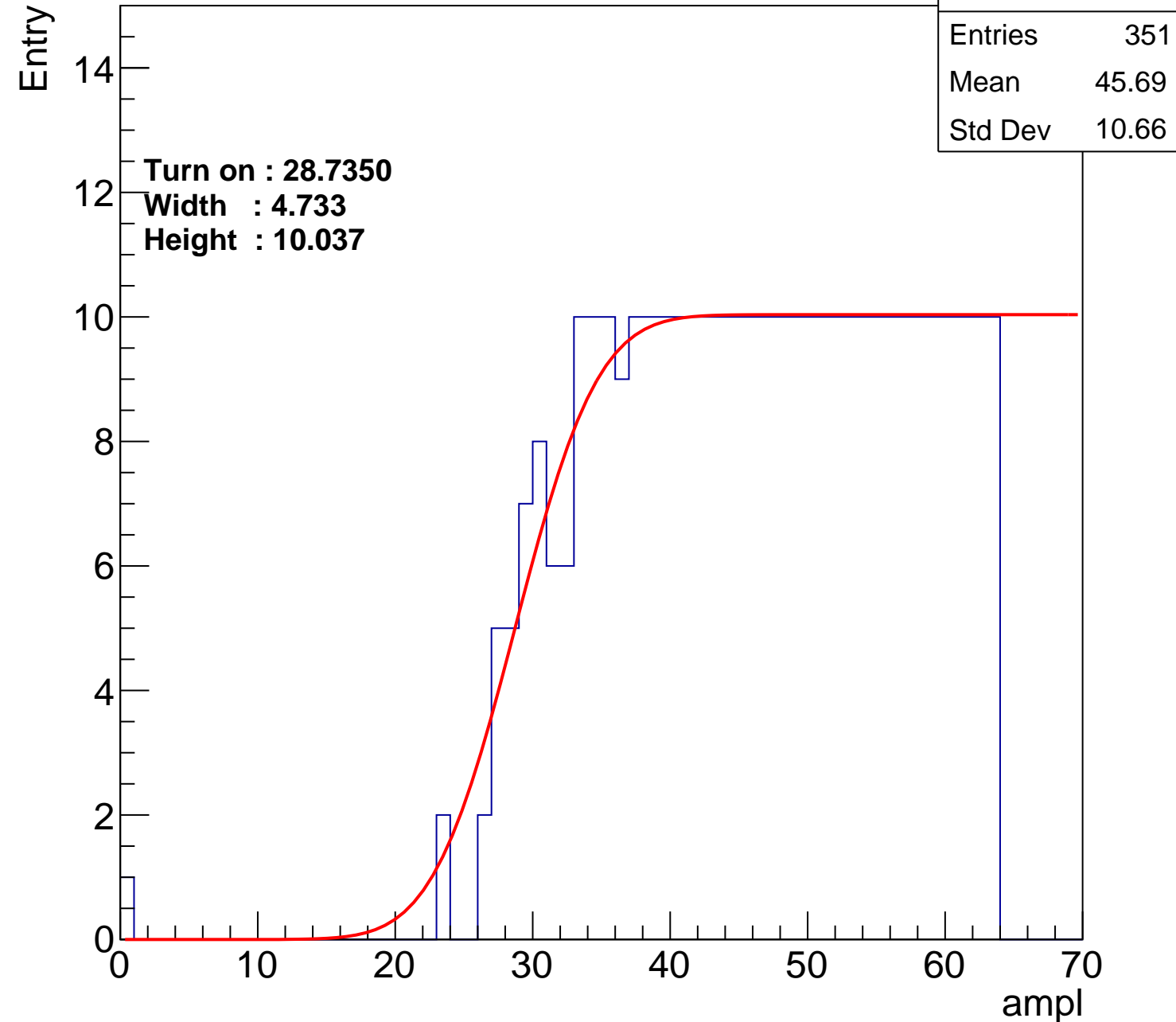
Width : 4.733

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 26.7168

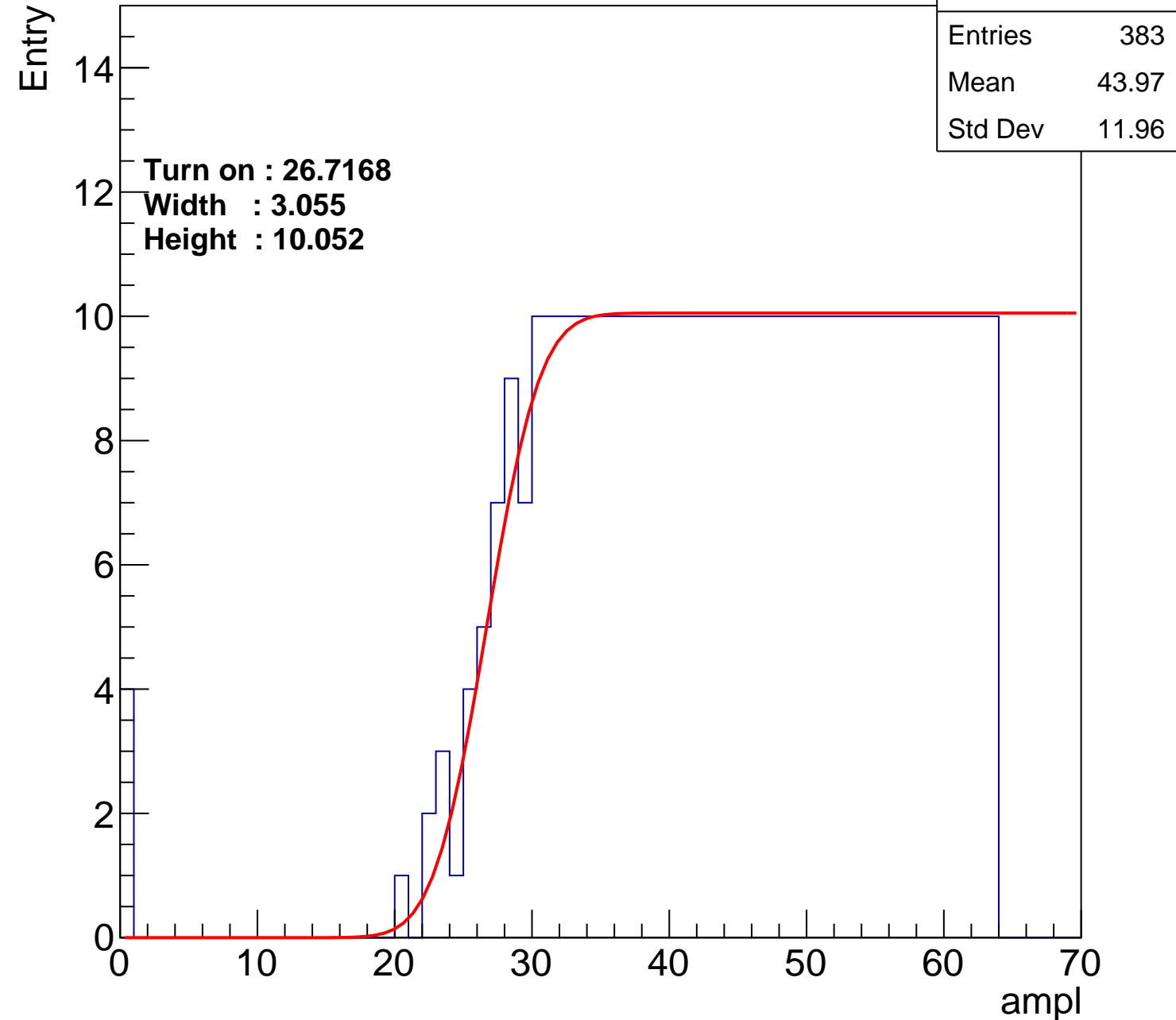
Width : 3.055

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.4179

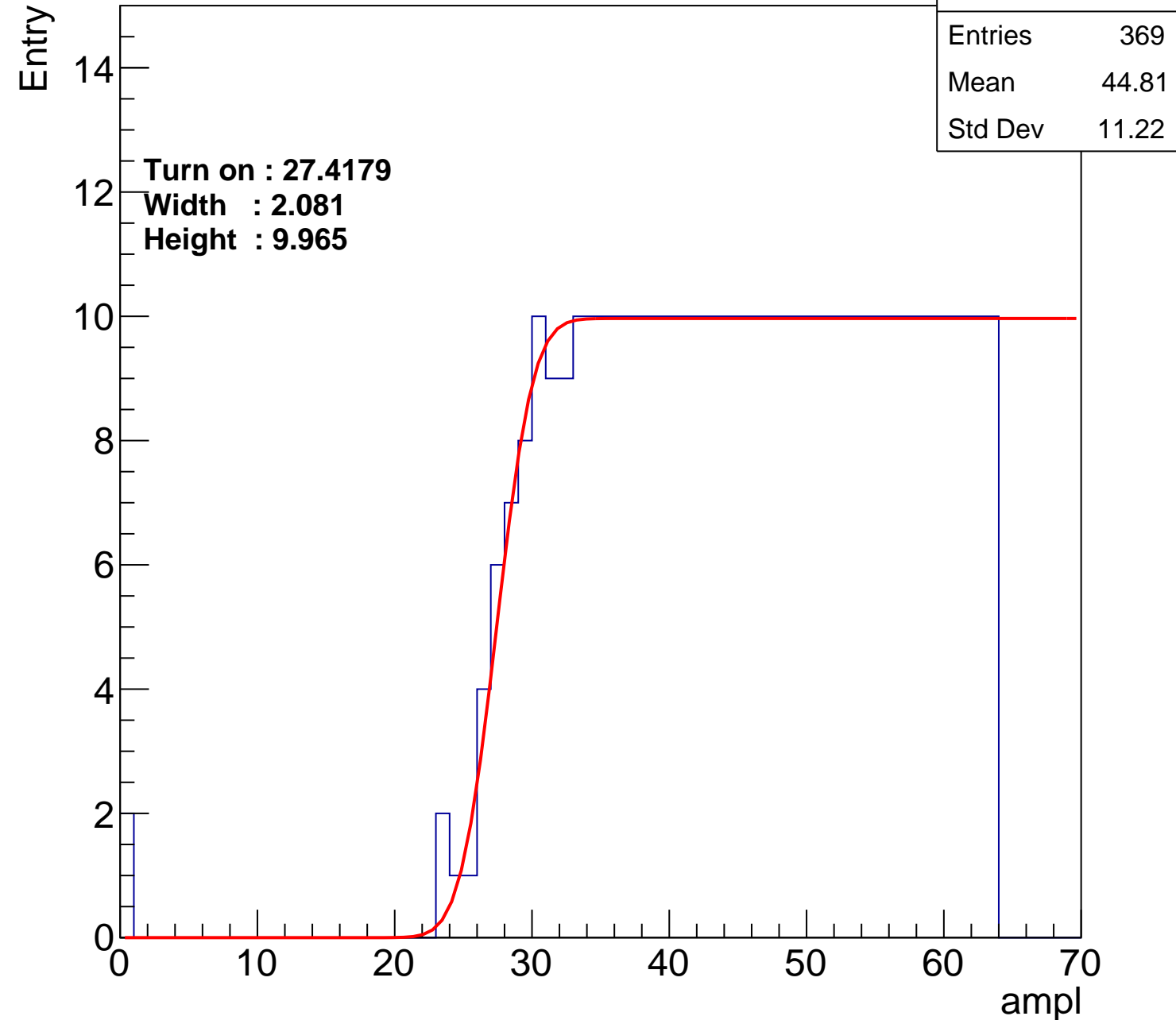
Width : 2.081

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 26.5207

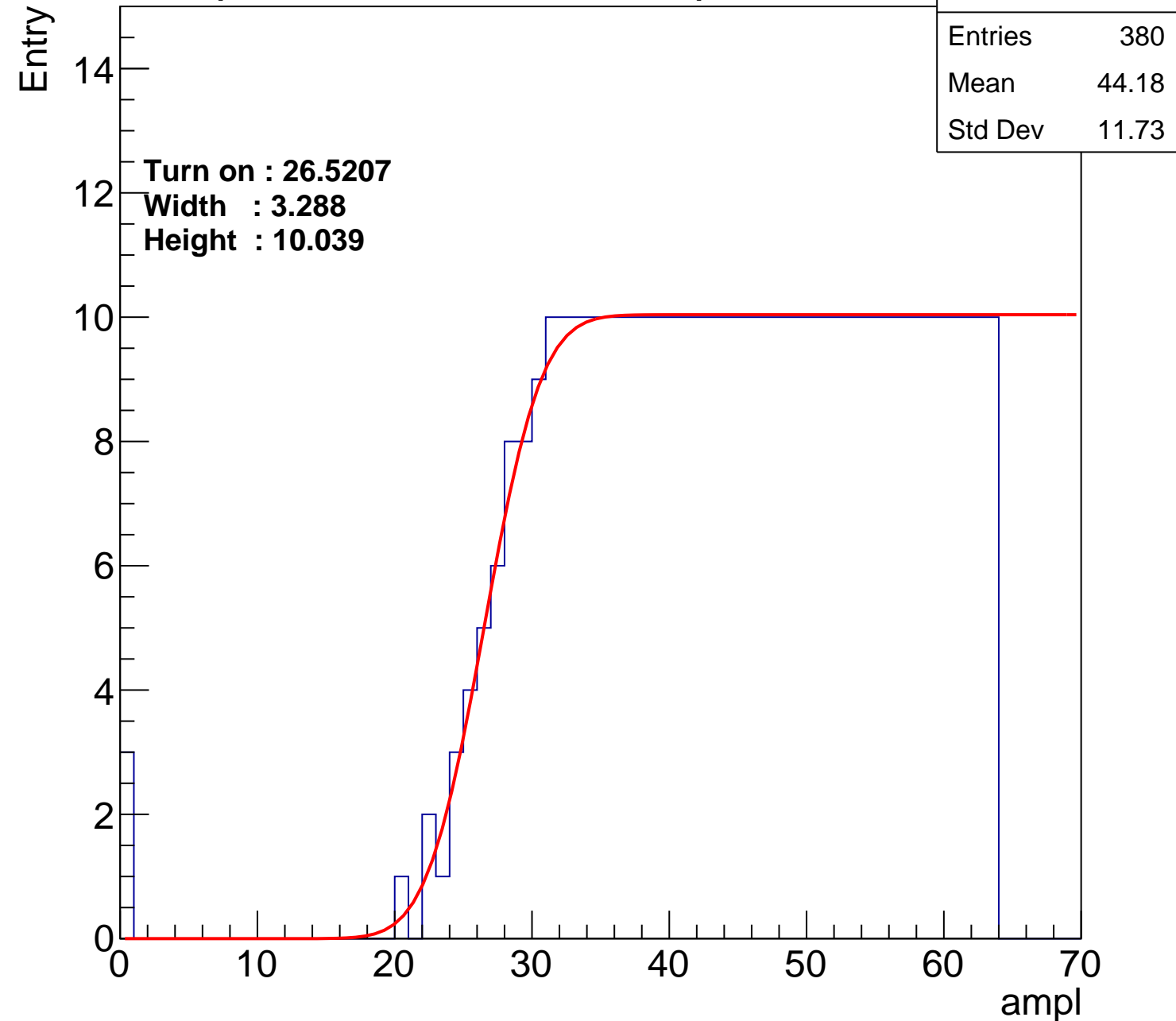
Width : 3.288

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.02
Std Dev	11.1

Turn on : 27.5482

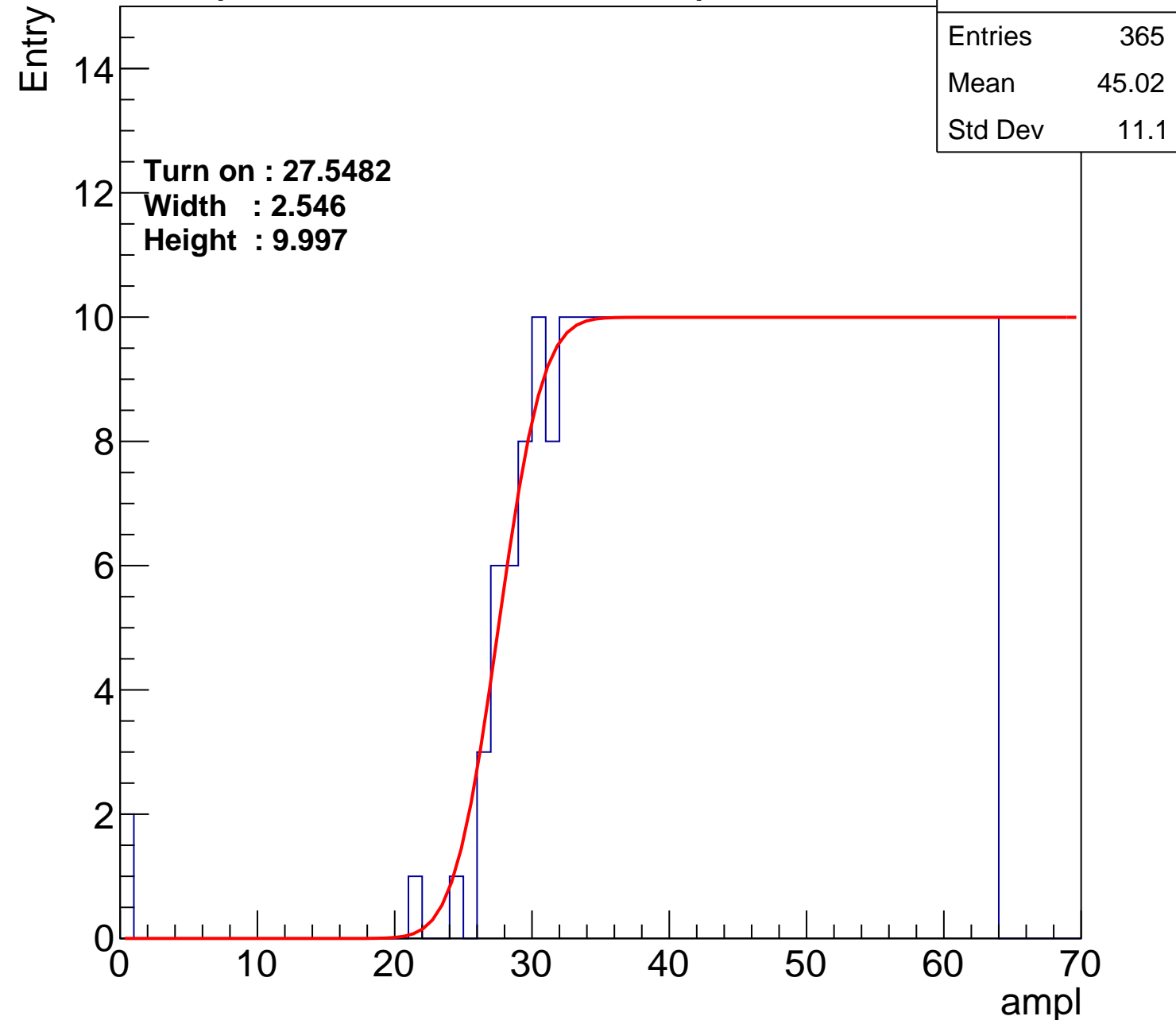
Width : 2.546

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 26.1817

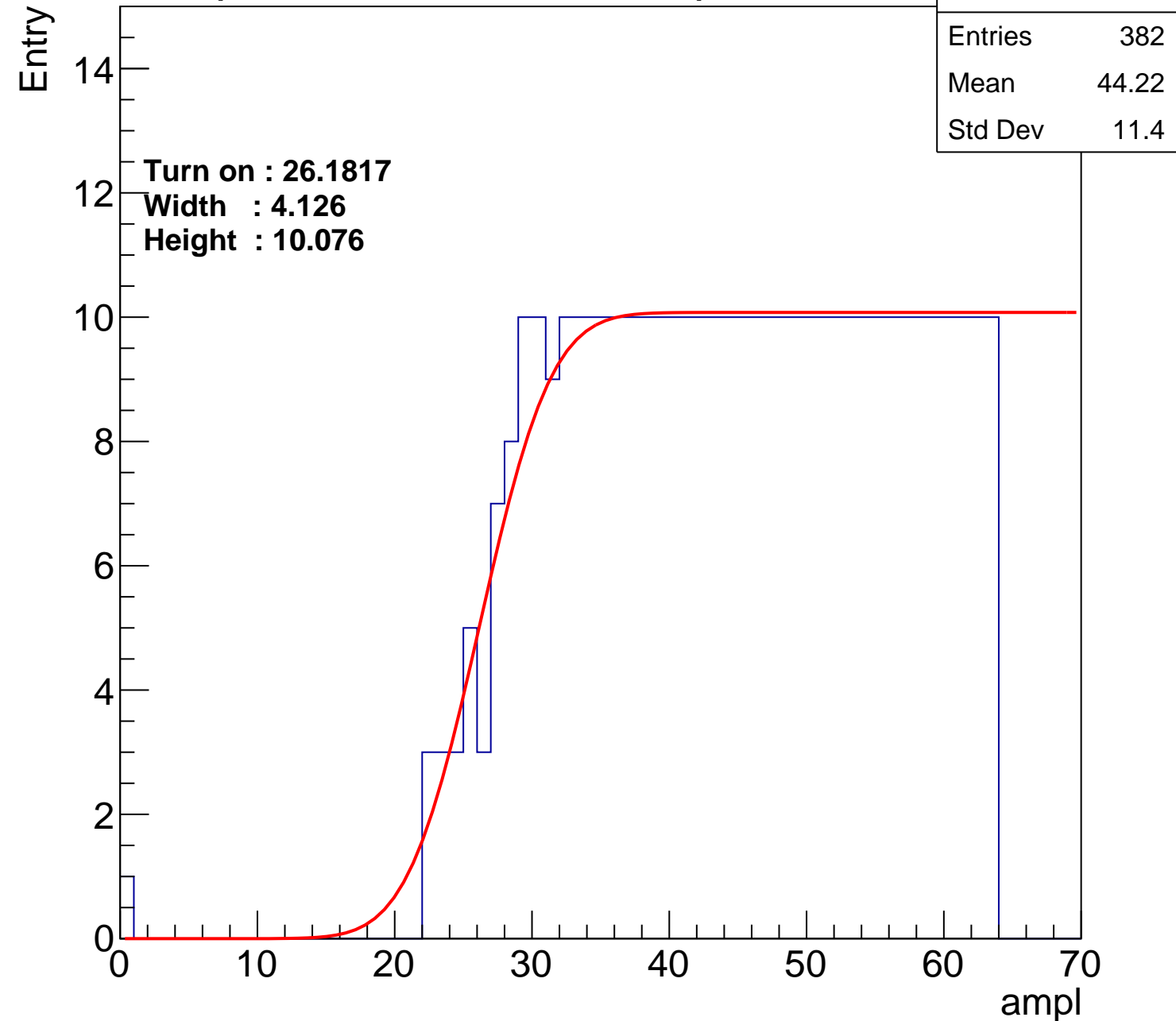
Width : 4.126

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.52
Std Dev	10.89

Turn on : 29.1033

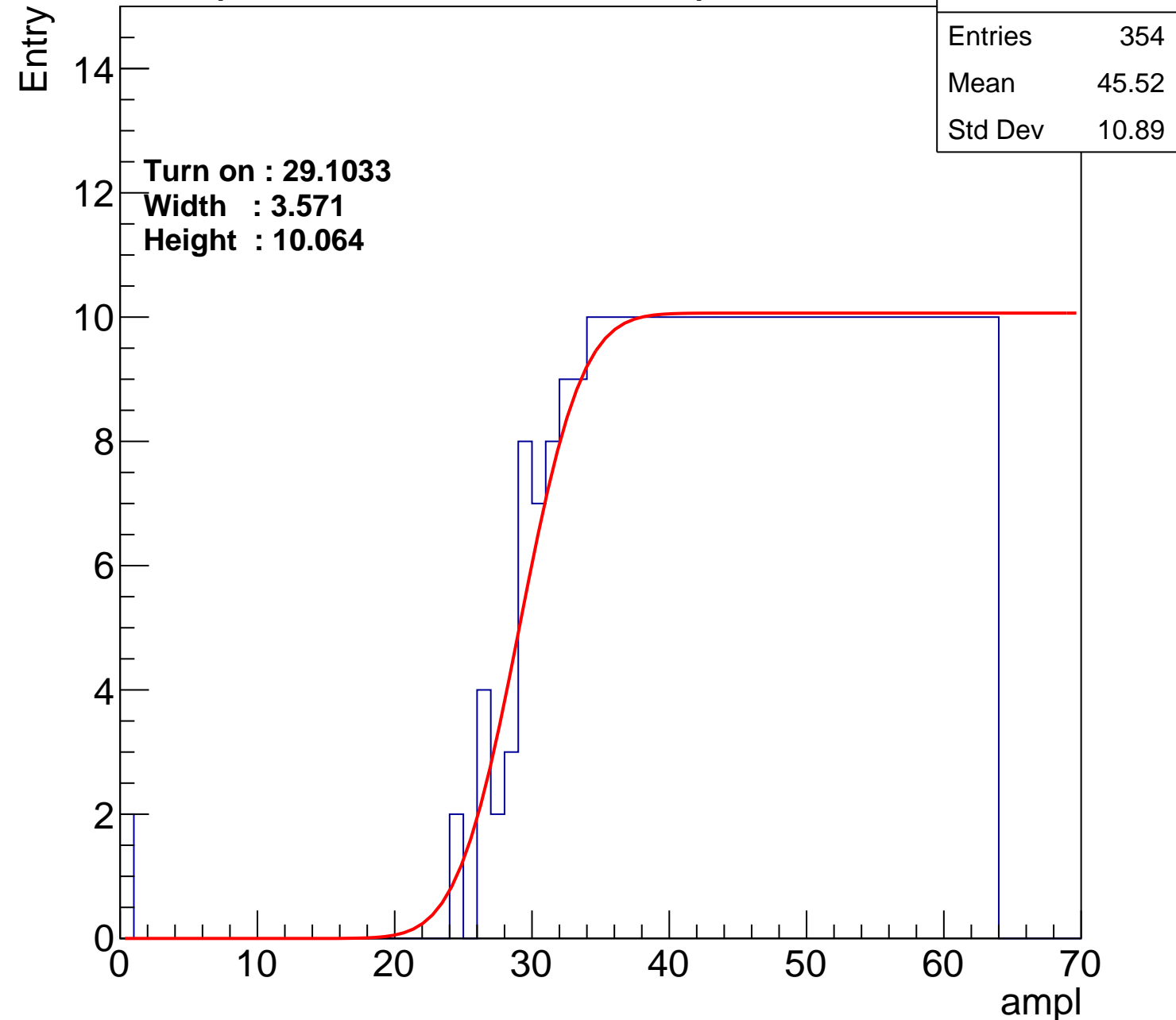
Width : 3.571

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch76

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.6291

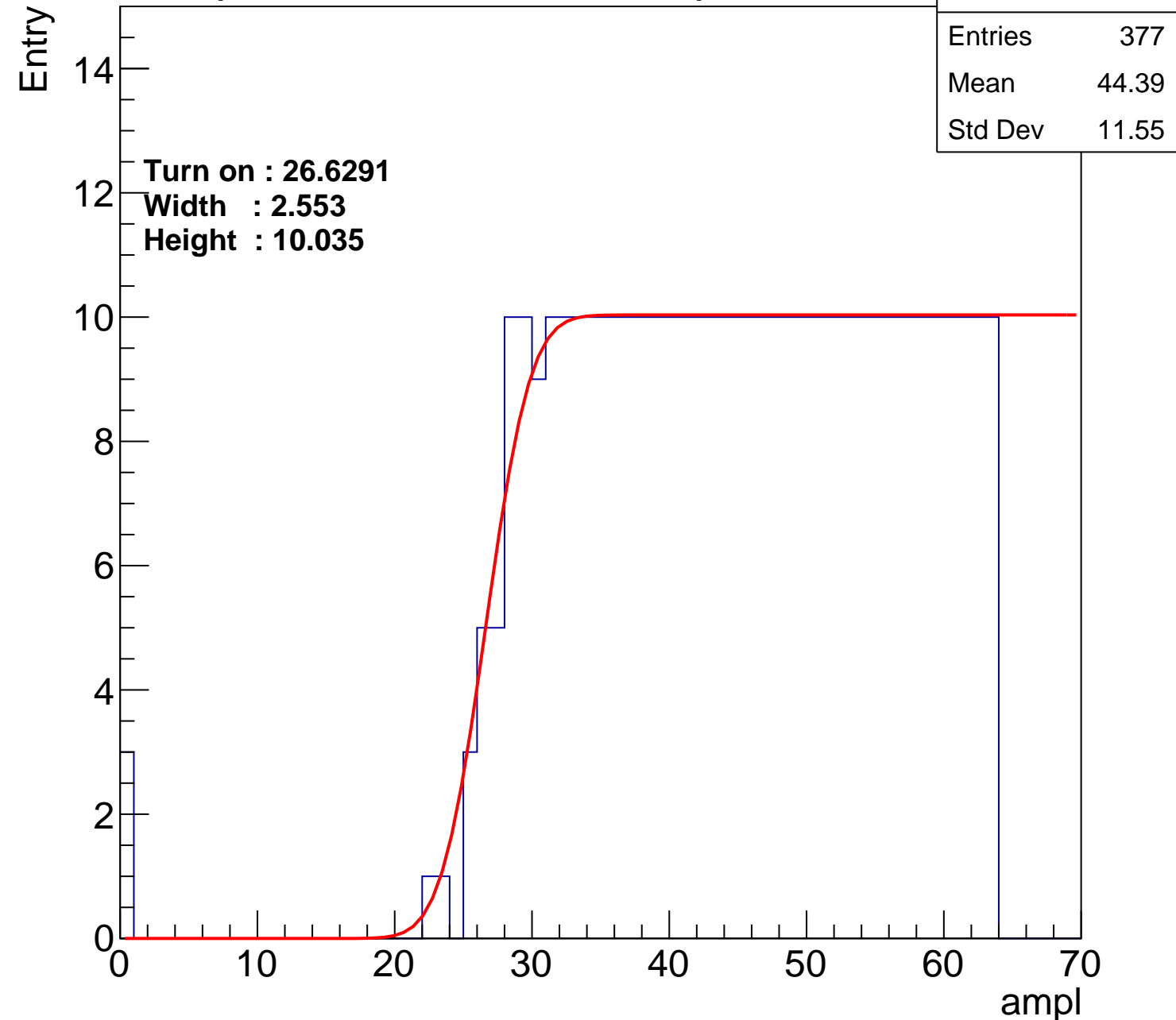
Width : 2.553

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 28.0465

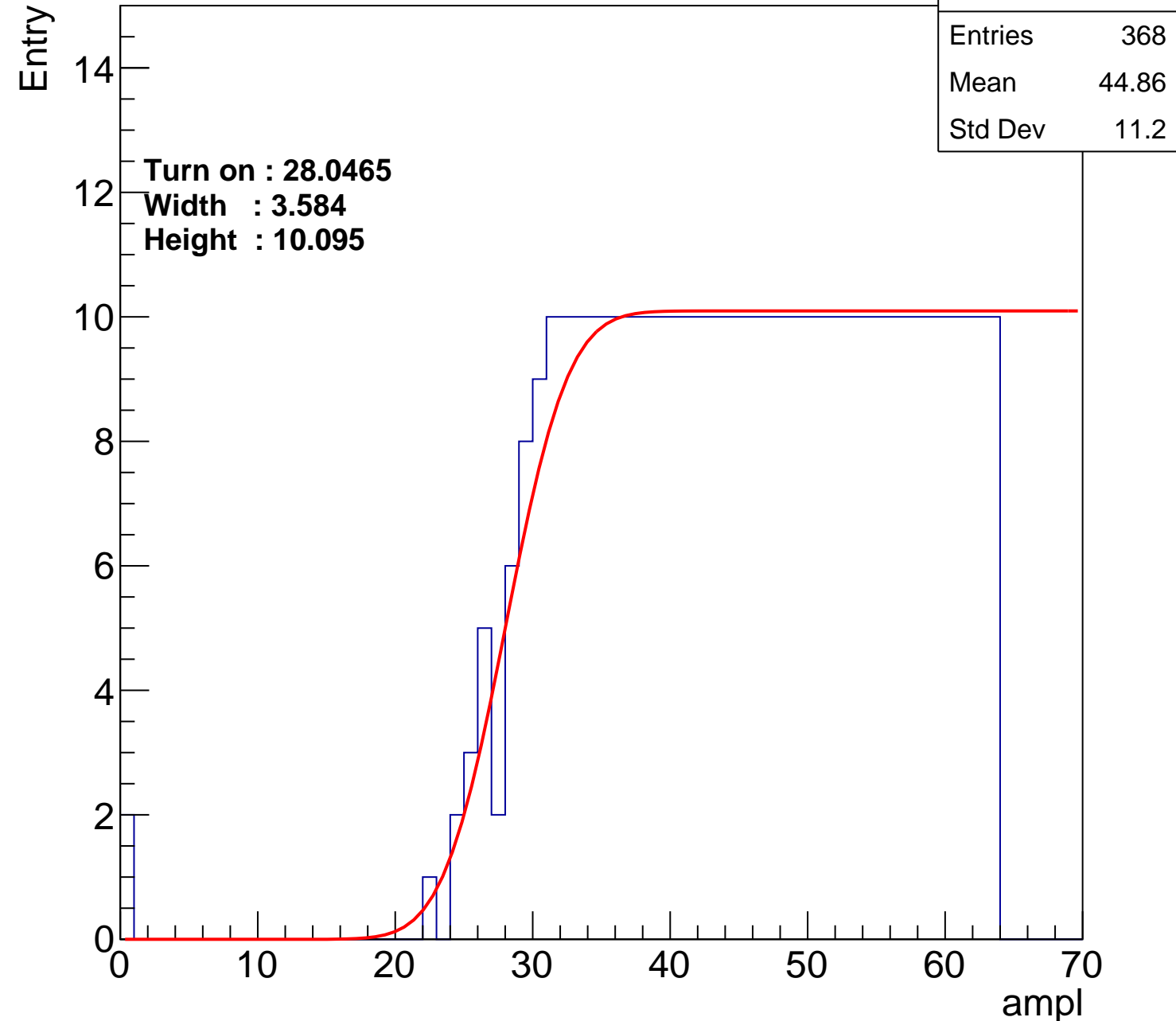
Width : 3.584

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.46
Std Dev	11.46

Turn on : 27.0979

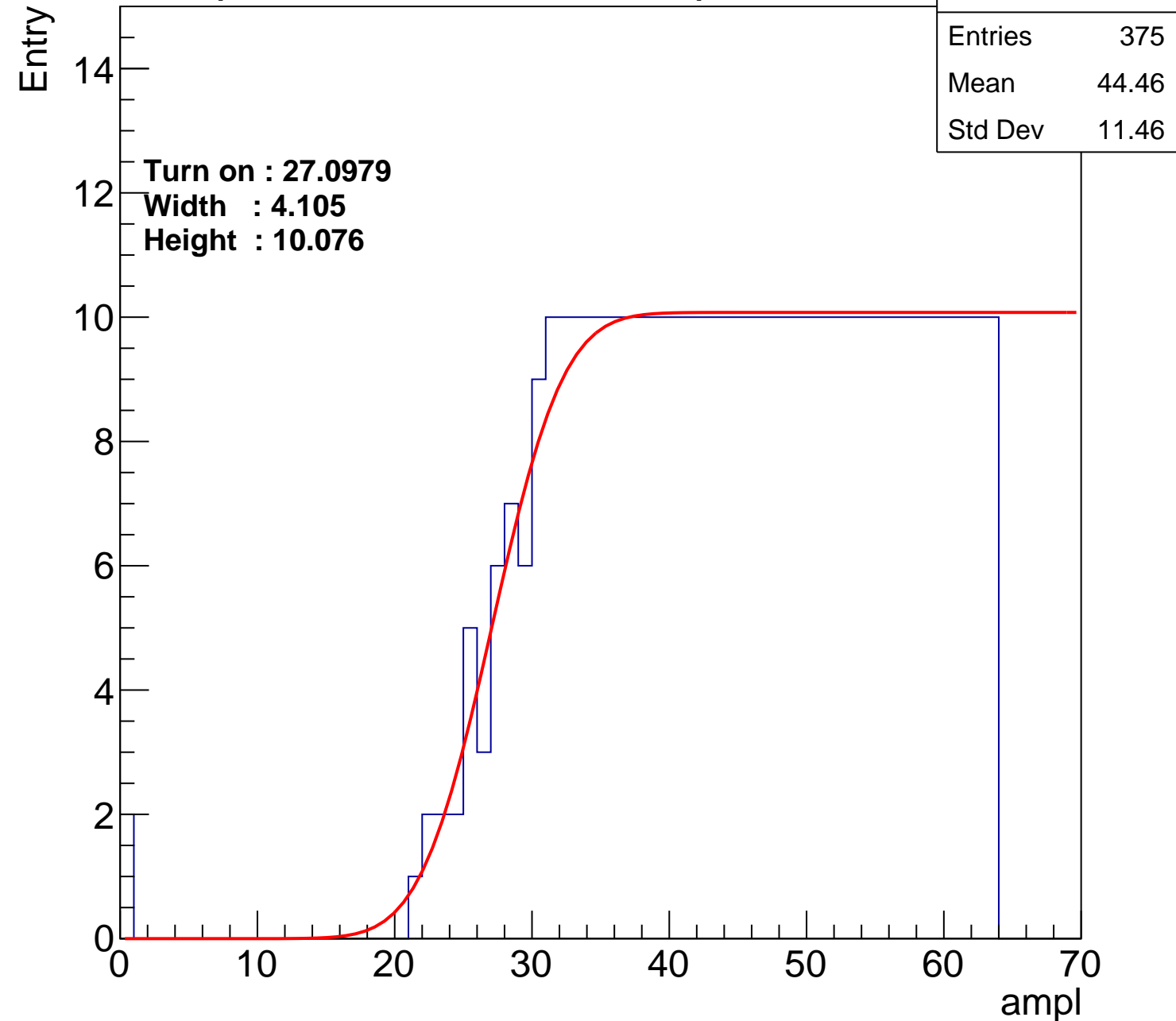
Width : 4.105

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch79

calib_packv5_042523_0143.root, FC#5, port B1

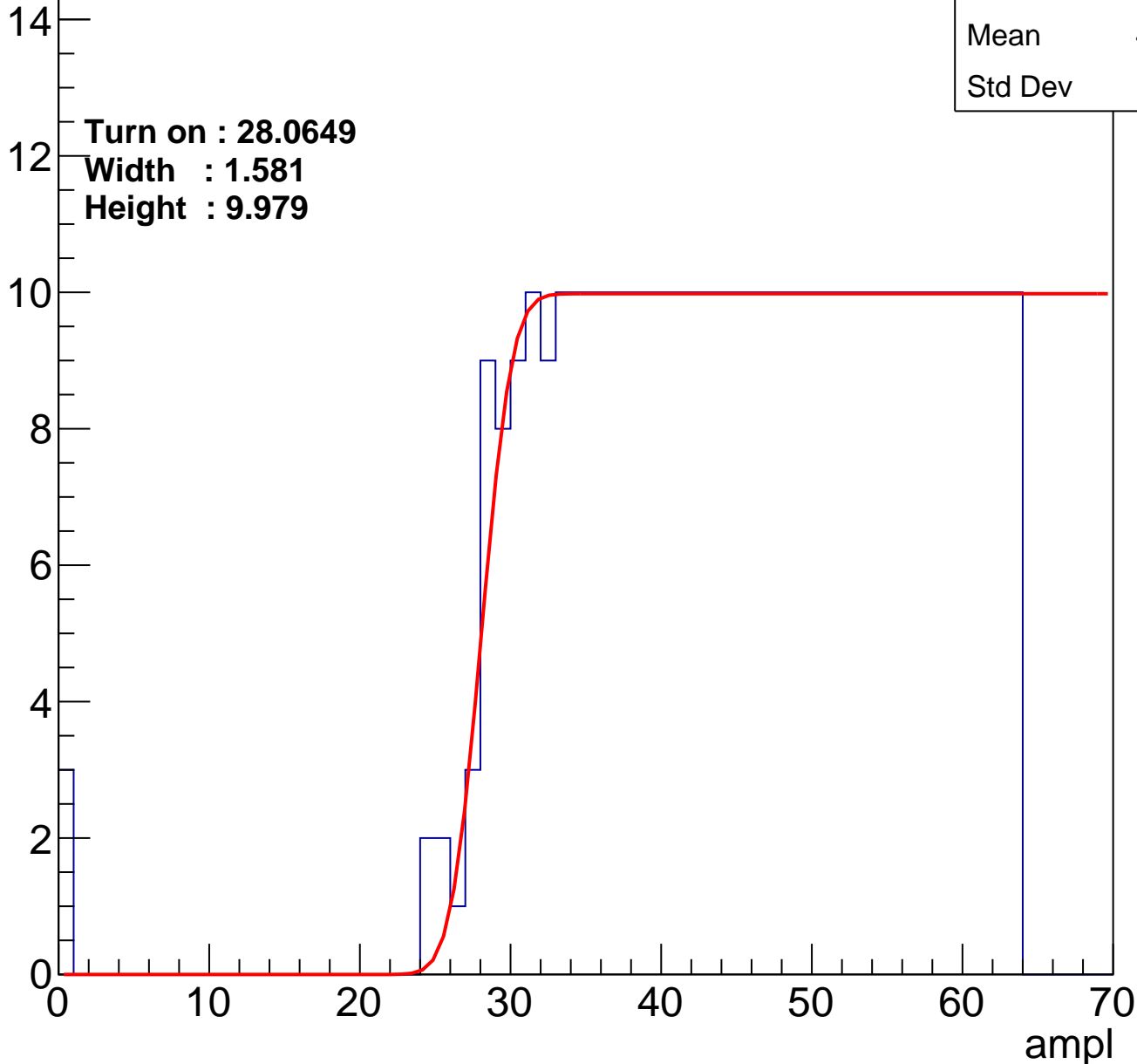
Entry

Entries	366
Mean	44.91
Std Dev	11.31

Turn on : 28.0649

Width : 1.581

Height : 9.979



B0L000S, U6-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.24
Std Dev	11.43

Turn on : 26.1710

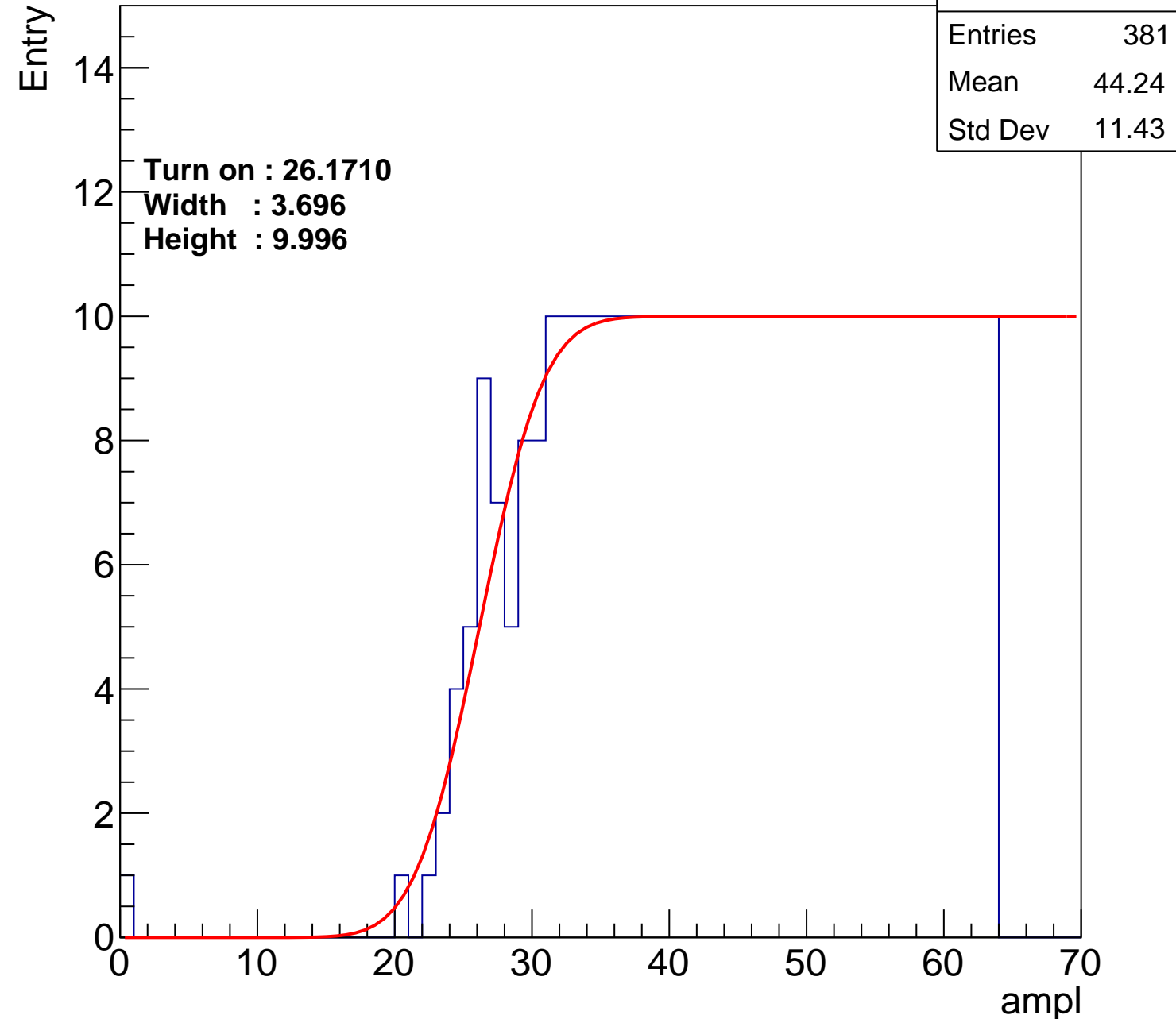
Width : 3.696

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.7695

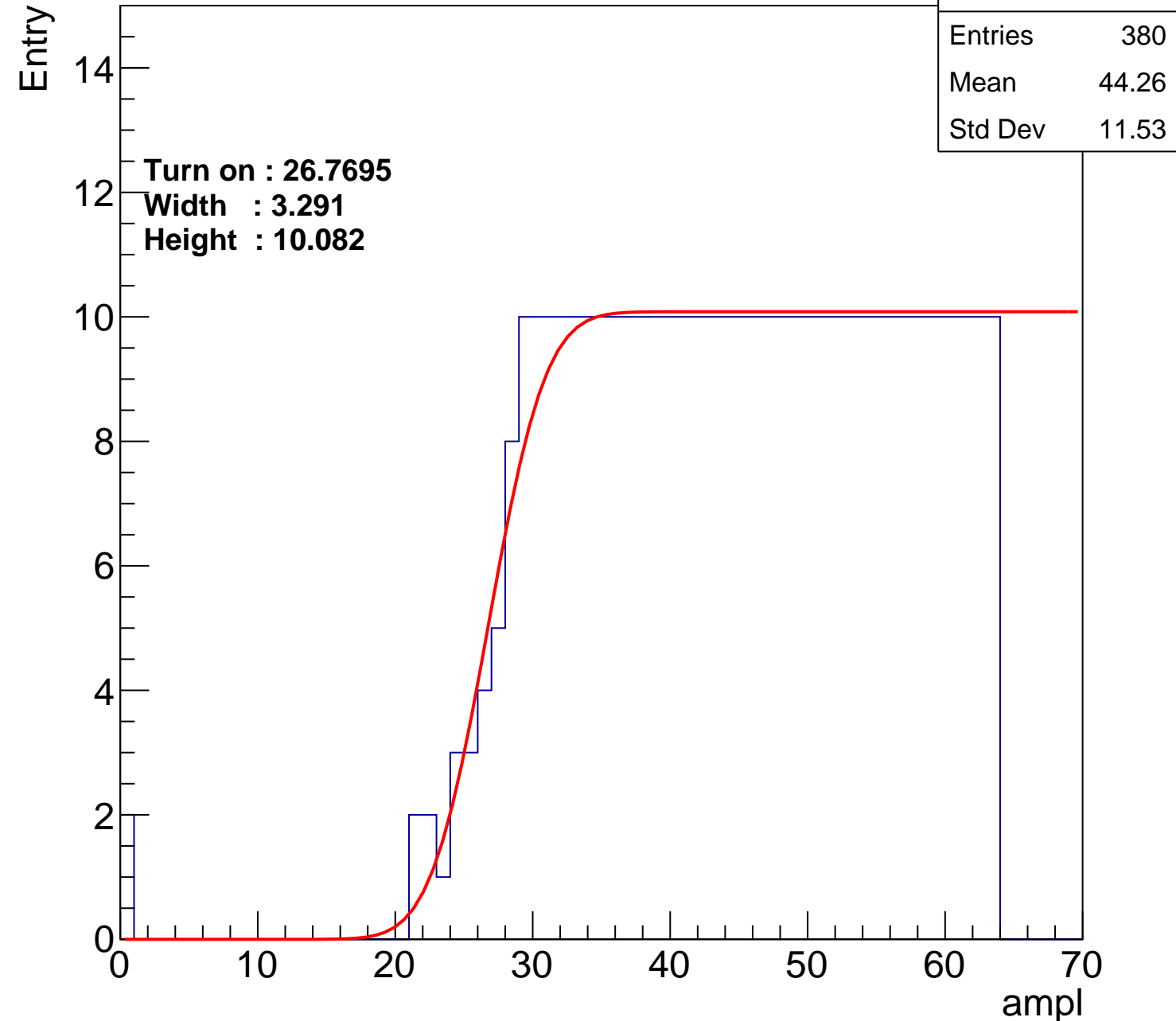
Width : 3.291

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch82

calib_packv5_042523_0143.root, FC#5, port B1

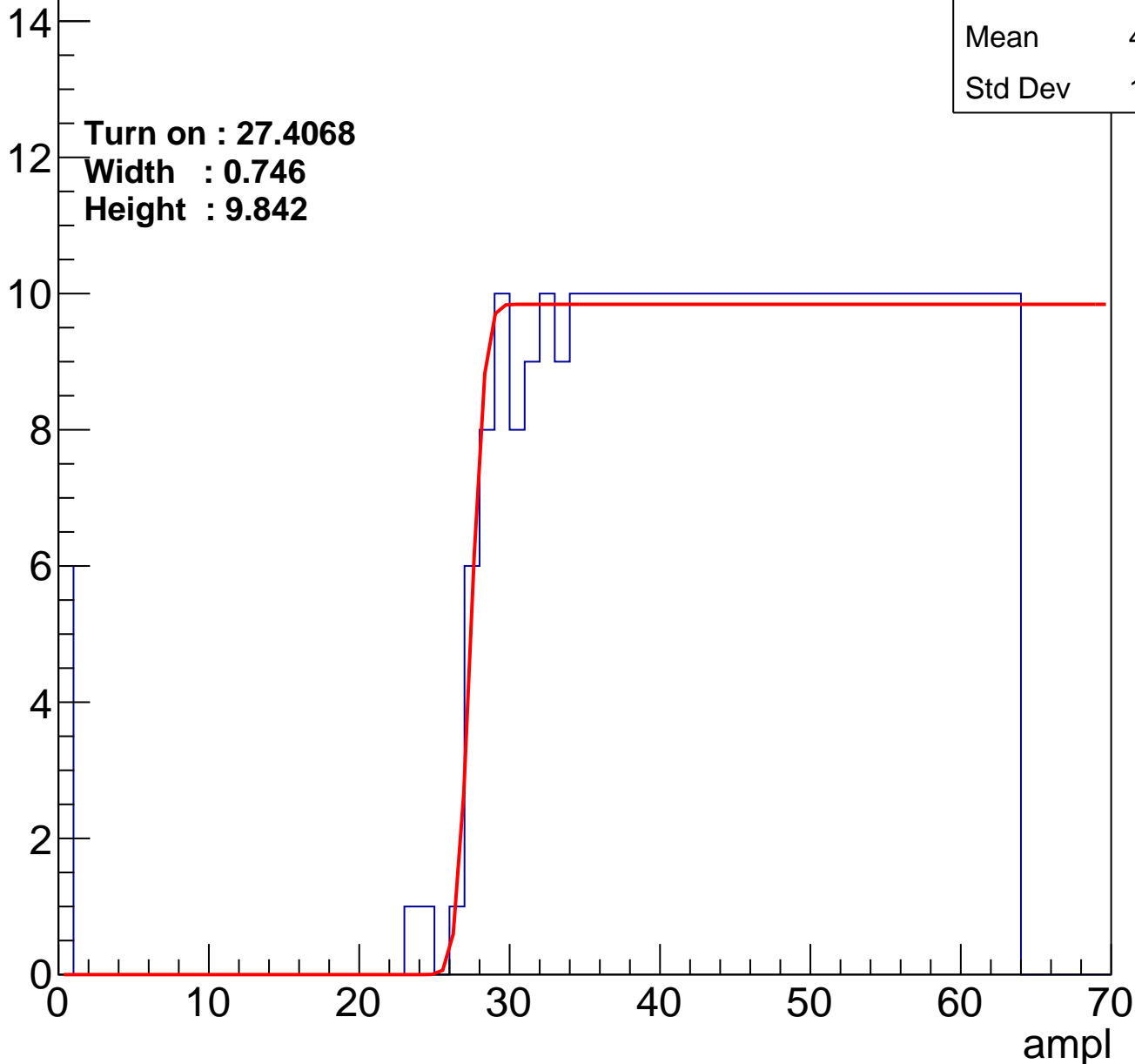
Entry

Entries	369
Mean	44.54
Std Dev	11.97

Turn on : 27.4068

Width : 0.746

Height : 9.842



B0L000S, U6-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.92
Std Dev	11.5

Turn on : 28.3492

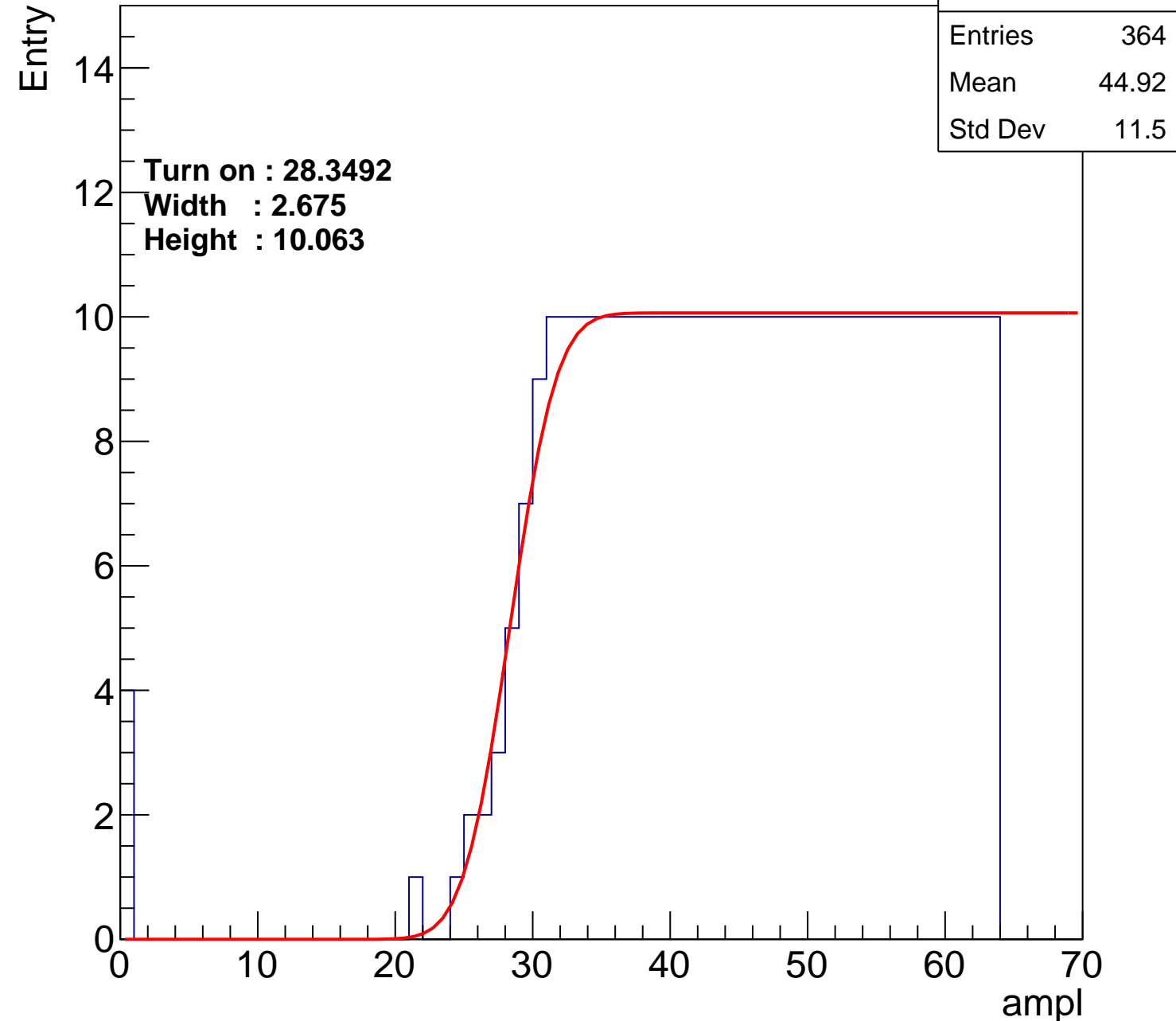
Width : 2.675

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.5806

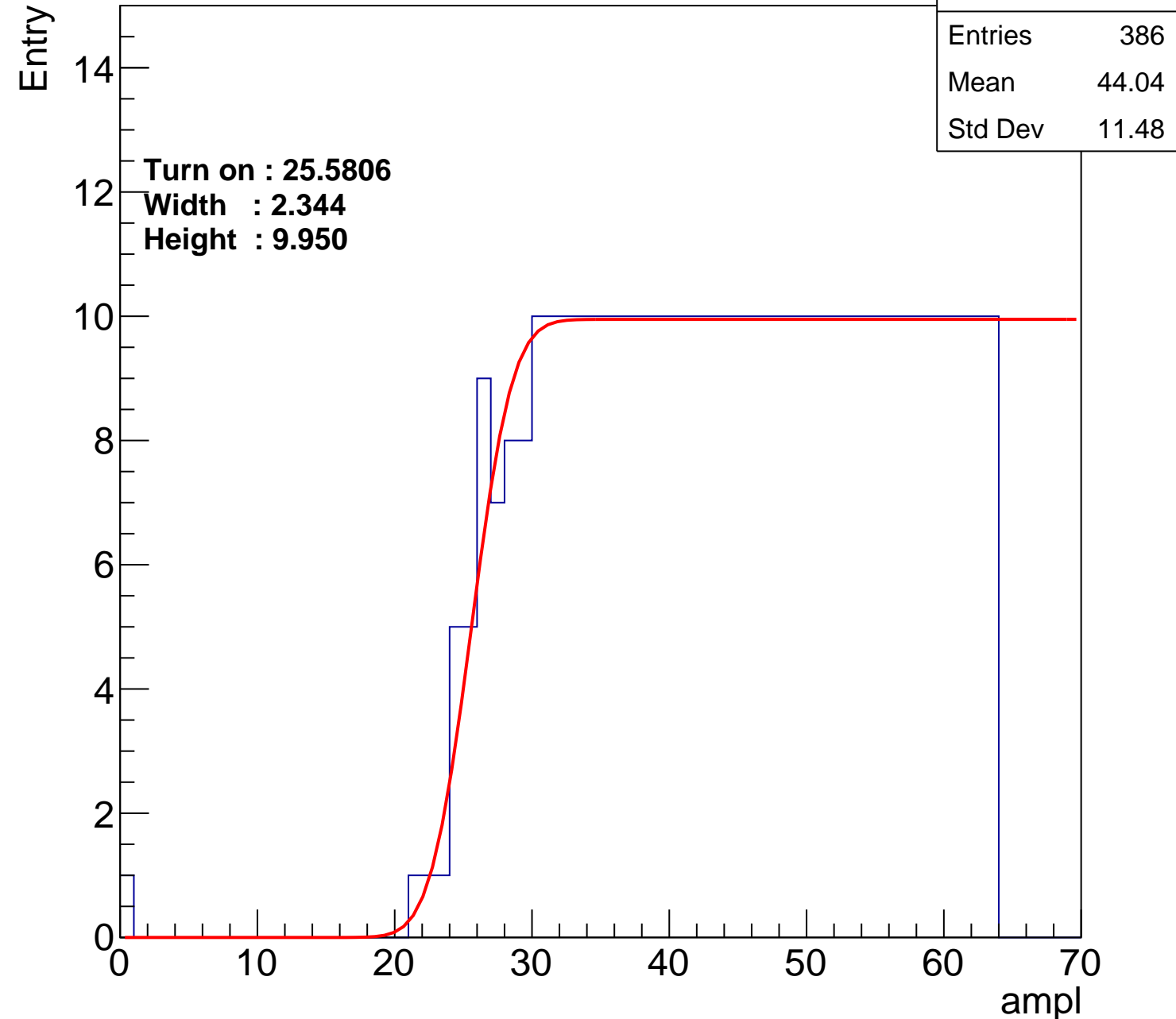
Width : 2.344

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.03
Std Dev	11.28

Turn on : 28.0644

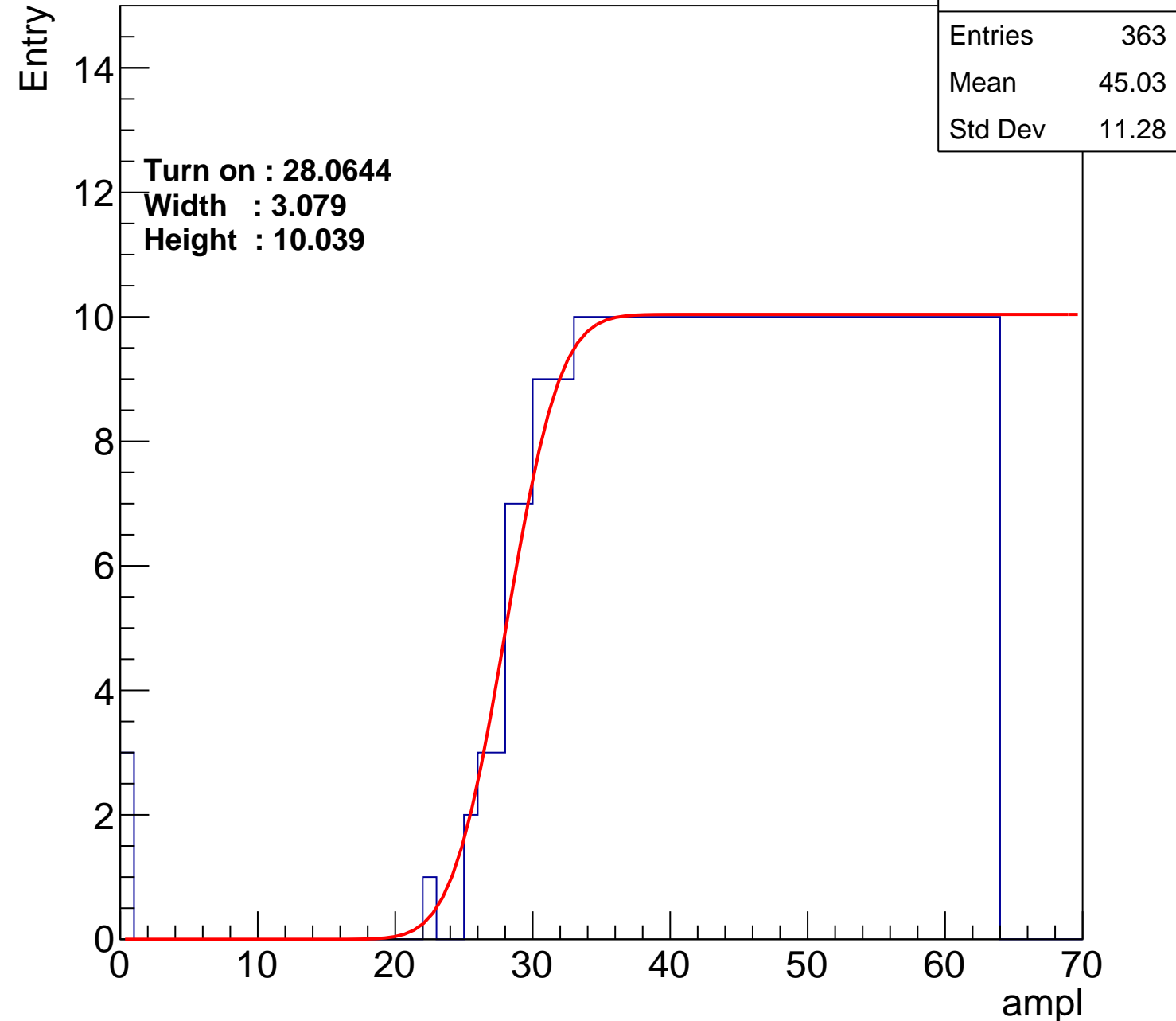
Width : 3.079

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.95
Std Dev	11.3

Turn on : 27.5868

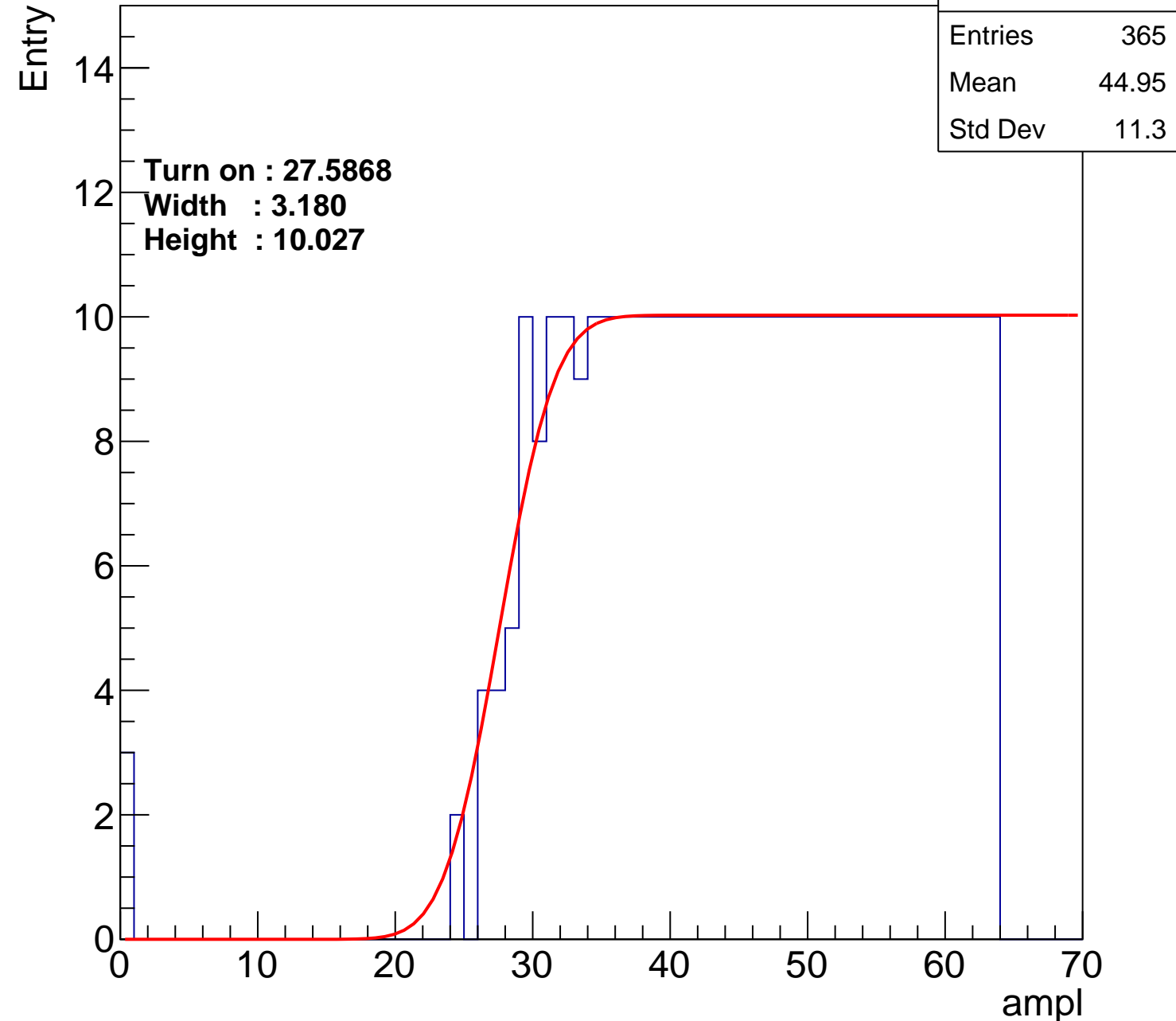
Width : 3.180

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.56
Std Dev	11.23

Turn on : 26.5307

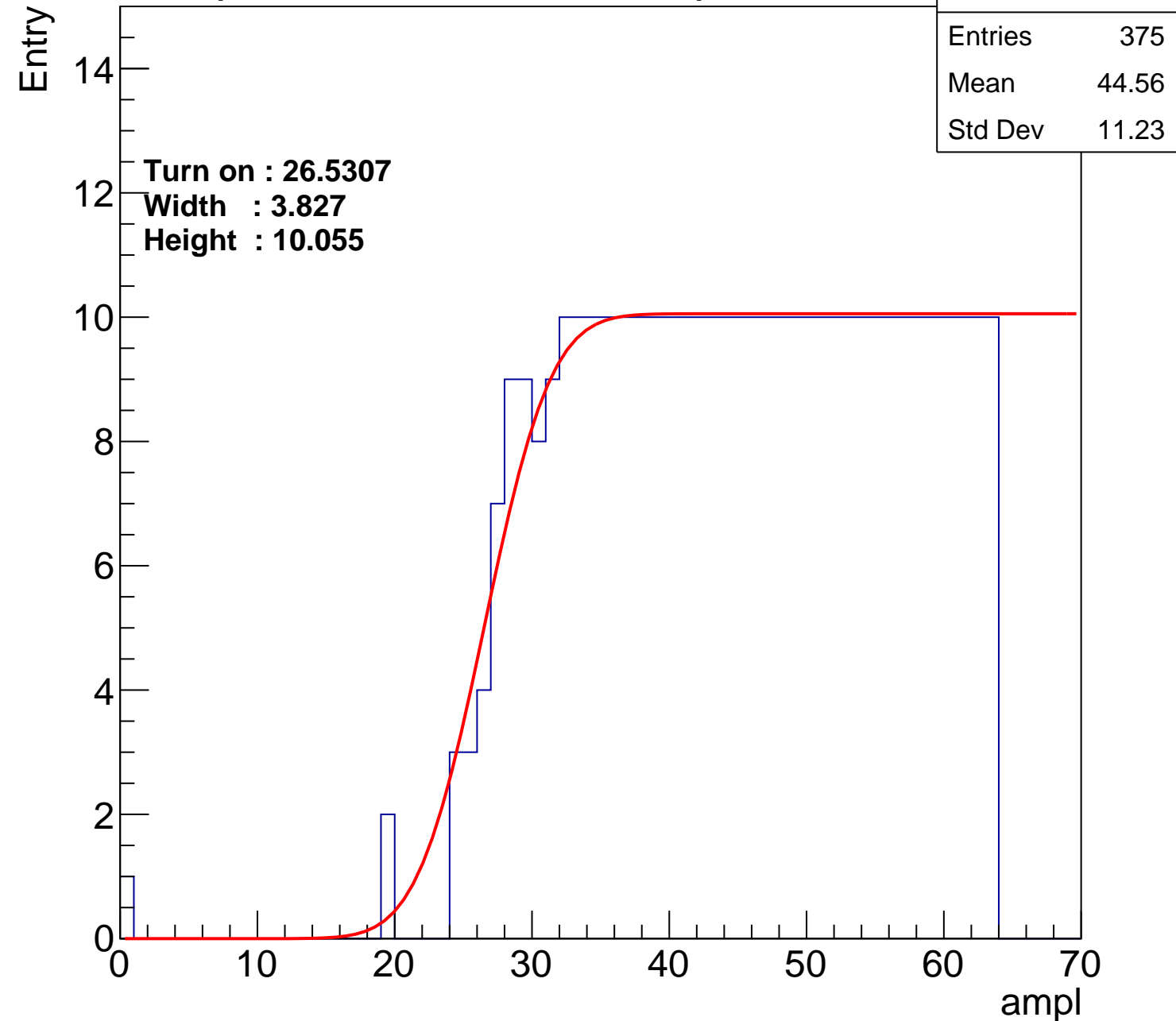
Width : 3.827

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch88

calib_packv5_042523_0143.root, FC#5, port B1

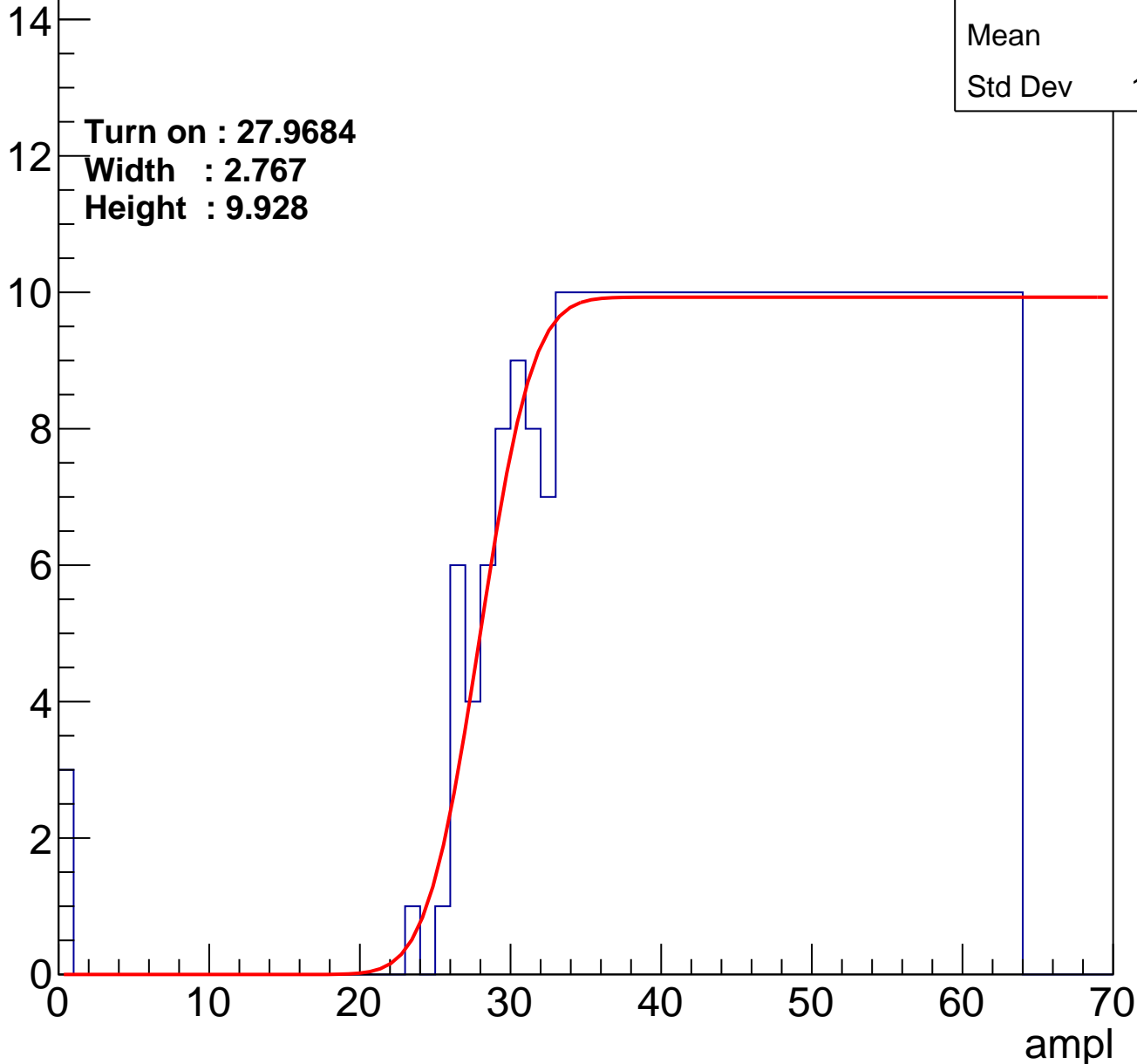
Entry

Entries	363
Mean	45
Std Dev	11.33

Turn on : 27.9684

Width : 2.767

Height : 9.928



B0L000S, U6-ch89

calib_packv5_042523_0143.root, FC#5, port B1

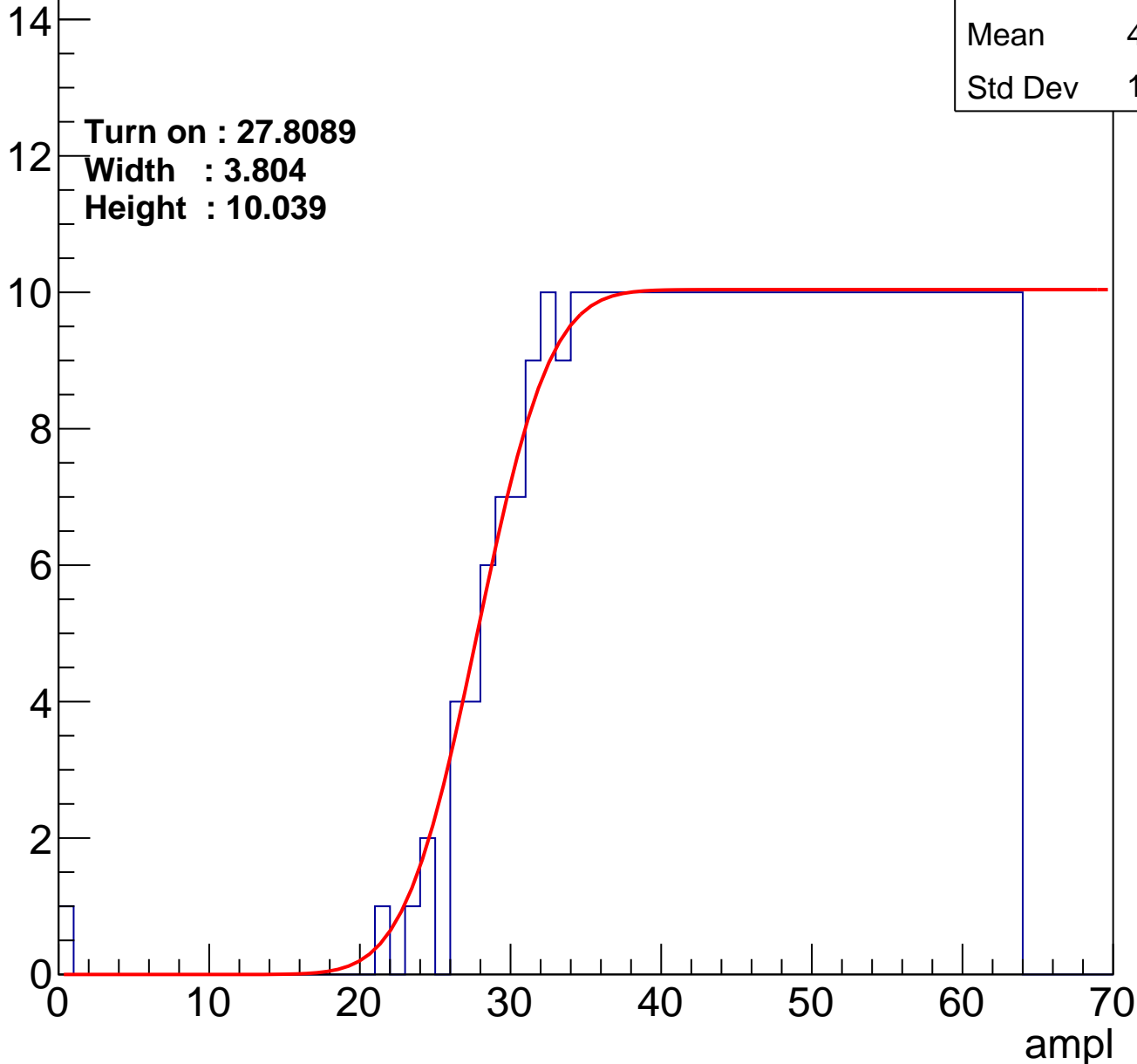
Entry

Entries	361
Mean	45.24
Std Dev	10.87

Turn on : 27.8089

Width : 3.804

Height : 10.039



B0L000S, U6-ch90

calib_packv5_042523_0143.root, FC#5, port B1

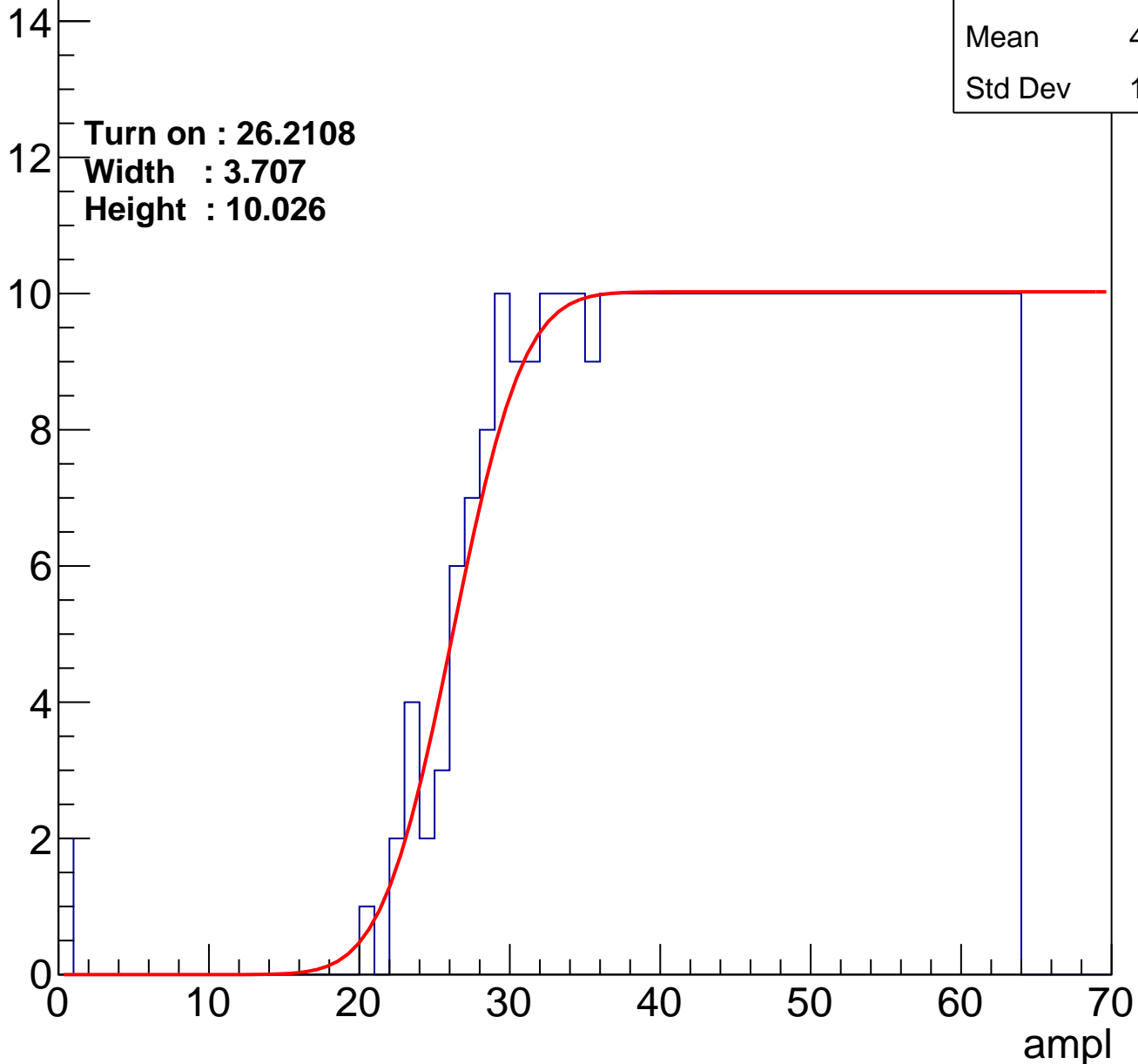
Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.2108

Width : 3.707

Height : 10.026

Entry



B0L000S, U6-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 25.8200

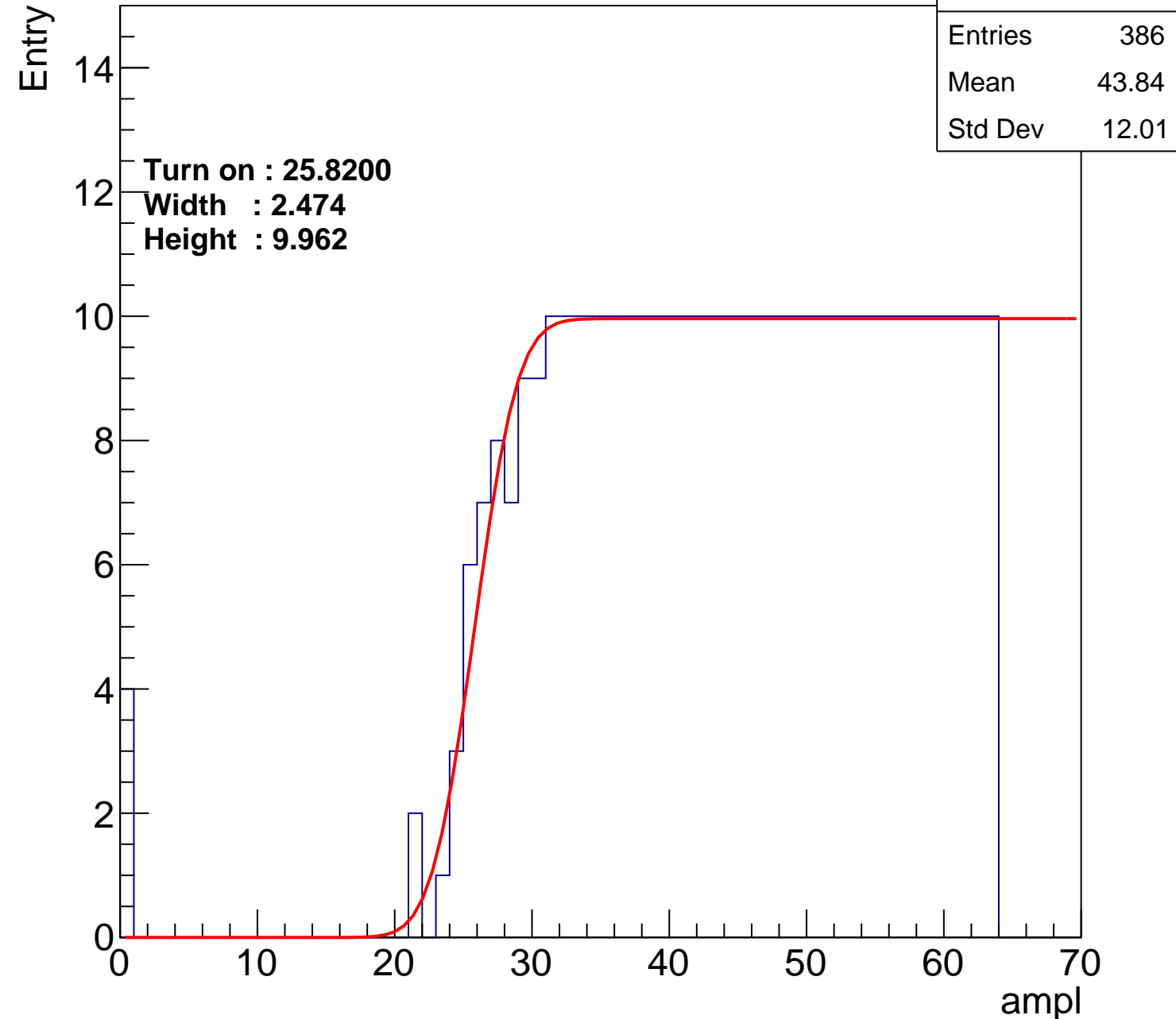
Width : 2.474

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.3345

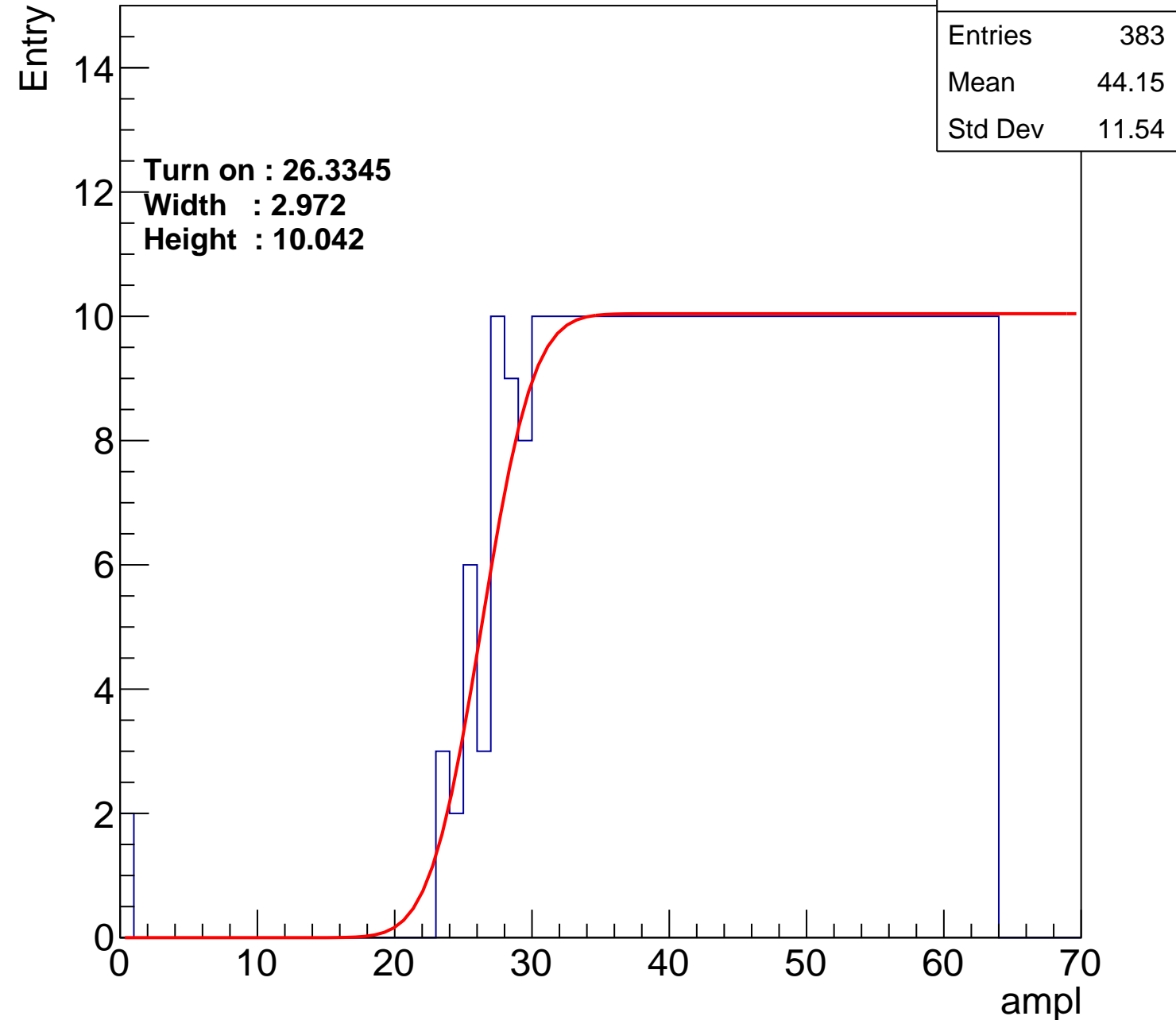
Width : 2.972

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch93

calib_packv5_042523_0143.root, FC#5, port B1

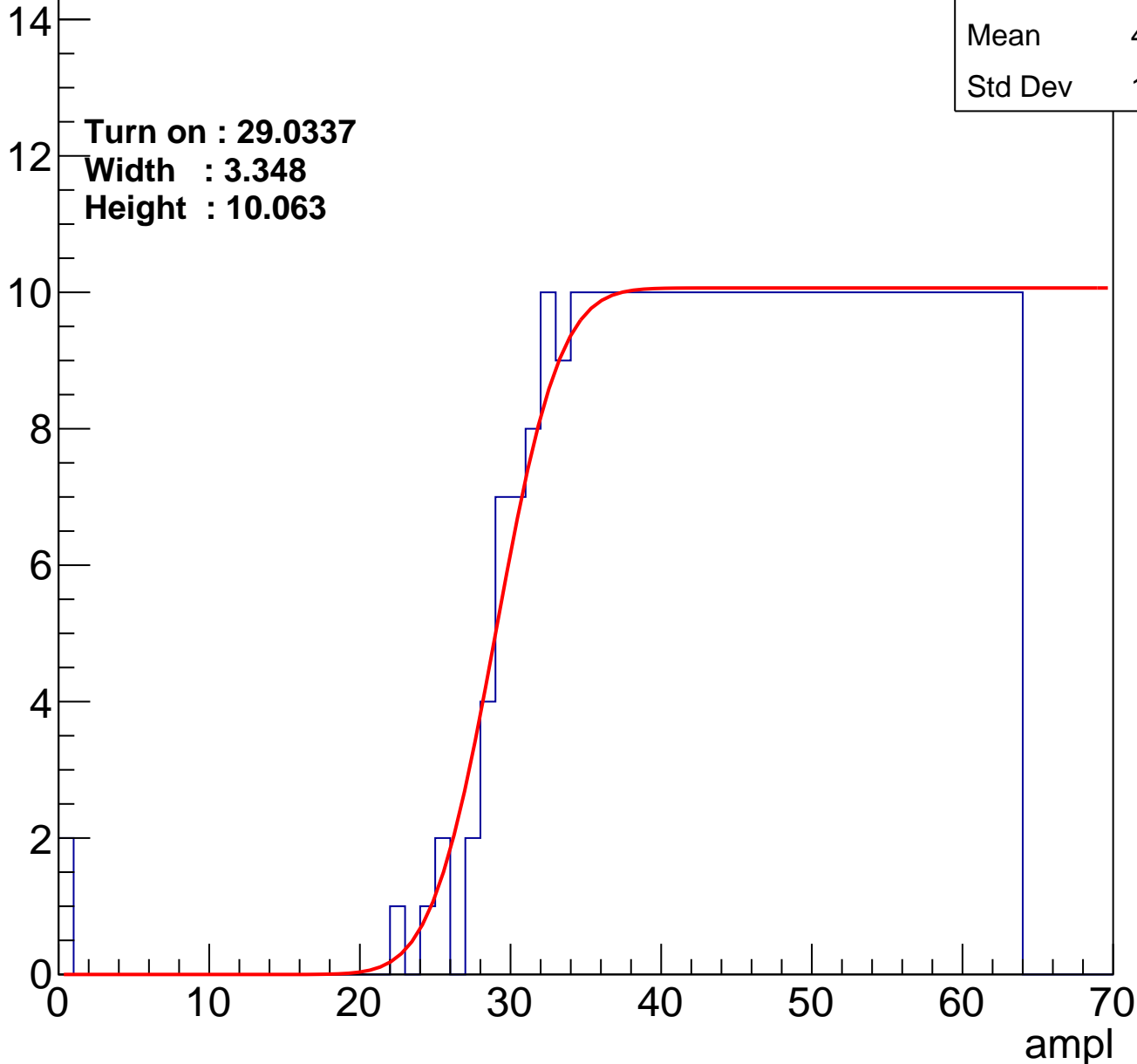
Entry

Entries	353
Mean	45.58
Std Dev	10.85

Turn on : 29.0337

Width : 3.348

Height : 10.063



B0L000S, U6-ch94

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.2607

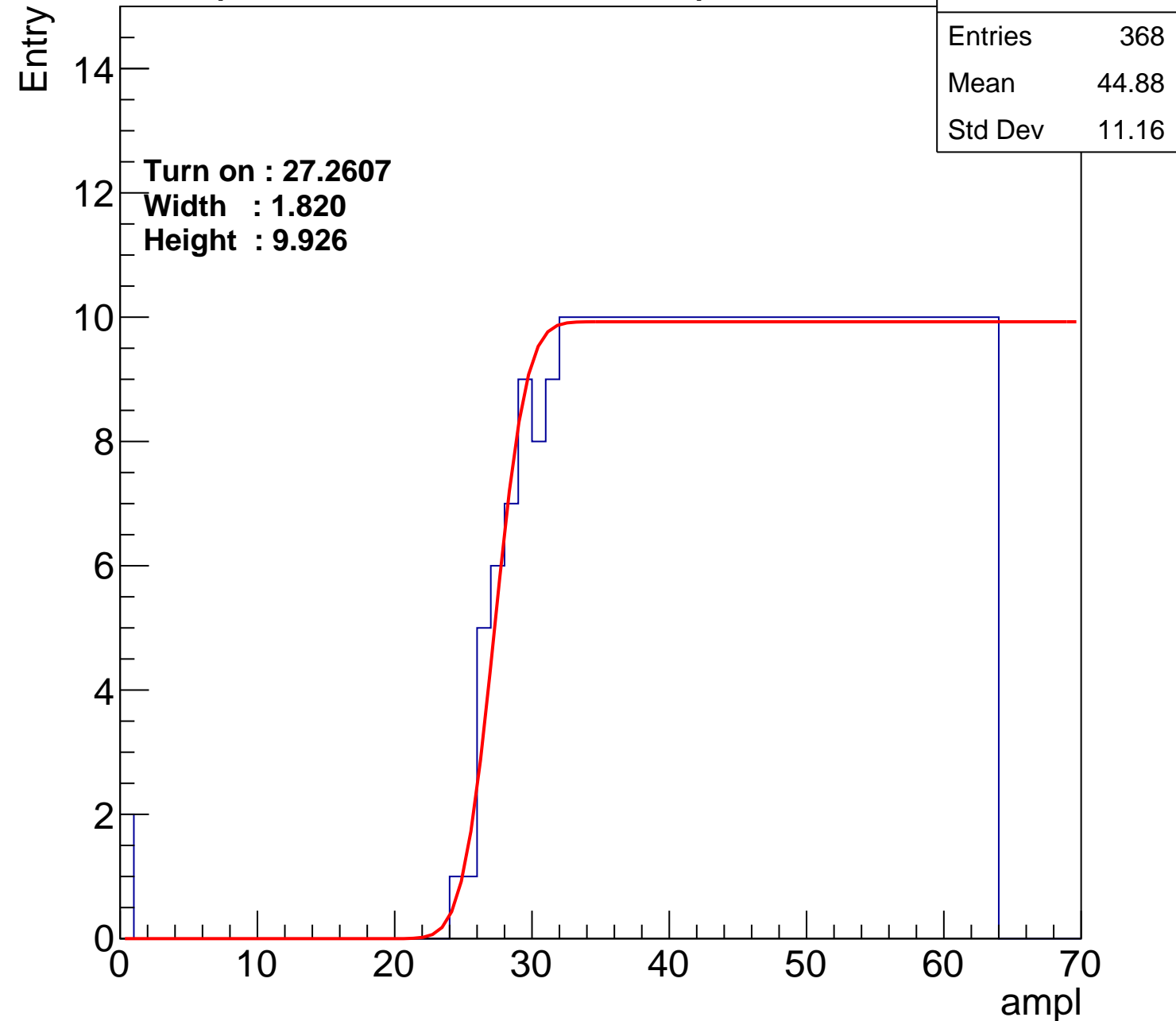
Width : 1.820

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.7465

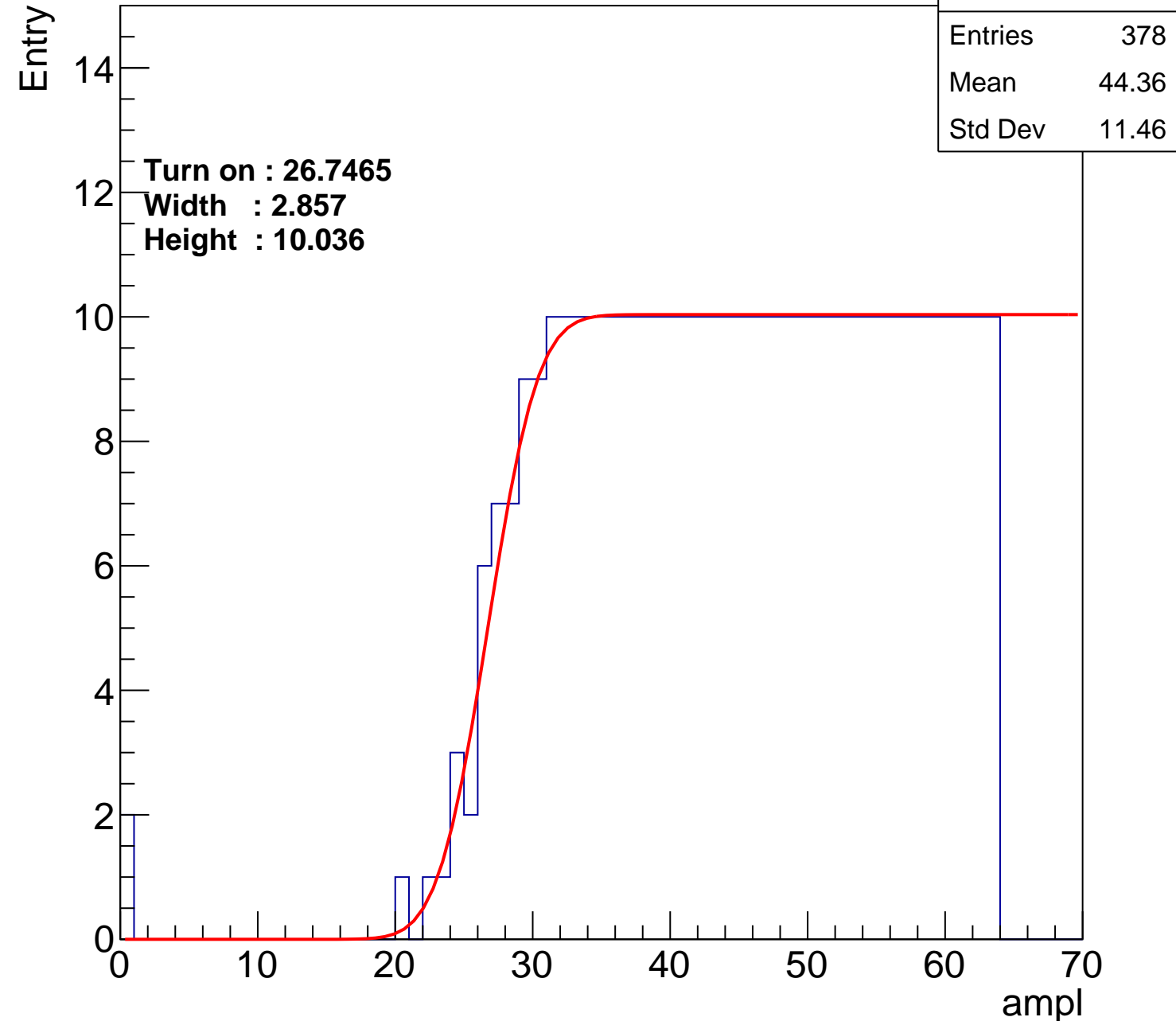
Width : 2.857

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch96

calib_packv5_042523_0143.root, FC#5, port B1

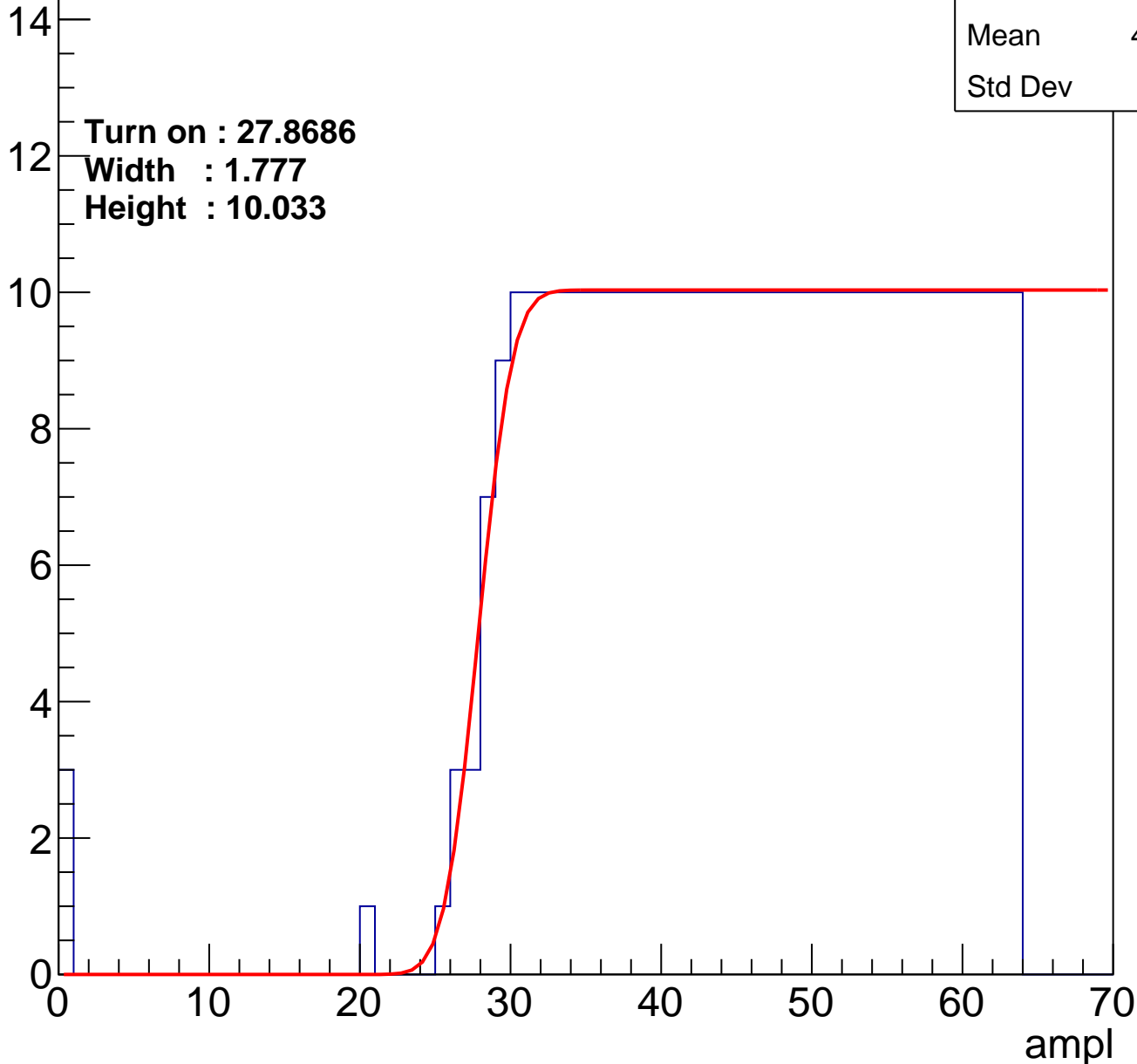
Entry

Entries	367
Mean	44.88
Std Dev	11.31

Turn on : 27.8686

Width : 1.777

Height : 10.033



B0L000S, U6-ch97

calib_packv5_042523_0143.root, FC#5, port B1

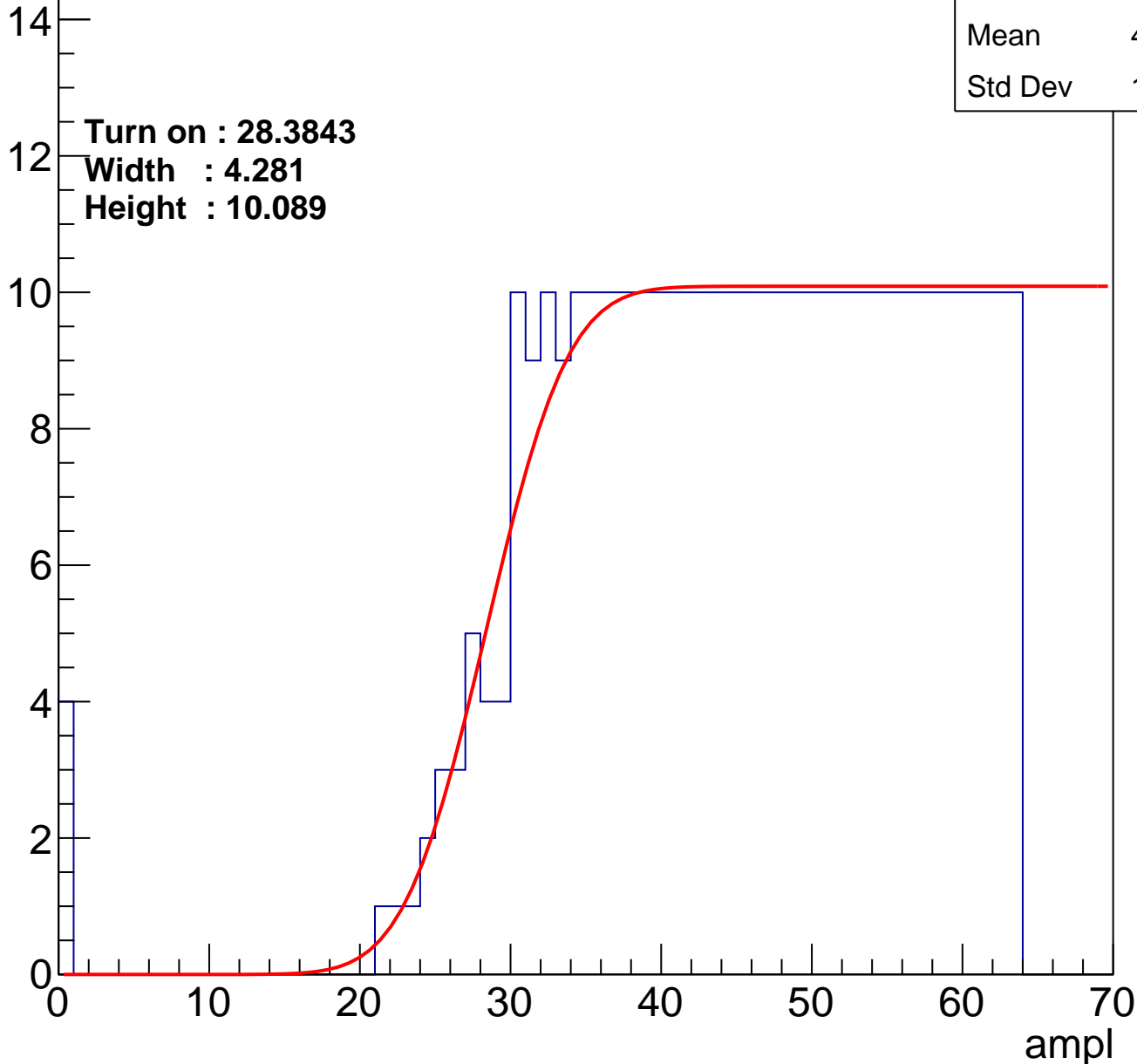
Entry

Entries	366
Mean	44.74
Std Dev	11.66

Turn on : 28.3843

Width : 4.281

Height : 10.089



B0L000S, U6-ch98

calib_packv5_042523_0143.root, FC#5, port B1

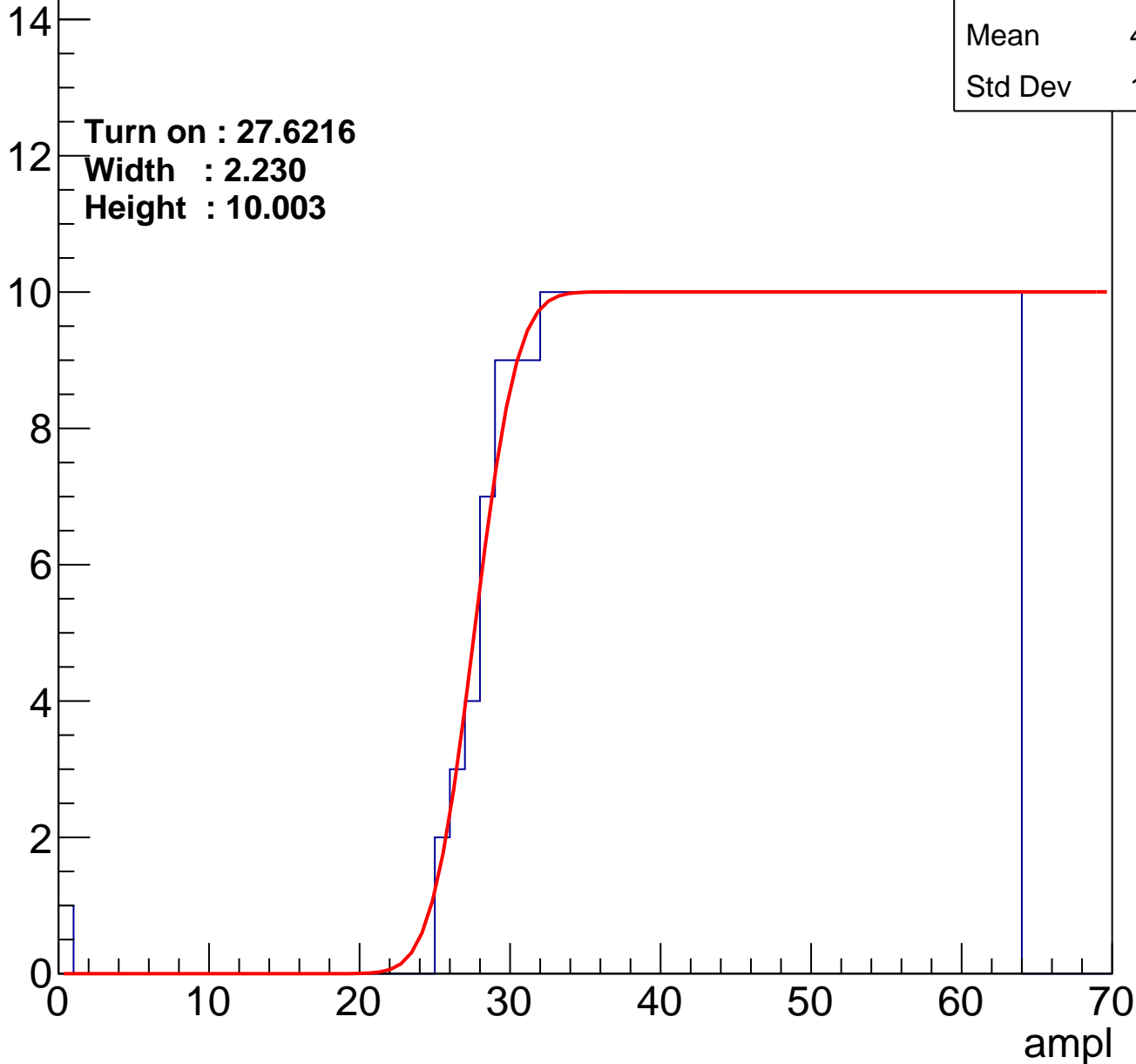
Entry

Entries	364
Mean	45.17
Std Dev	10.82

Turn on : 27.6216

Width : 2.230

Height : 10.003



B0L000S, U6-ch99

calib_packv5_042523_0143.root, FC#5, port B1

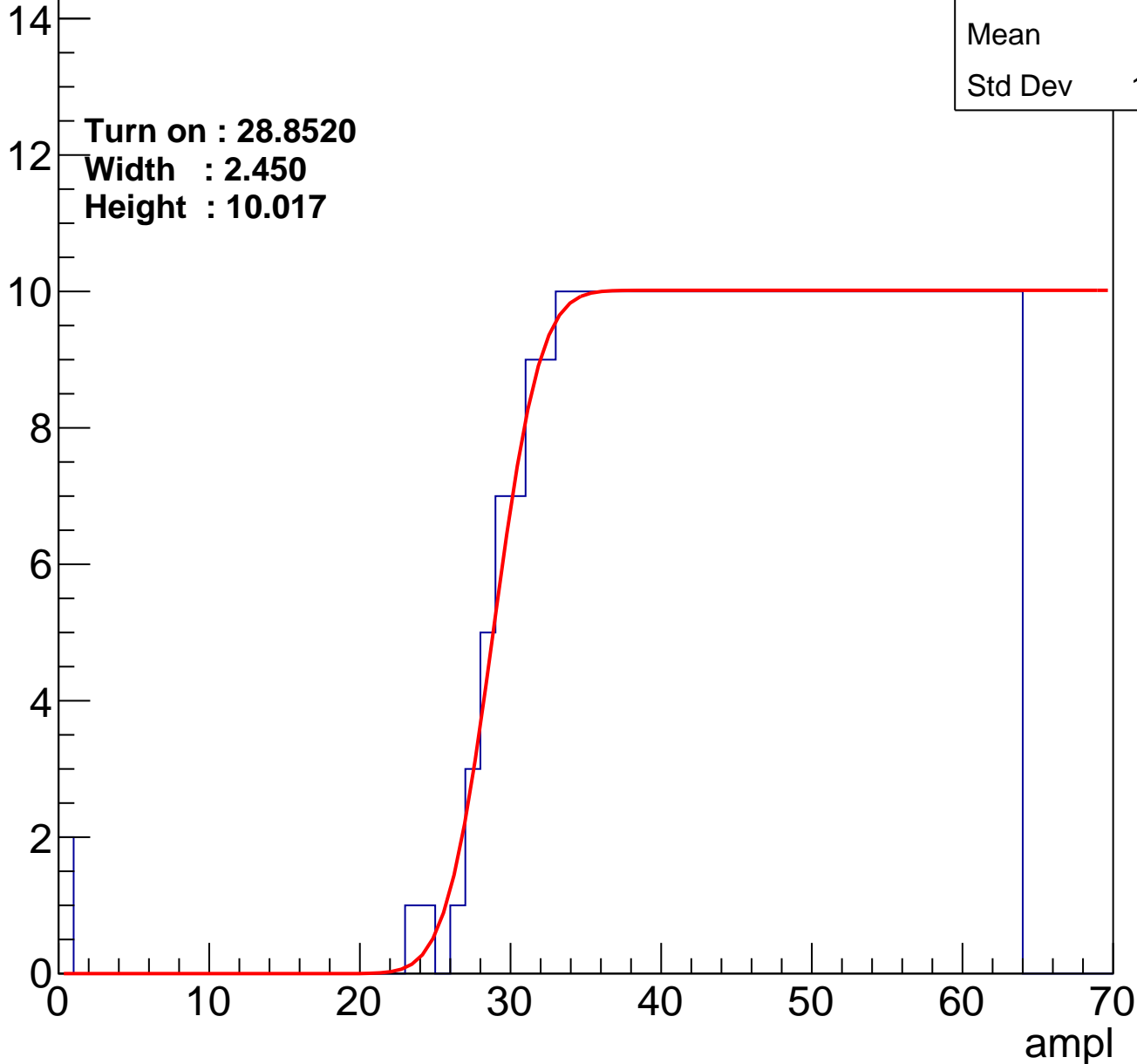
Entry

Entries	355
Mean	45.5
Std Dev	10.86

Turn on : 28.8520

Width : 2.450

Height : 10.017



B0L000S, U6-ch100

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.15
Std Dev	11.05

Turn on : 28.2685

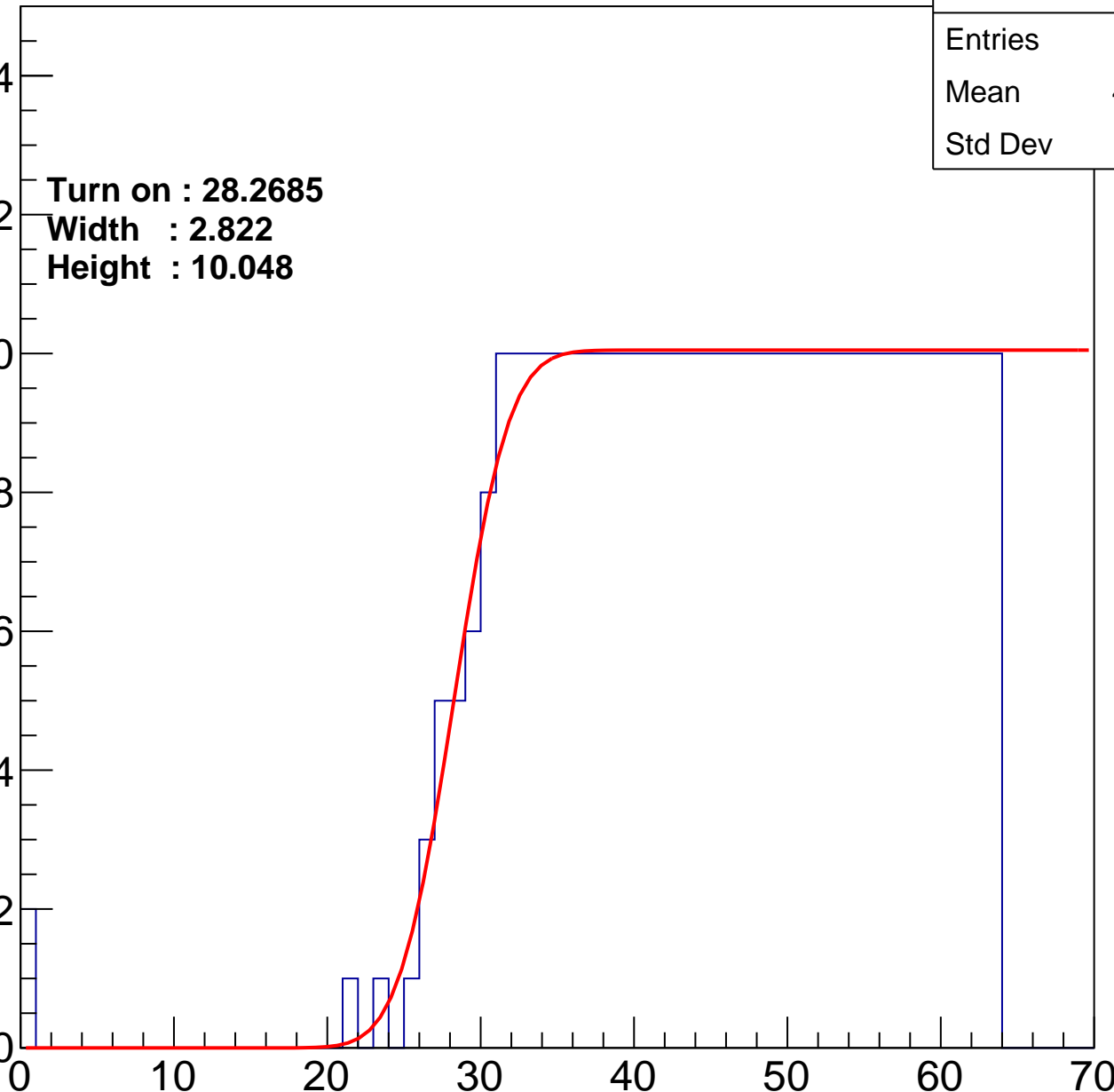
Width : 2.822

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.22
Std Dev	12.39

Turn on : 27.3262

Width : 1.689

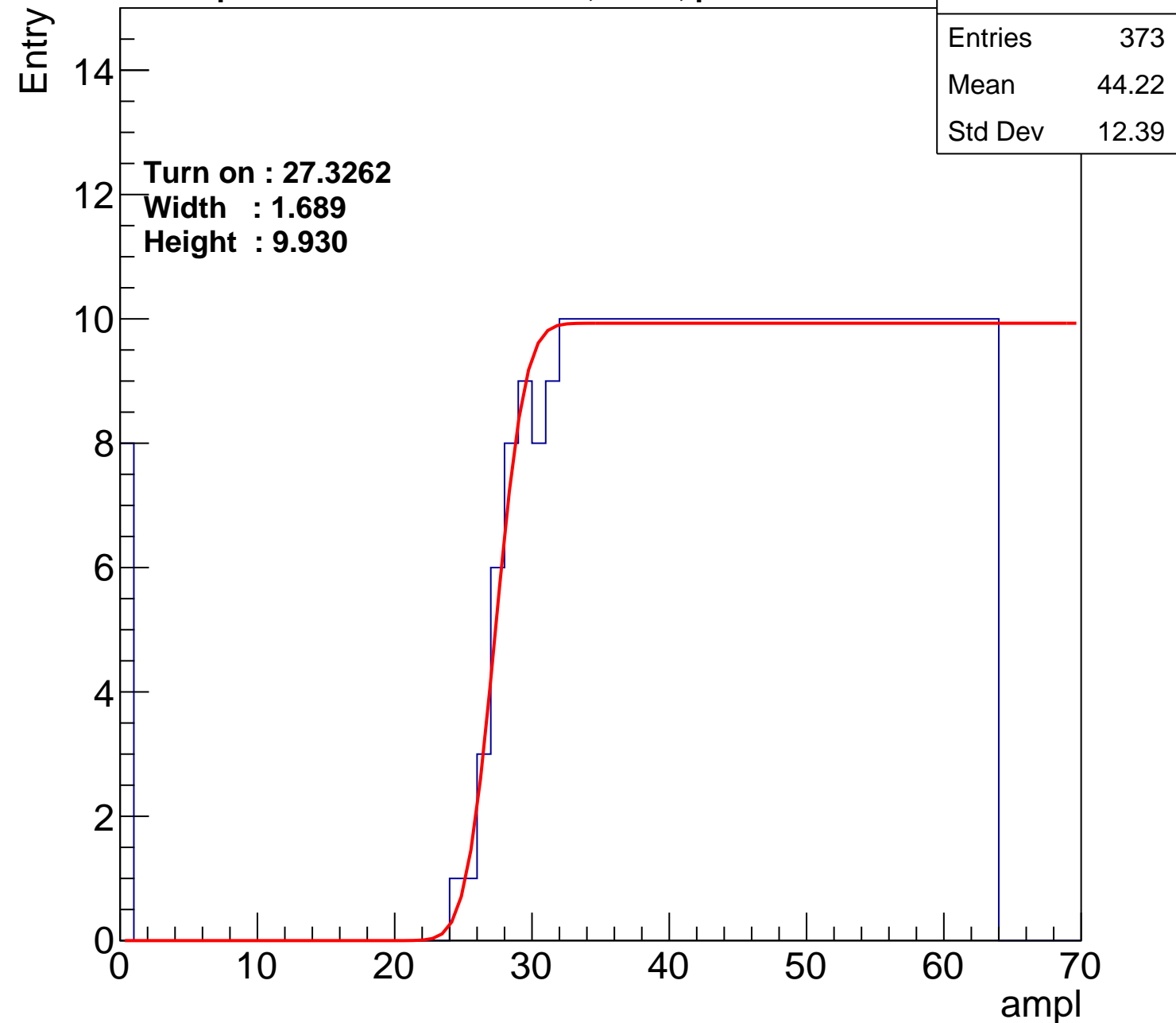
Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U6-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.69
Std Dev	12.09

Turn on : 25.4811

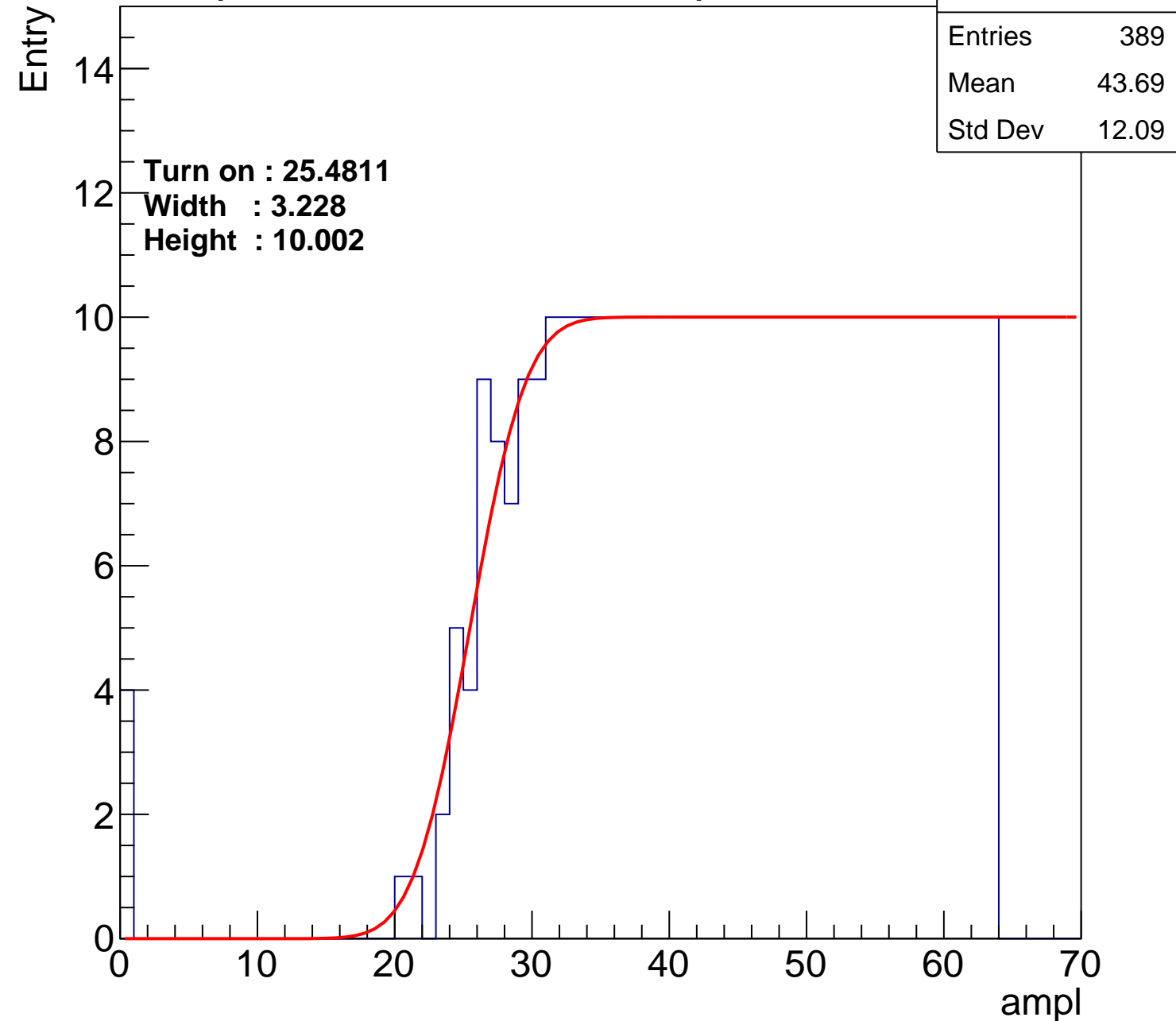
Width : 3.228

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch103

calib_packv5_042523_0143.root, FC#5, port B1

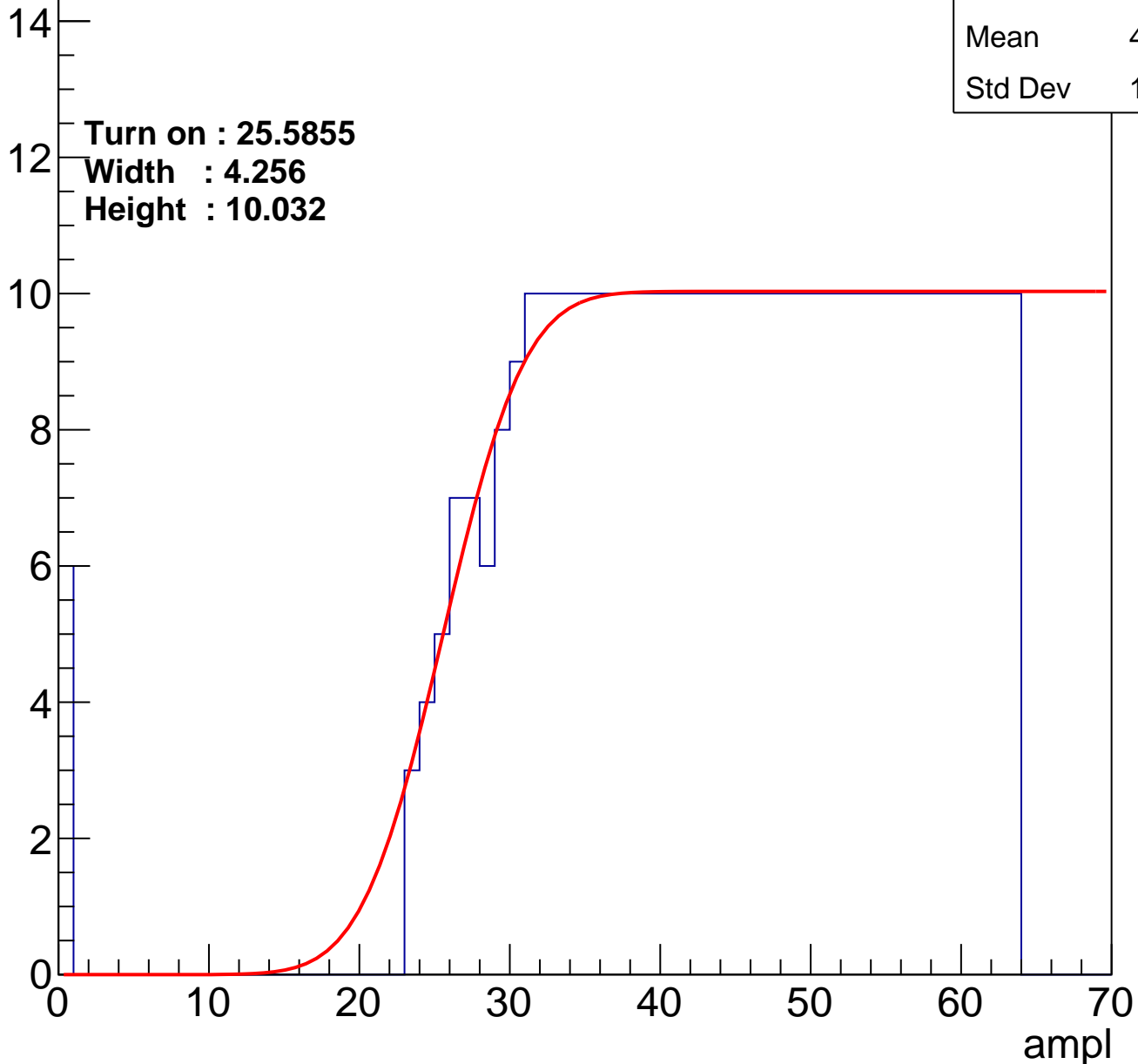
Entries	385
Mean	43.74
Std Dev	12.34

Turn on : 25.5855

Width : 4.256

Height : 10.032

Entry



B0L000S, U6-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	44.74
Std Dev	11.99

Turn on : 28.5898

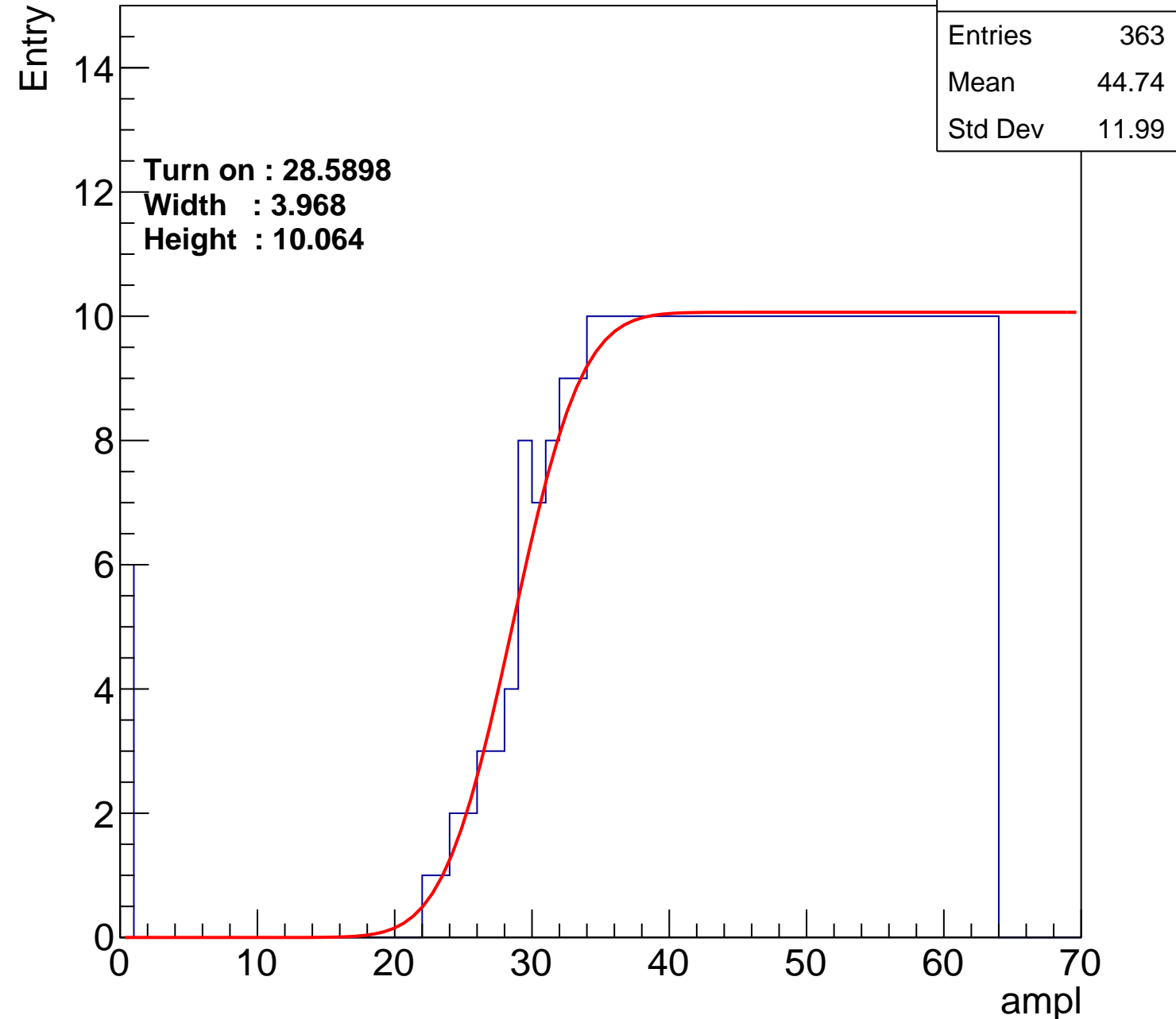
Width : 3.968

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch105

calib_packv5_042523_0143.root, FC#5, port B1

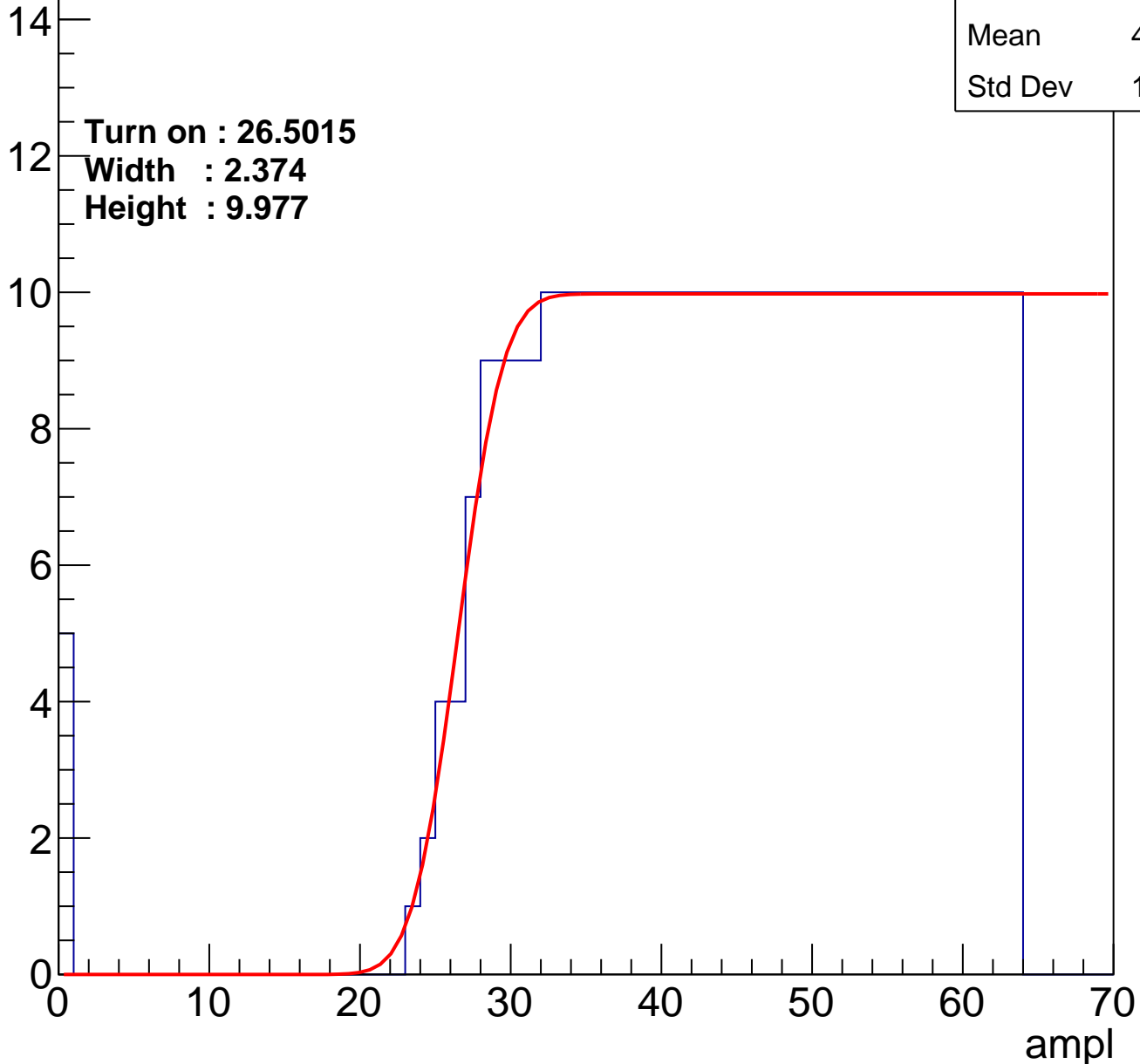
Entries	379
Mean	44.13
Std Dev	11.99

Turn on : 26.5015

Width : 2.374

Height : 9.977

Entry



B0L000S, U6-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.45
Std Dev	11.09

Turn on : 28.6962

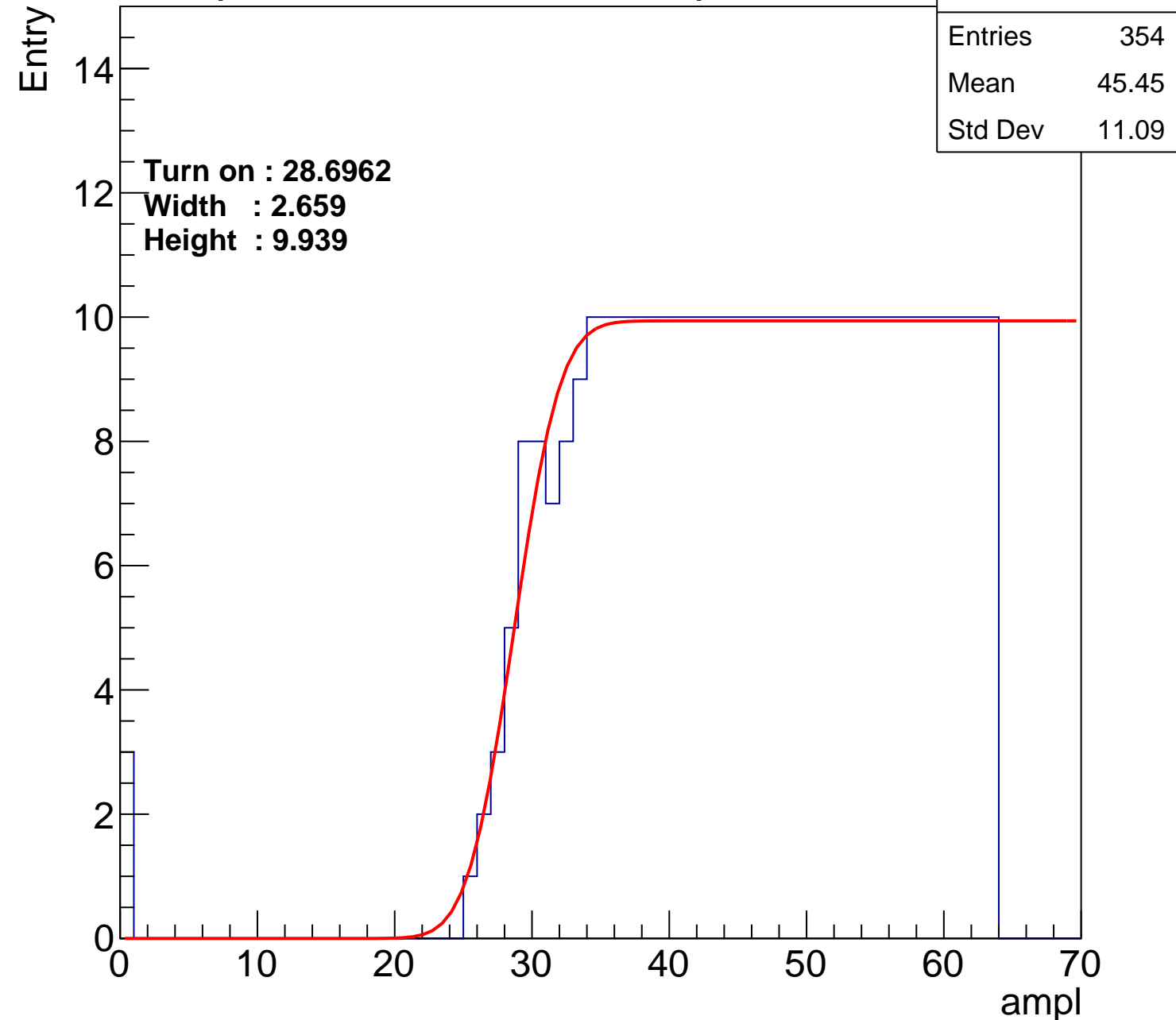
Width : 2.659

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	46.02
Std Dev	10.67

Turn on : 29.9814

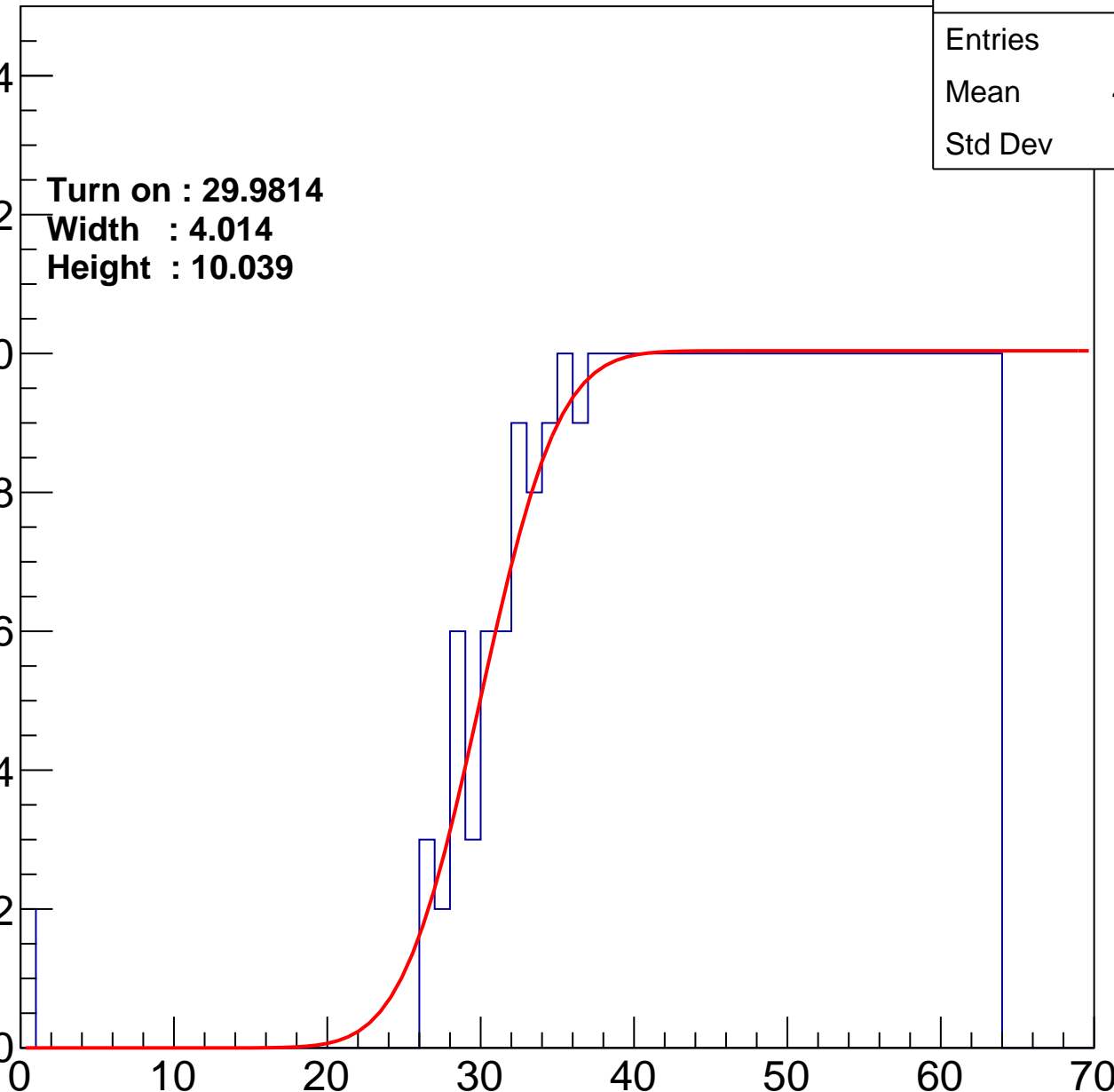
Width : 4.014

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch108

calib_packv5_042523_0143.root, FC#5, port B1

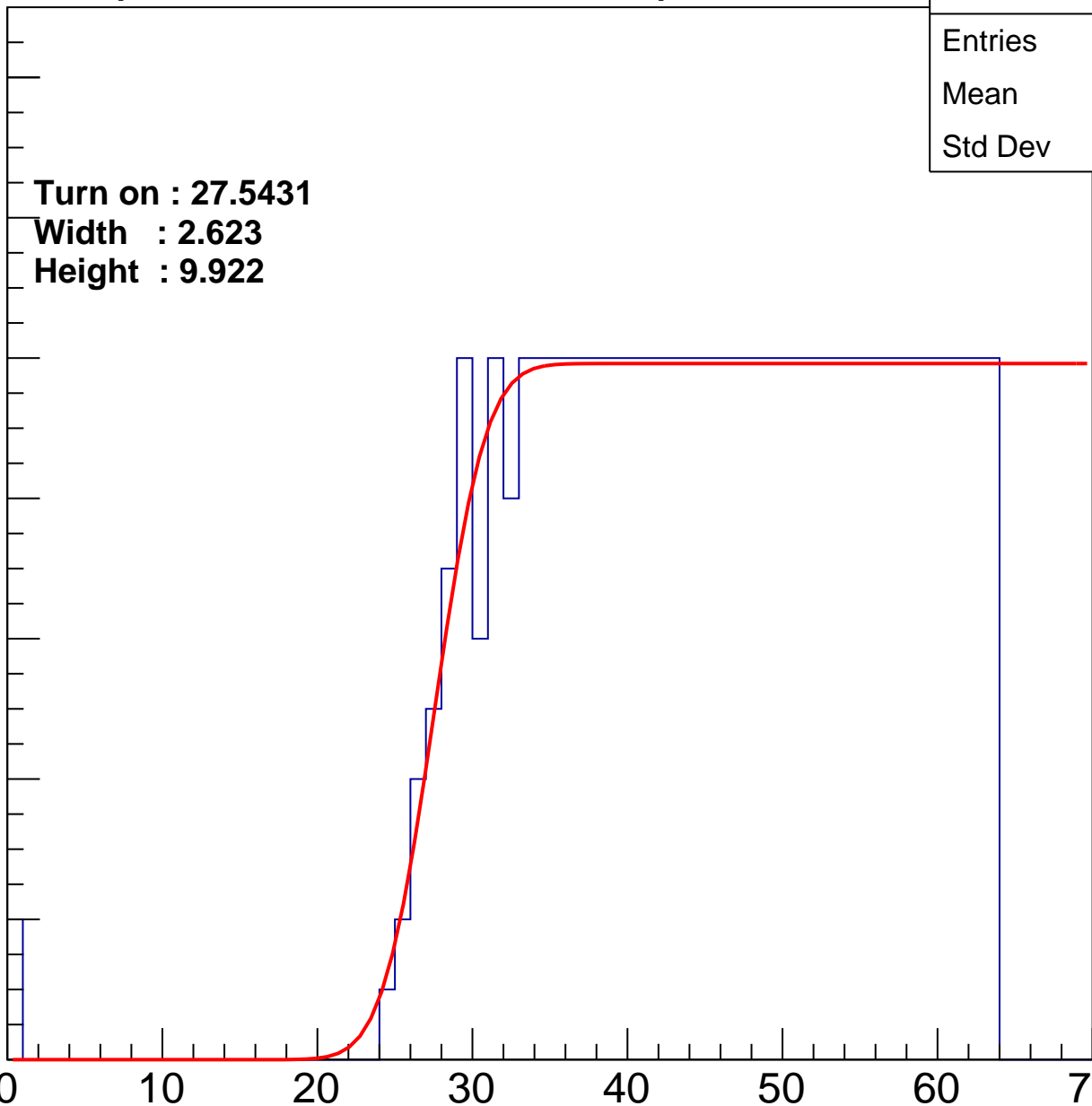
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5431
Width : 2.623
Height : 9.922

Entries	365
Mean	45
Std Dev	11.13

ampl



B0L000S, U6-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.34
Std Dev	11.37

Turn on : 26.1925

Width : 3.446

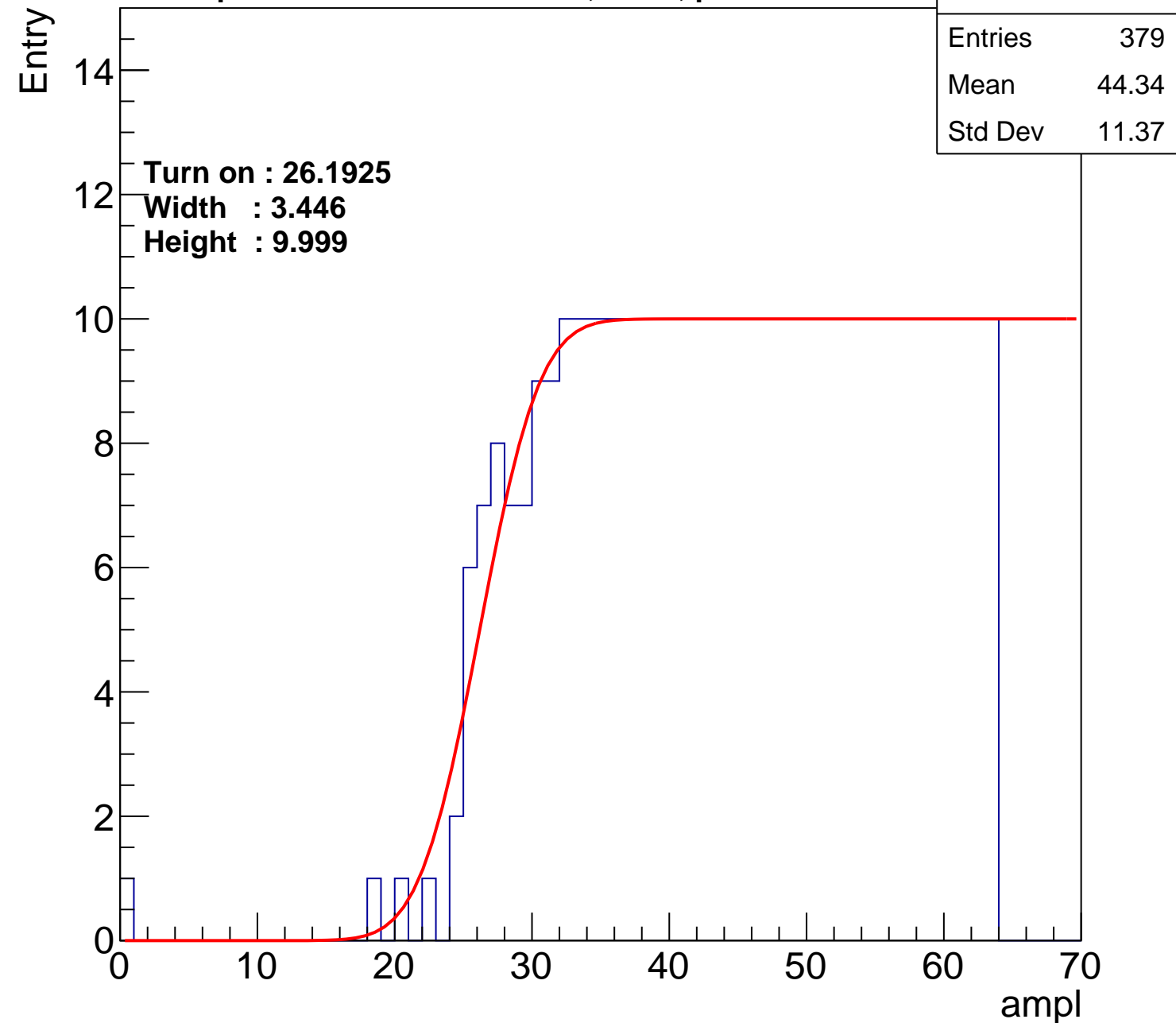
Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

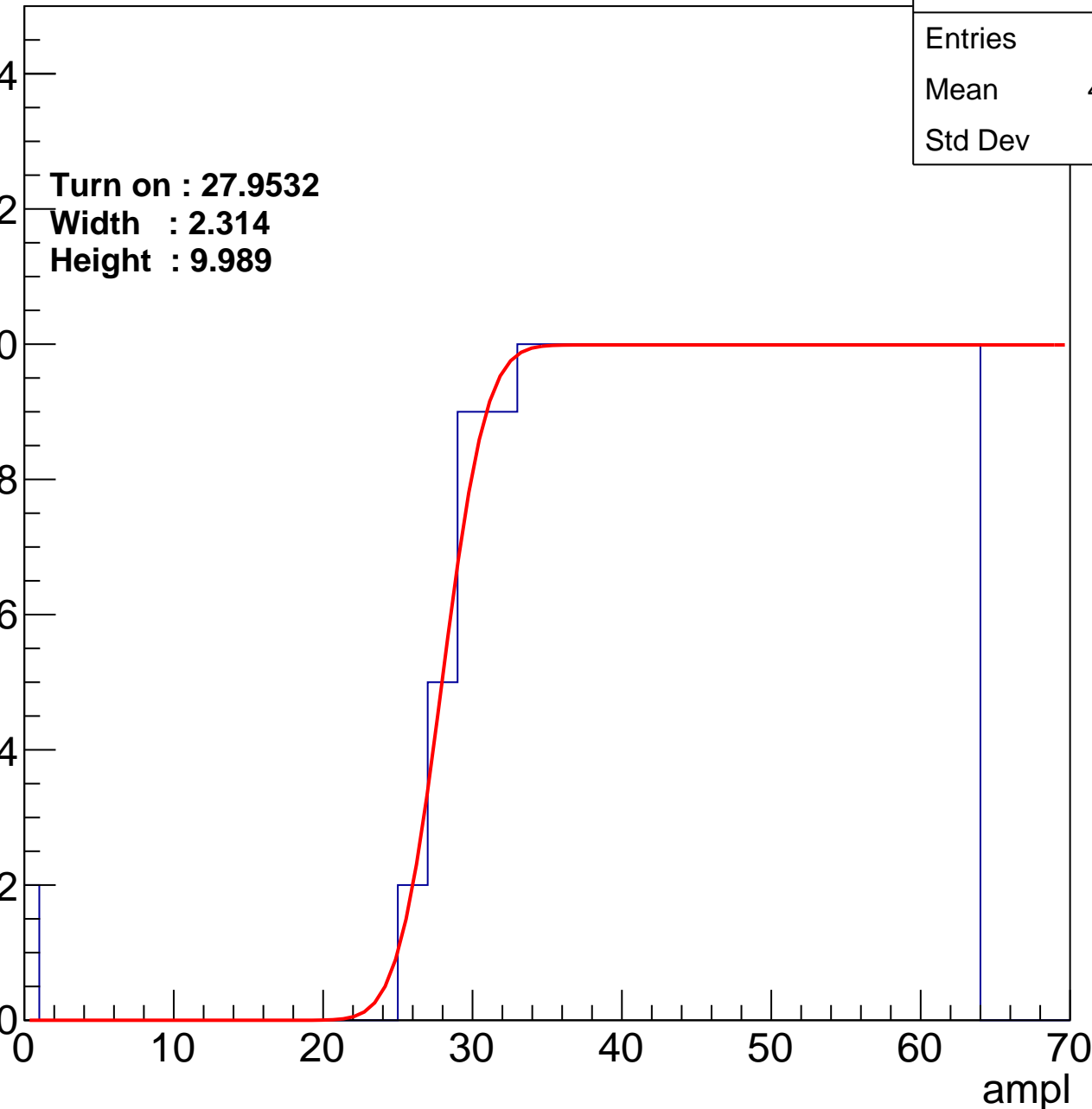


calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.18
Std Dev	11

Std Dev	11
---------	----

Height : 9.989



B0L000S, U6-ch111

calib_packv5_042523_0143.root, FC#5, port B1

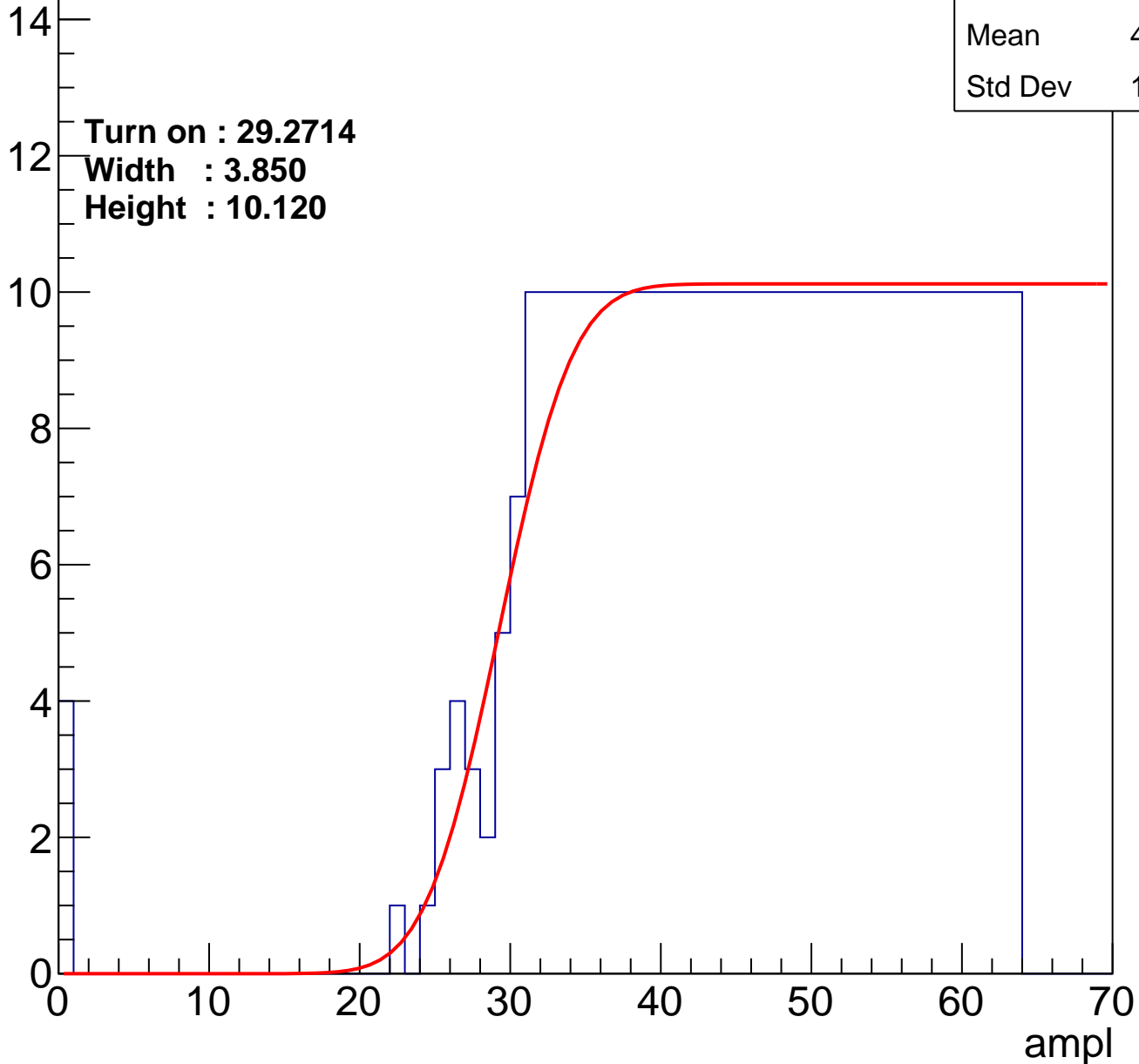
Entries	360
Mean	45.08
Std Dev	11.47

Turn on : 29.2714

Width : 3.850

Height : 10.120

Entry



B0L000S, U6-ch112

calib_packv5_042523_0143.root, FC#5, port B1

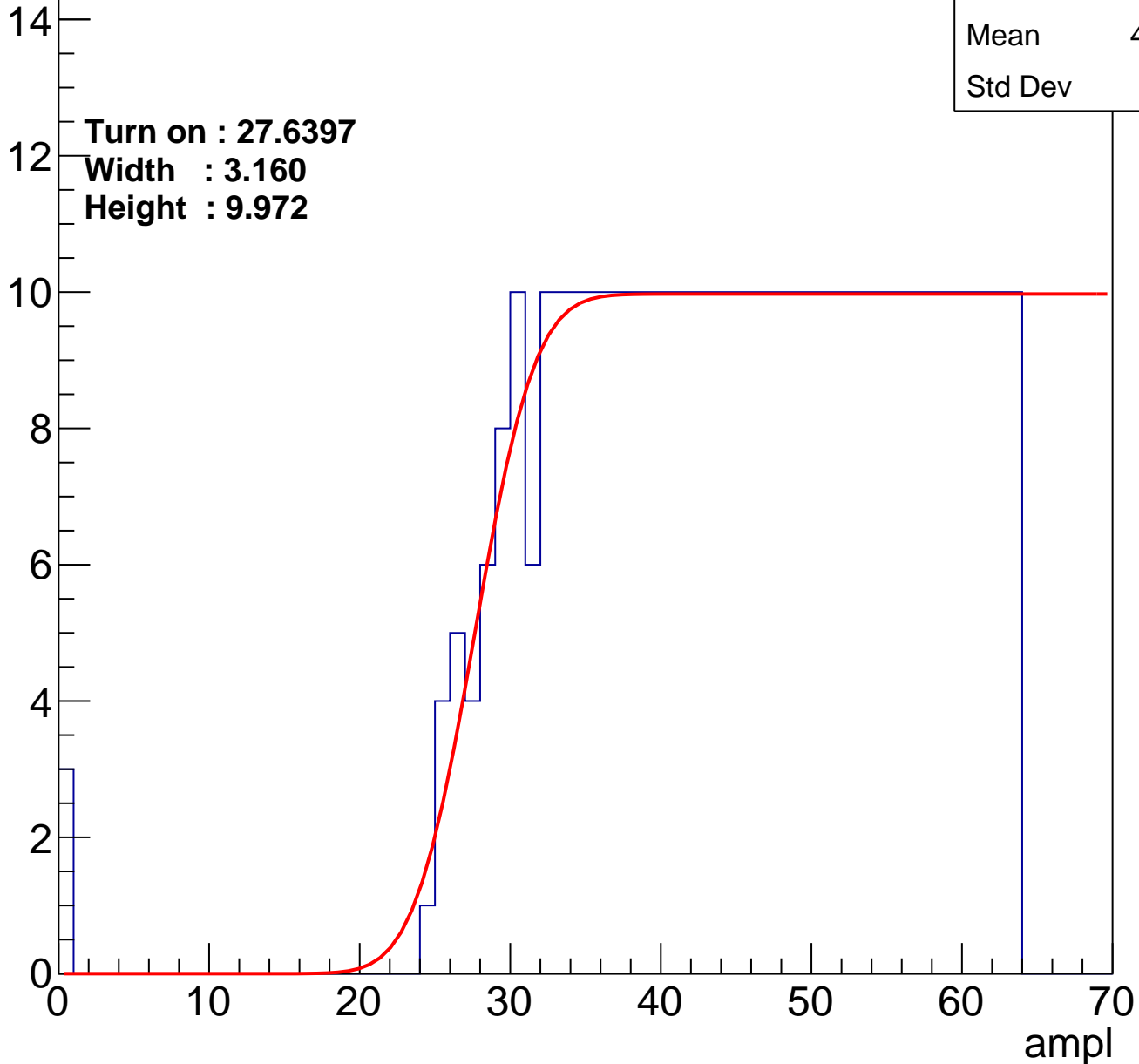
Entries	367
Mean	44.82
Std Dev	11.4

Turn on : 27.6397

Width : 3.160

Height : 9.972

Entry



B0L000S, U6-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 26.1368

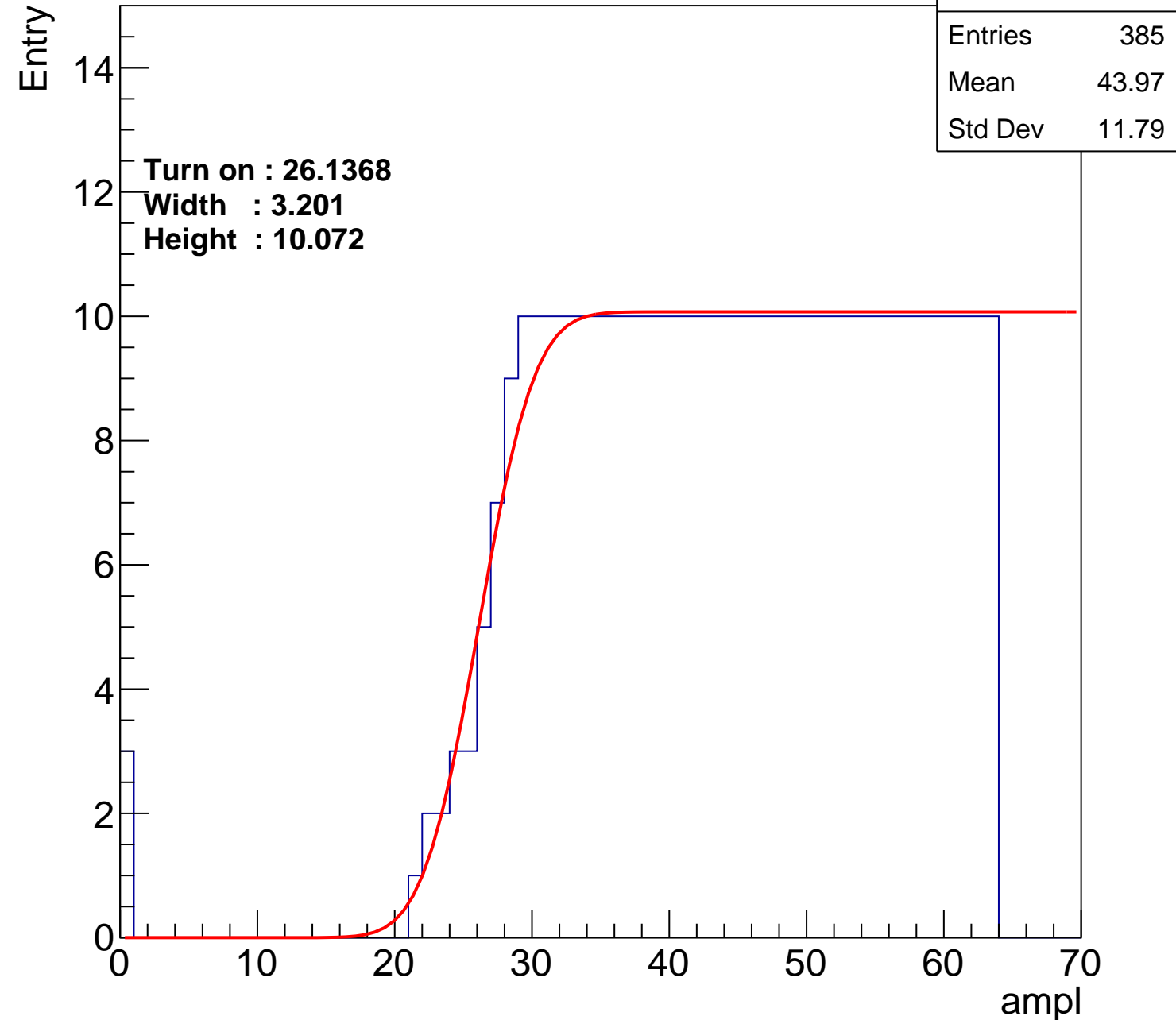
Width : 3.201

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.62
Std Dev	11.69

Turn on : 27.7775

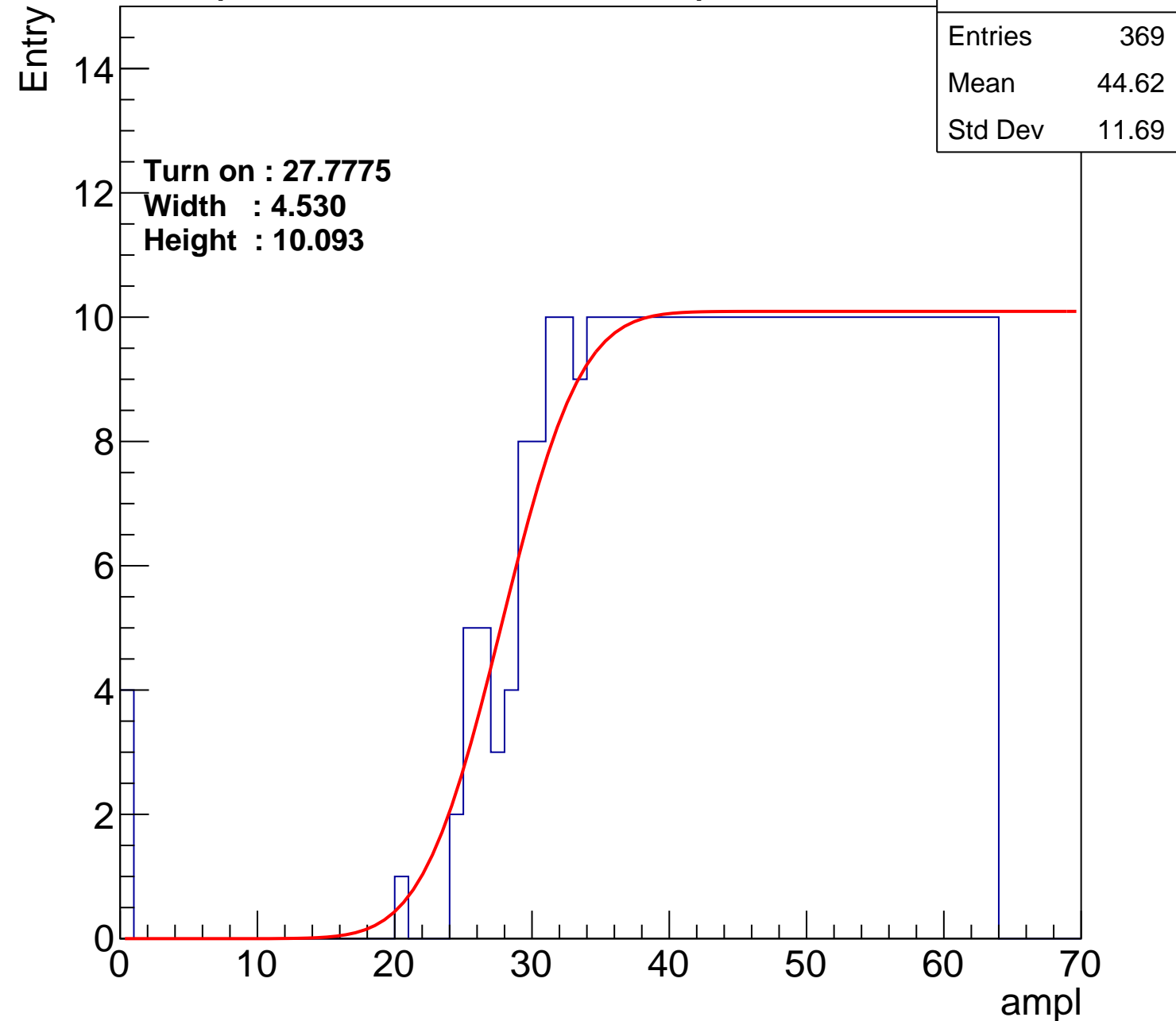
Width : 4.530

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch115

calib_packv5_042523_0143.root, FC#5, port B1

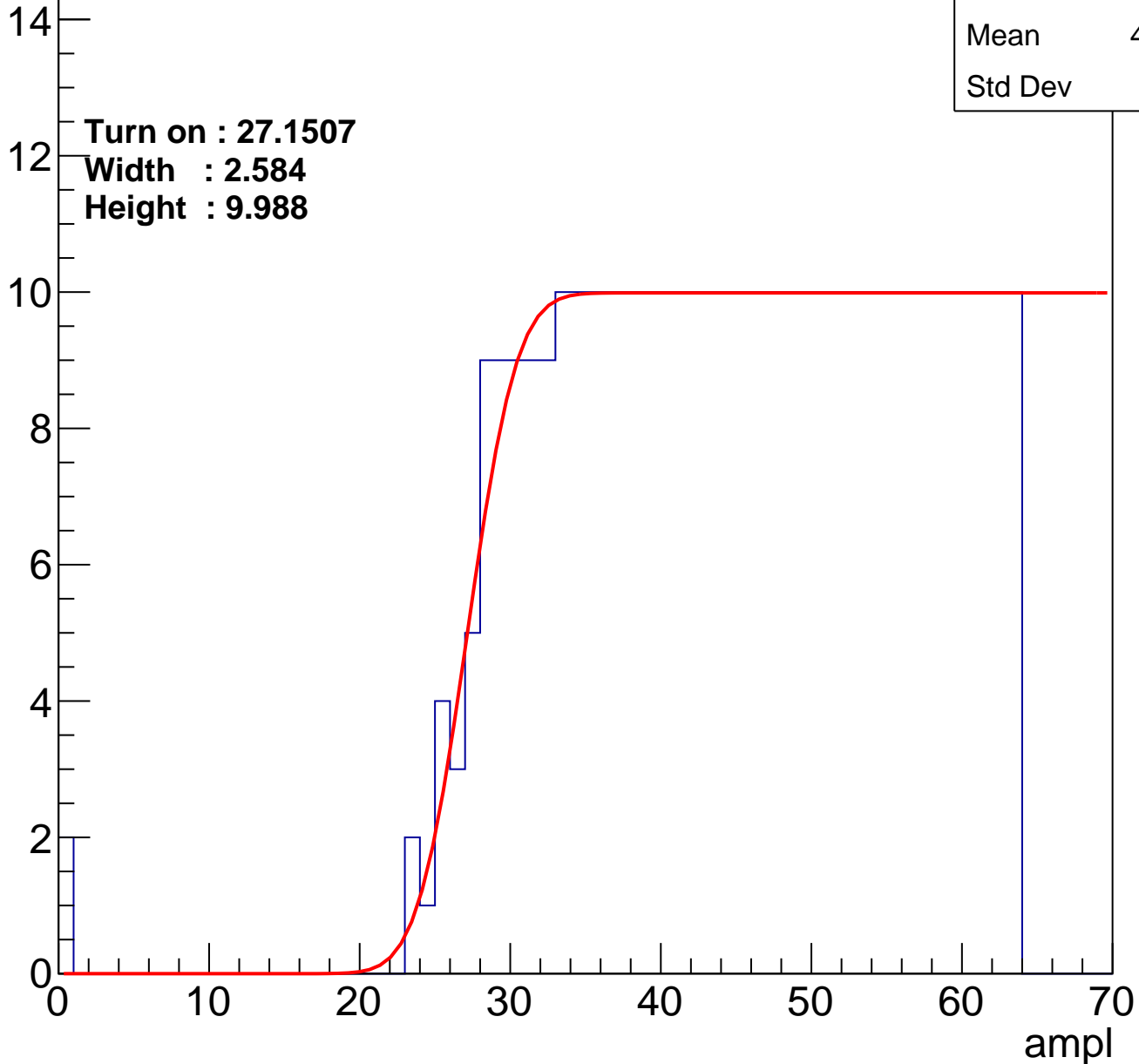
Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.1507

Width : 2.584

Height : 9.988

Entry



B0L000S, U6-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.49
Std Dev	11.87

Turn on : 28.0376

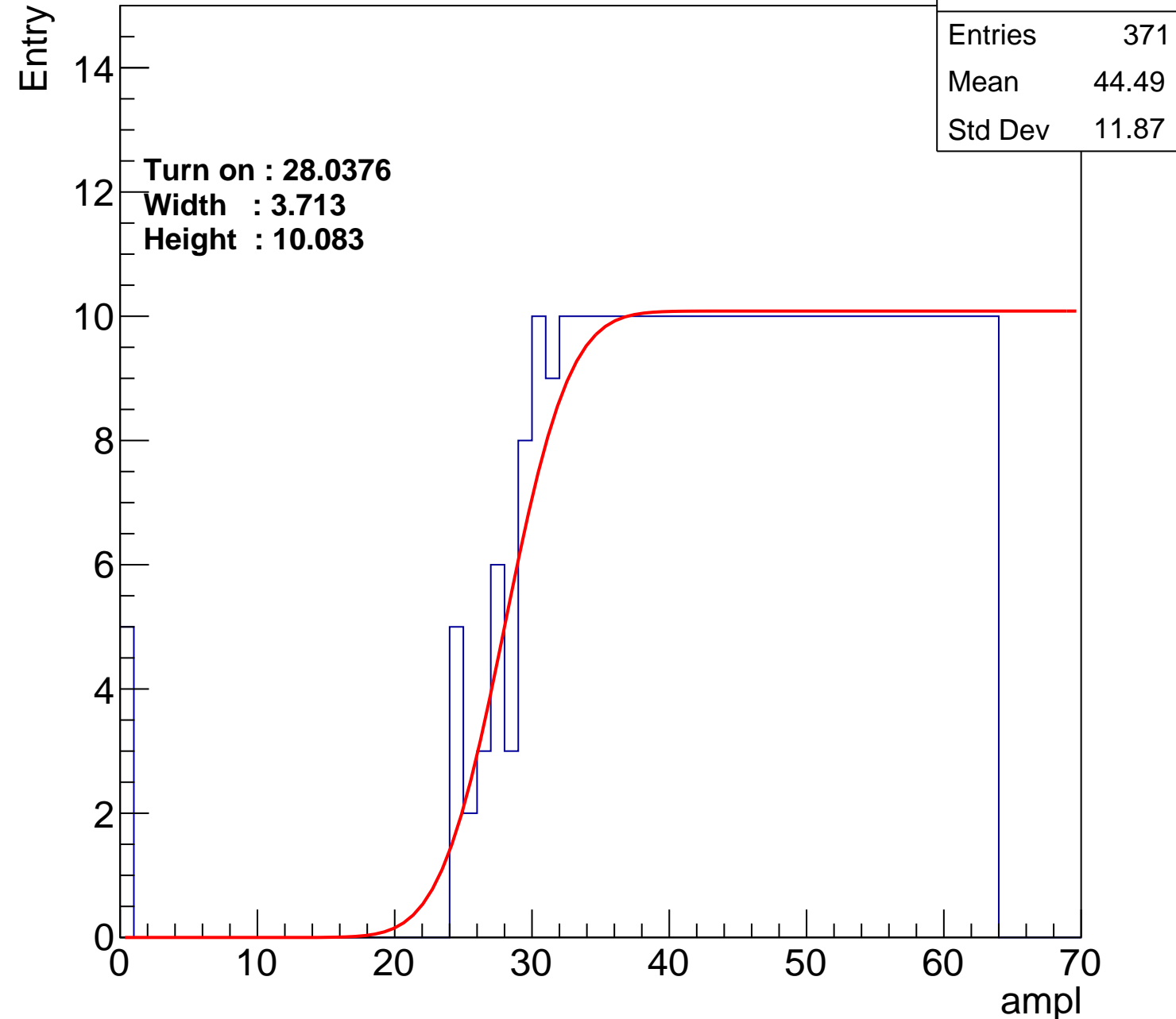
Width : 3.713

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.73
Std Dev	11.81

Turn on : 28.3315

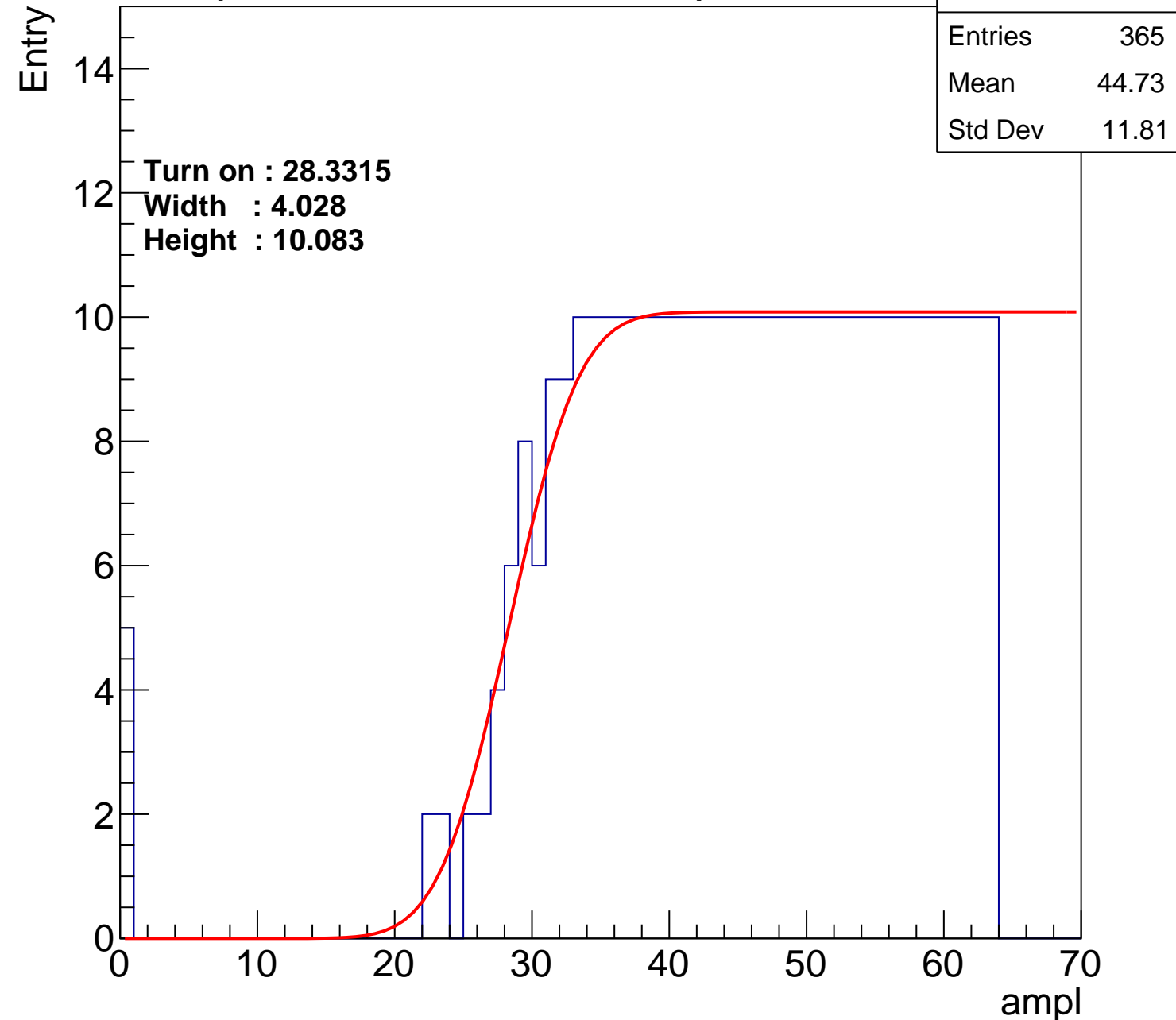
Width : 4.028

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.41
Std Dev	10.8

Turn on : 28.7783

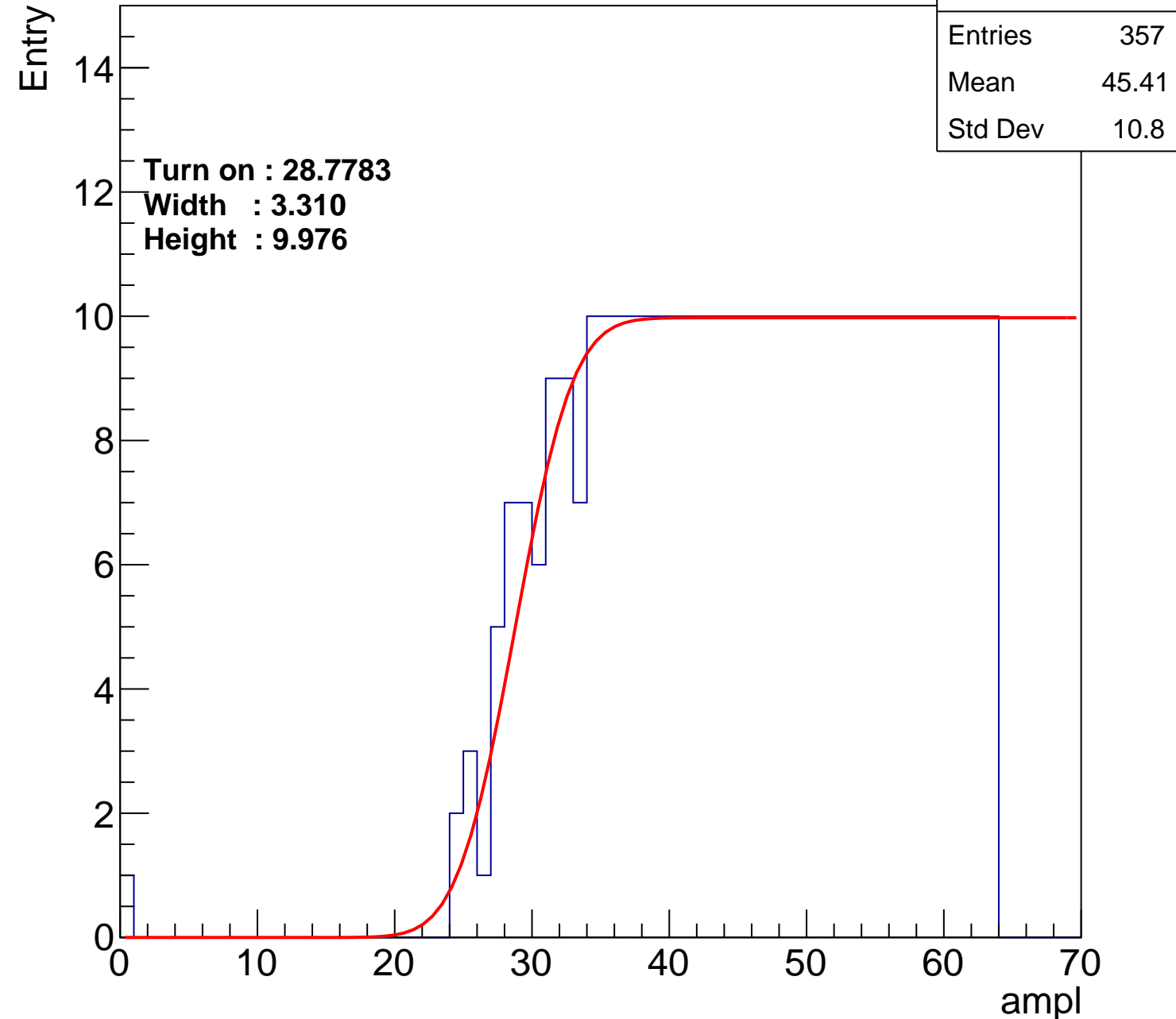
Width : 3.310

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.4962

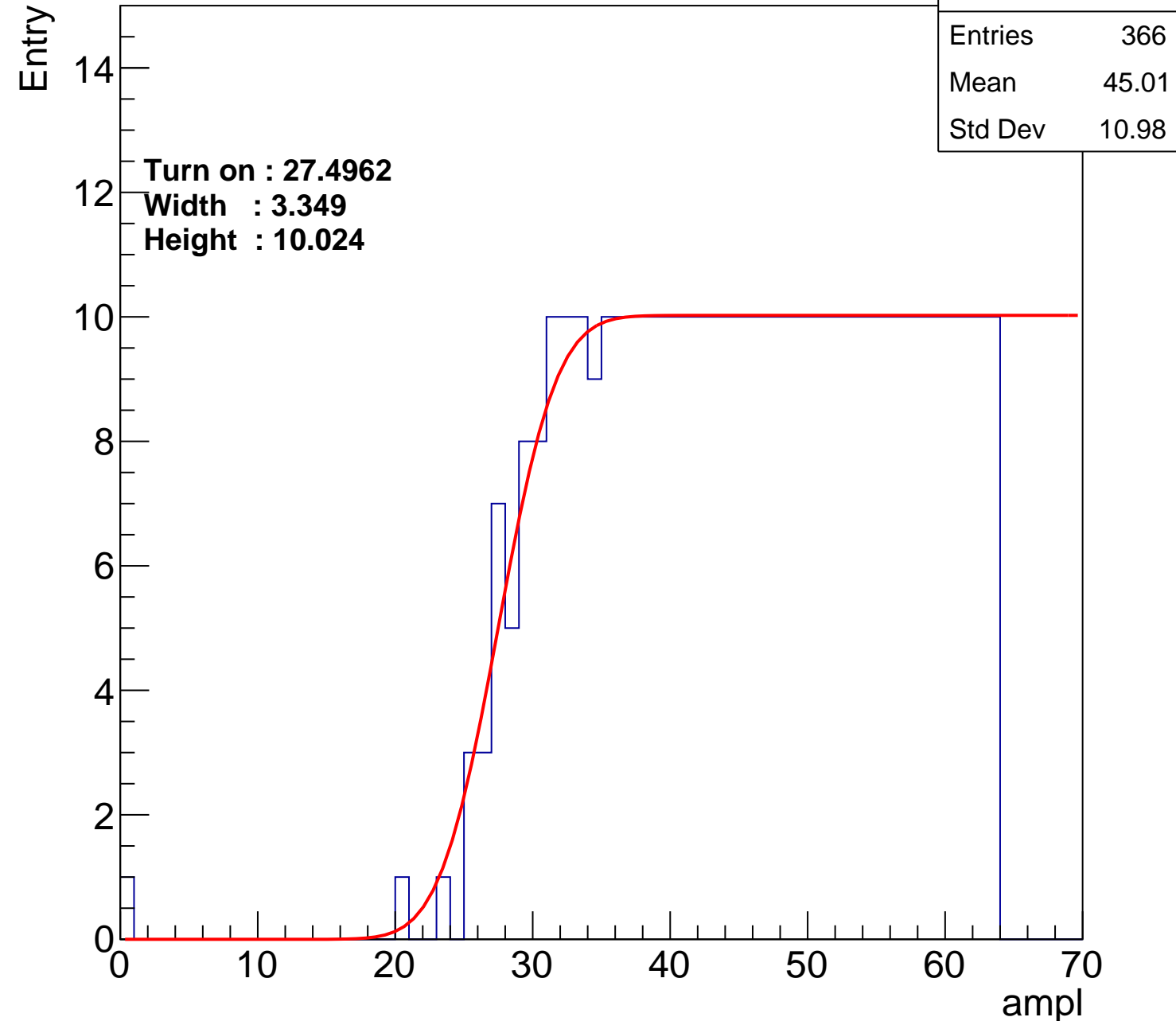
Width : 3.349

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch120

calib_packv5_042523_0143.root, FC#5, port B1

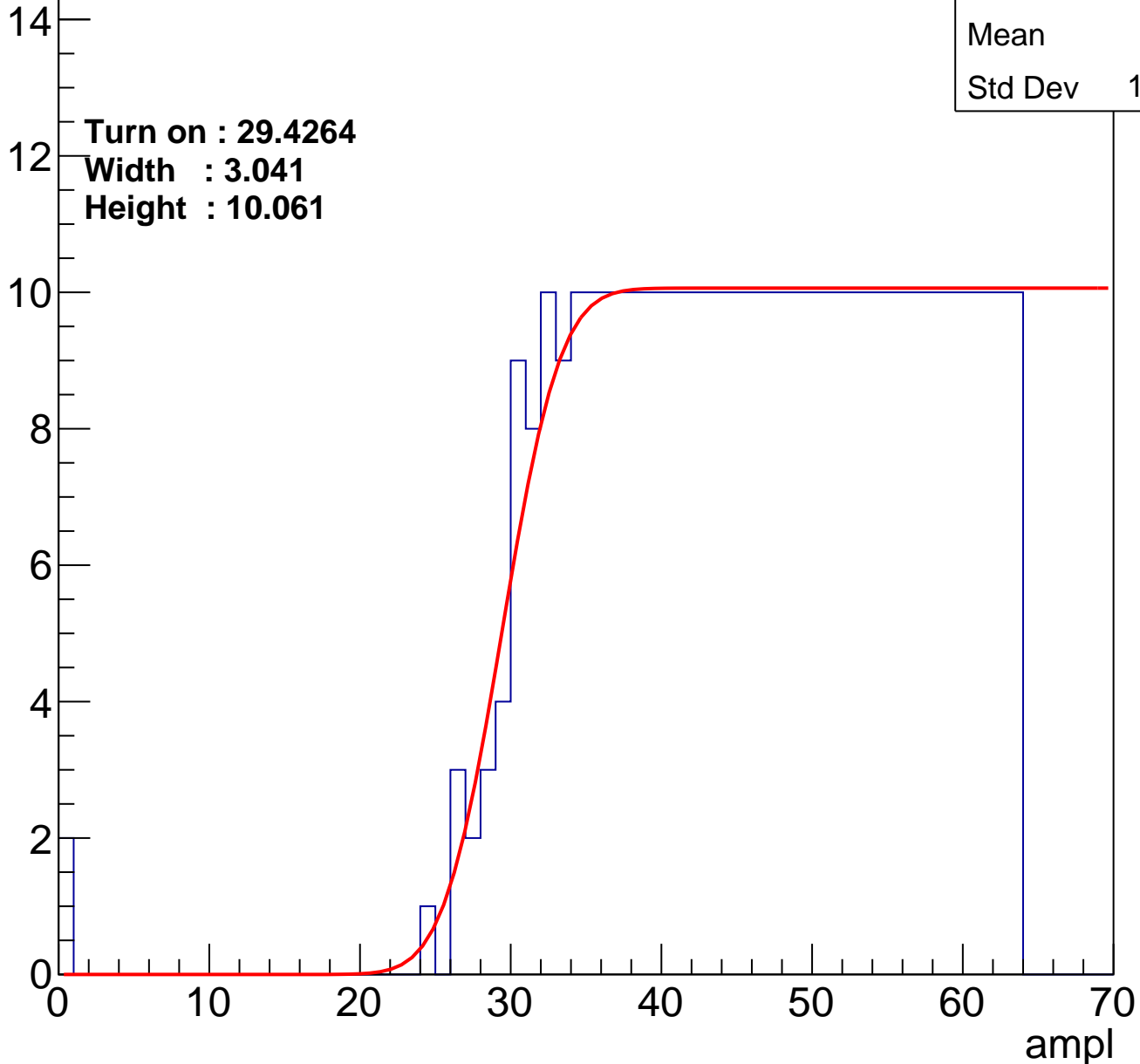
Entries	351
Mean	45.7
Std Dev	10.76

Turn on : 29.4264

Width : 3.041

Height : 10.061

Entry



B0L000S, U6-ch121

calib_packv5_042523_0143.root, FC#5, port B1

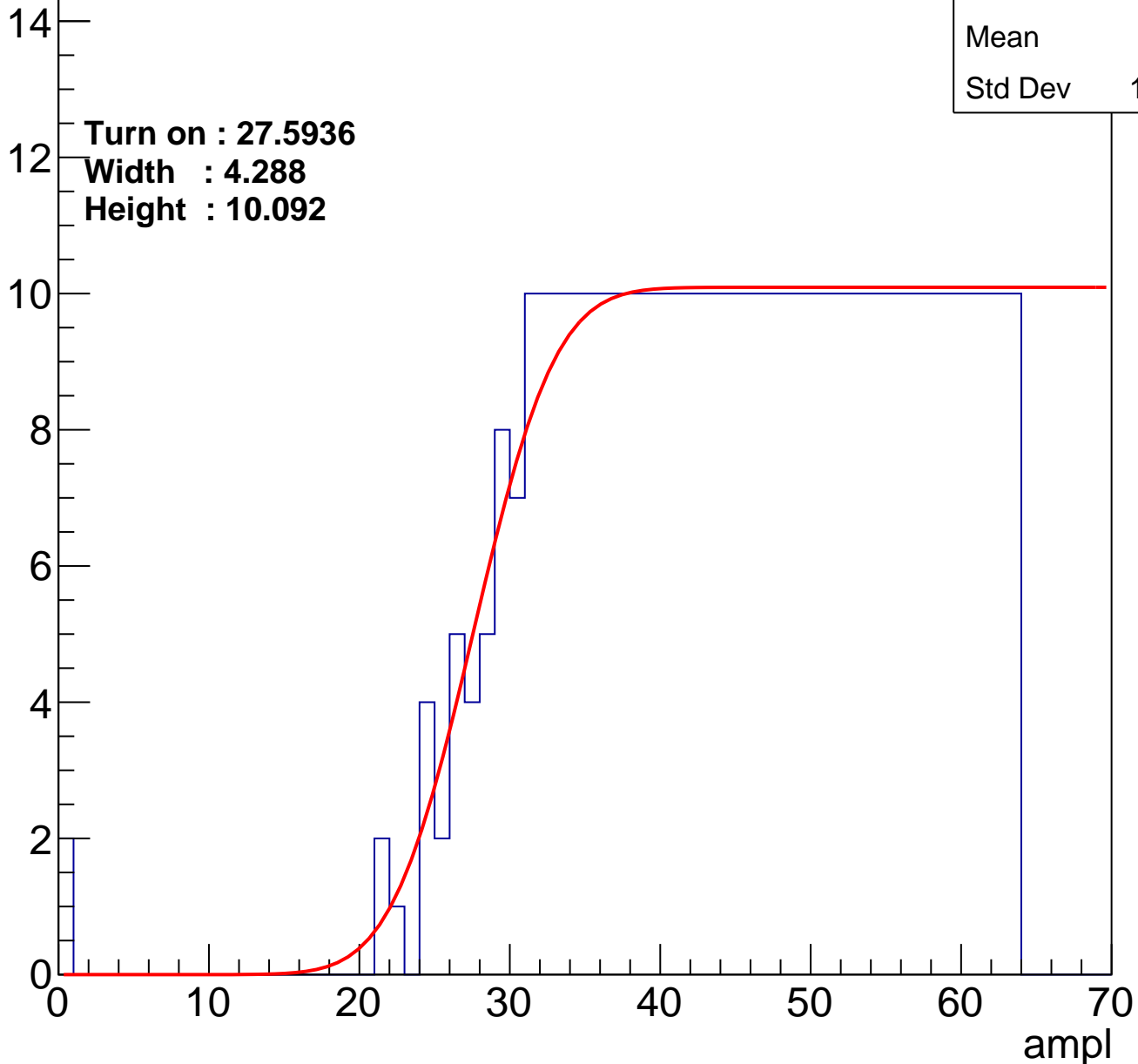
Entries	370
Mean	44.7
Std Dev	11.35

Turn on : 27.5936

Width : 4.288

Height : 10.092

Entry



B0L000S, U6-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.4707

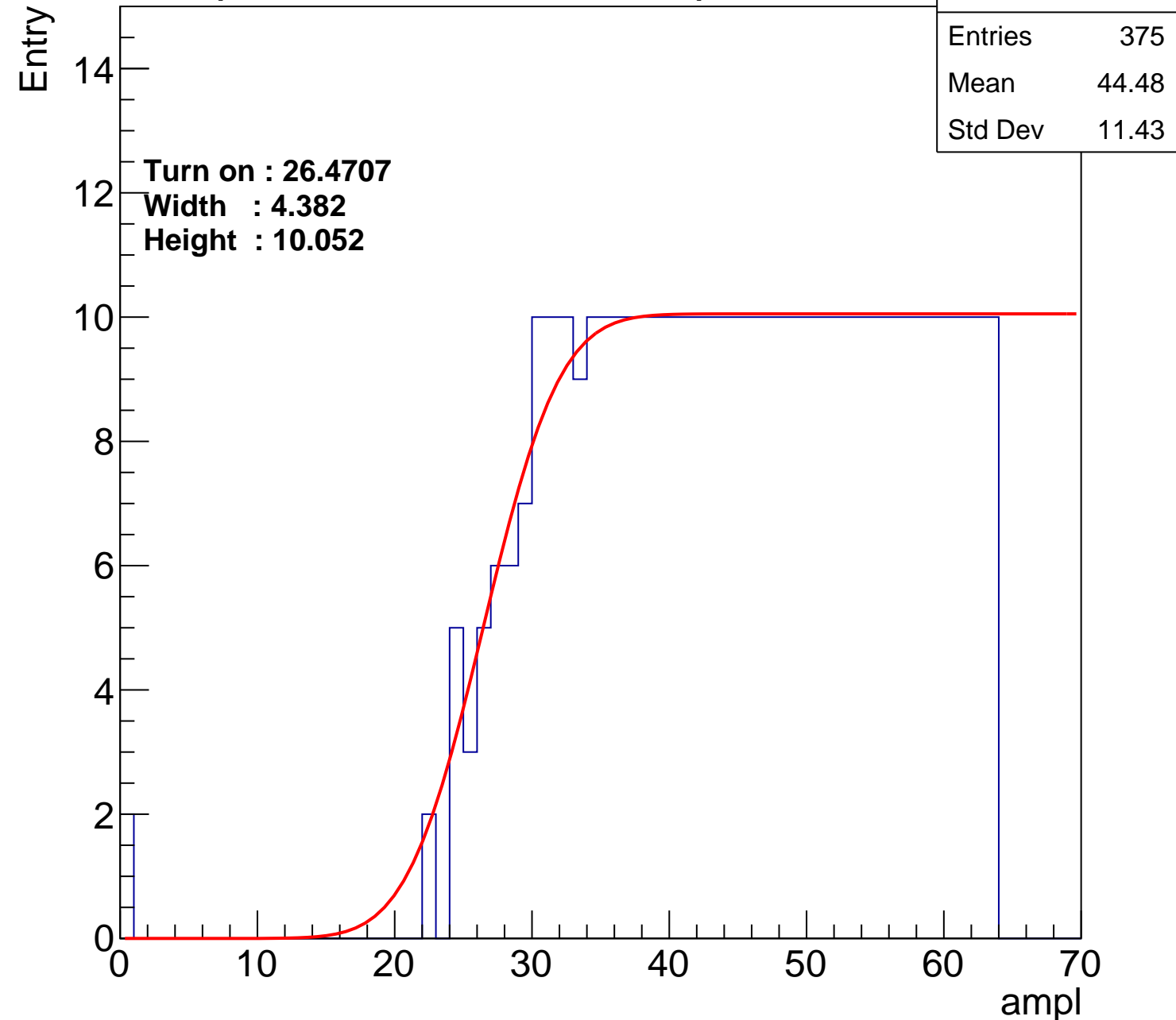
Width : 4.382

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.97
Std Dev	11.12

Turn on : 27.3001

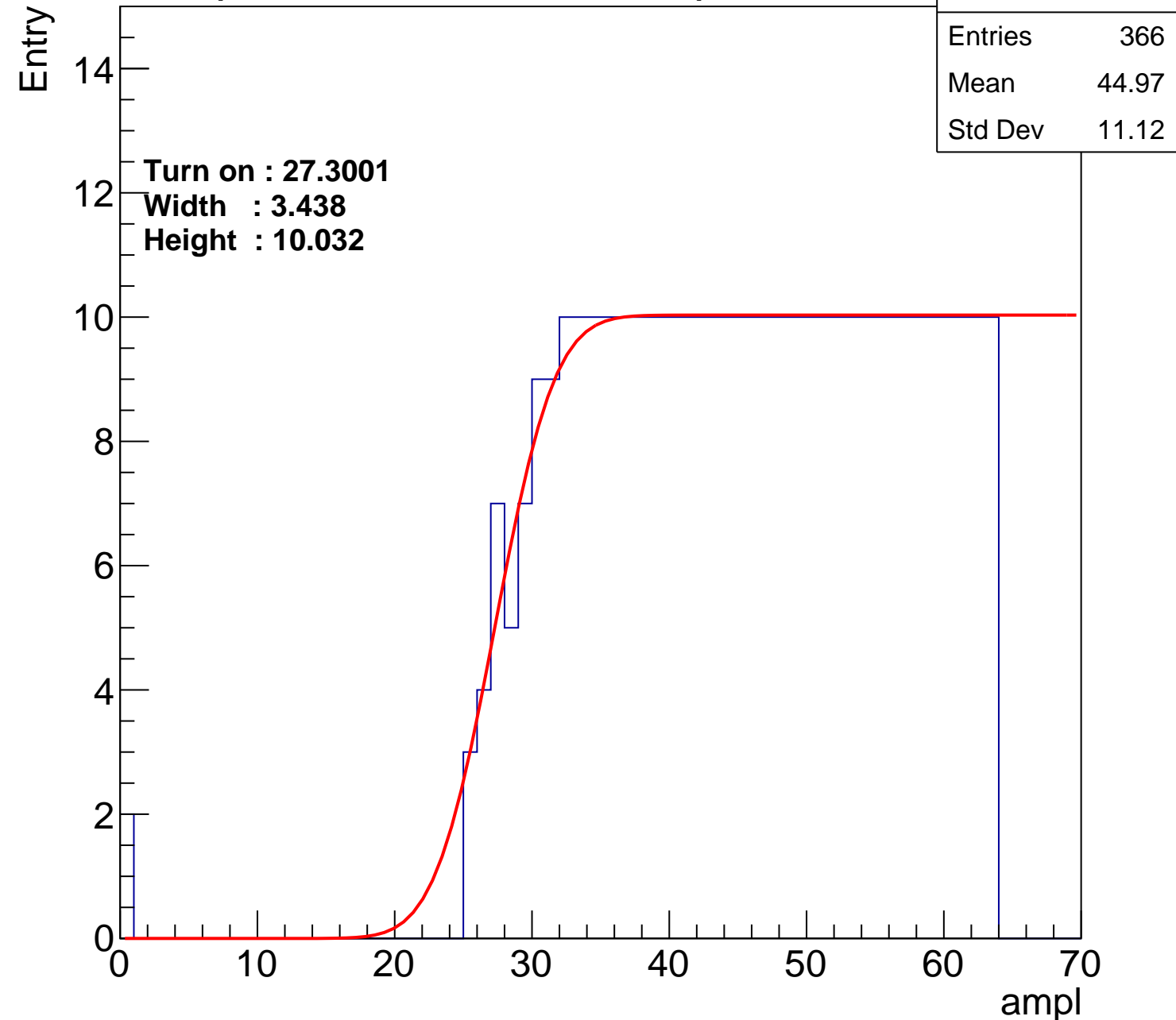
Width : 3.438

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 26.8845

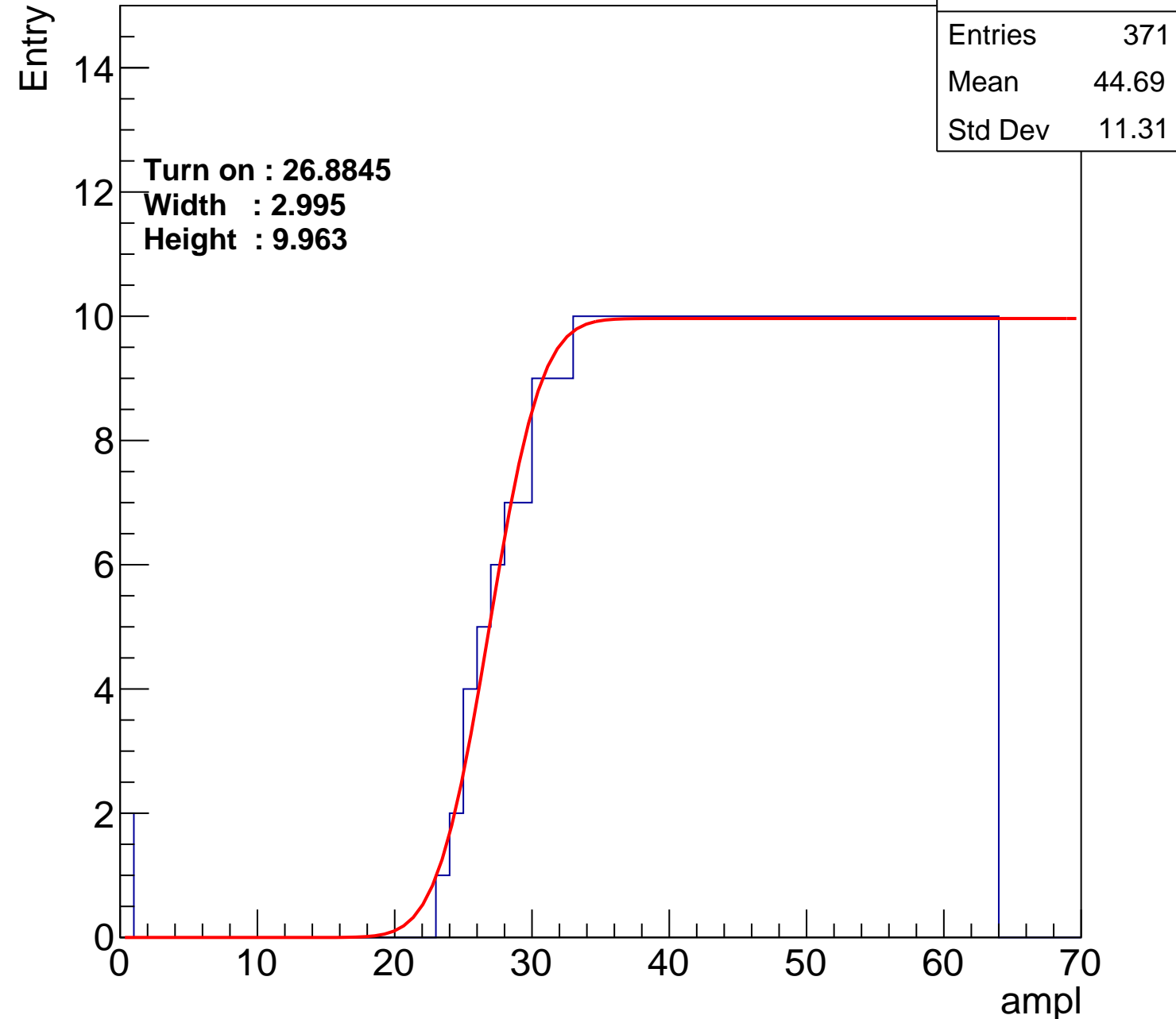
Width : 2.995

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.8023

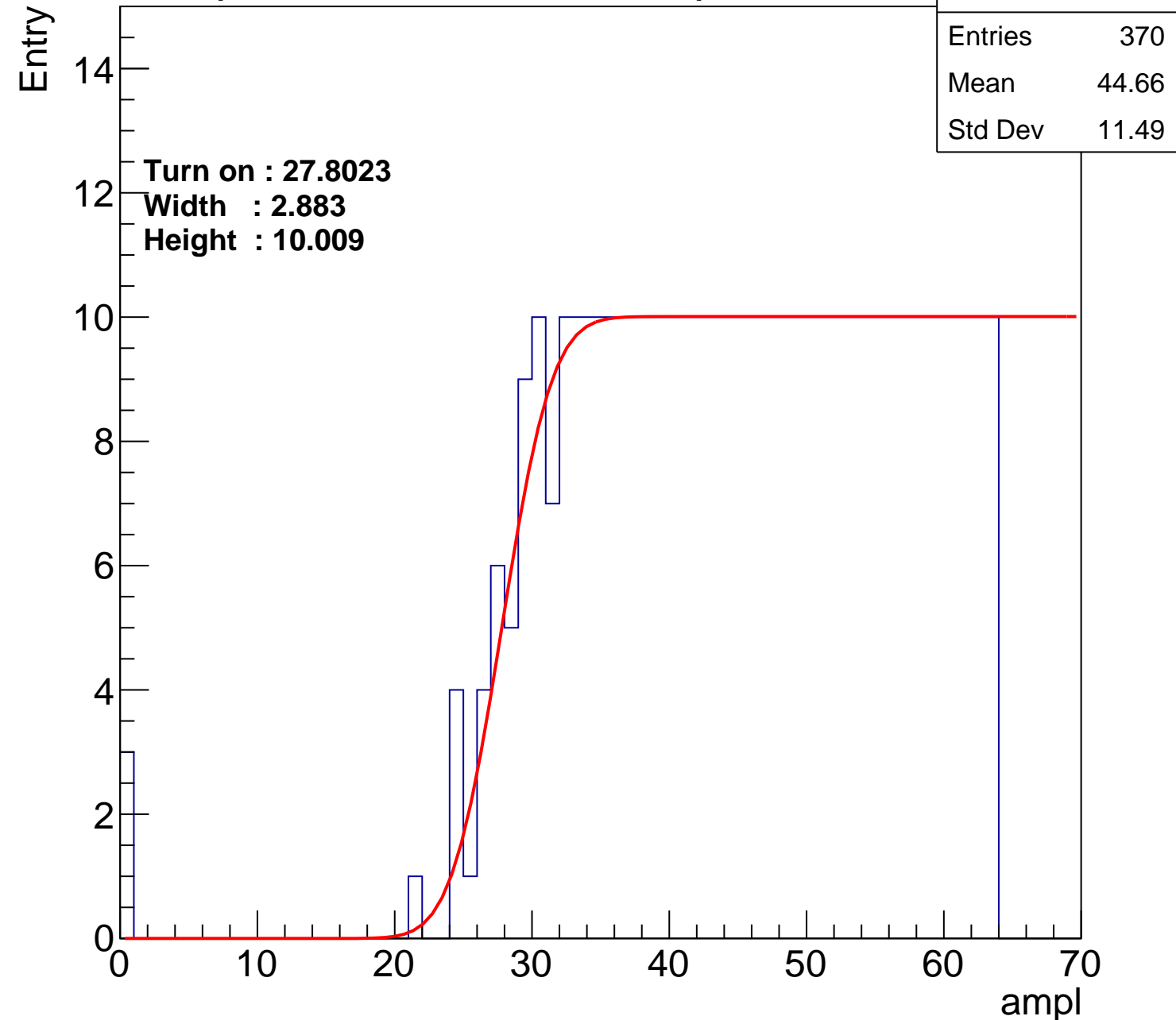
Width : 2.883

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.12
Std Dev	11.84

Turn on : 26.6921

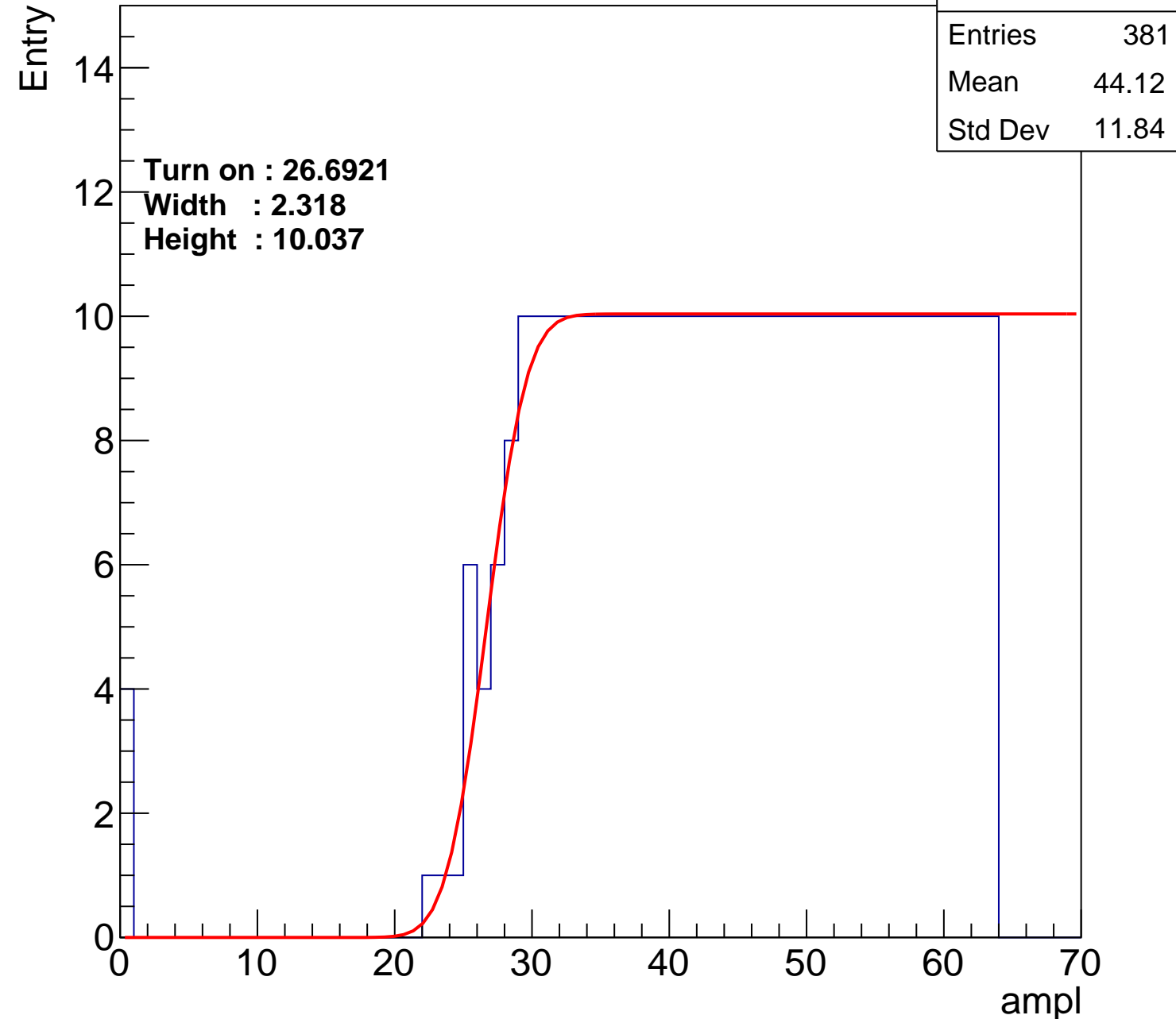
Width : 2.318

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	46.06
Std Dev	10.62

Turn on : 30.5467

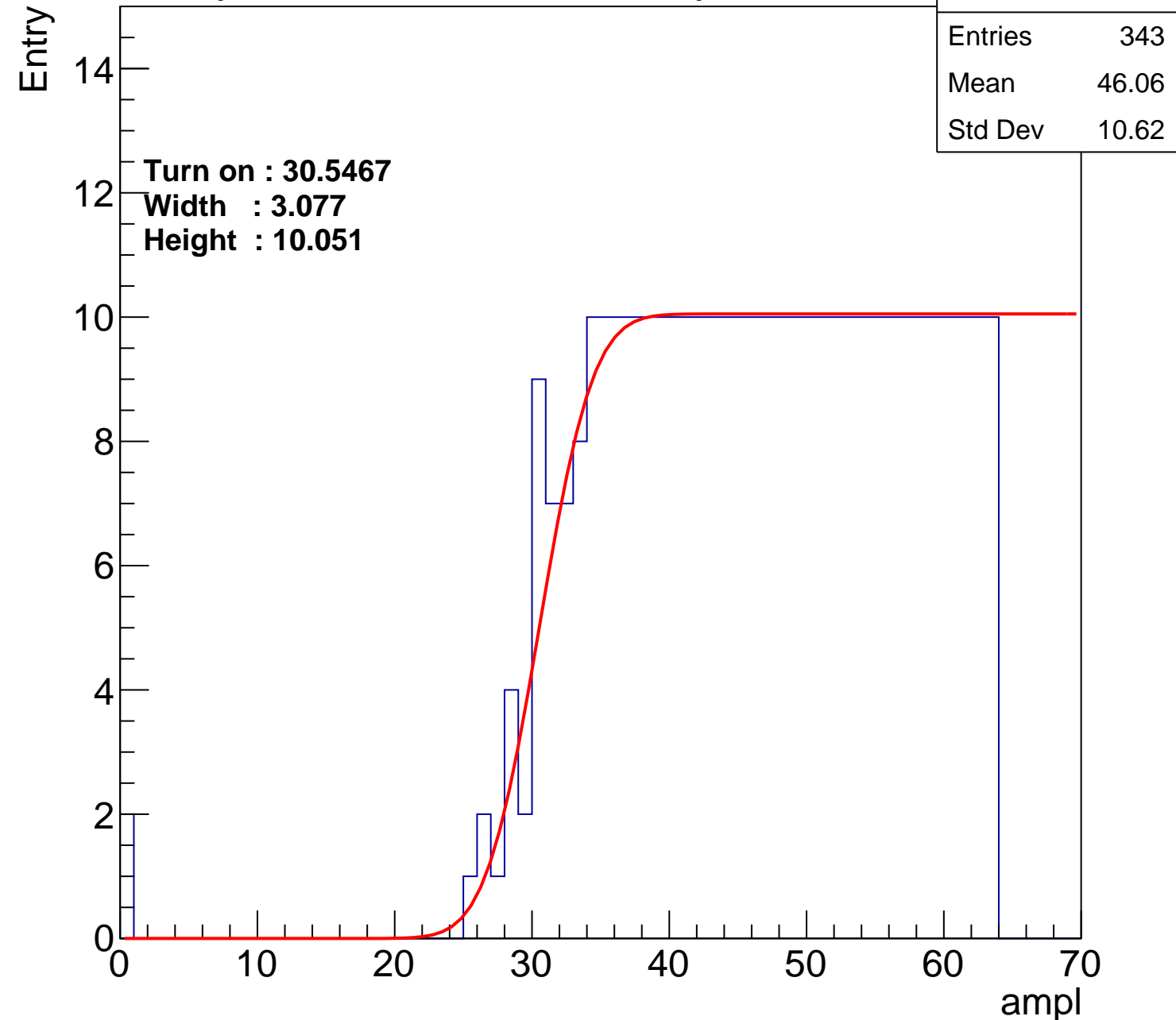
Width : 3.077

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U6-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	46.06
Std Dev	10.62

Turn on : 30.5467

Width : 3.077

Height : 10.051

Entry

