

B1L000S, U3-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.19
Std Dev	12.18

Turn on : 24.1788

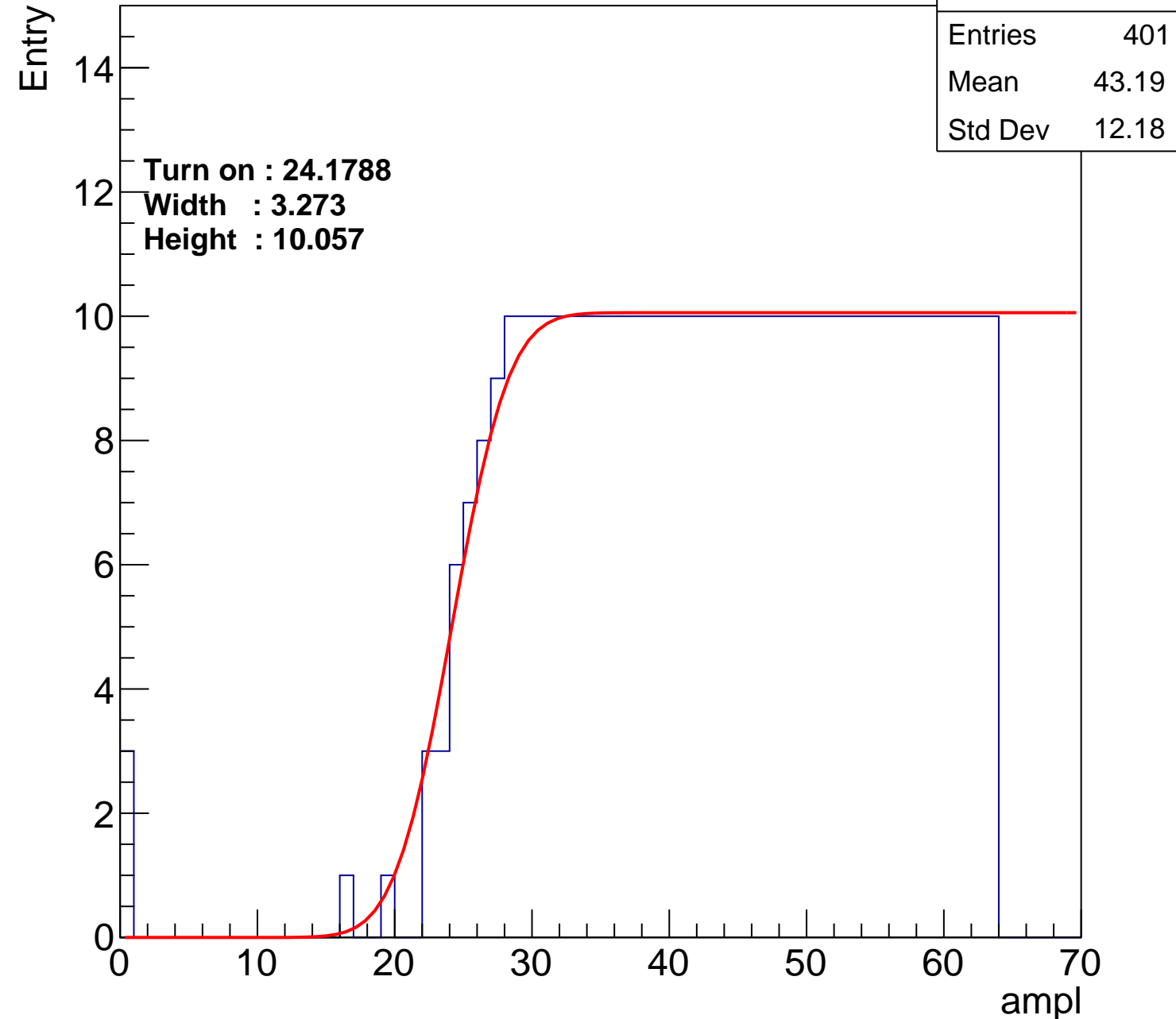
Width : 3.273

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.3
Std Dev	12.27

Turn on : 24.8442

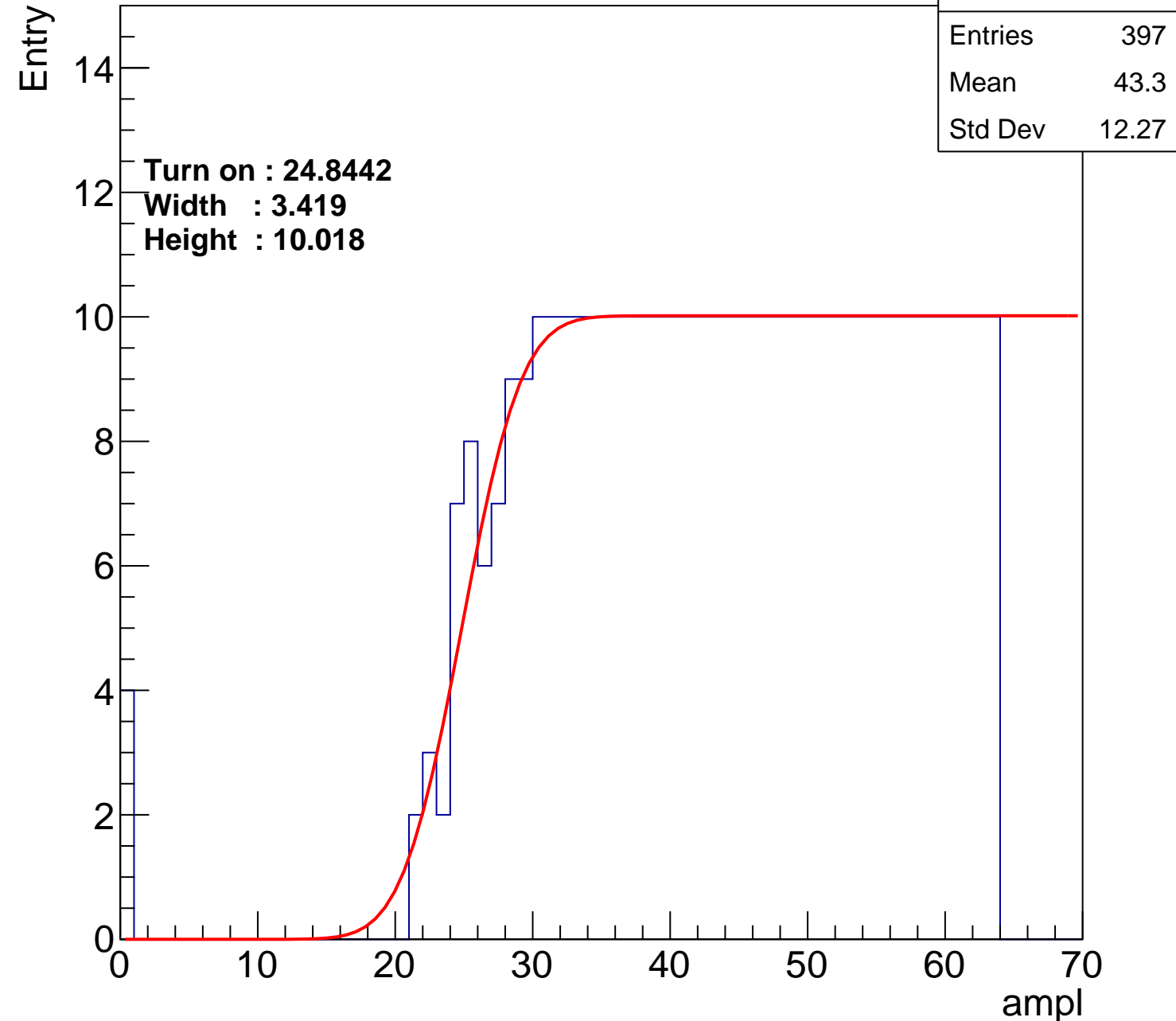
Width : 3.419

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.62
Std Dev	11.97

Turn on : 25.7248

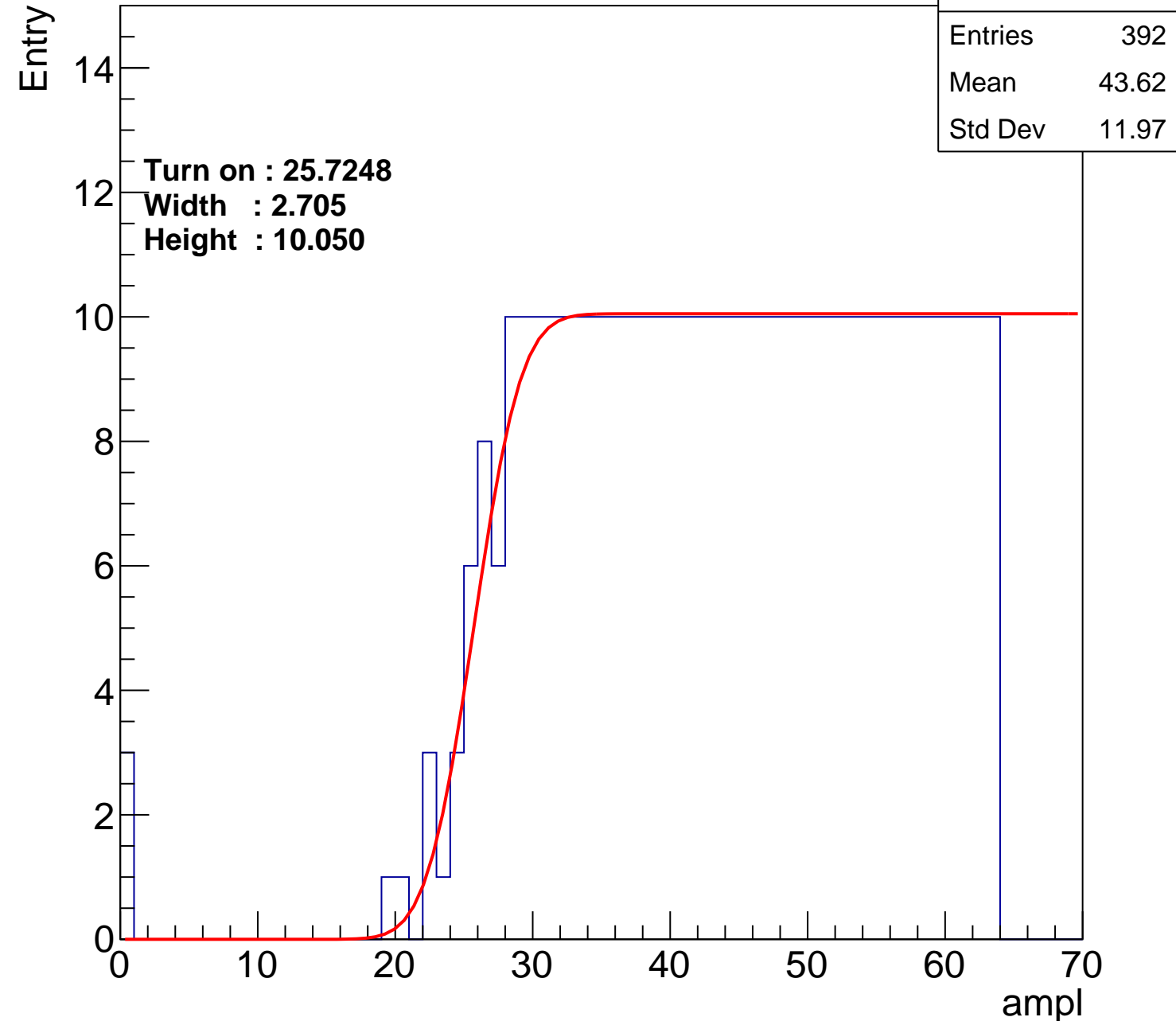
Width : 2.705

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.88
Std Dev	11.89

Turn on : 26.5664

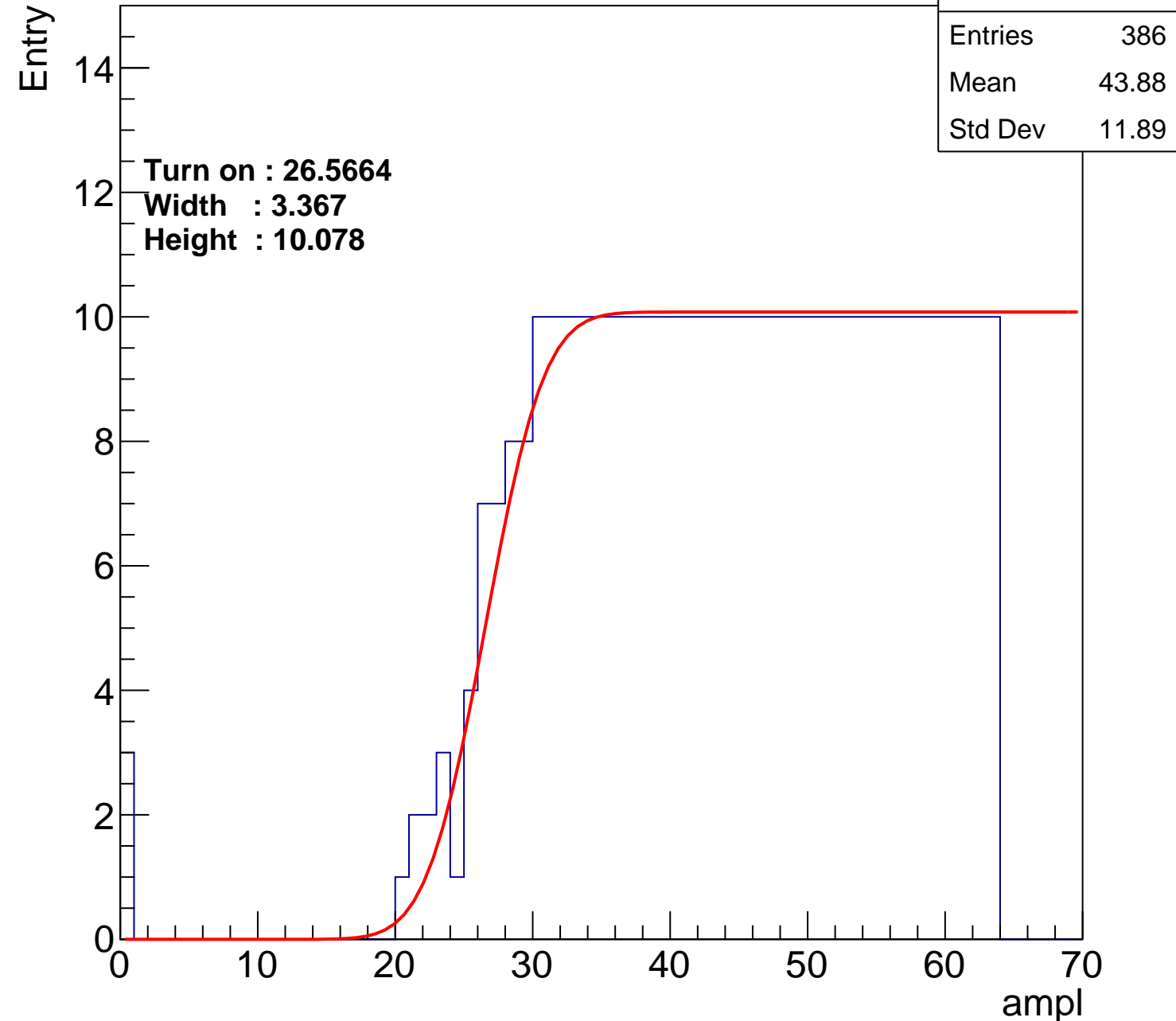
Width : 3.367

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch4

calib_packv5_042523_0143.root, FC#3, port B3

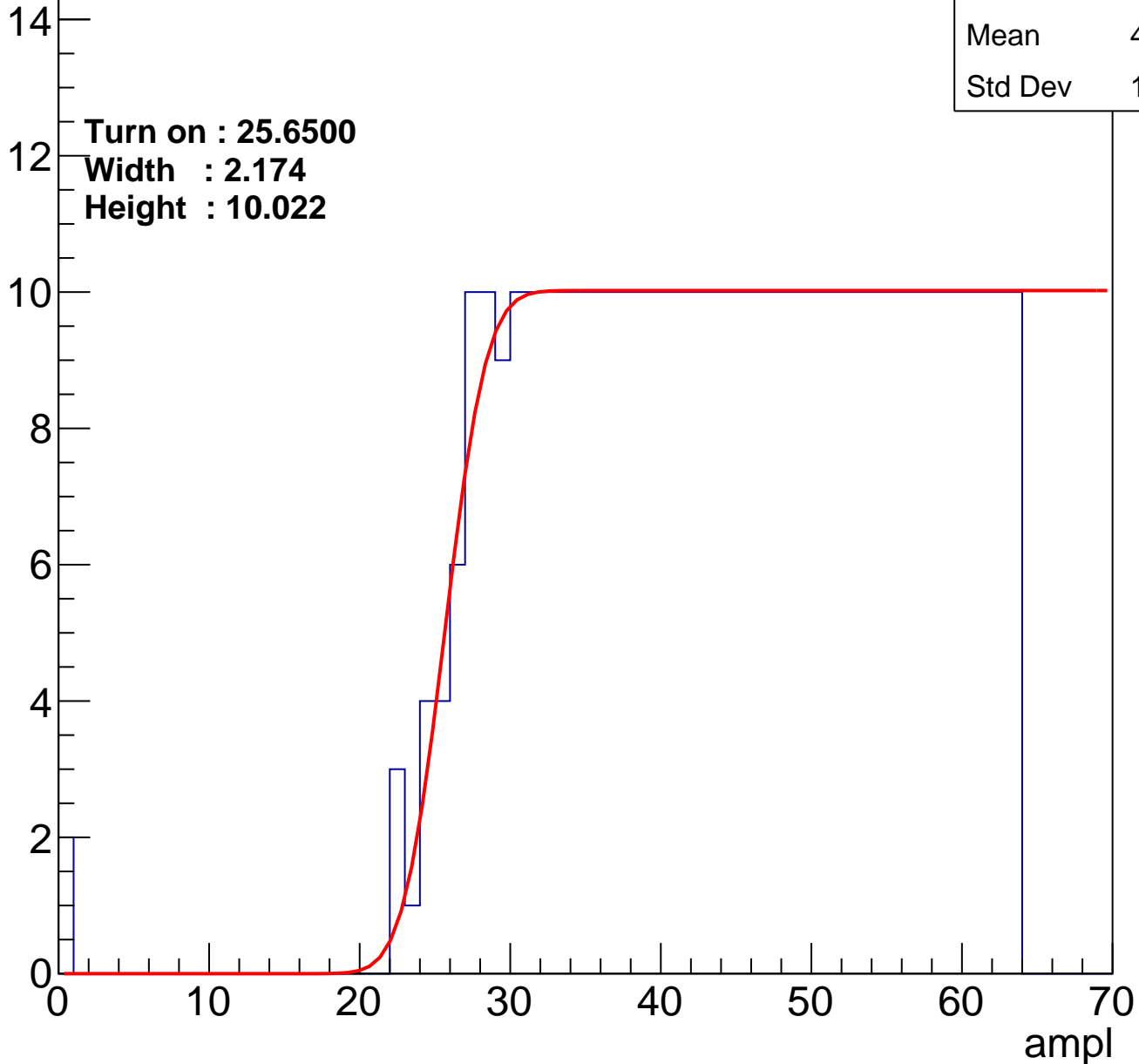
Entries	389
Mean	43.86
Std Dev	11.68

Turn on : 25.6500

Width : 2.174

Height : 10.022

Entry



B1L000S, U3-ch5

calib_packv5_042523_0143.root, FC#3, port B3

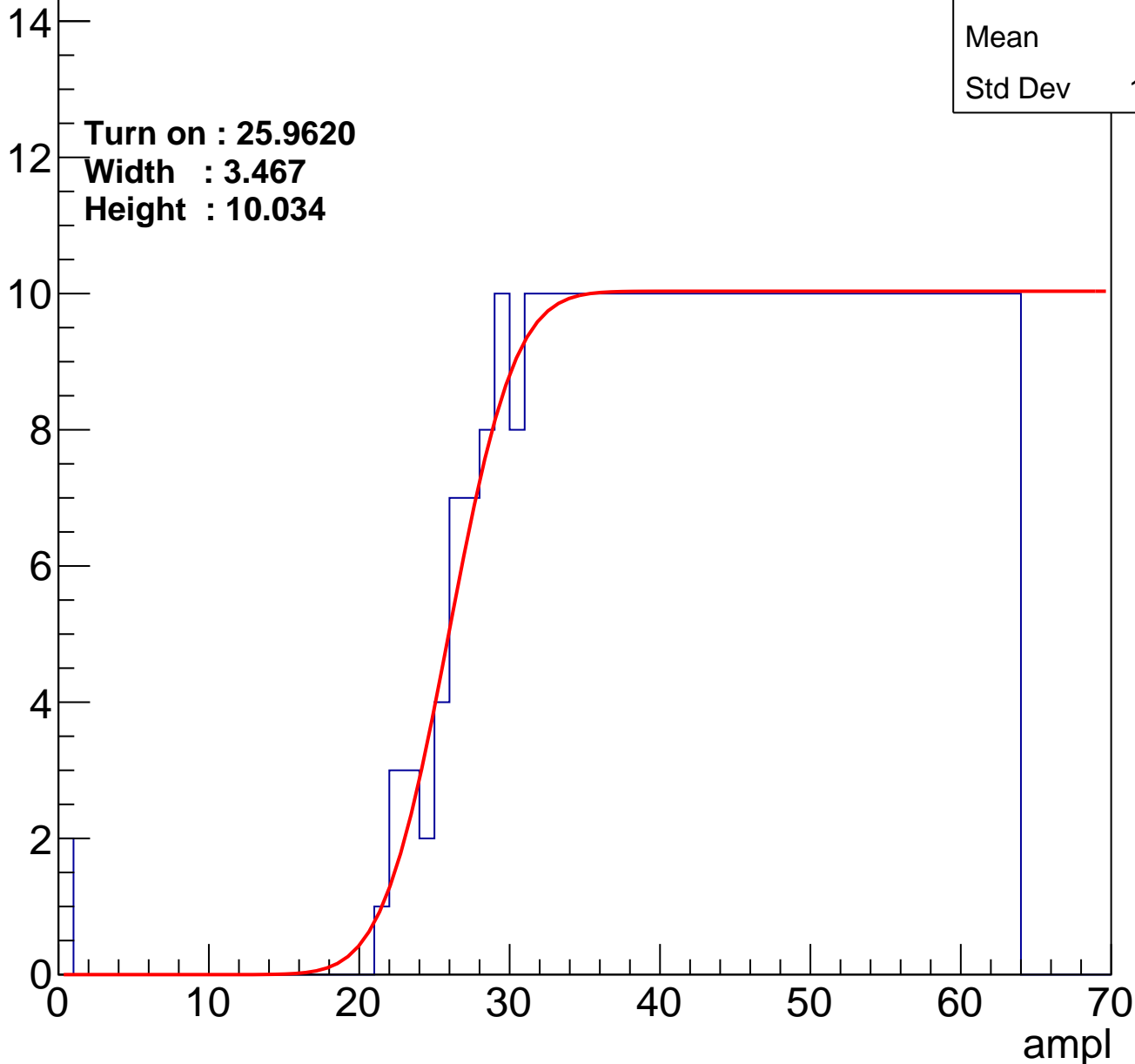
Entry

Entries	385
Mean	44
Std Dev	11.67

Turn on : 25.9620

Width : 3.467

Height : 10.034



B1L000S, U3-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	413
Mean	42.38
Std Dev	13.02

Turn on : 23.8234

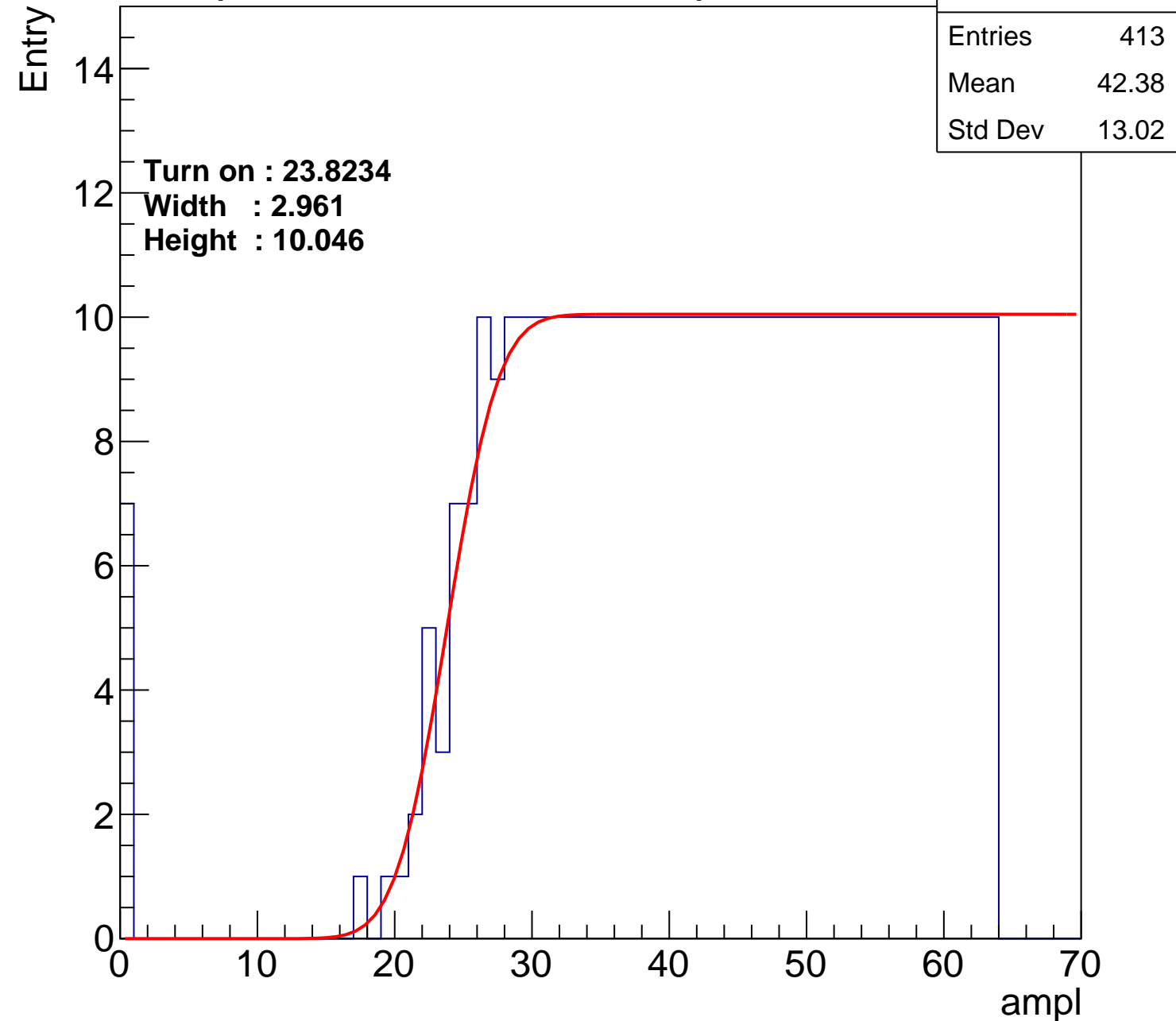
Width : 2.961

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.75
Std Dev	11.8

Turn on : 24.8348

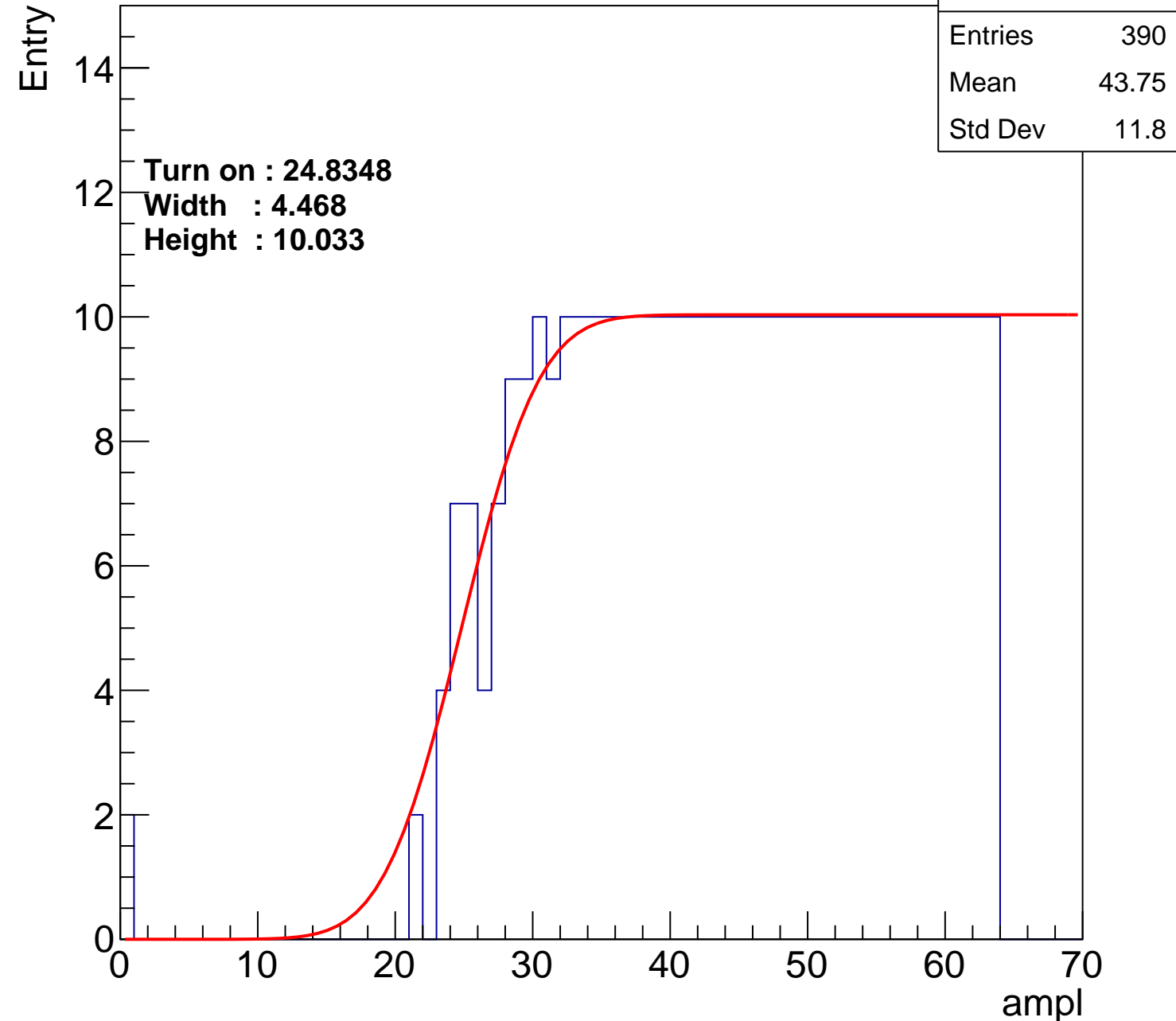
Width : 4.468

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch8

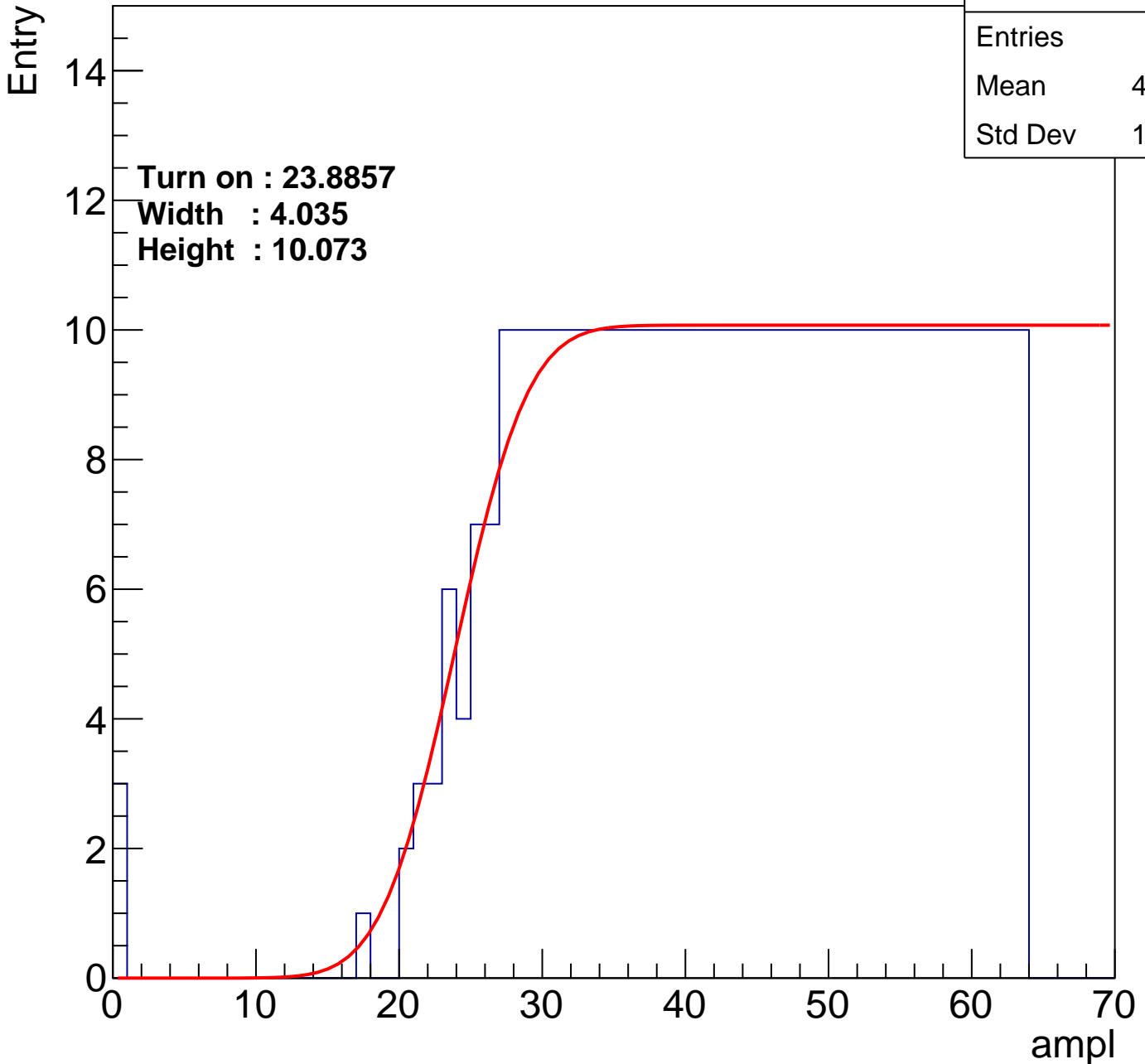
calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	42.92
Std Dev	12.34

Turn on : 23.8857

Width : 4.035

Height : 10.073



B1L000S, U3-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.66
Std Dev	11.25

Turn on : 27.1784

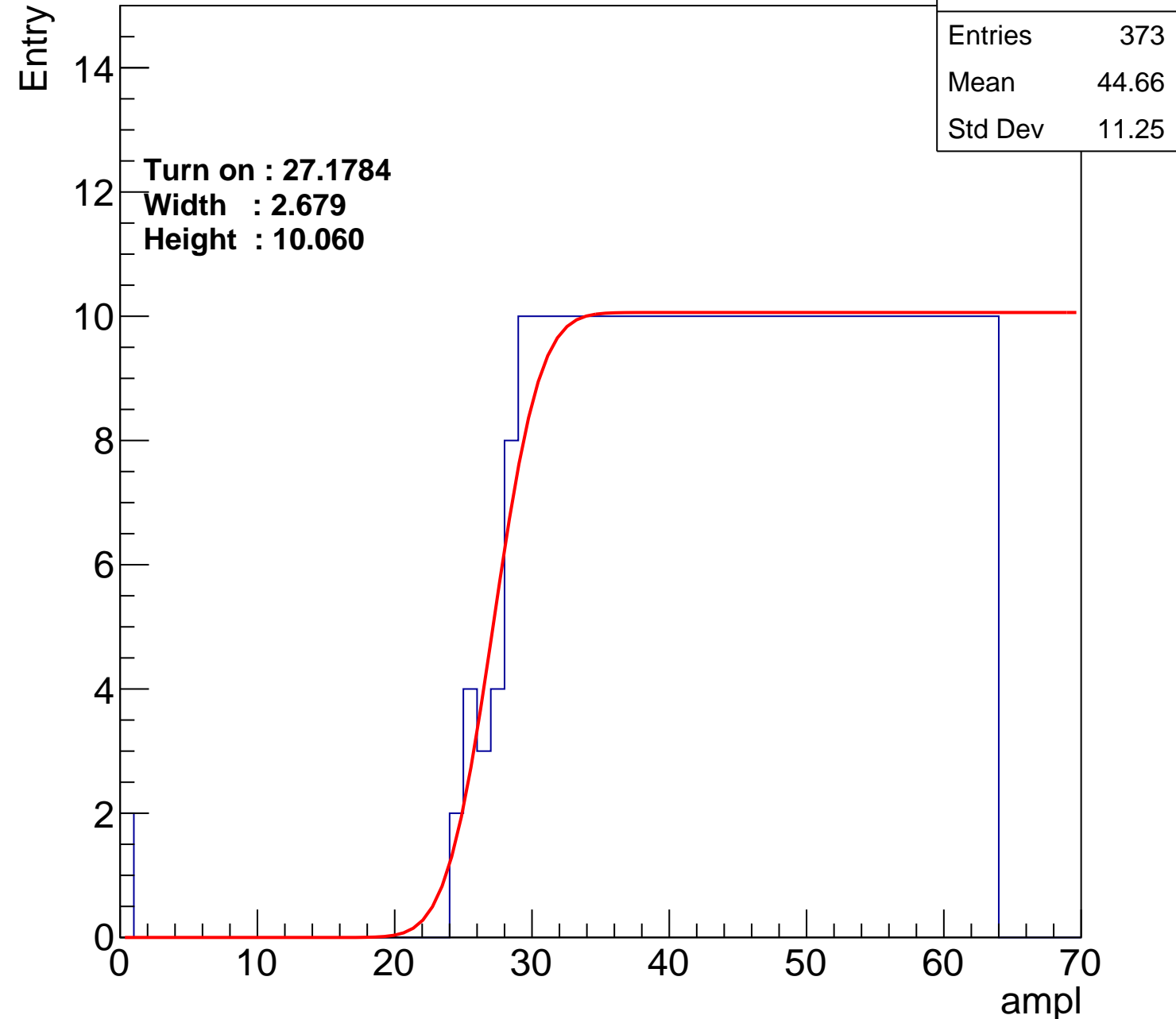
Width : 2.679

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 26.3091

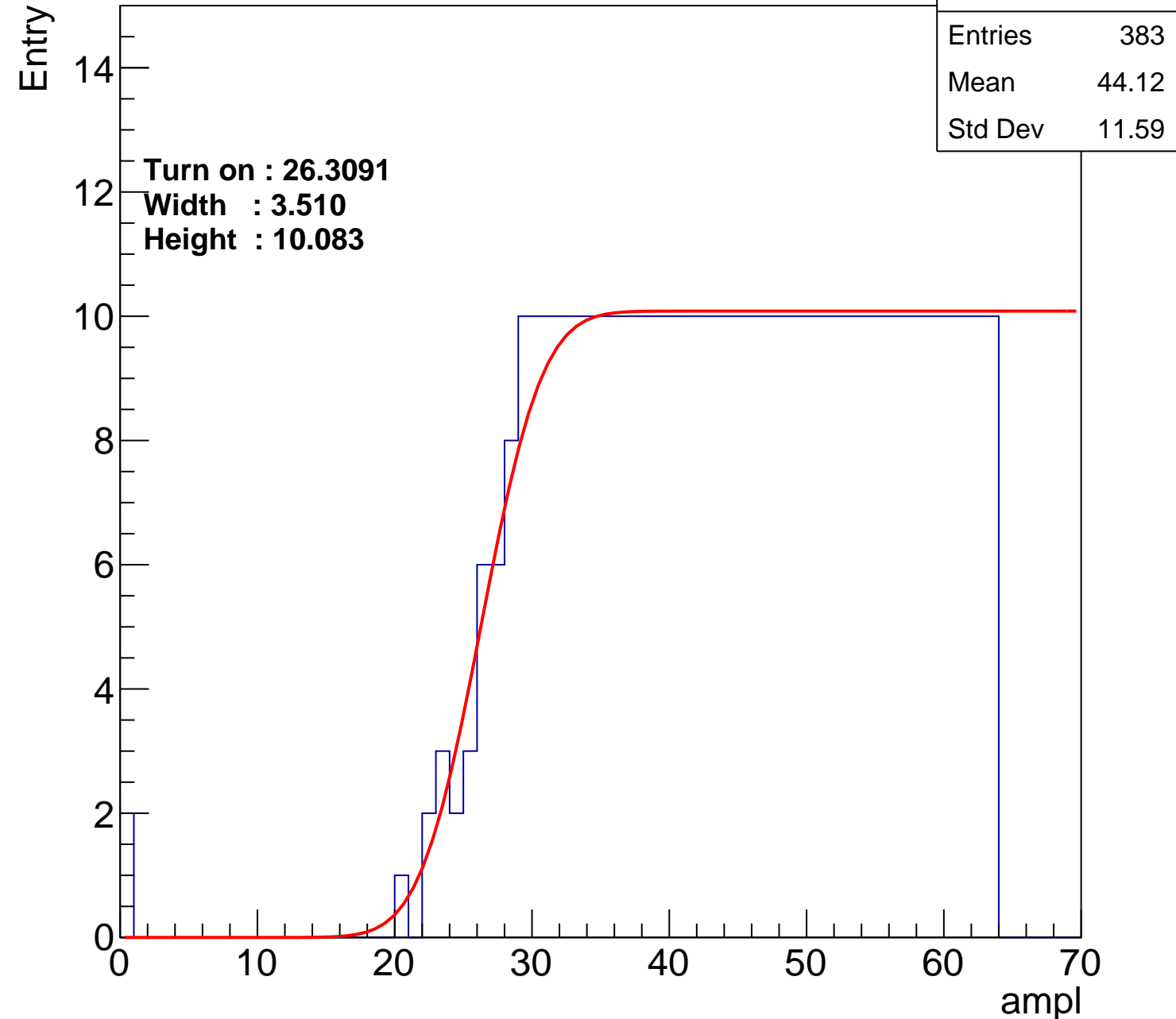
Width : 3.510

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 26.4341

Width : 2.570

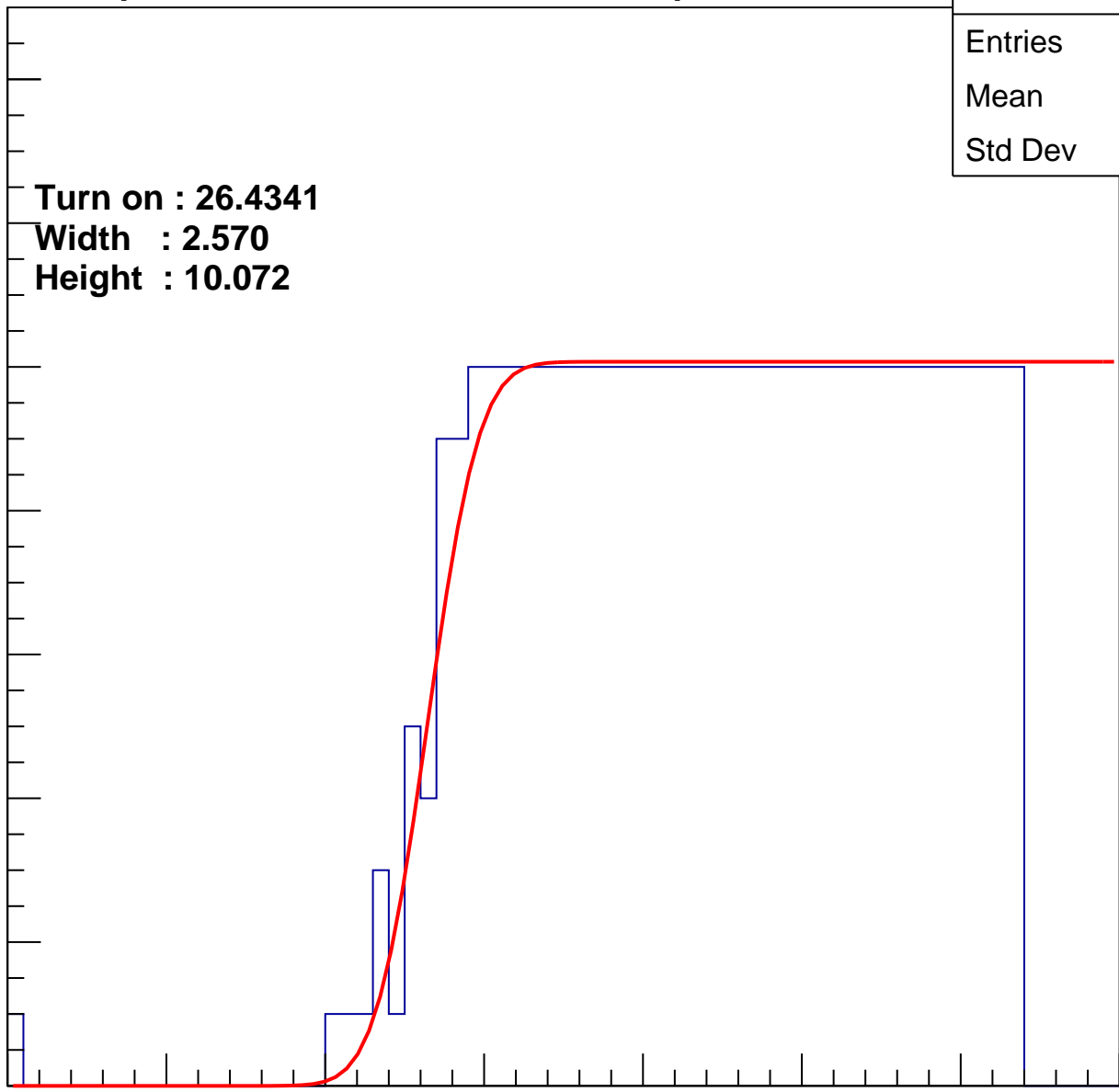
Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U3-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.2920

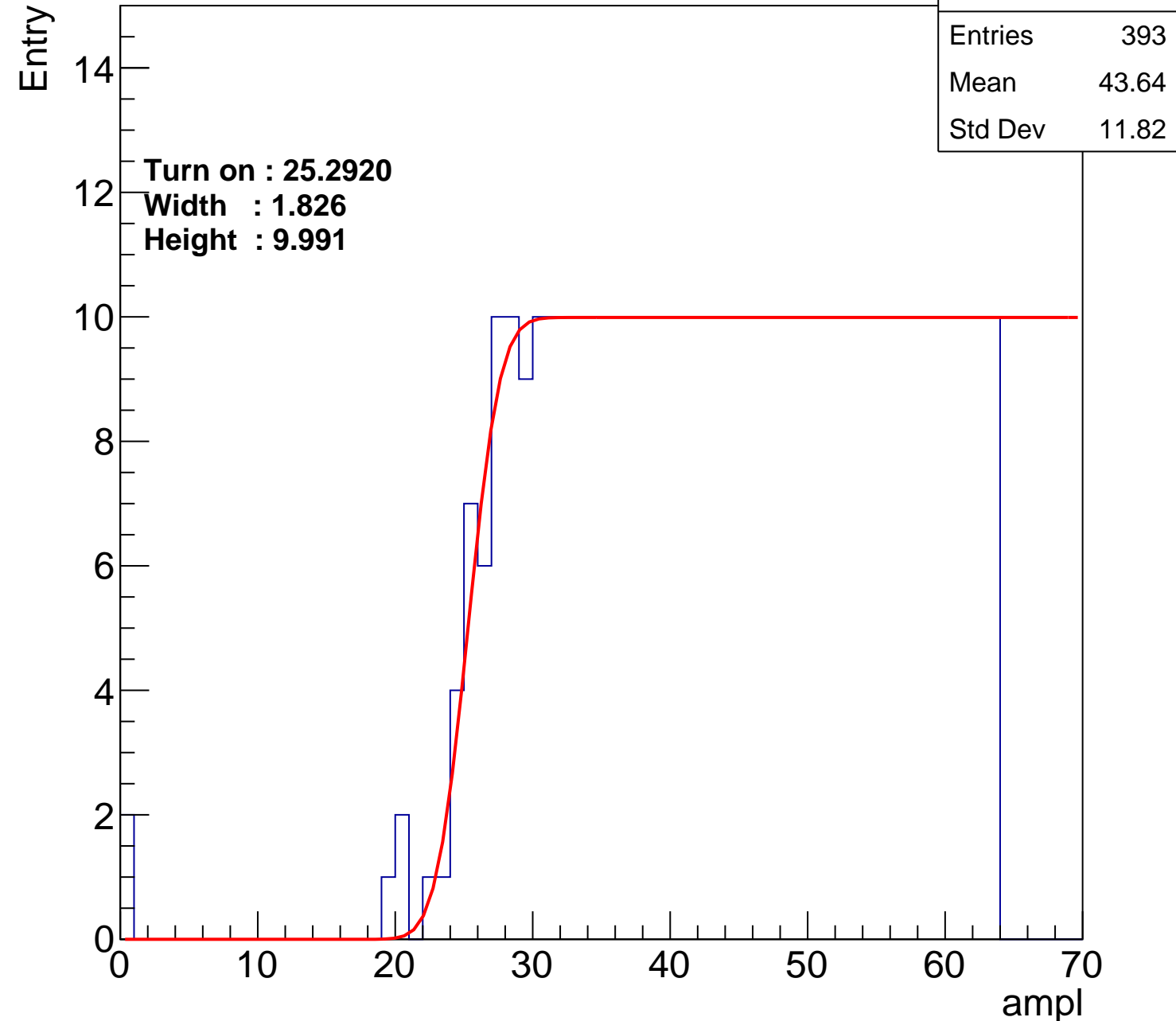
Width : 1.826

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.4
Std Dev	11.88

Turn on : 24.4942

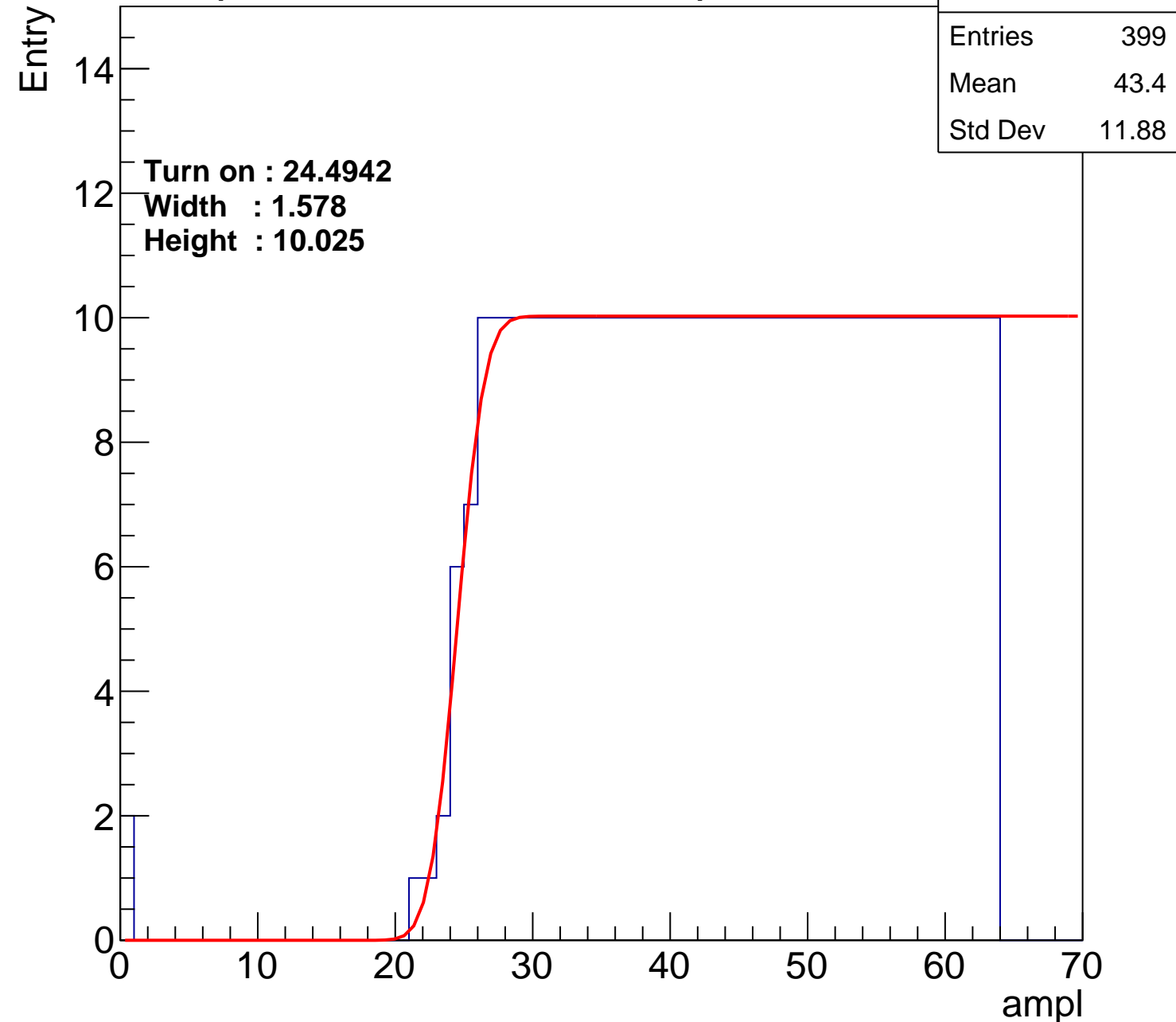
Width : 1.578

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.4719

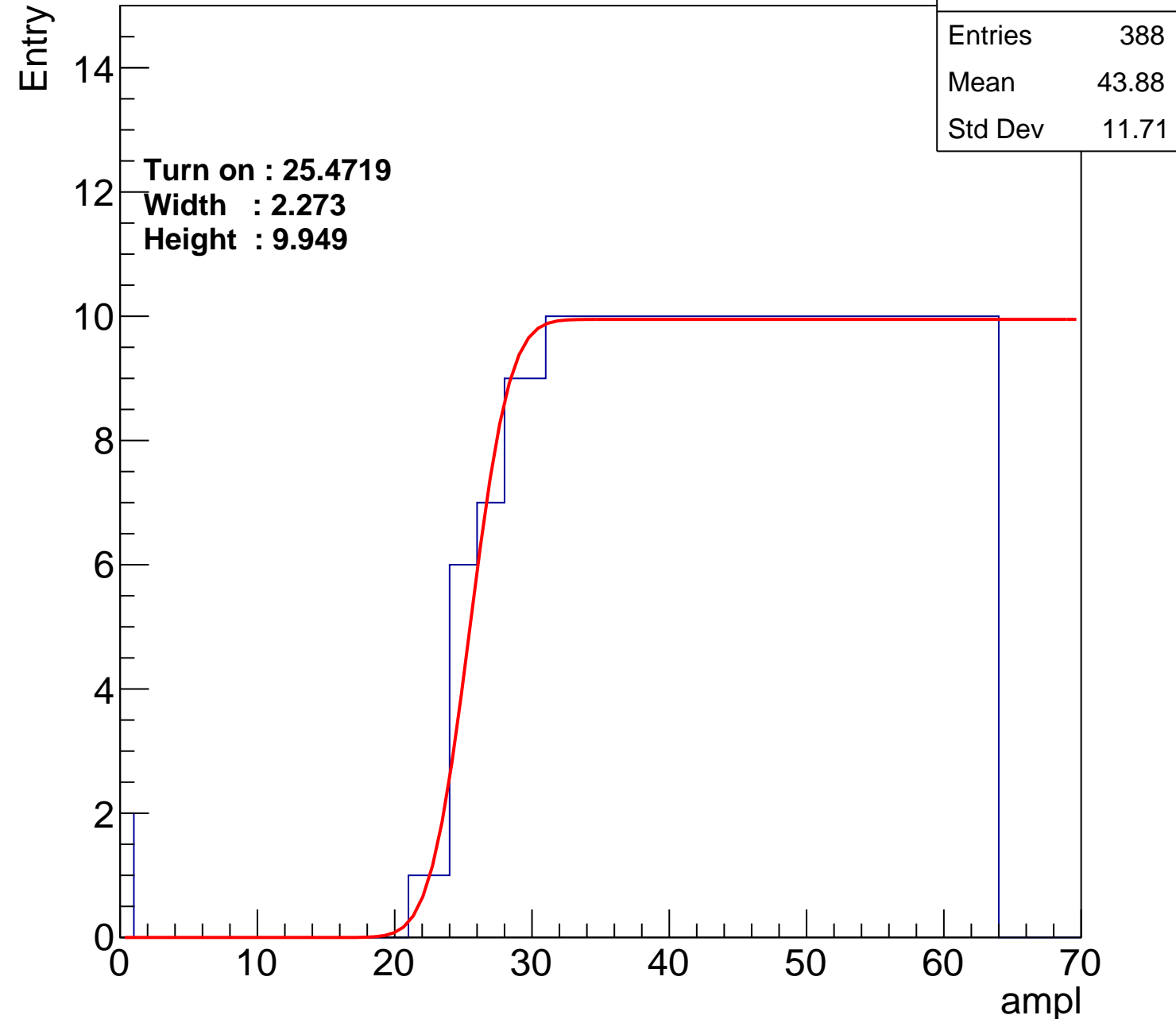
Width : 2.273

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.41
Std Dev	10.69

Turn on : 28.2682

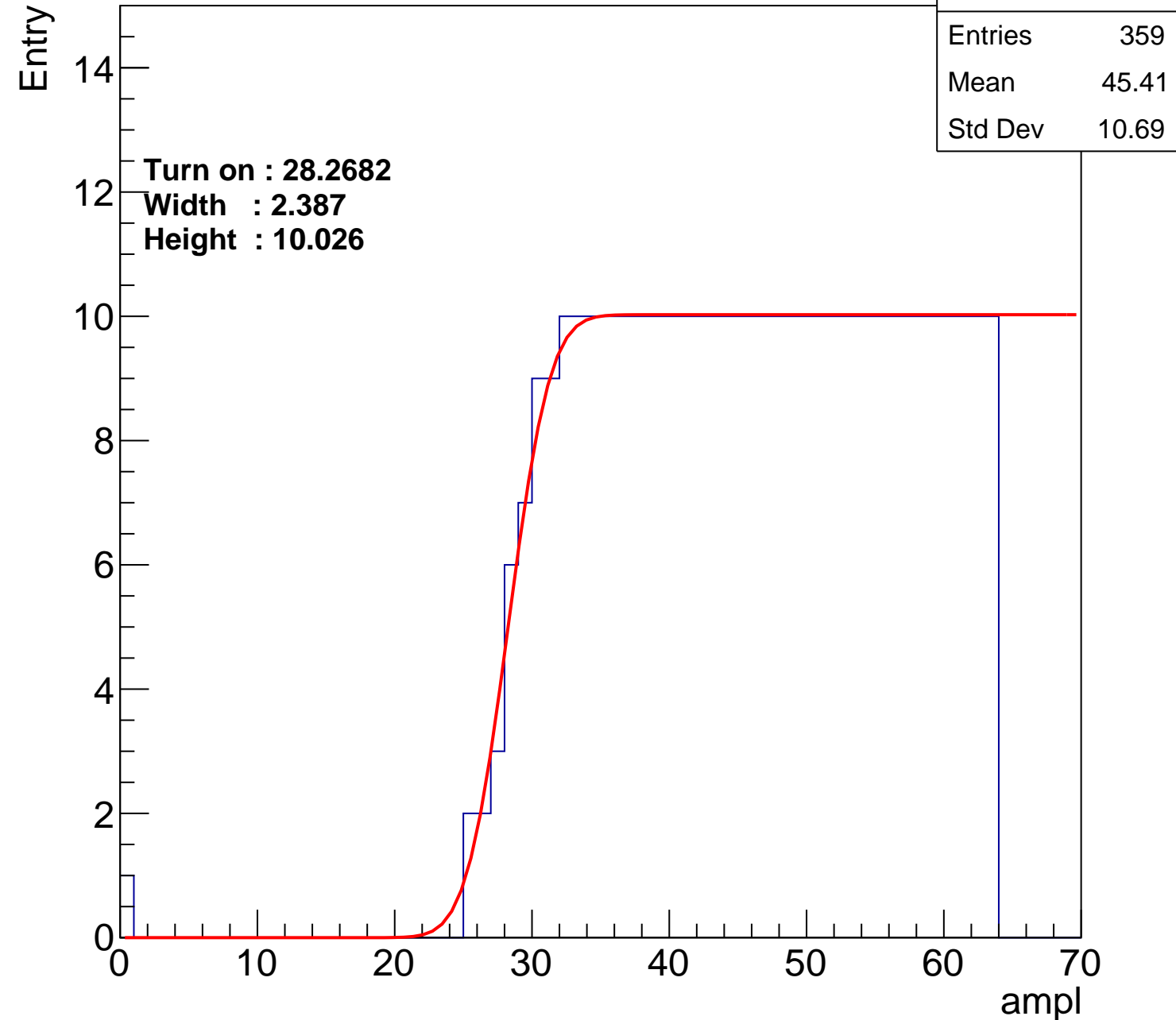
Width : 2.387

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.0737

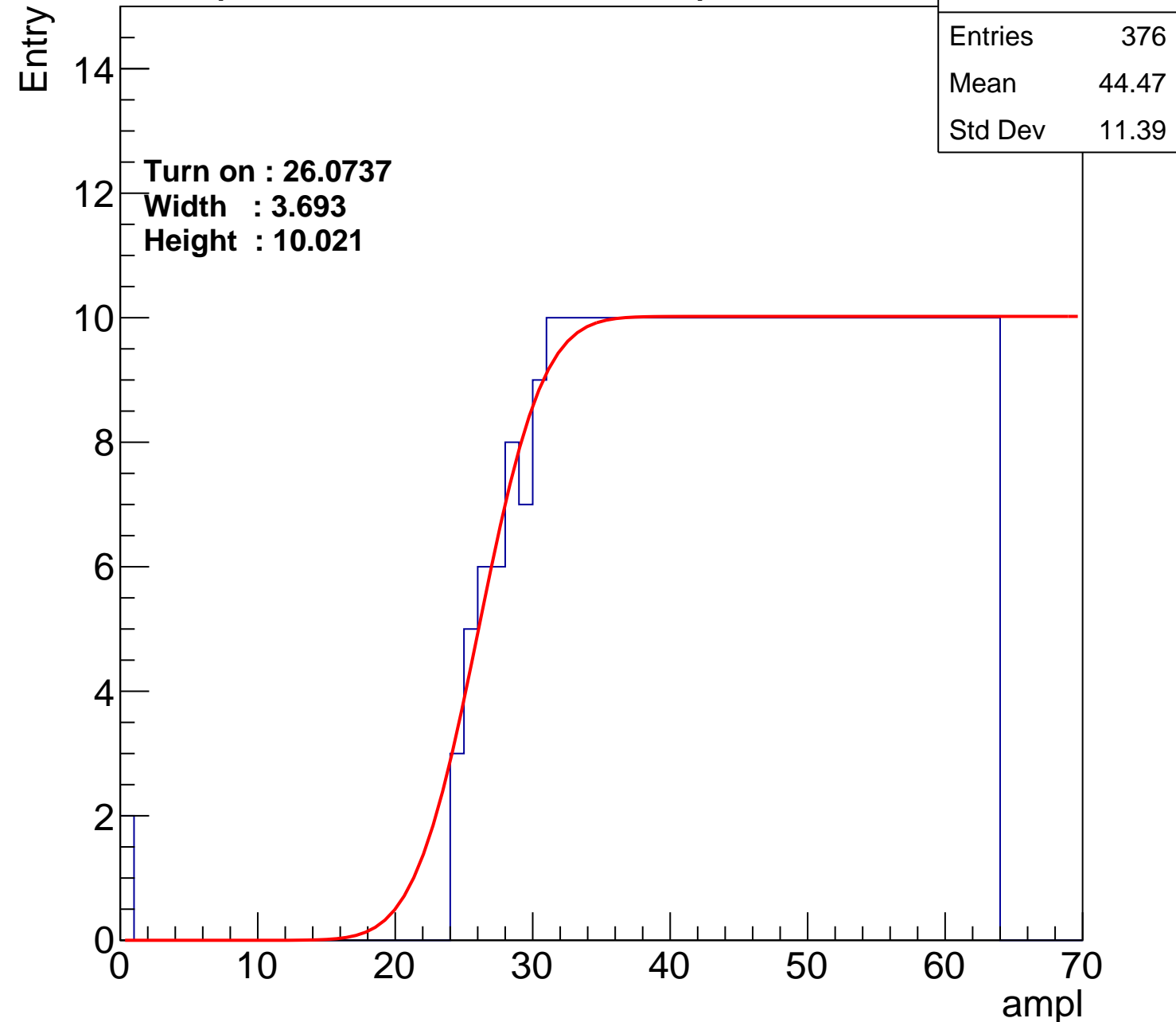
Width : 3.693

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 26.9834

Width : 2.248

Height : 10.019

Entry

14

12

10

8

6

4

2

0

0

10

20

30

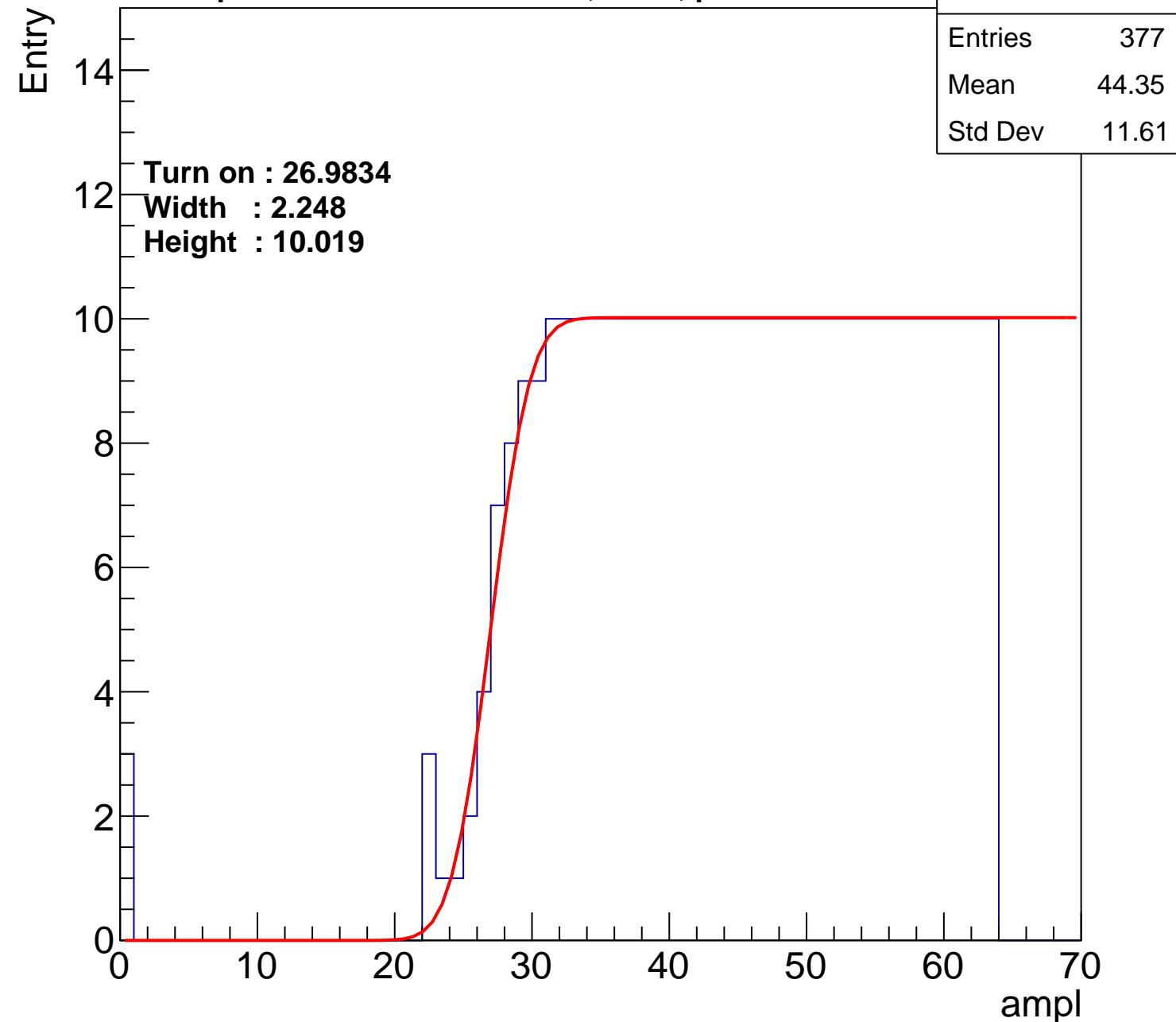
40

50

60

70

ampl



B1L000S, U3-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 27.8970

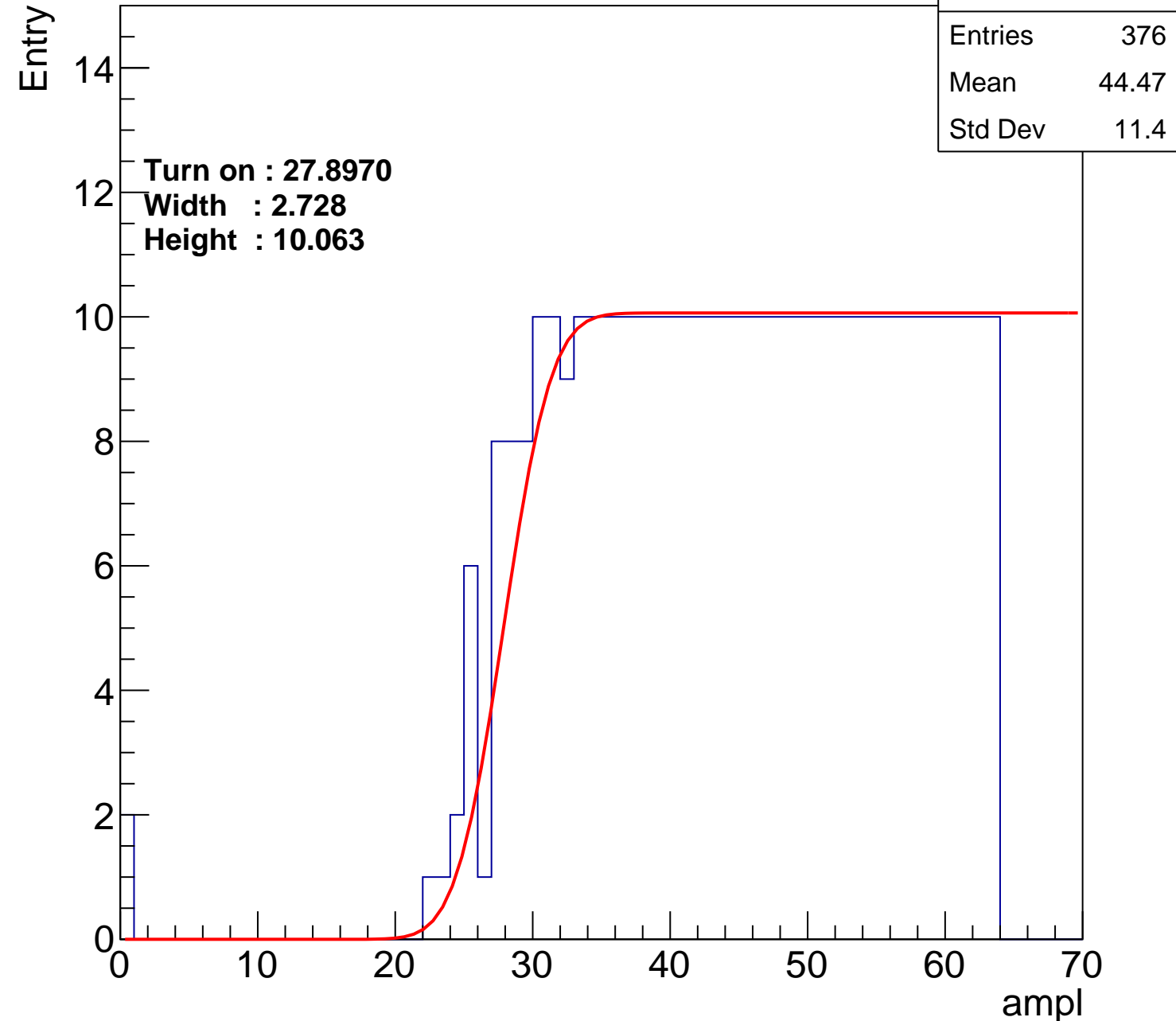
Width : 2.728

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 26.1506

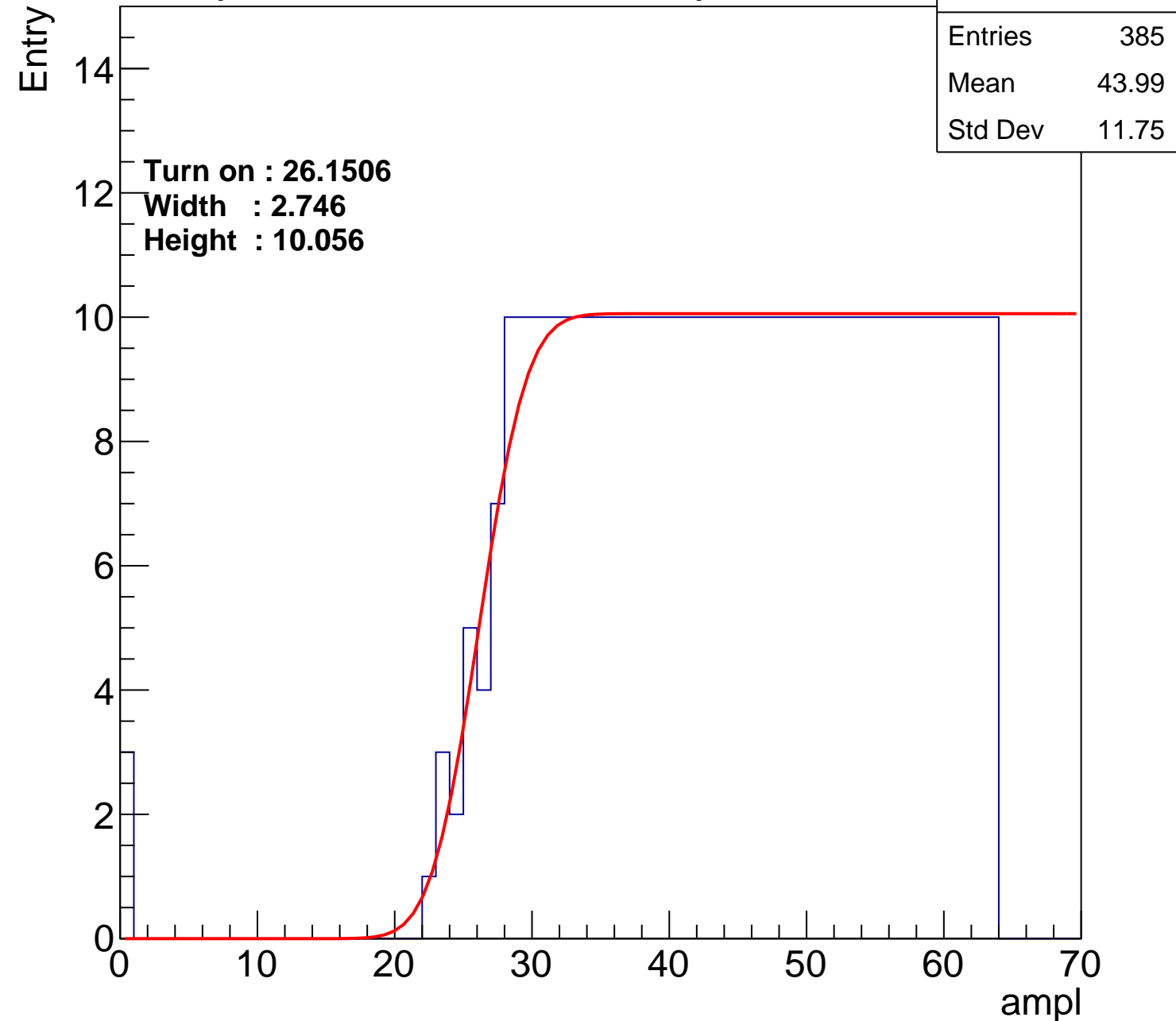
Width : 2.746

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.57
Std Dev	11.52

Turn on : 27.6180

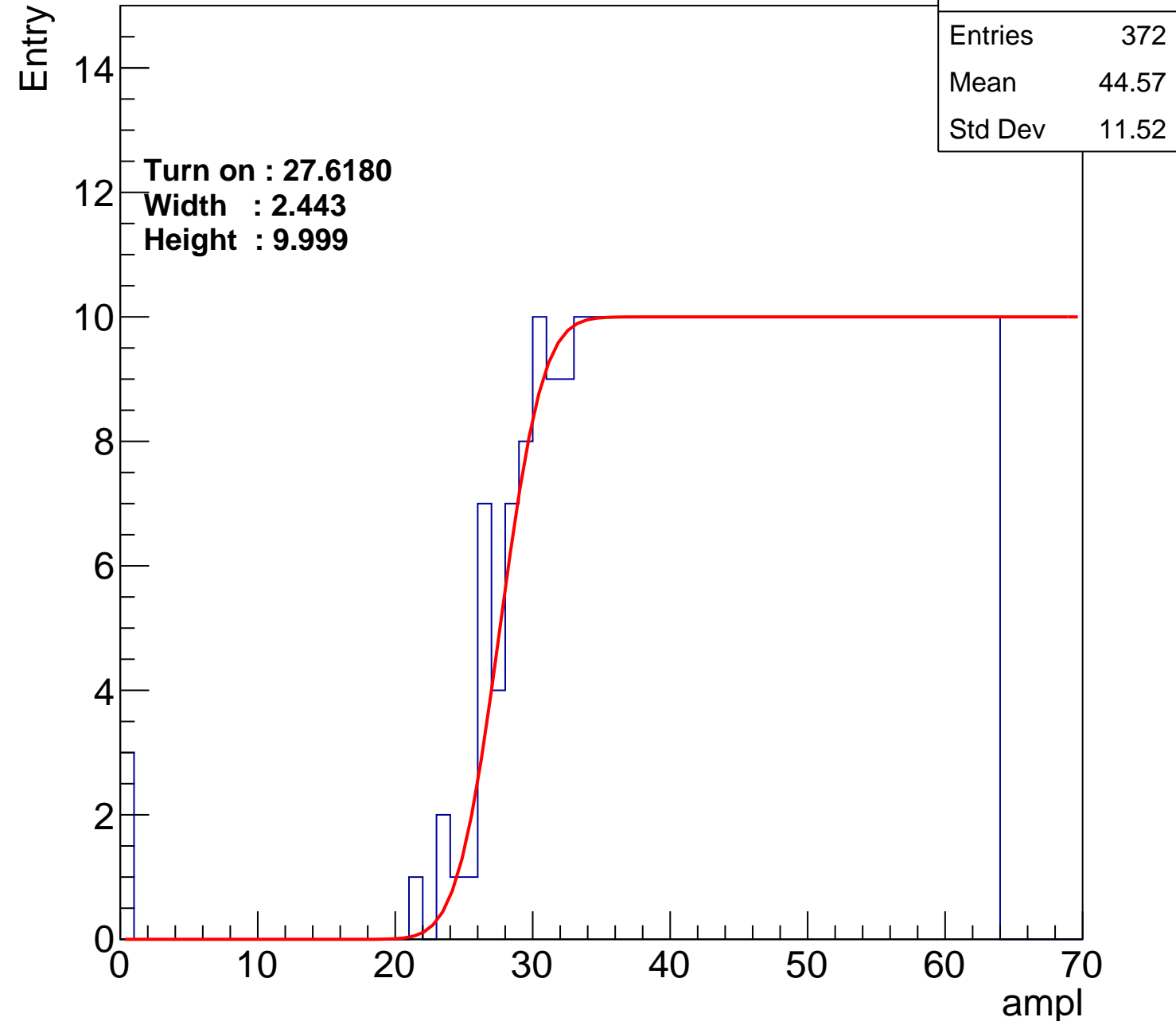
Width : 2.443

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.33
Std Dev	11.69

Turn on : 26.8758

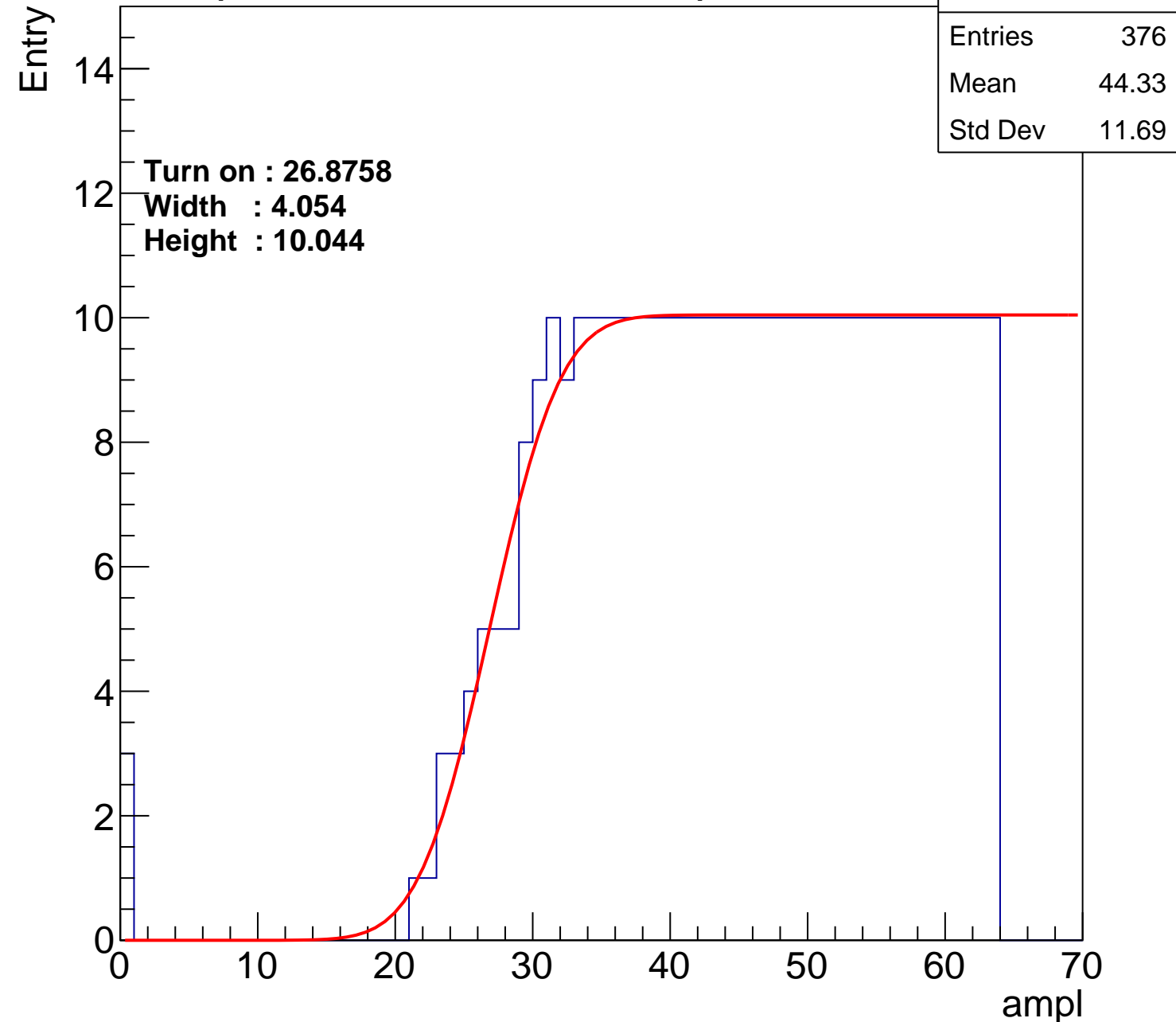
Width : 4.054

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.6835

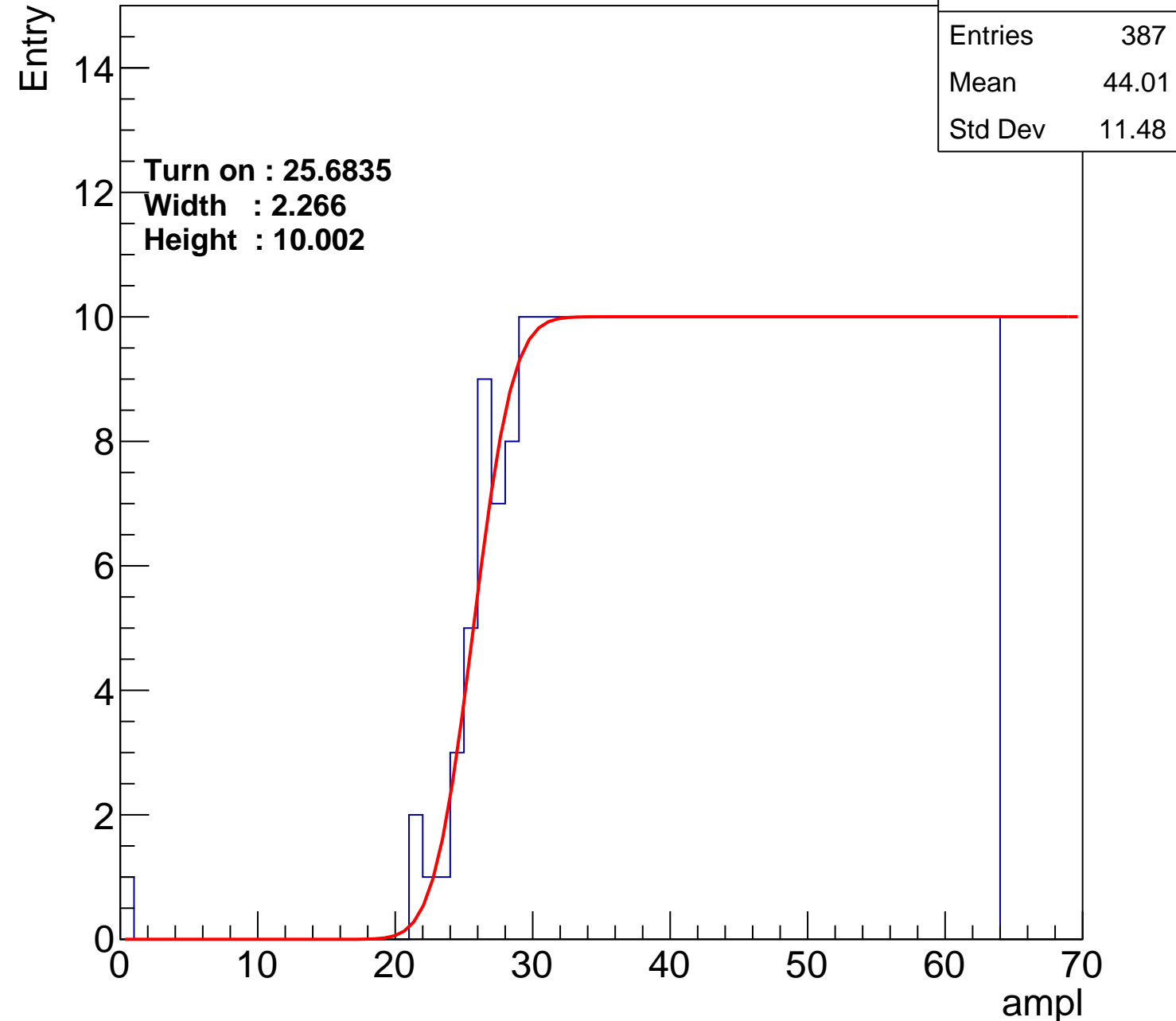
Width : 2.266

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 25.4180

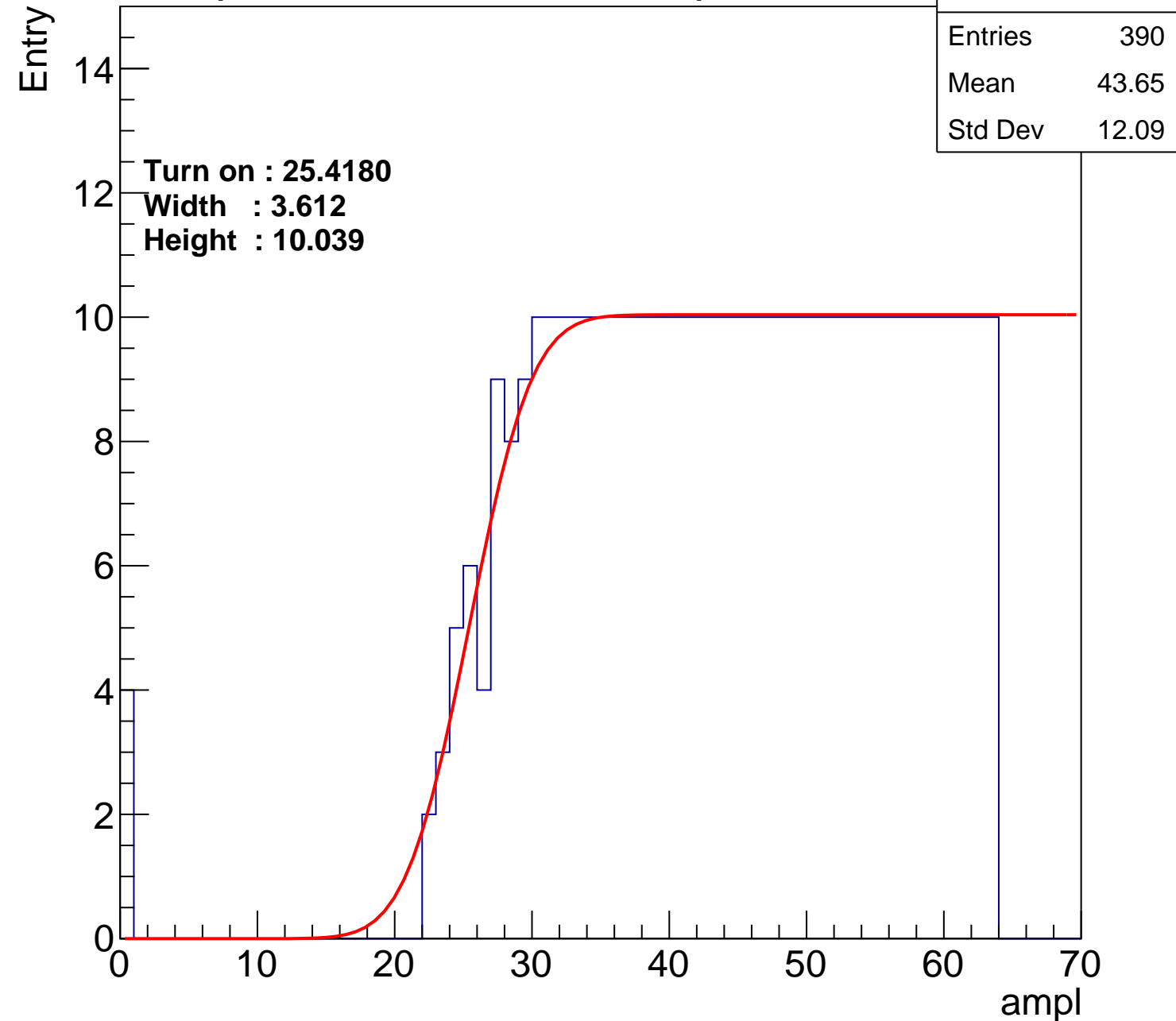
Width : 3.612

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch24

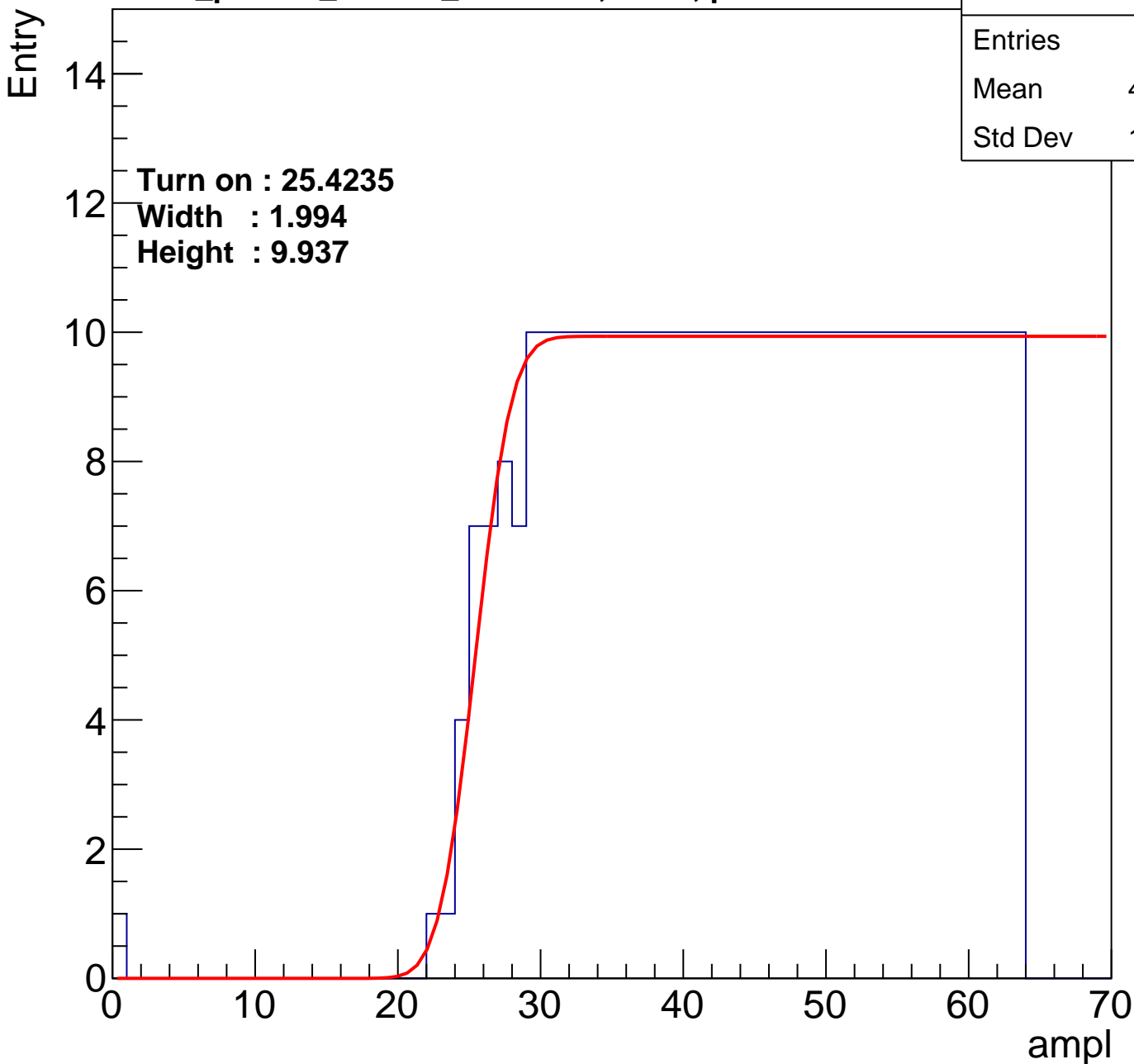
calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 25.4235

Width : 1.994

Height : 9.937



B1L000S, U3-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.2204

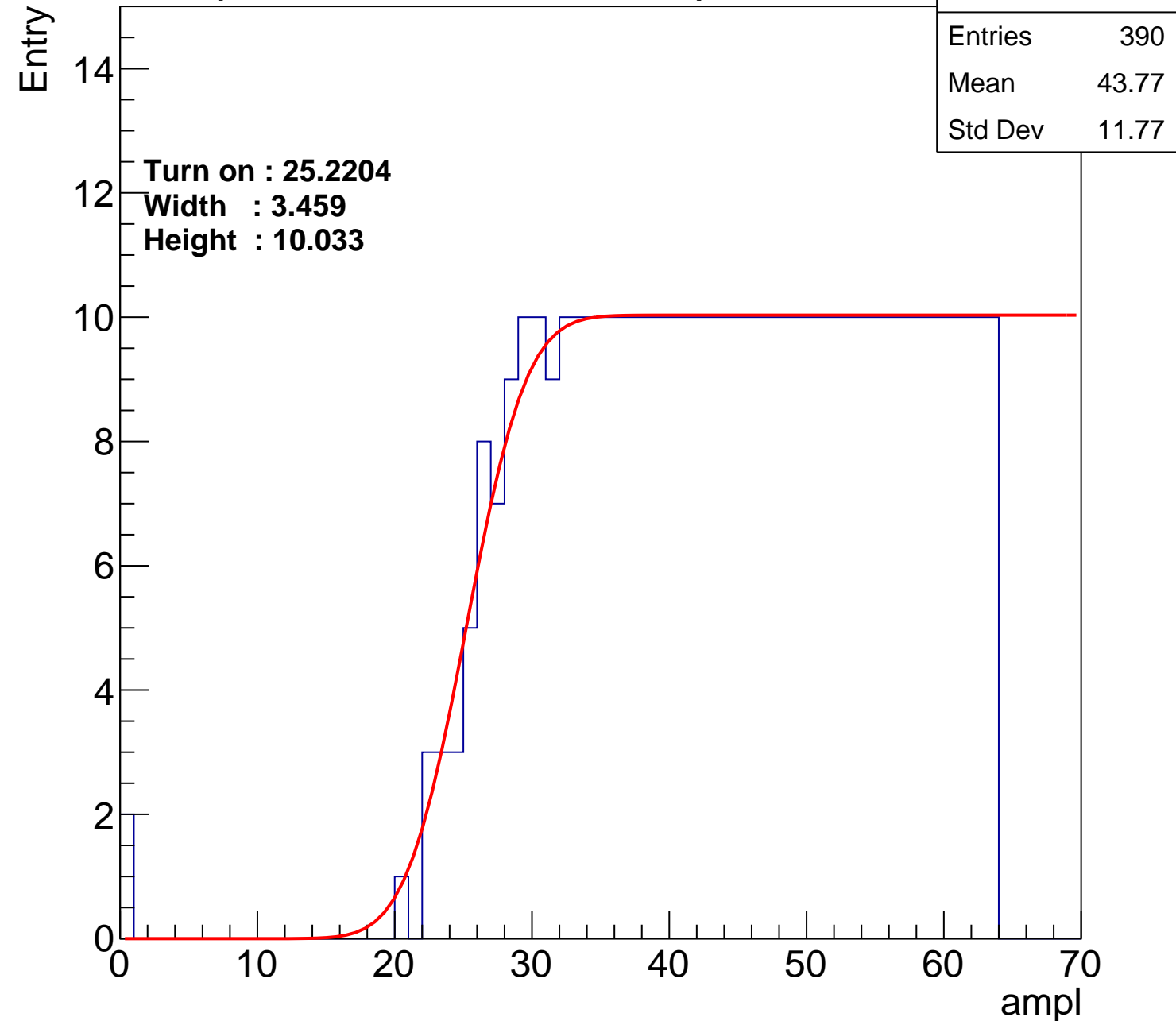
Width : 3.459

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch26

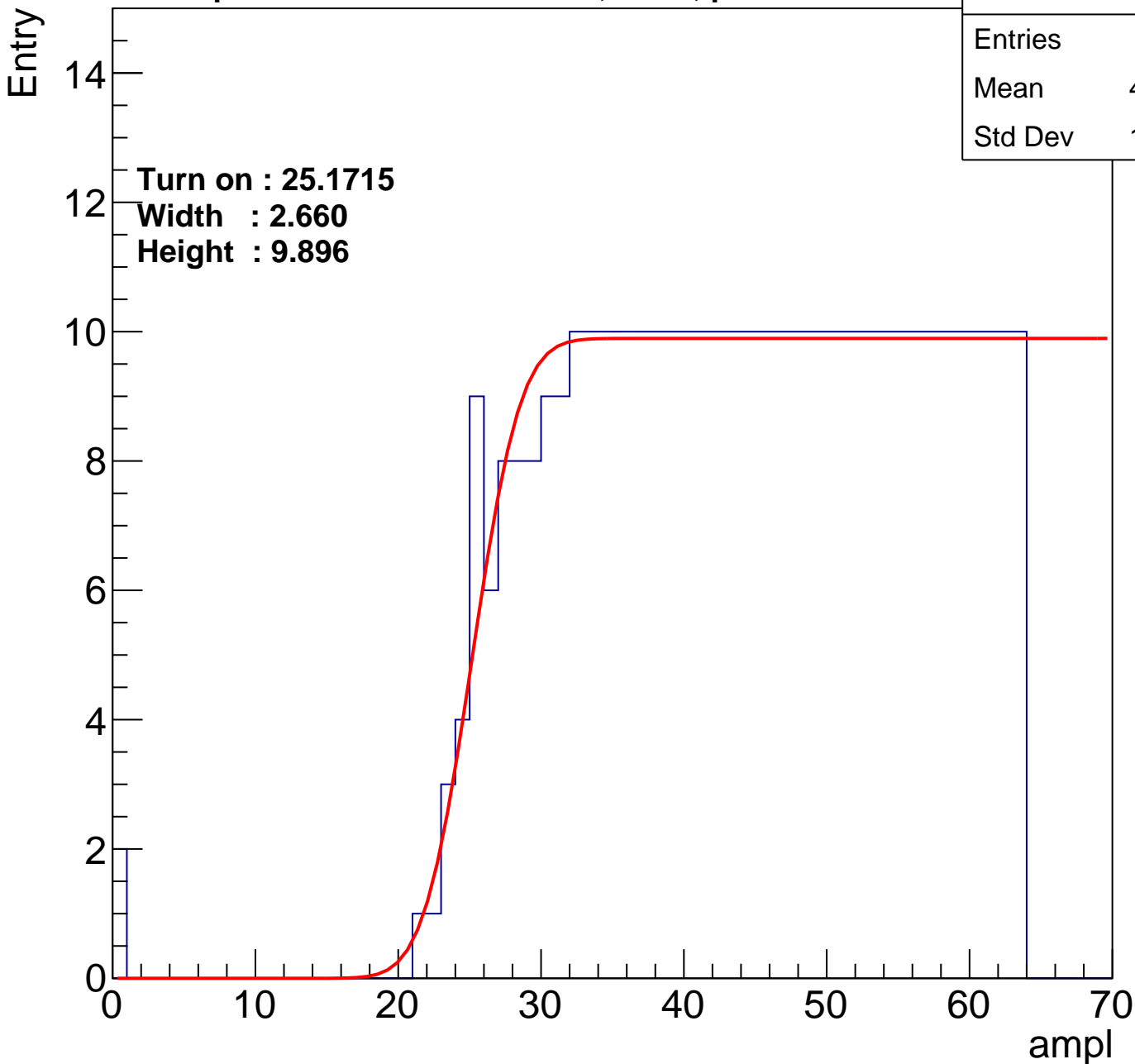
calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.84
Std Dev	11.76

Turn on : 25.1715

Width : 2.660

Height : 9.896



B1L000S, U3-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.0999

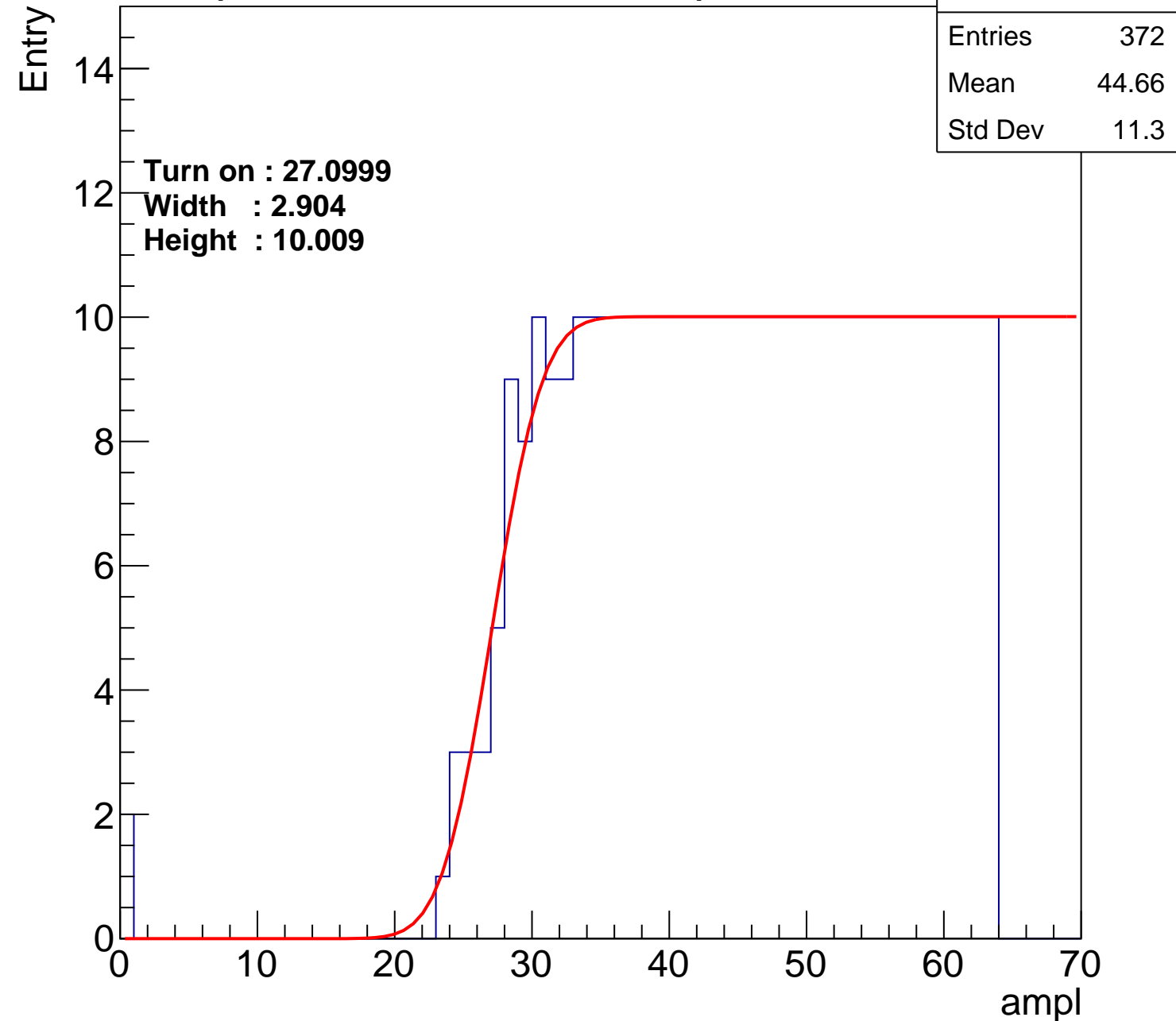
Width : 2.904

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 25.8978

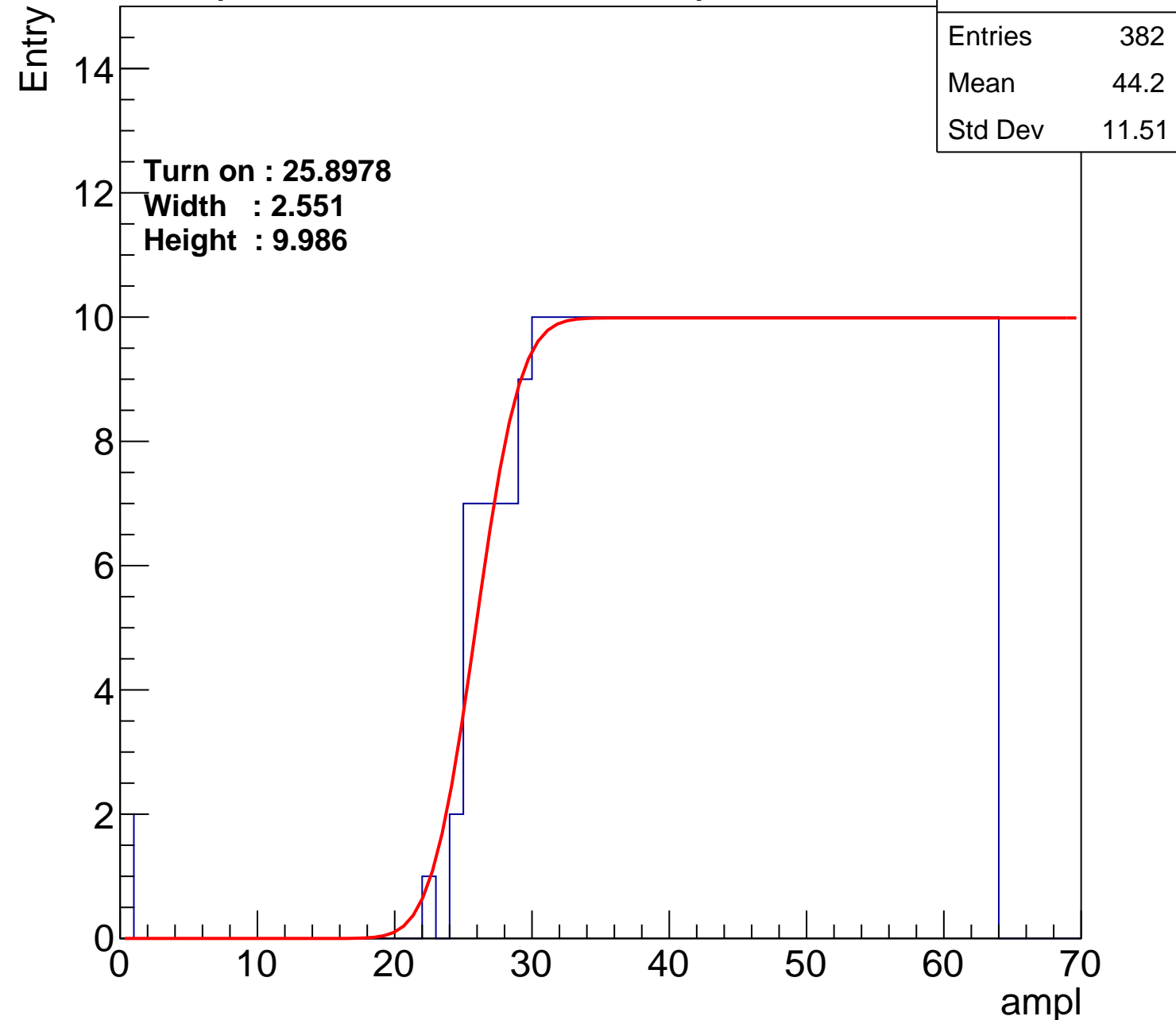
Width : 2.551

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.9460

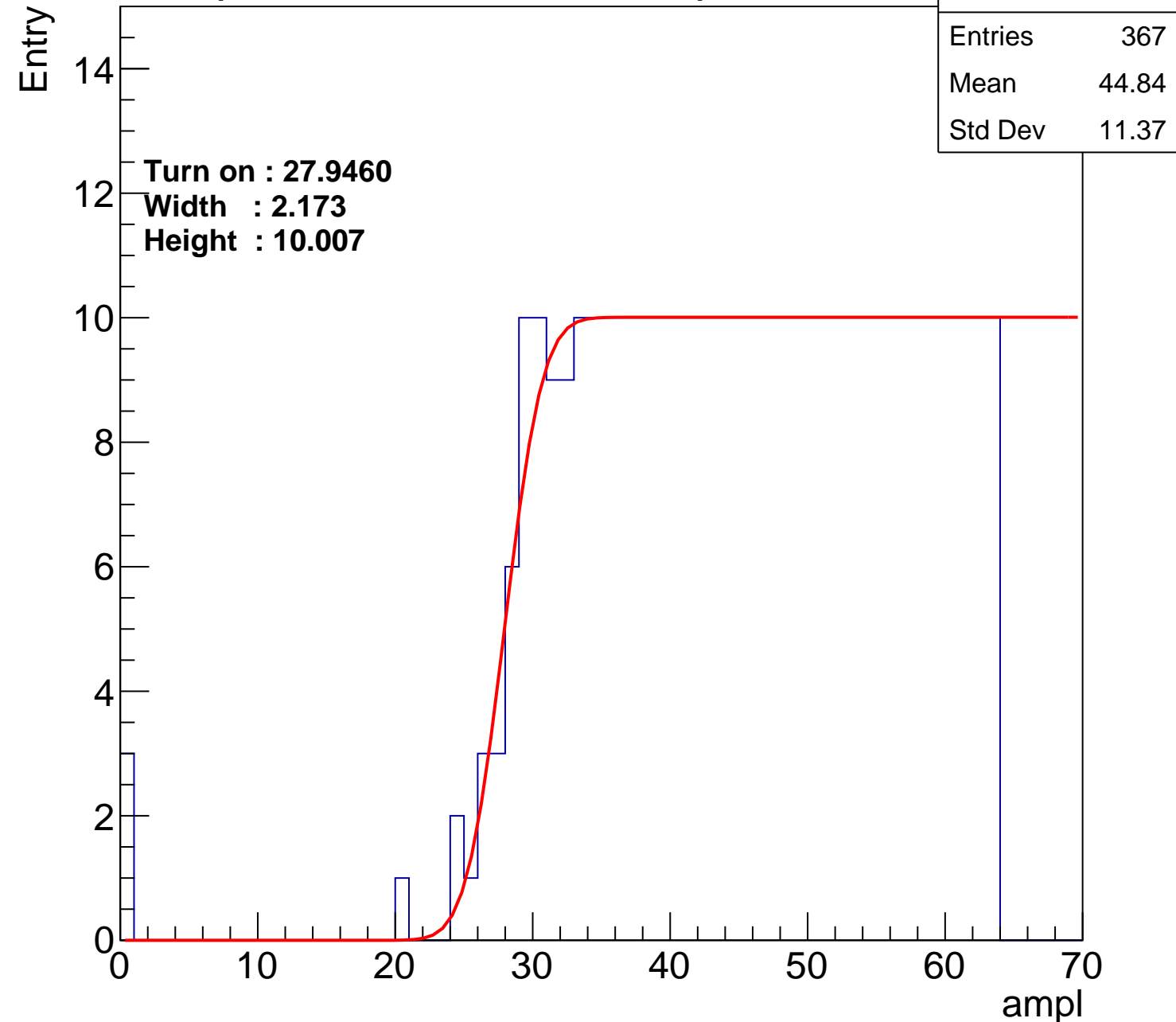
Width : 2.173

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch30

calib_packv5_042523_0143.root, FC#3, port B3

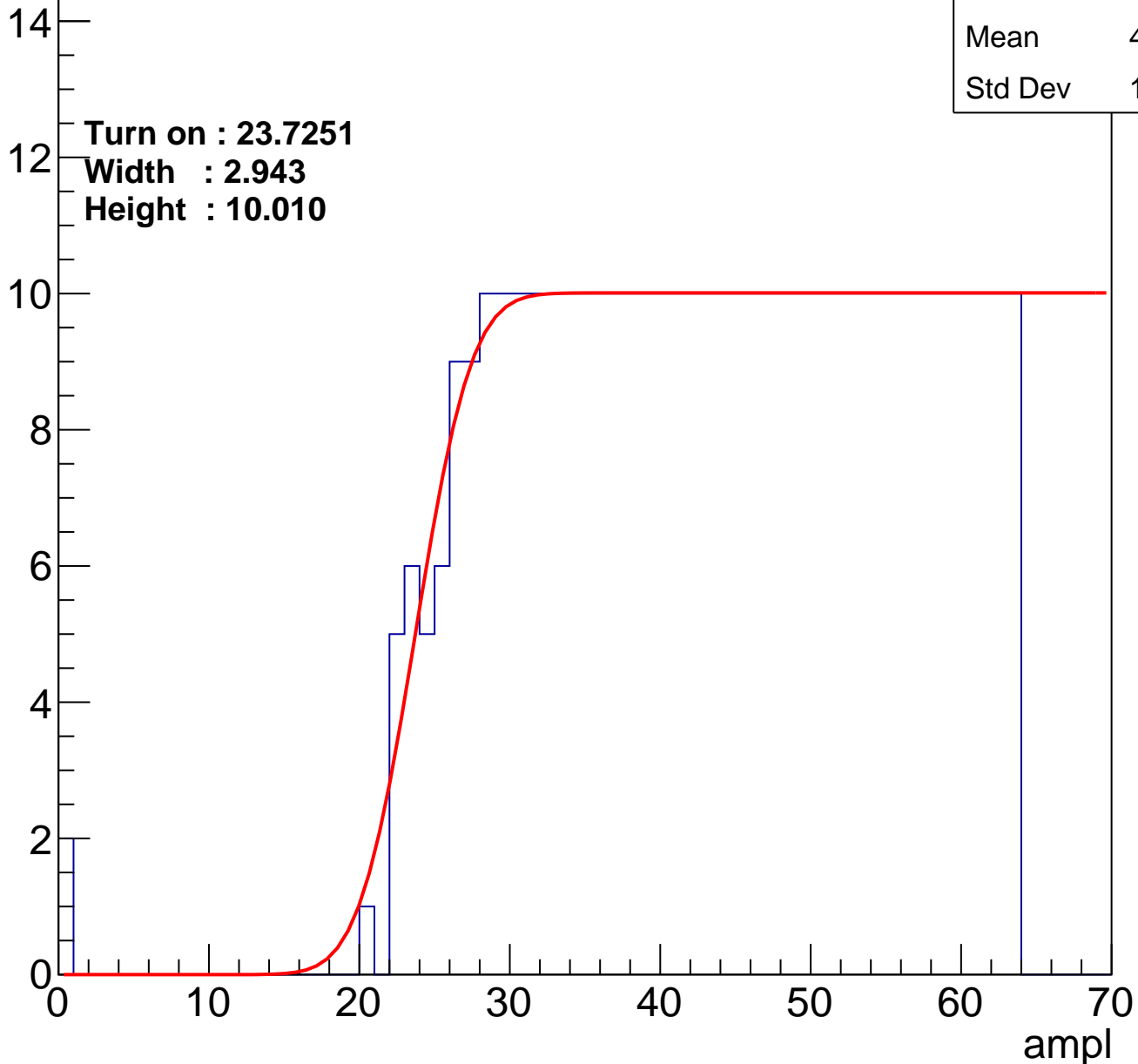
Entries	403
Mean	43.16
Std Dev	12.06

Turn on : 23.7251

Width : 2.943

Height : 10.010

Entry



B1L000S, U3-ch31

calib_packv5_042523_0143.root, FC#3, port B3

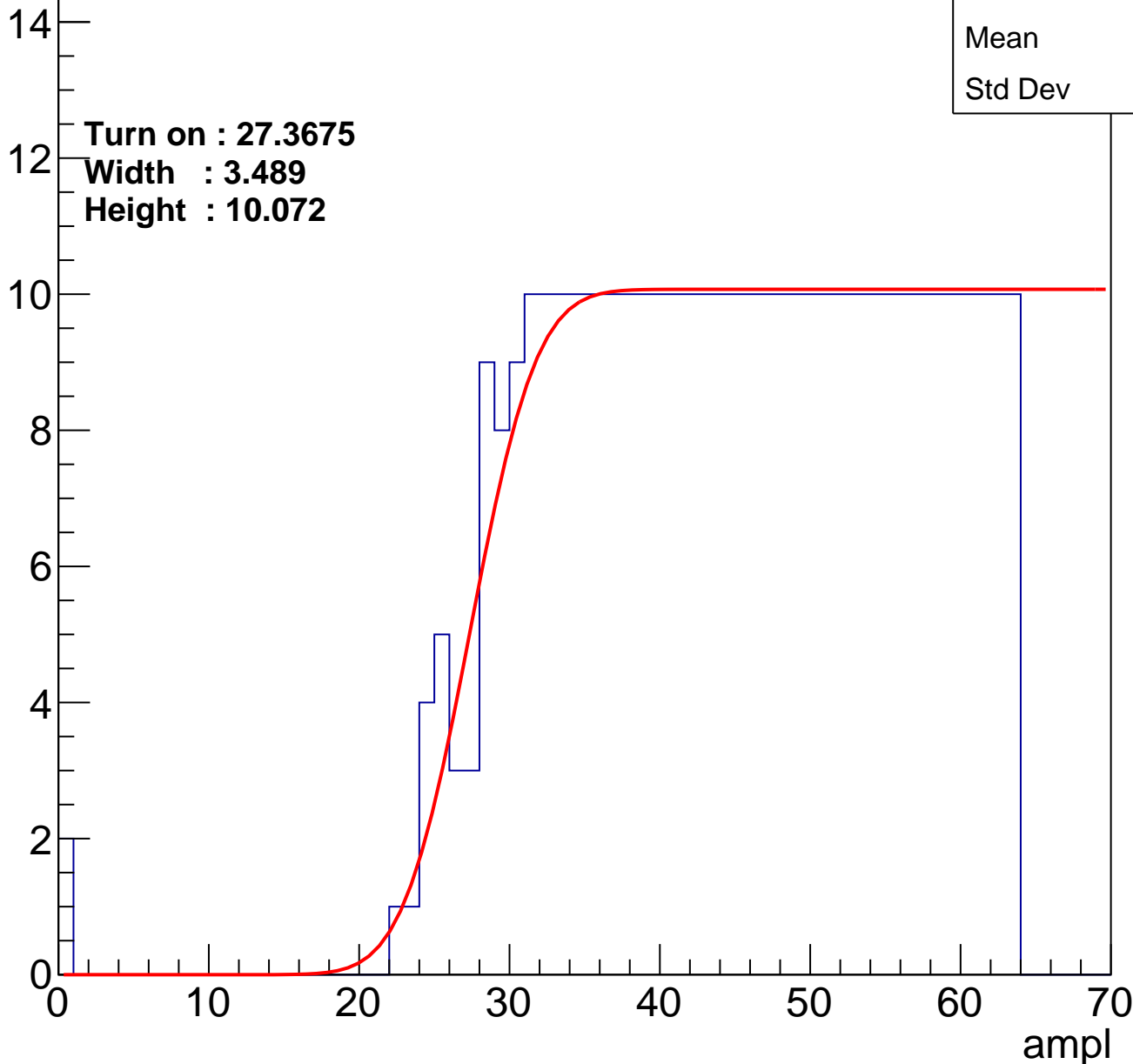
Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 27.3675

Width : 3.489

Height : 10.072

Entry



B1L000S, U3-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.11
Std Dev	12.19

Turn on : 23.9140

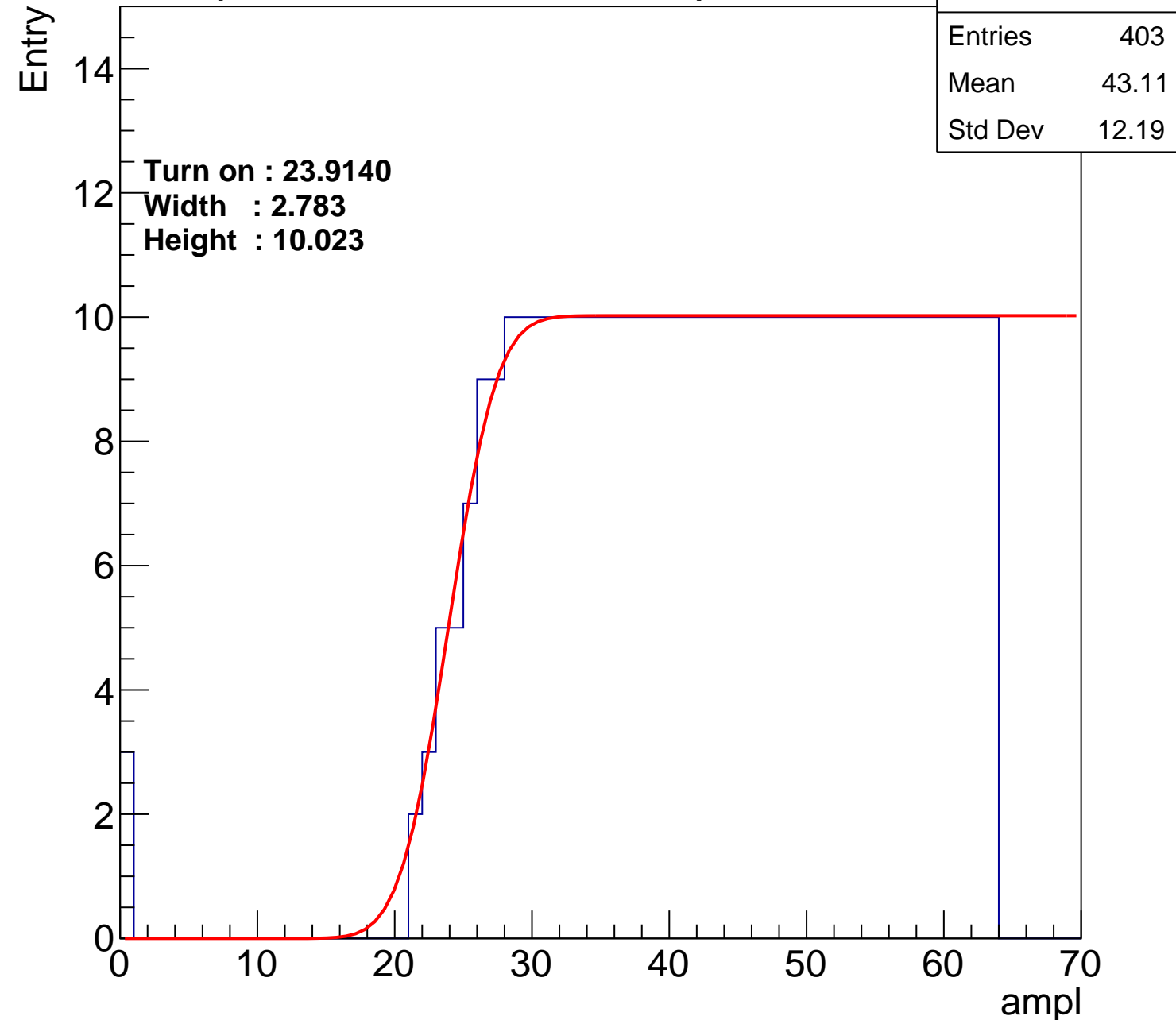
Width : 2.783

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch33

calib_packv5_042523_0143.root, FC#3, port B3

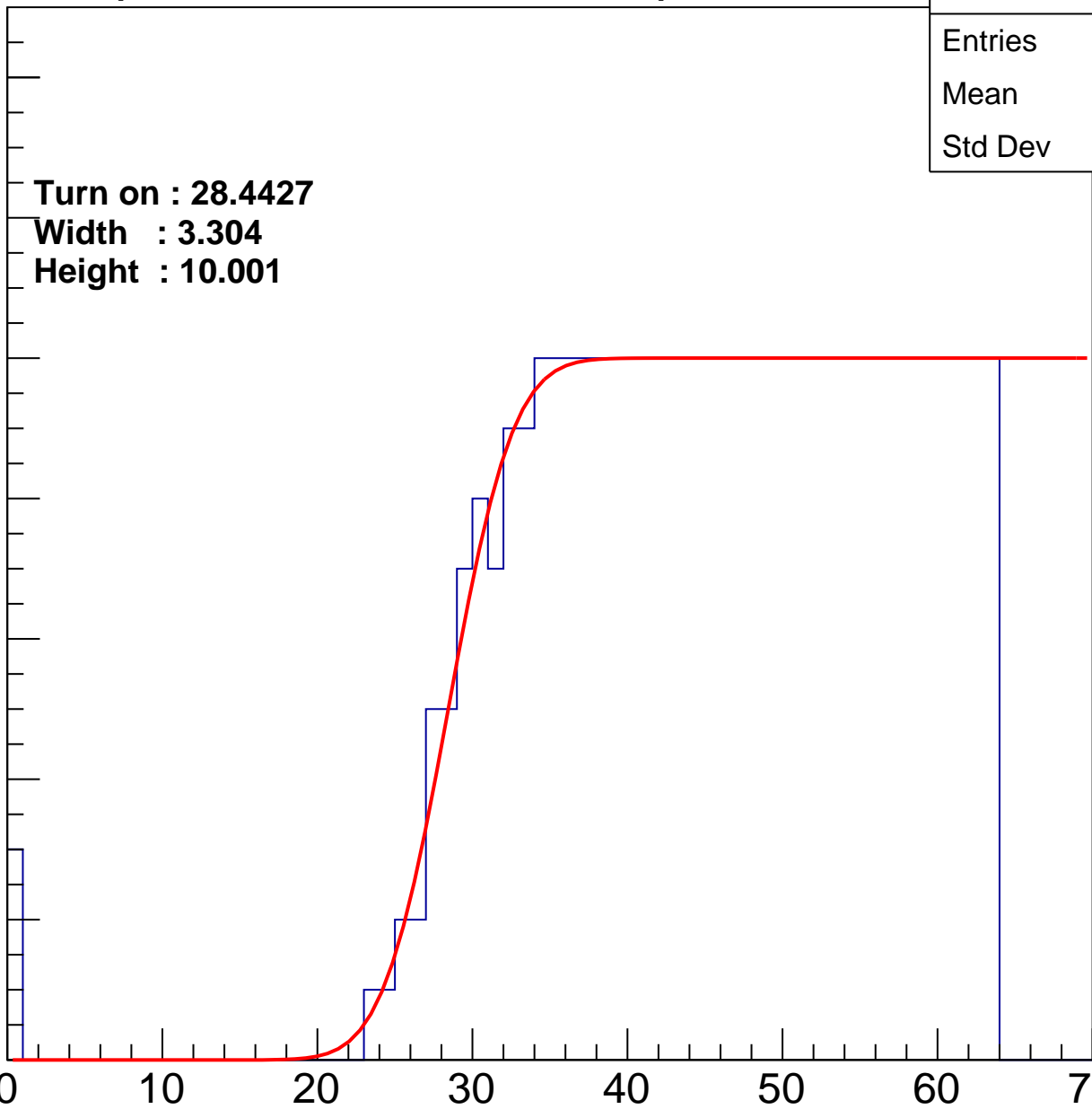
Entry

14
12
10
8
6
4
2
0

Turn on : 28.4427
Width : 3.304
Height : 10.001

Entries	359
Mean	45.18
Std Dev	11.26

ampl



B1L000S, U3-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.7870

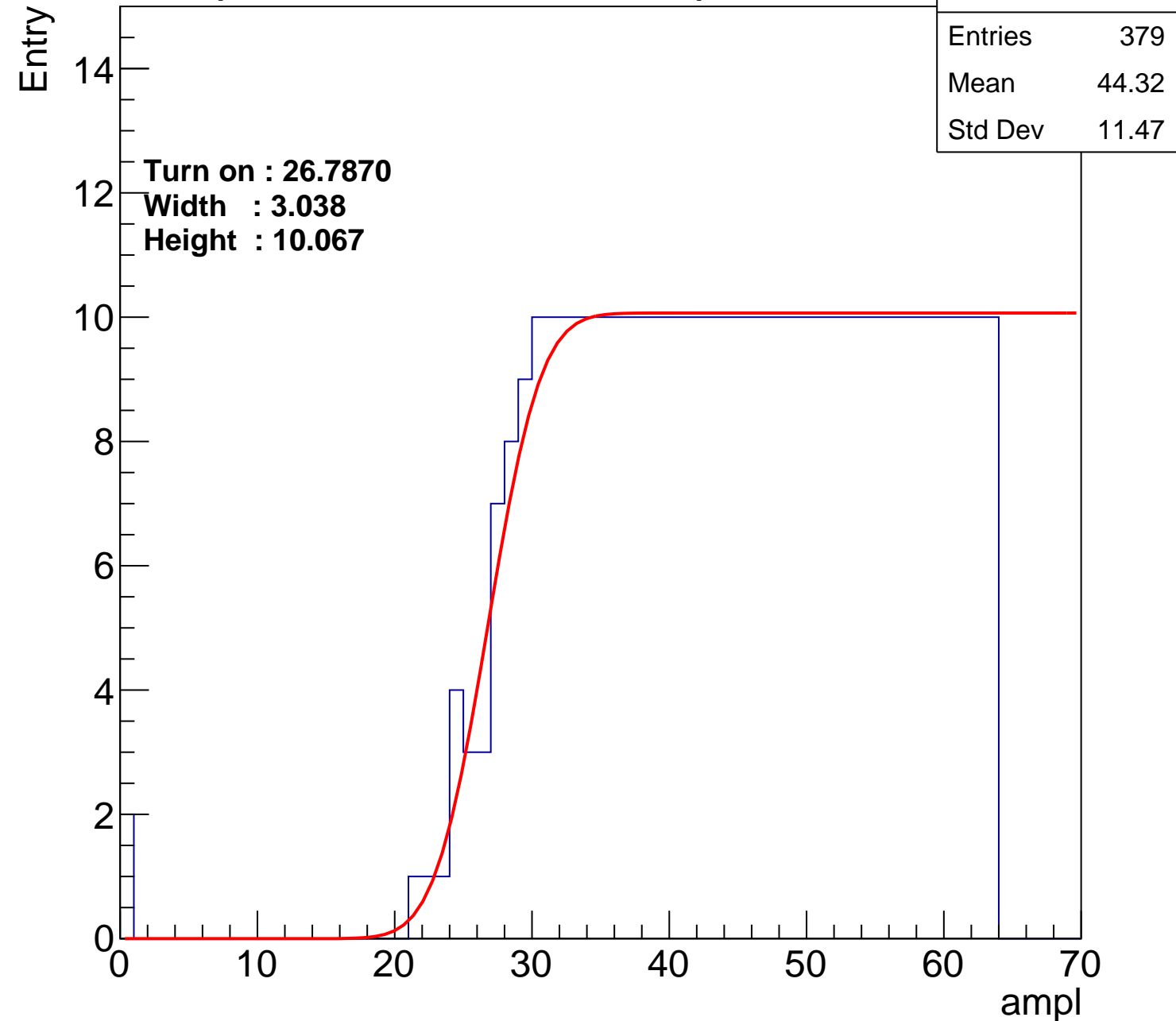
Width : 3.038

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.75
Std Dev	11.07

Turn on : 27.4593

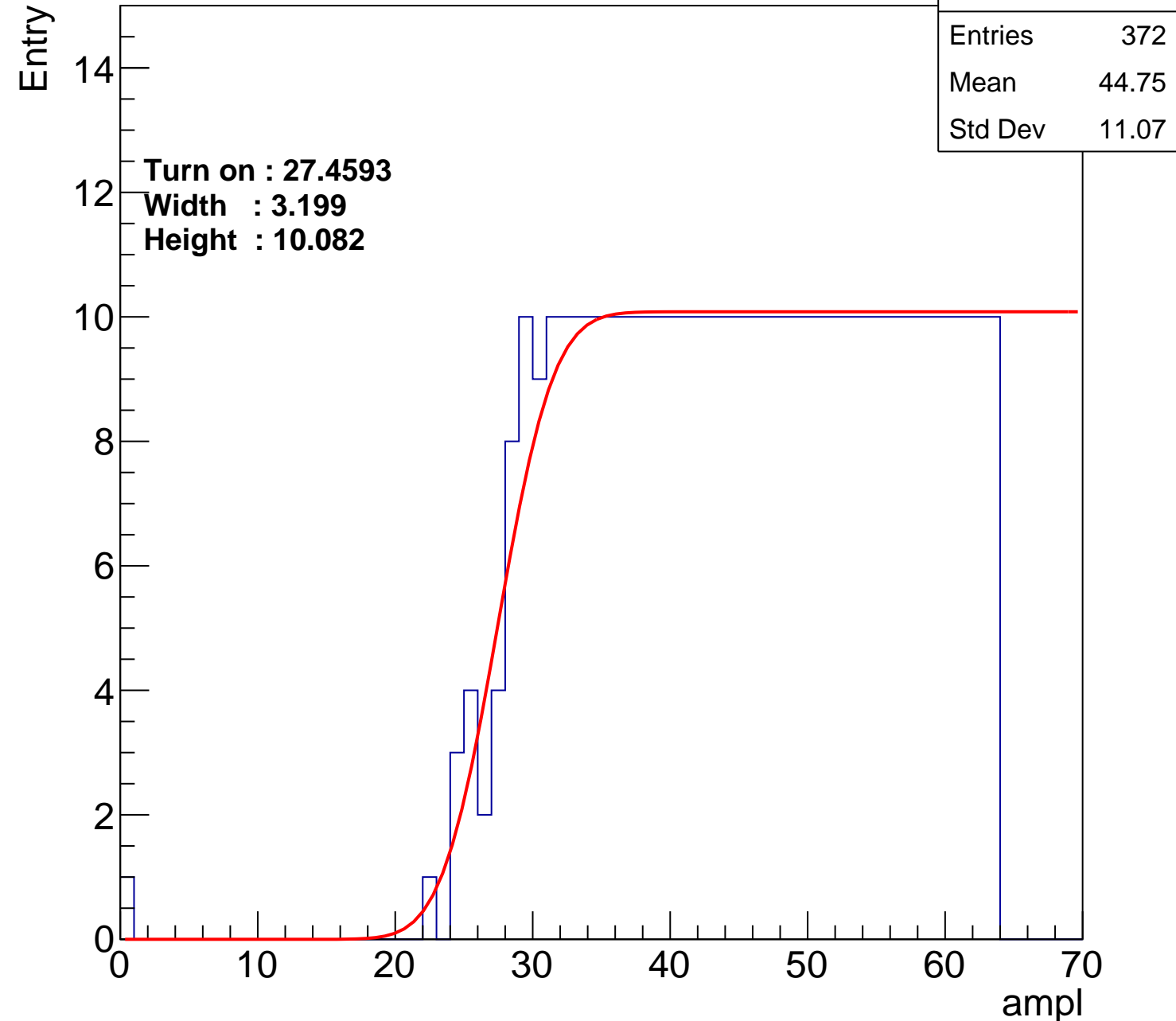
Width : 3.199

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.04
Std Dev	11.74

Turn on : 25.5693

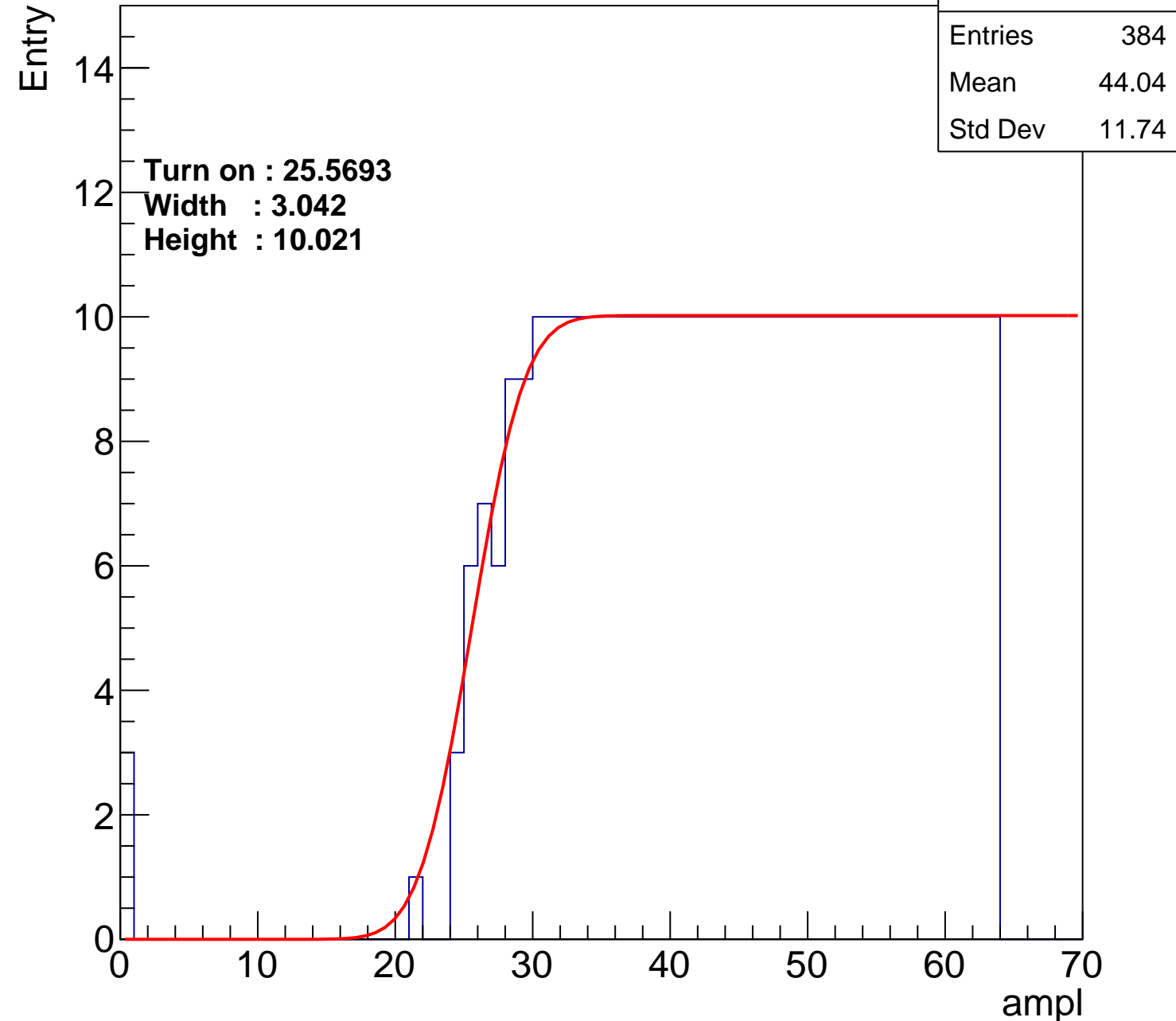
Width : 3.042

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	42.95
Std Dev	12.4

Turn on : 24.0064

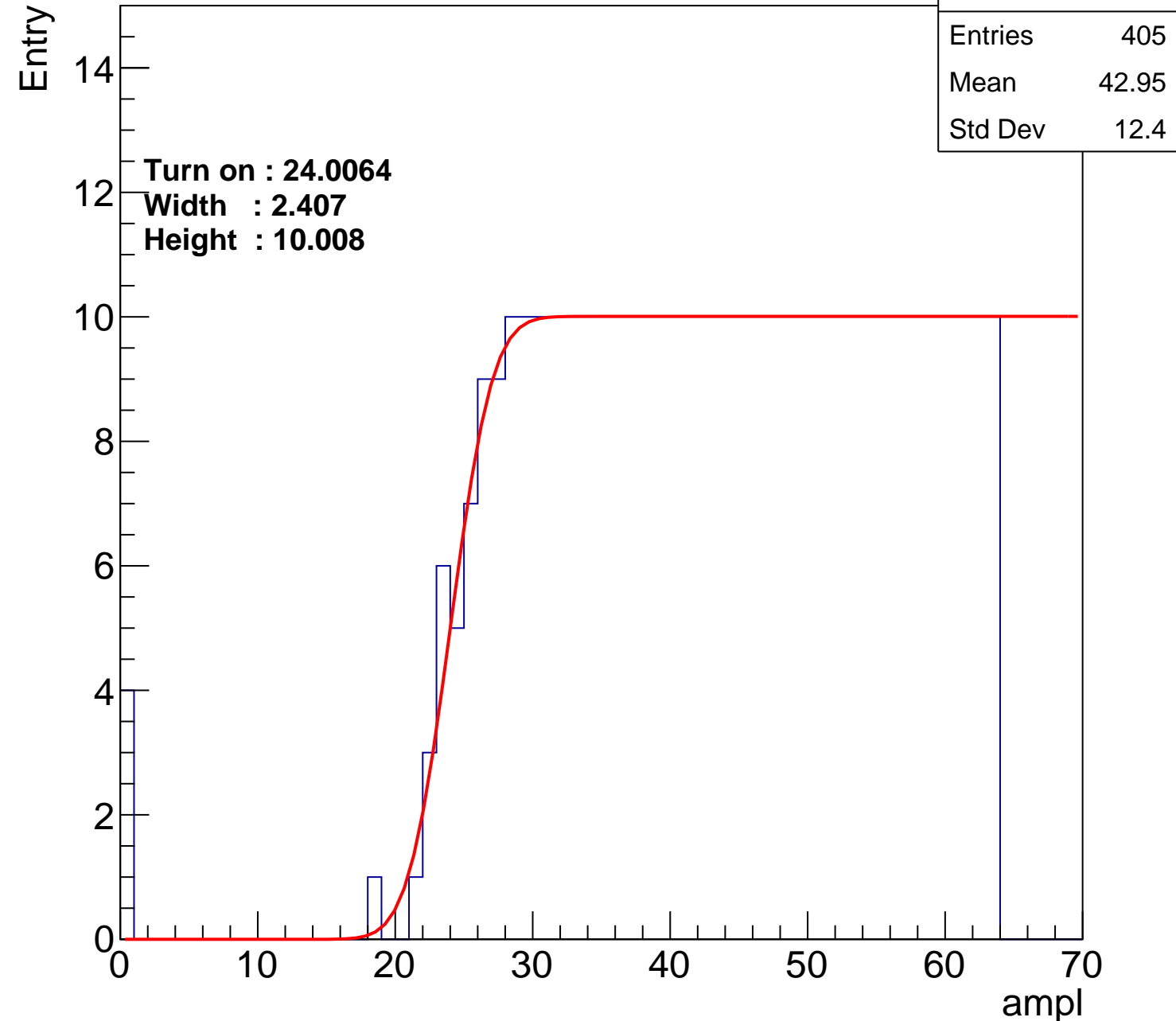
Width : 2.407

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.26
Std Dev	12

Turn on : 23.9963

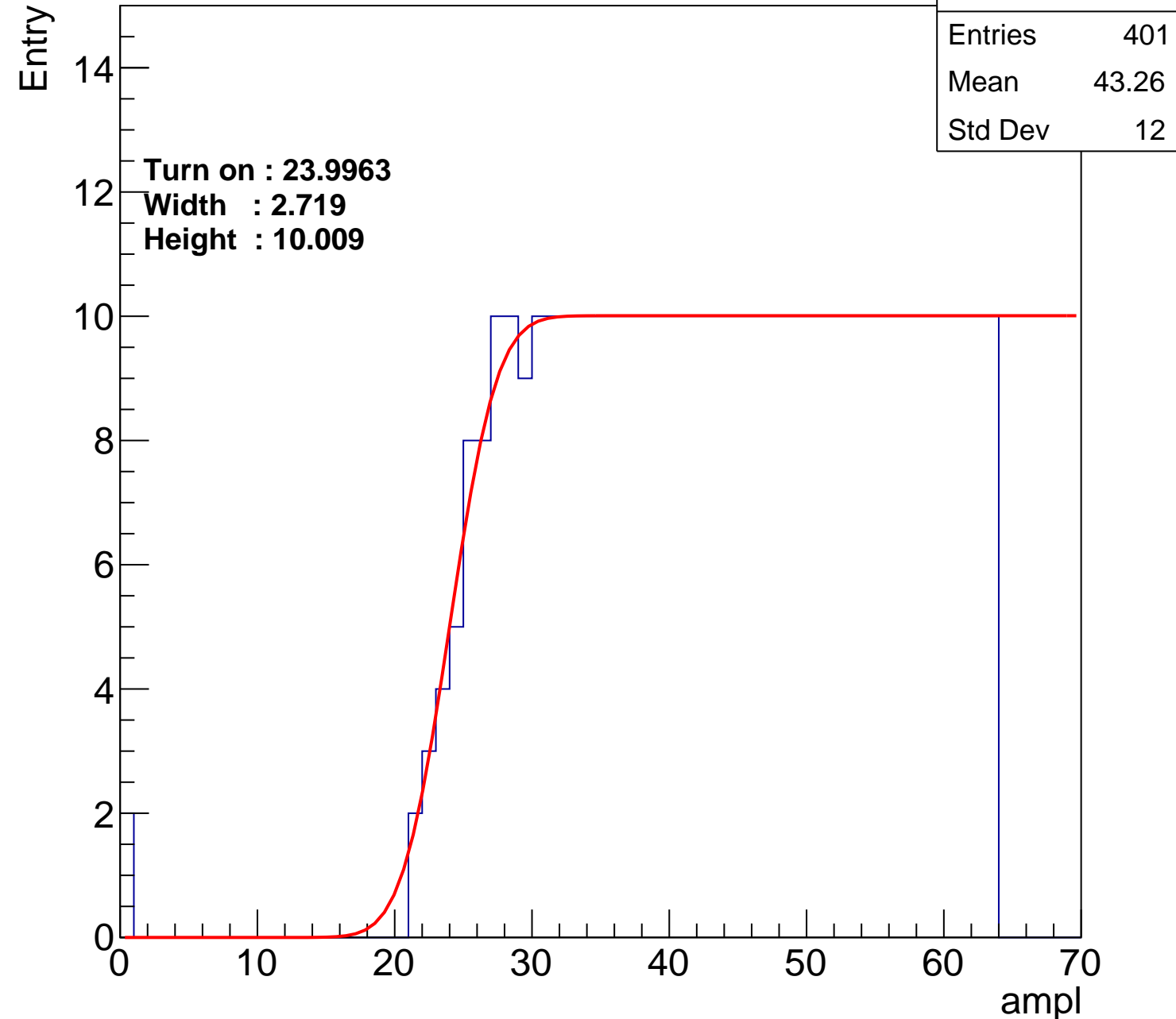
Width : 2.719

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.48
Std Dev	11.57

Turn on : 27.0873

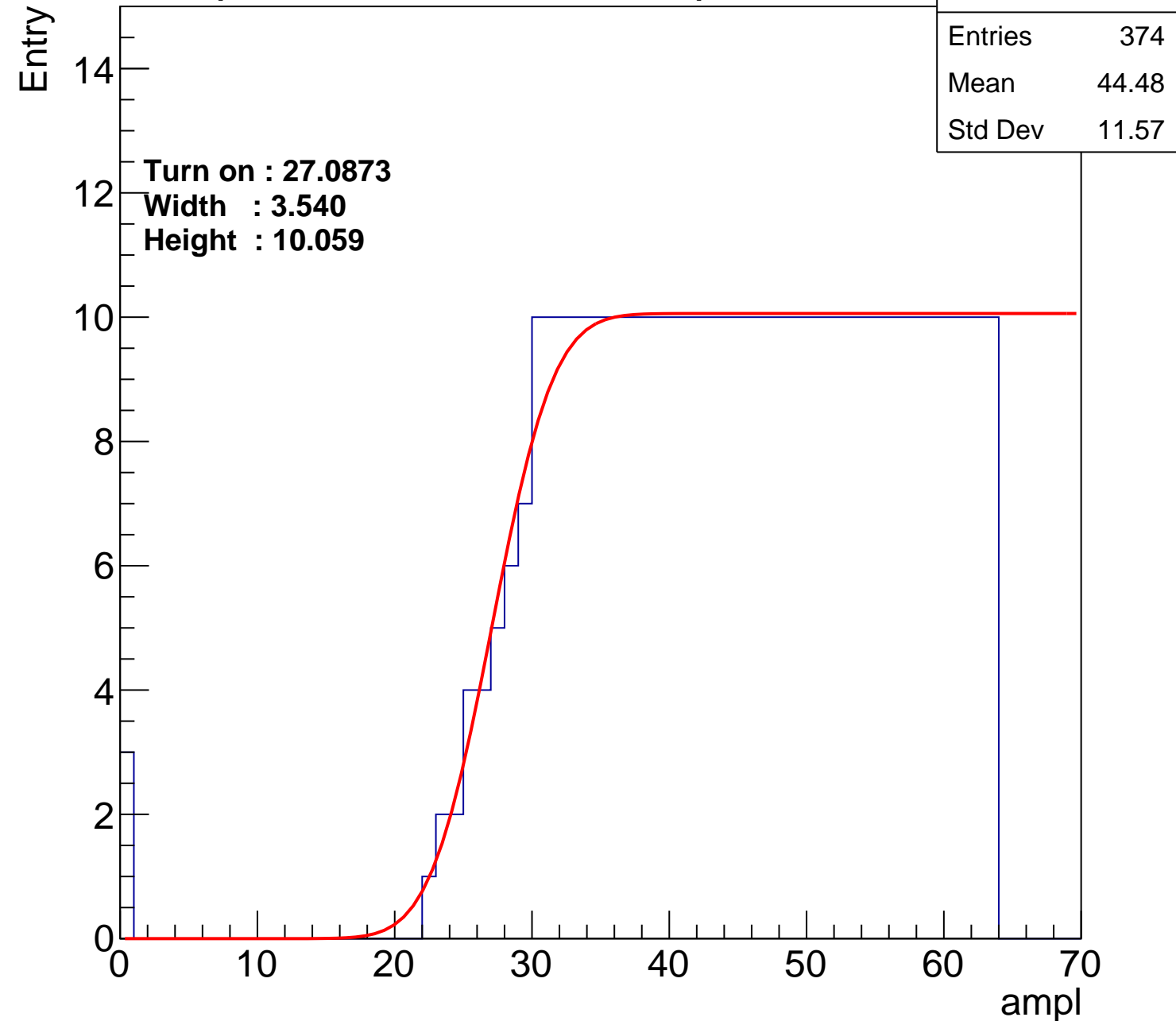
Width : 3.540

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 25.4420

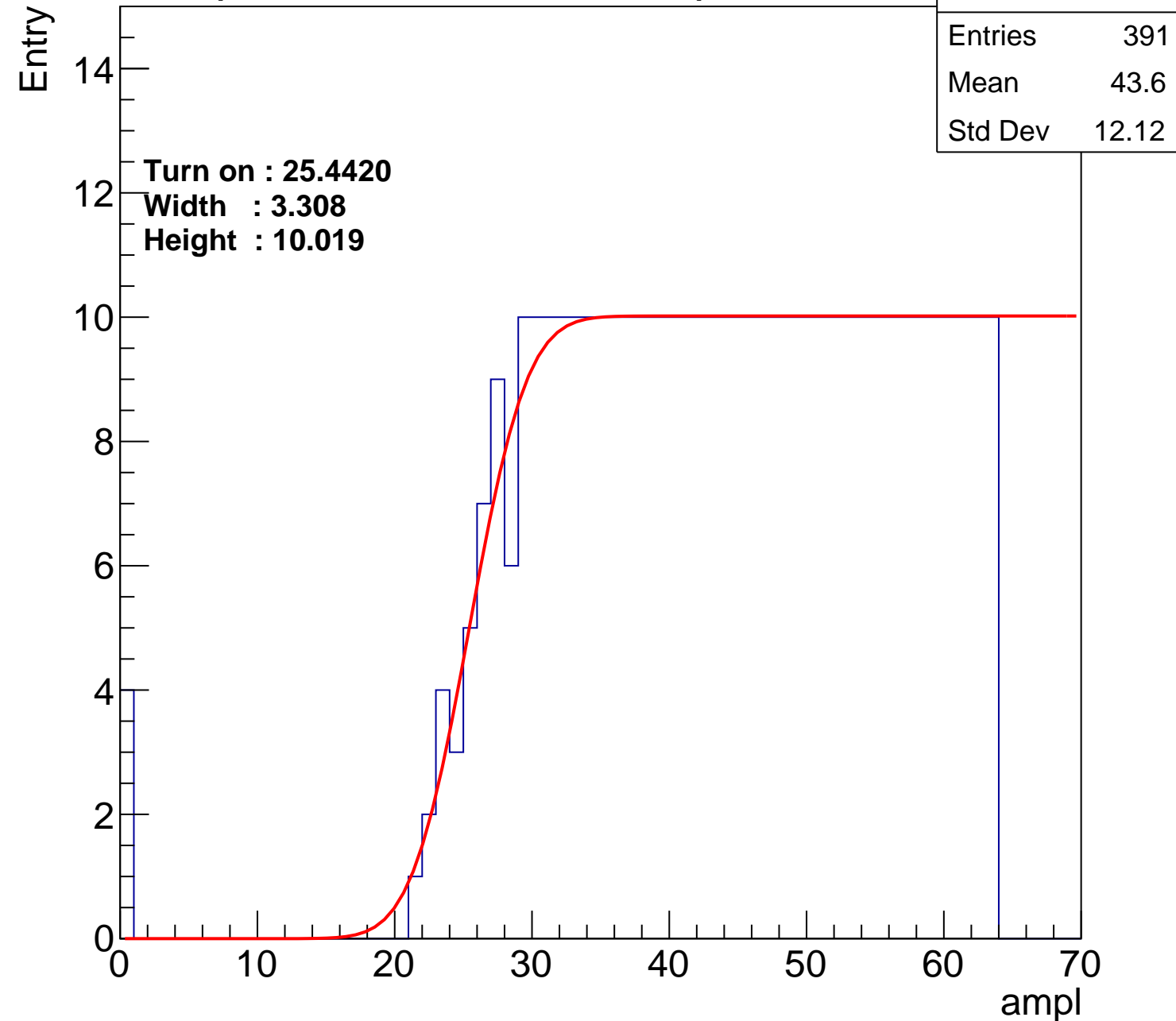
Width : 3.308

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.35
Std Dev	12.25

Turn on : 25.3326

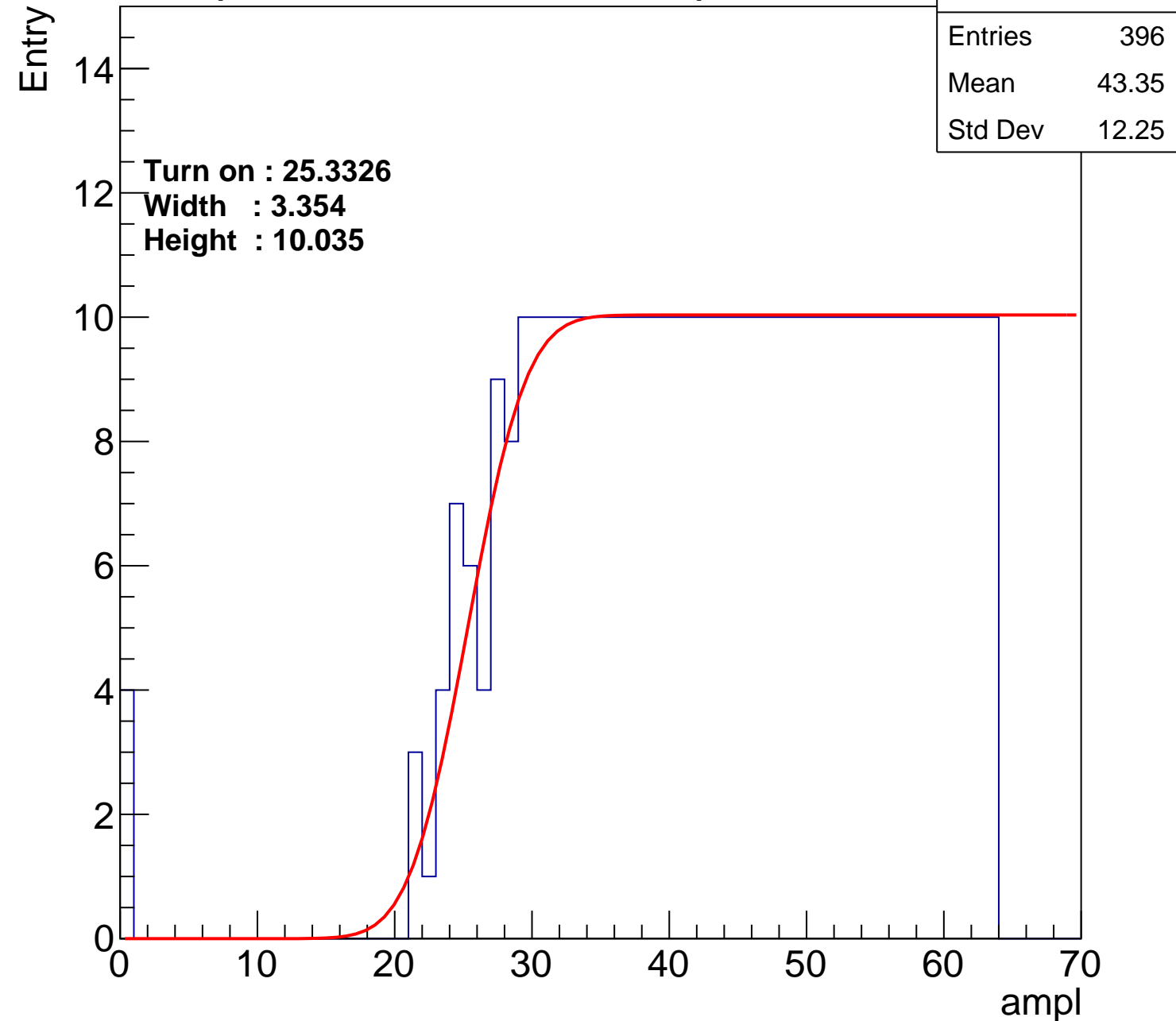
Width : 3.354

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 26.0728

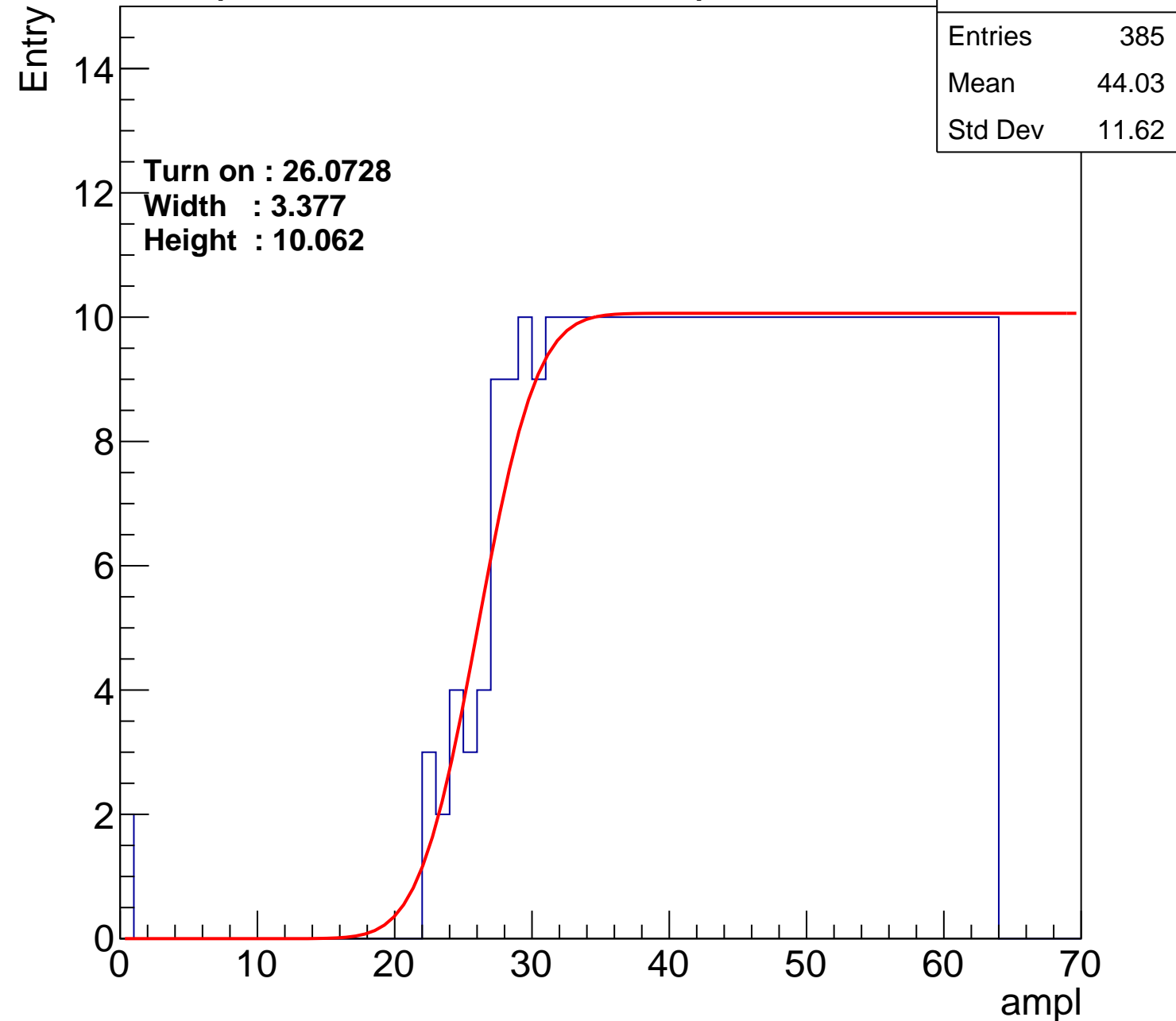
Width : 3.377

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.32
Std Dev	12.13

Turn on : 24.2943

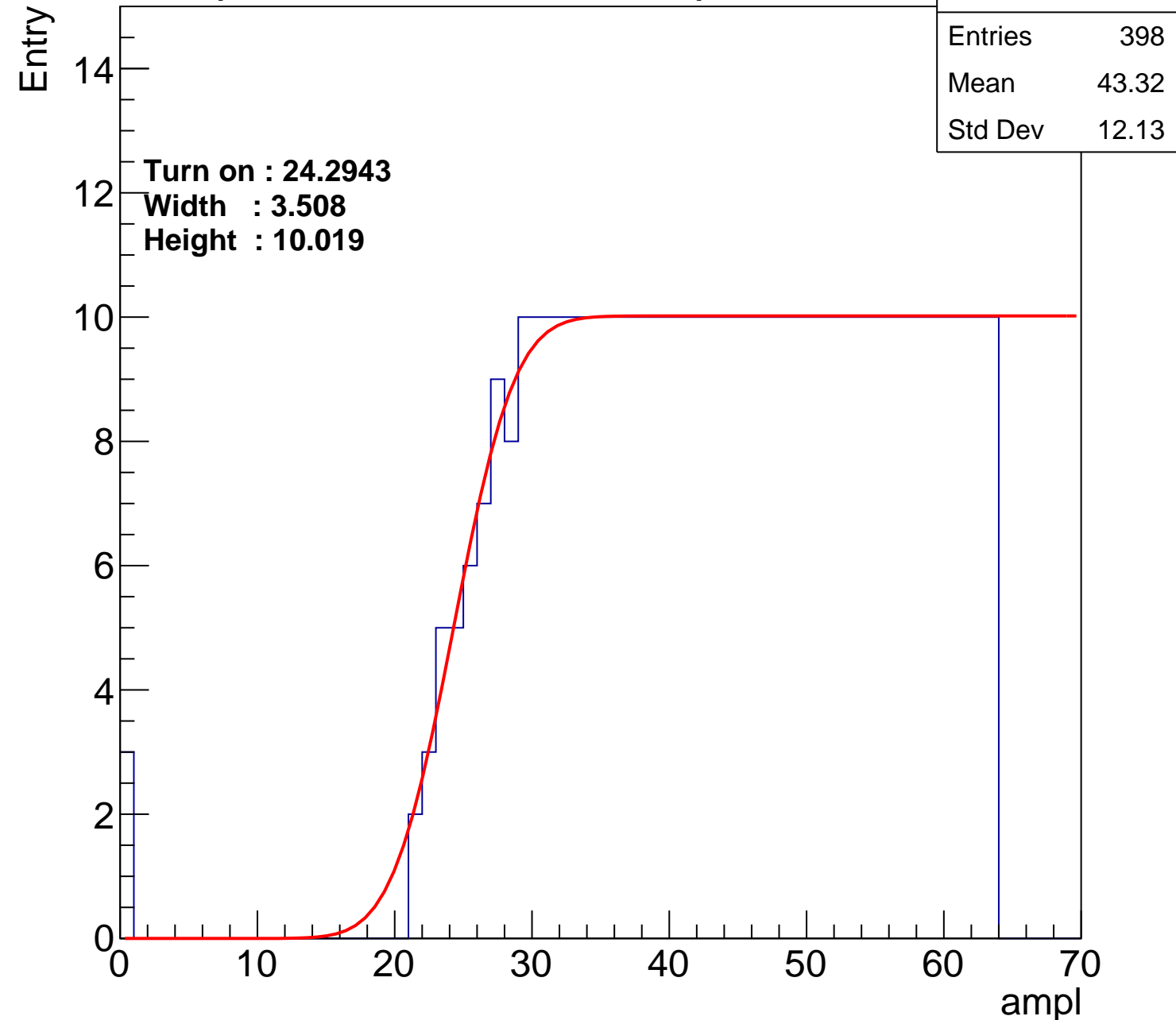
Width : 3.508

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.38
Std Dev	11.56

Turn on : 26.2933

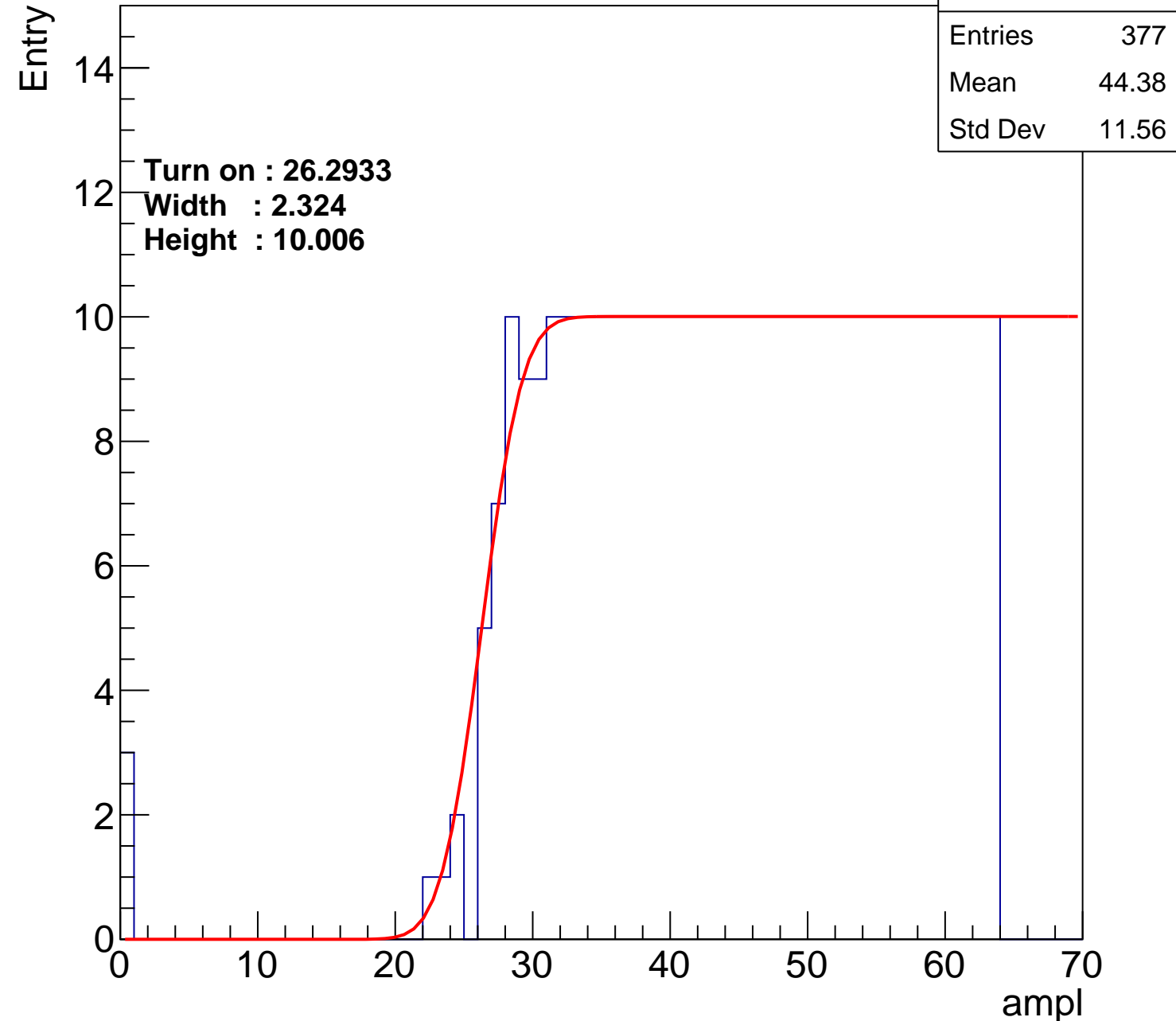
Width : 2.324

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.11
Std Dev	12.08

Turn on : 23.6401

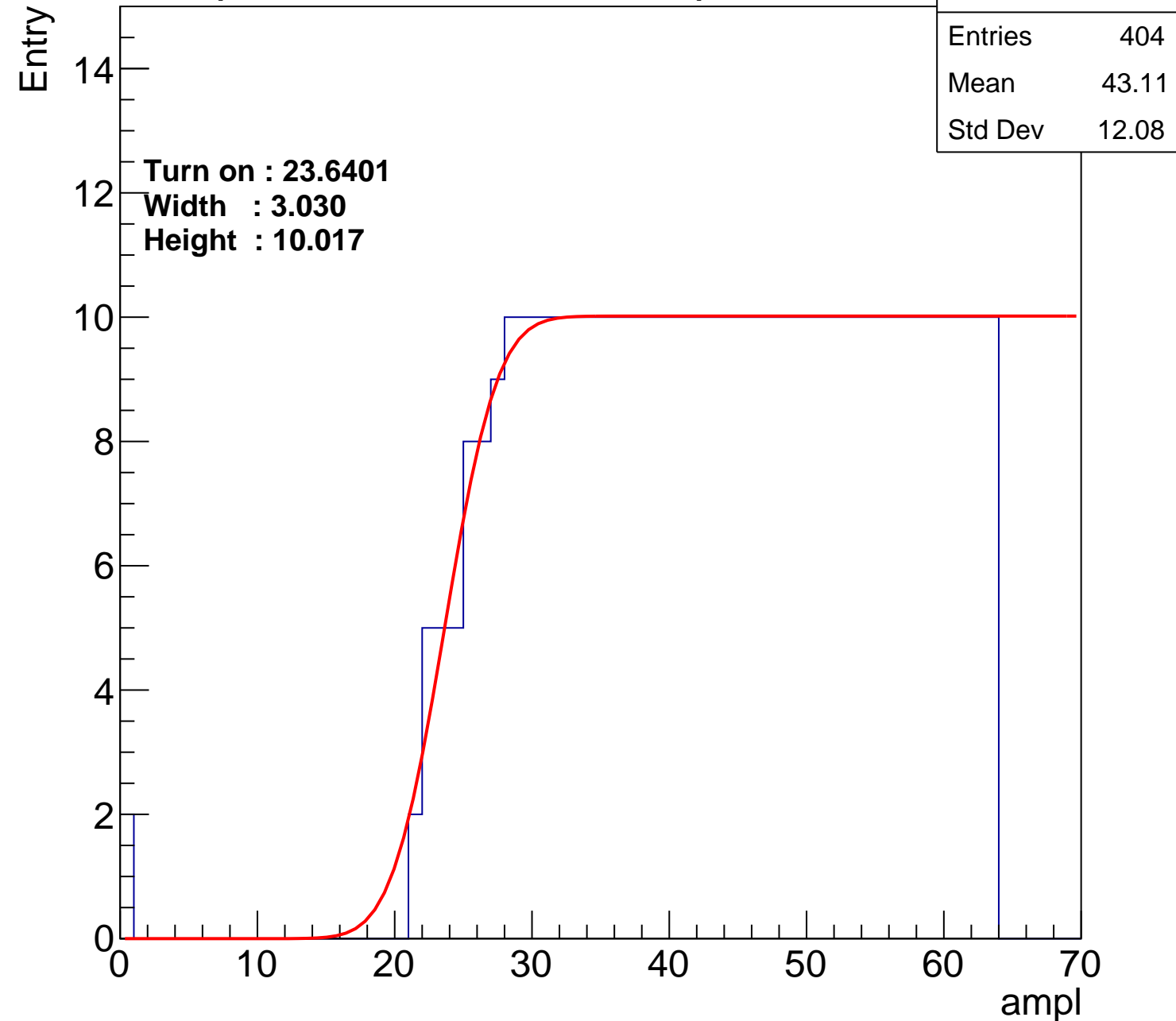
Width : 3.030

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch46

calib_packv5_042523_0143.root, FC#3, port B3

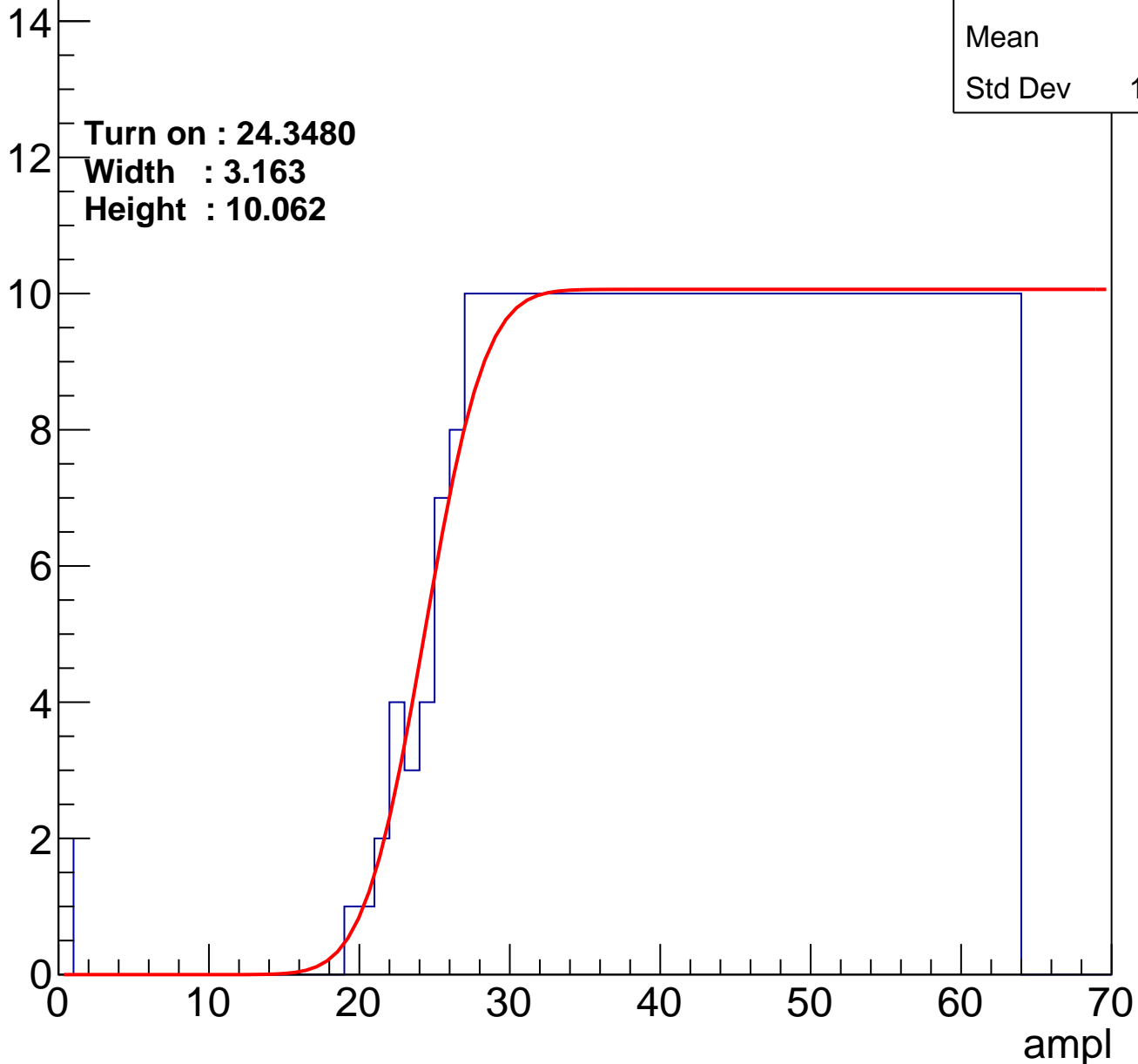
Entries	402
Mean	43.2
Std Dev	12.05

Turn on : 24.3480

Width : 3.163

Height : 10.062

Entry



B1L000S, U3-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.2
Std Dev	10.83

Turn on : 27.9894

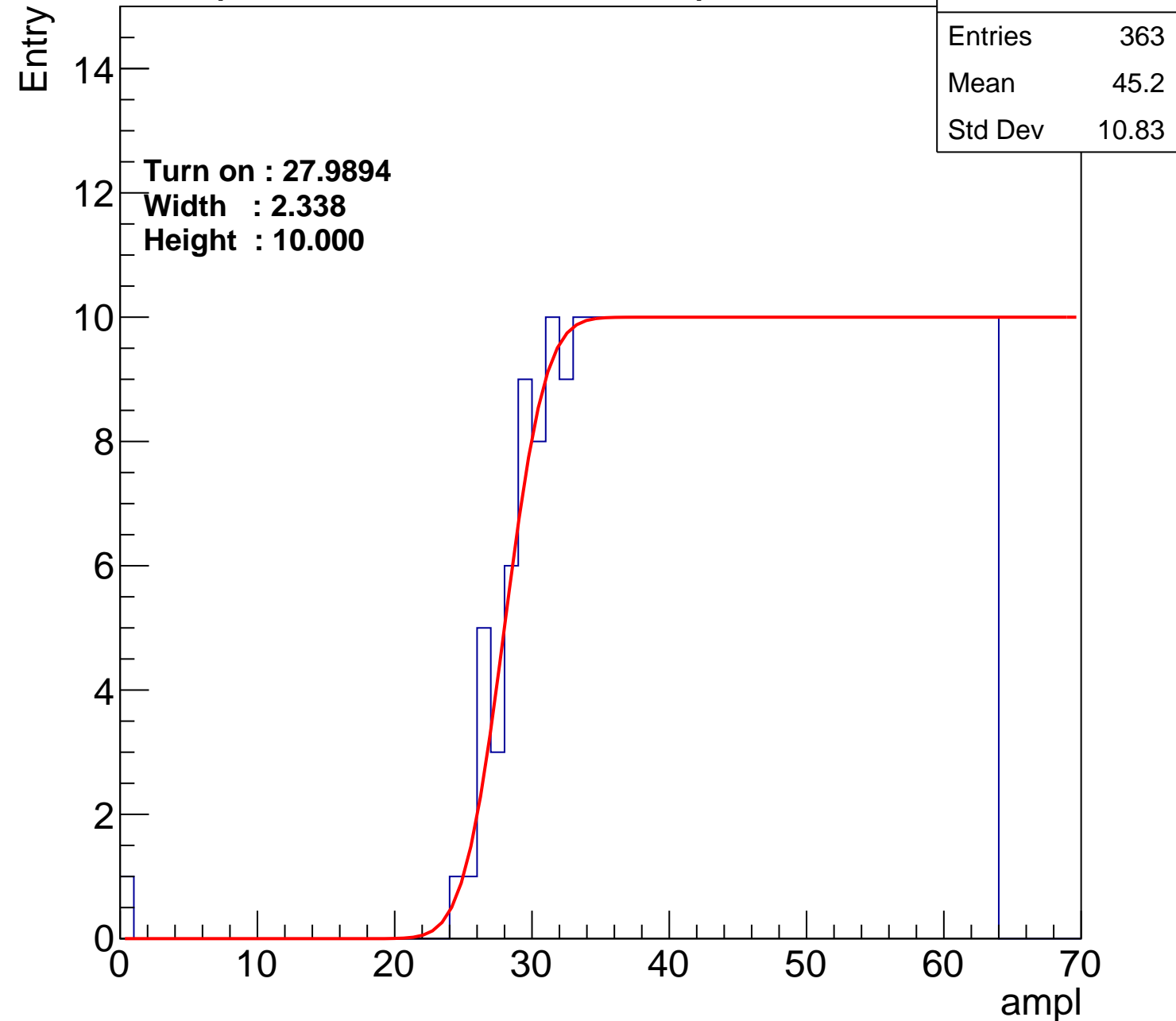
Width : 2.338

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.68
Std Dev	12.11

Turn on : 26.1713

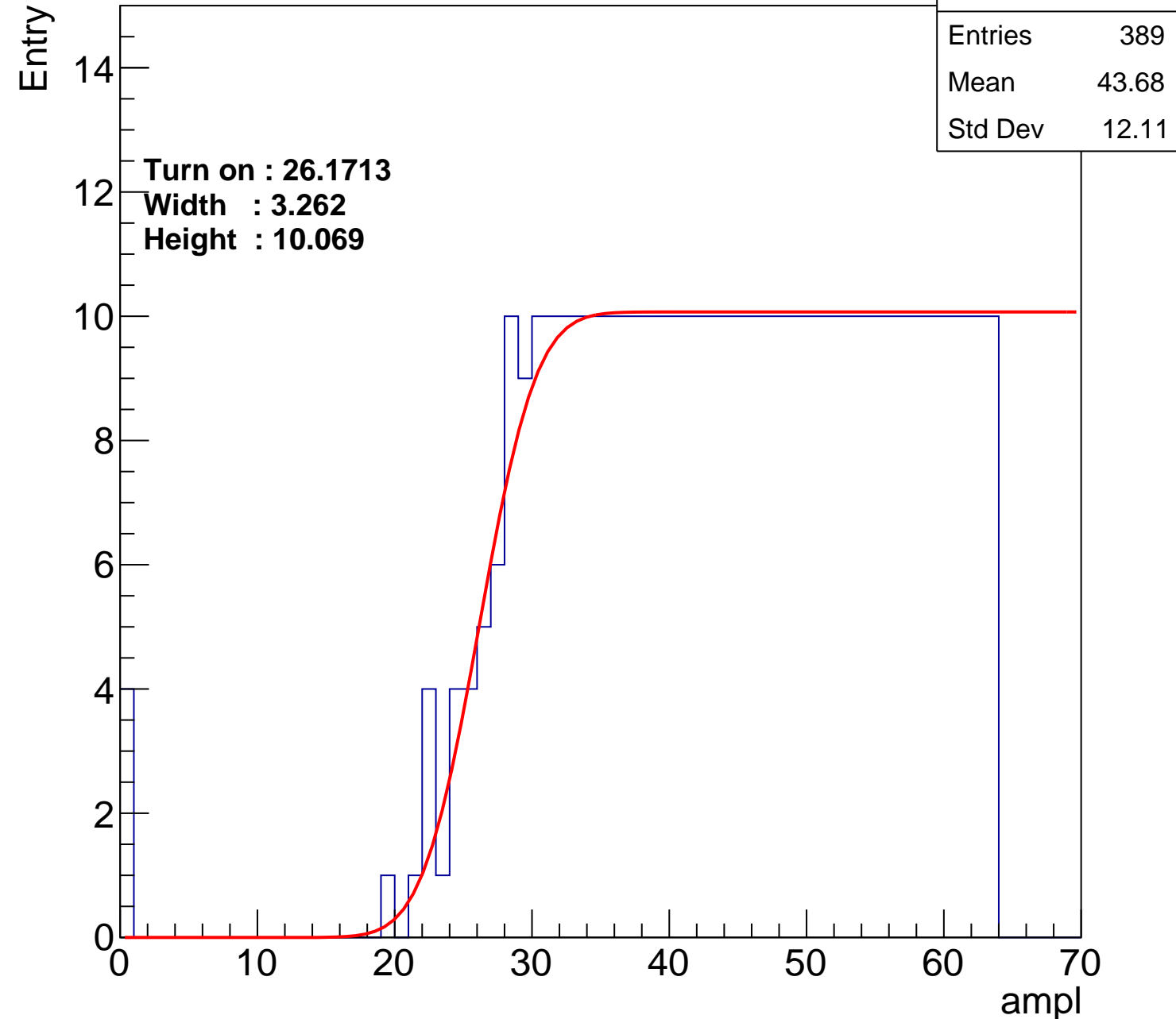
Width : 3.262

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch49

calib_packv5_042523_0143.root, FC#3, port B3

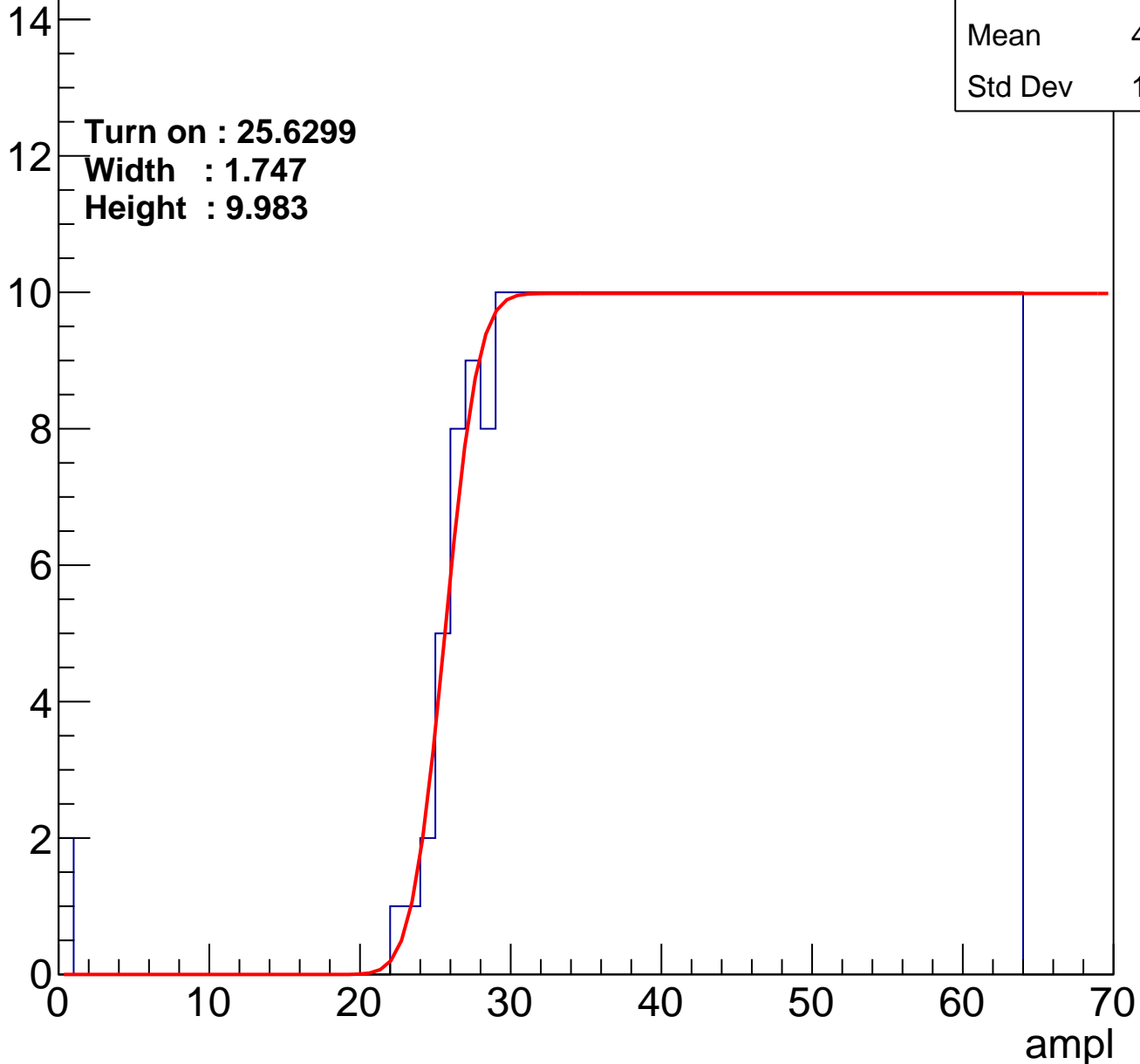
Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 25.6299

Width : 1.747

Height : 9.983

Entry



B1L000S, U3-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	411
Mean	42.55
Std Dev	12.76

Turn on : 23.7275

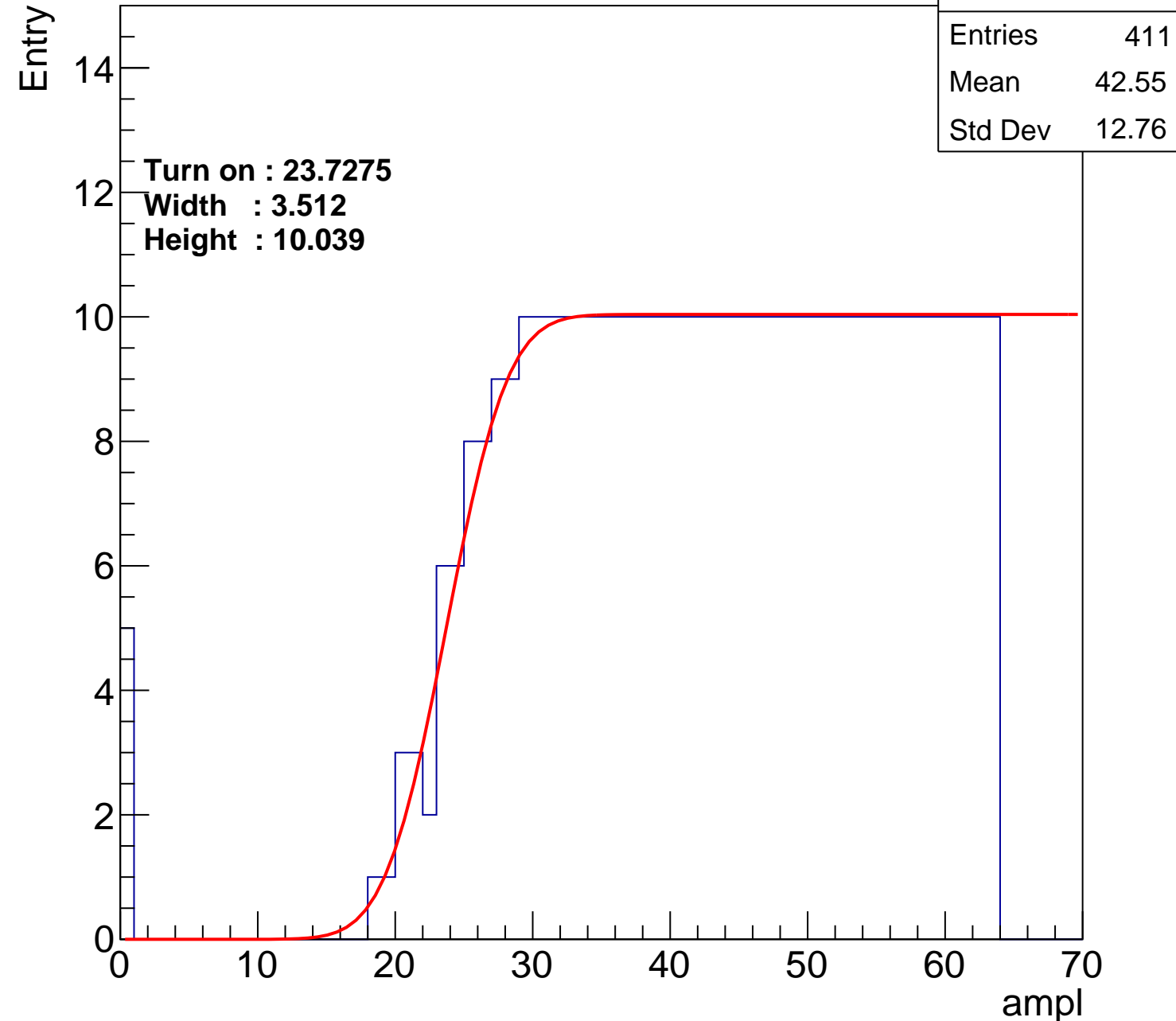
Width : 3.512

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.37
Std Dev	11.99

Turn on : 24.9818

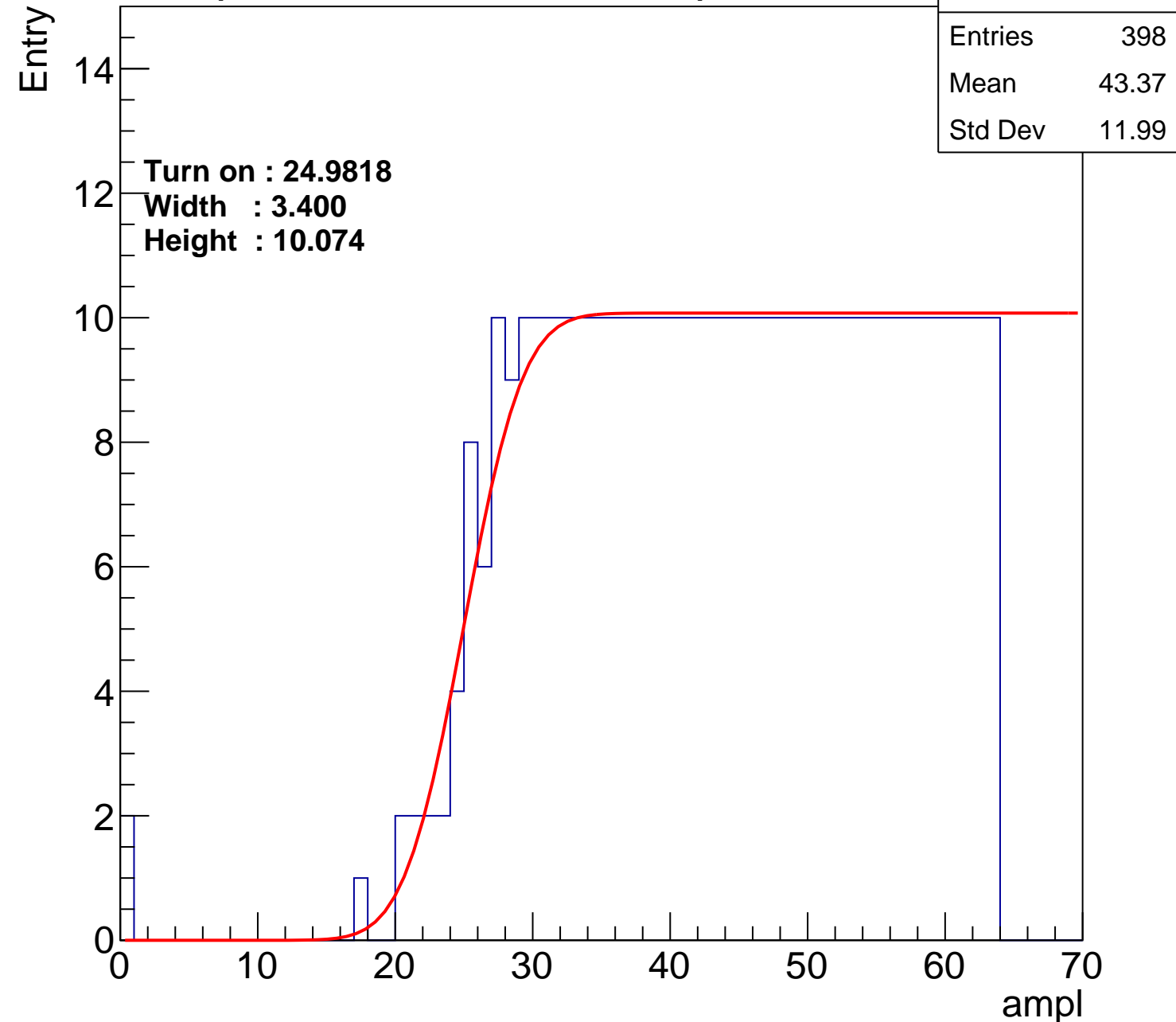
Width : 3.400

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.8216

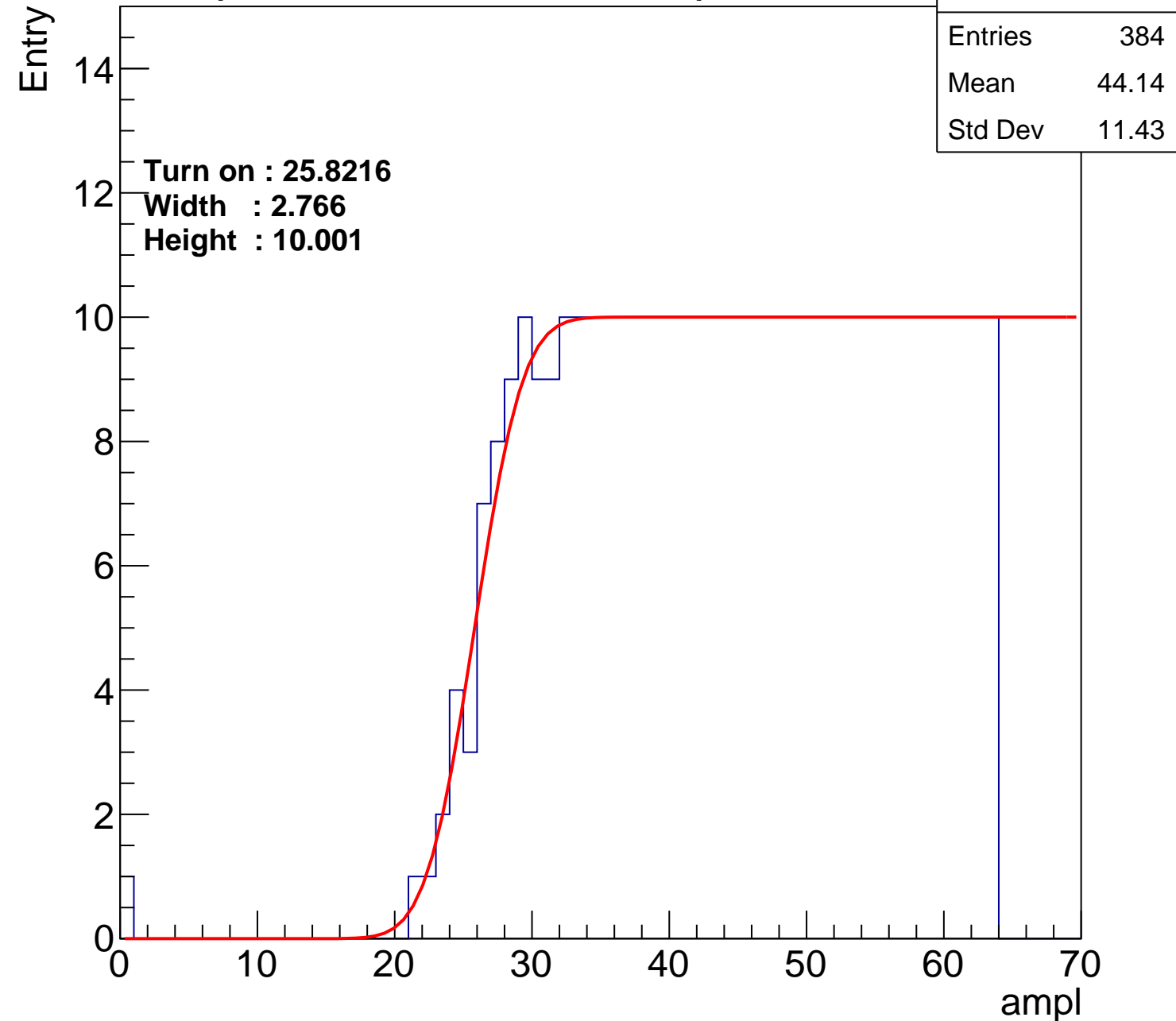
Width : 2.766

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.62
Std Dev	12.25

Turn on : 25.6495

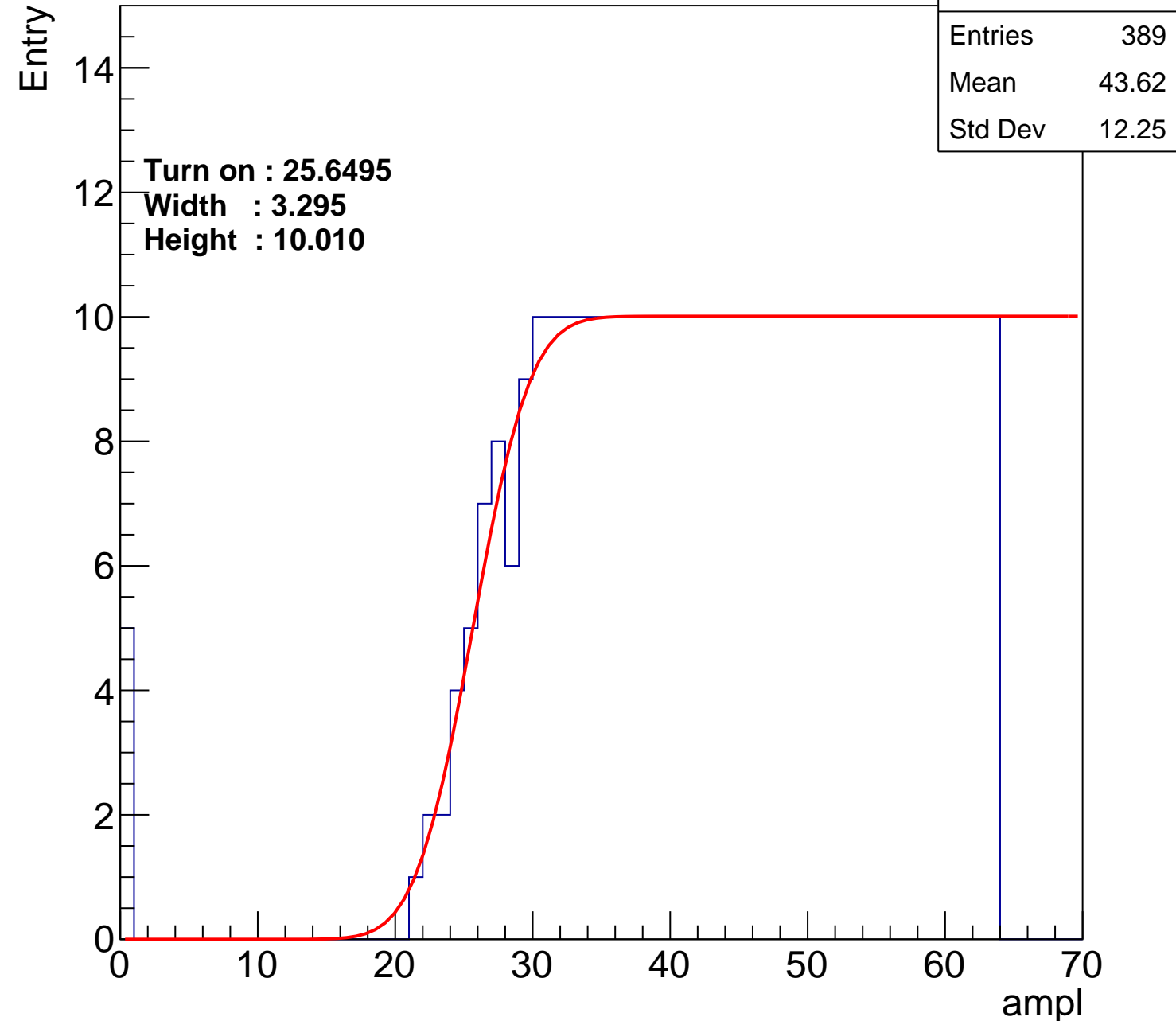
Width : 3.295

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.24
Std Dev	11.94

Turn on : 26.7182

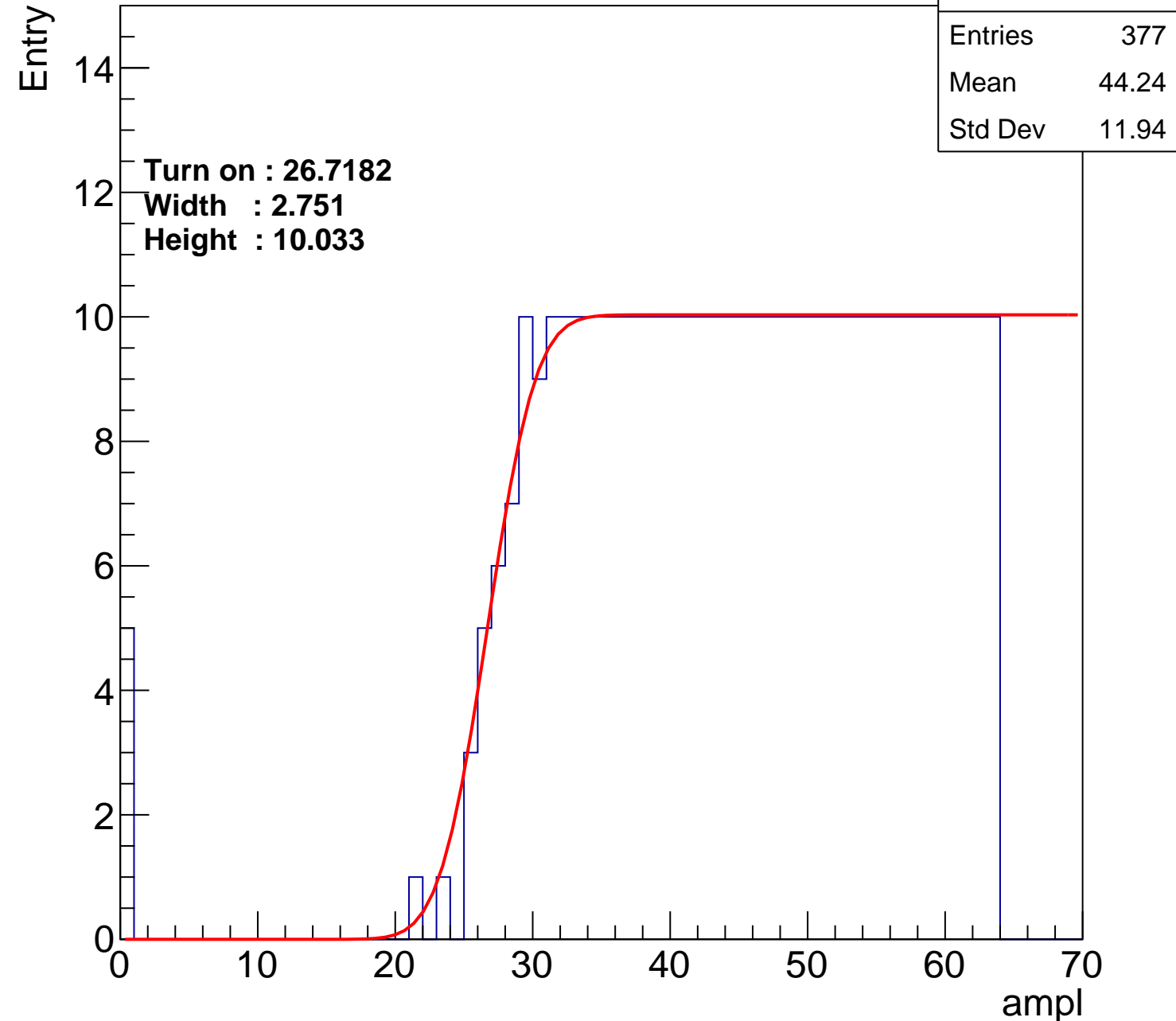
Width : 2.751

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.76
Std Dev	11.11

Turn on : 27.2270

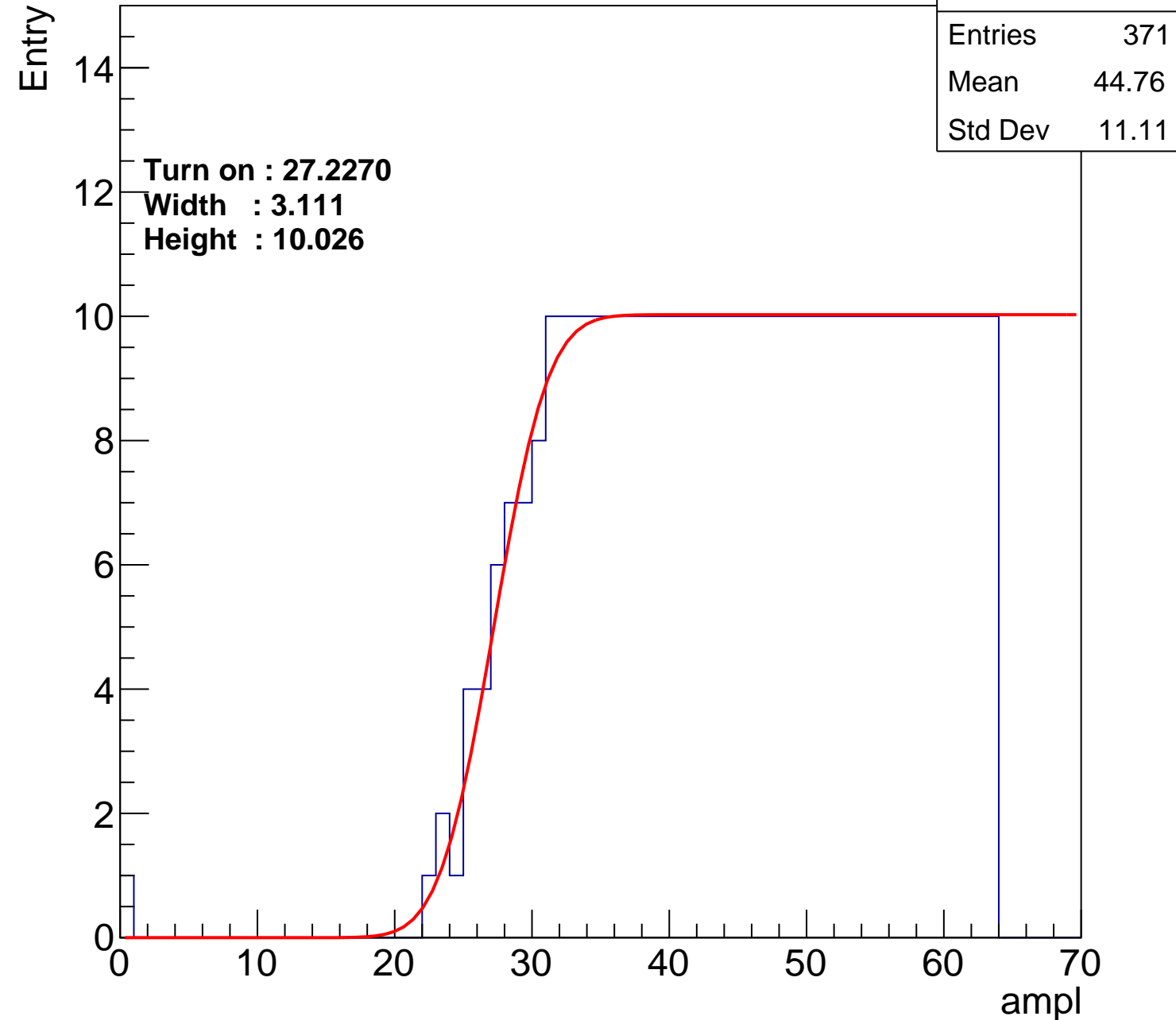
Width : 3.111

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.87
Std Dev	12.13

Turn on : 26.3632

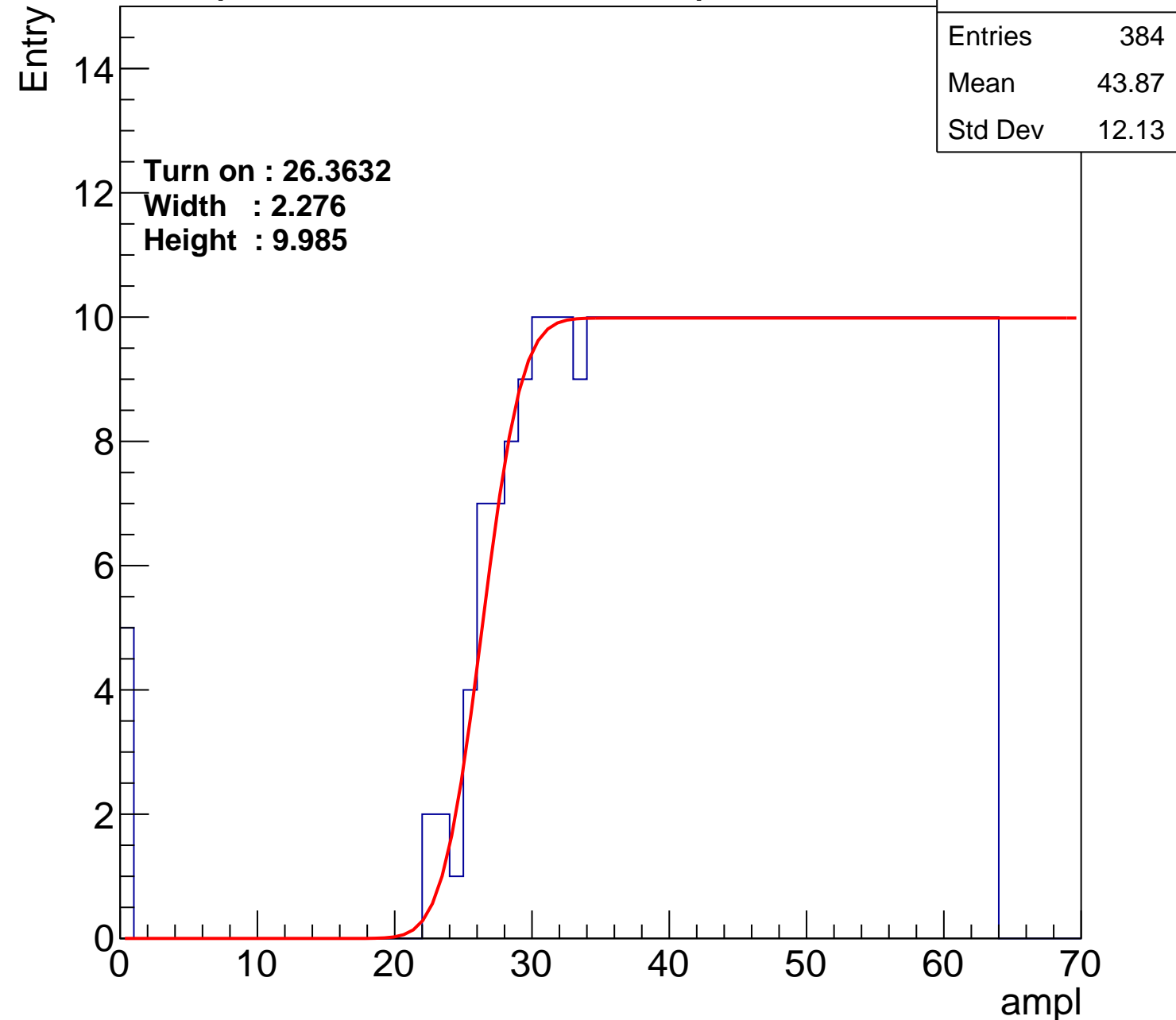
Width : 2.276

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.37
Std Dev	11.33

Turn on : 25.9640

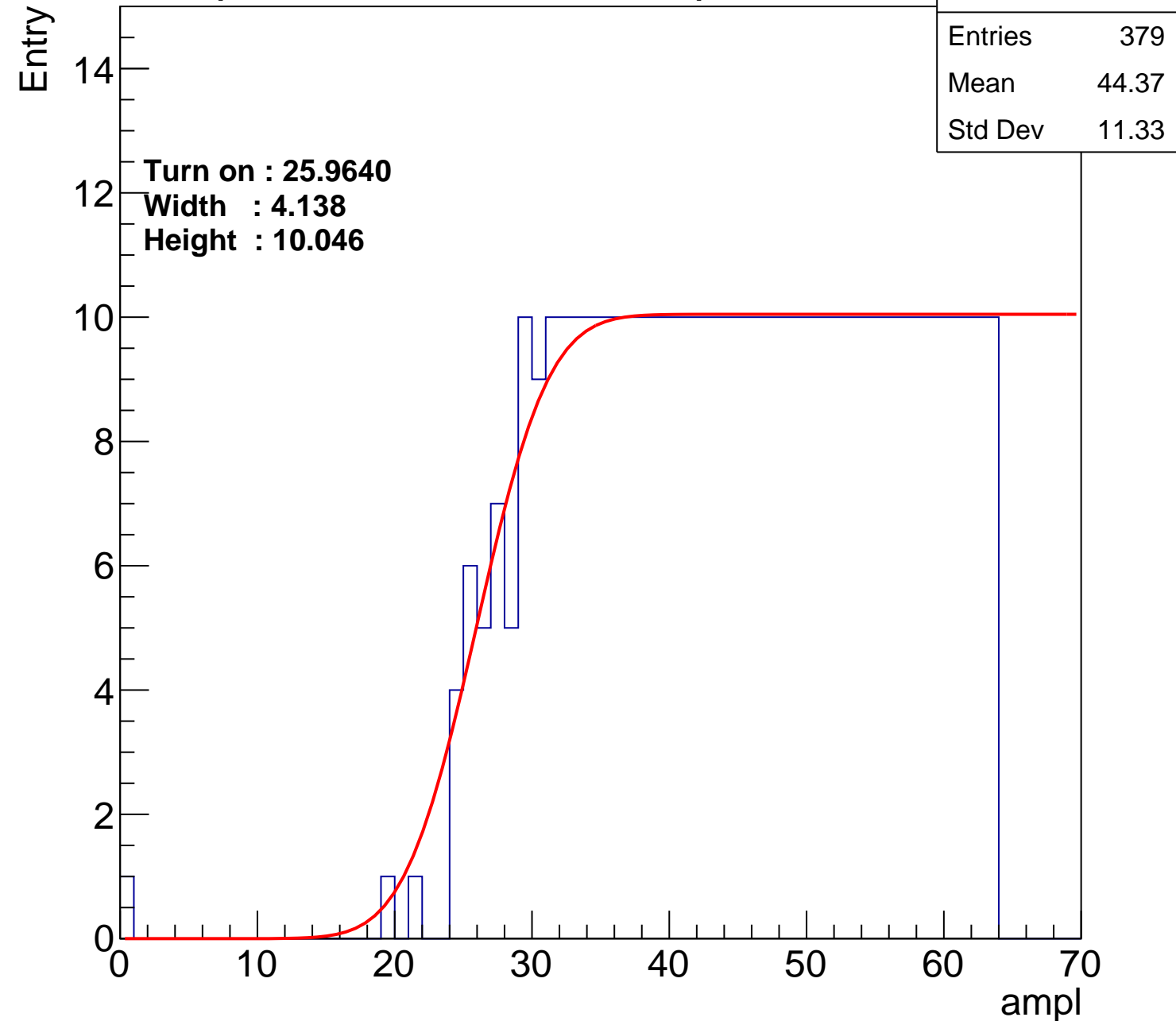
Width : 4.138

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 26.7269

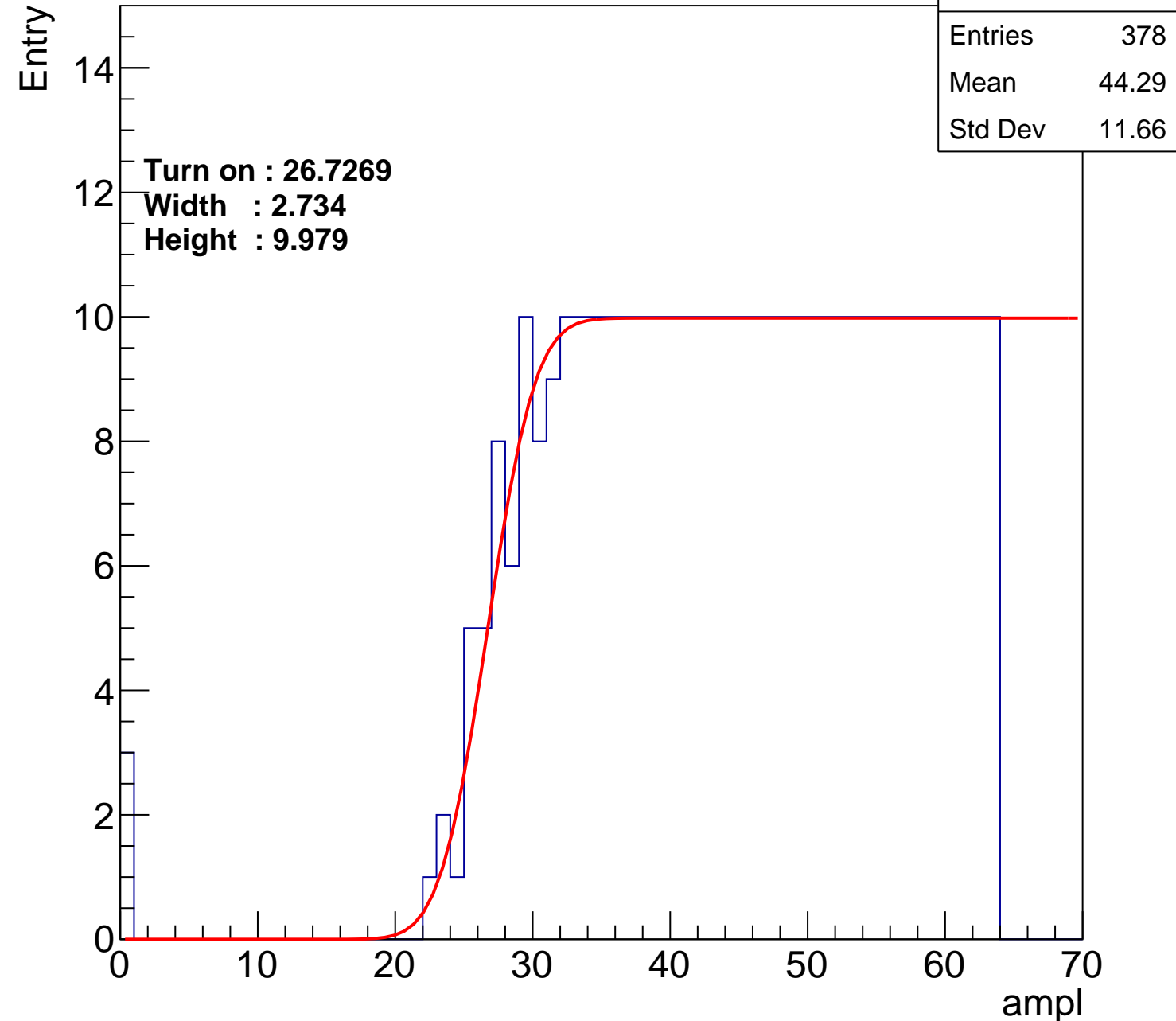
Width : 2.734

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.4935

Width : 3.533

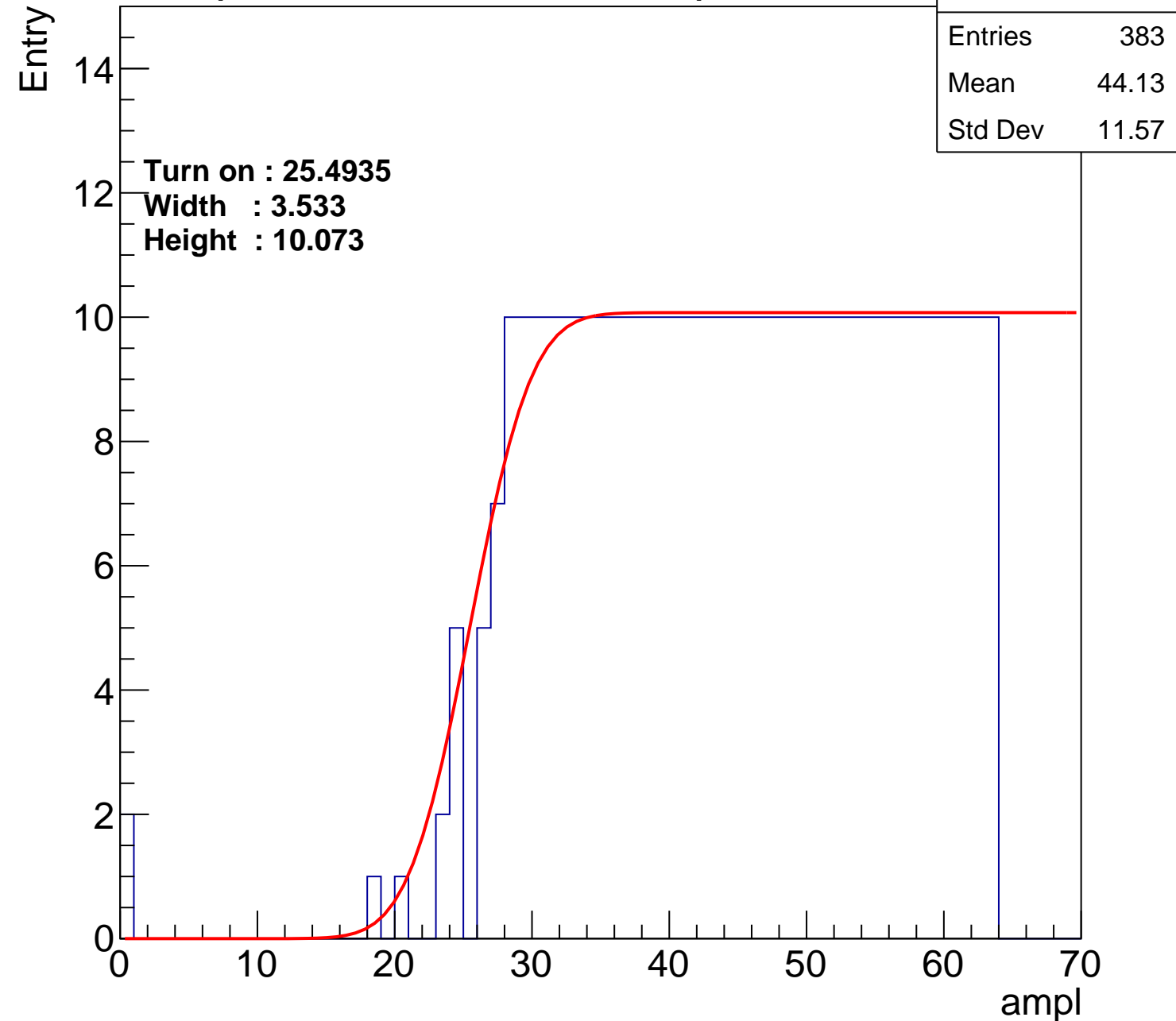
Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U3-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.79
Std Dev	12.27

Turn on : 26.2857

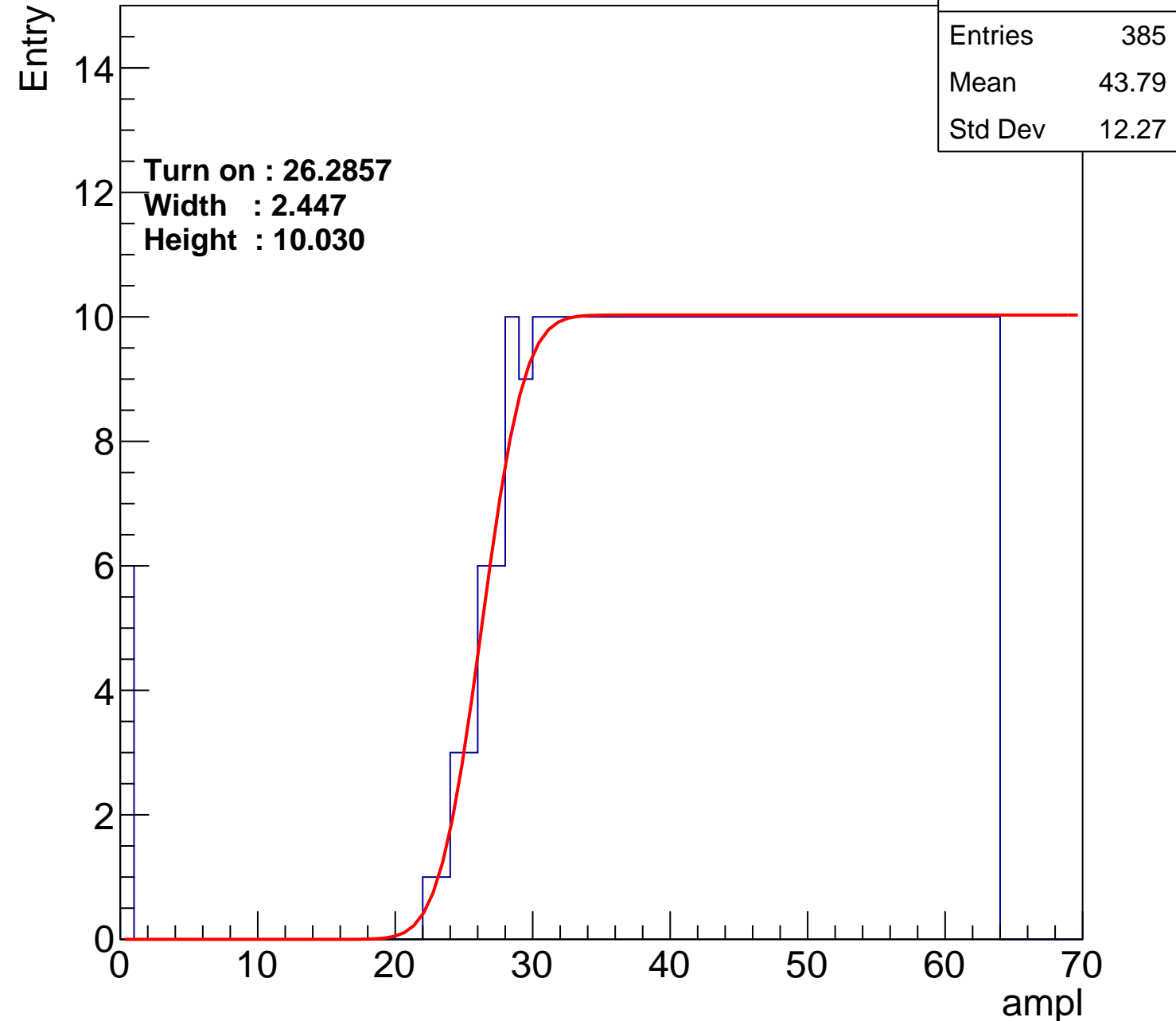
Width : 2.447

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch61

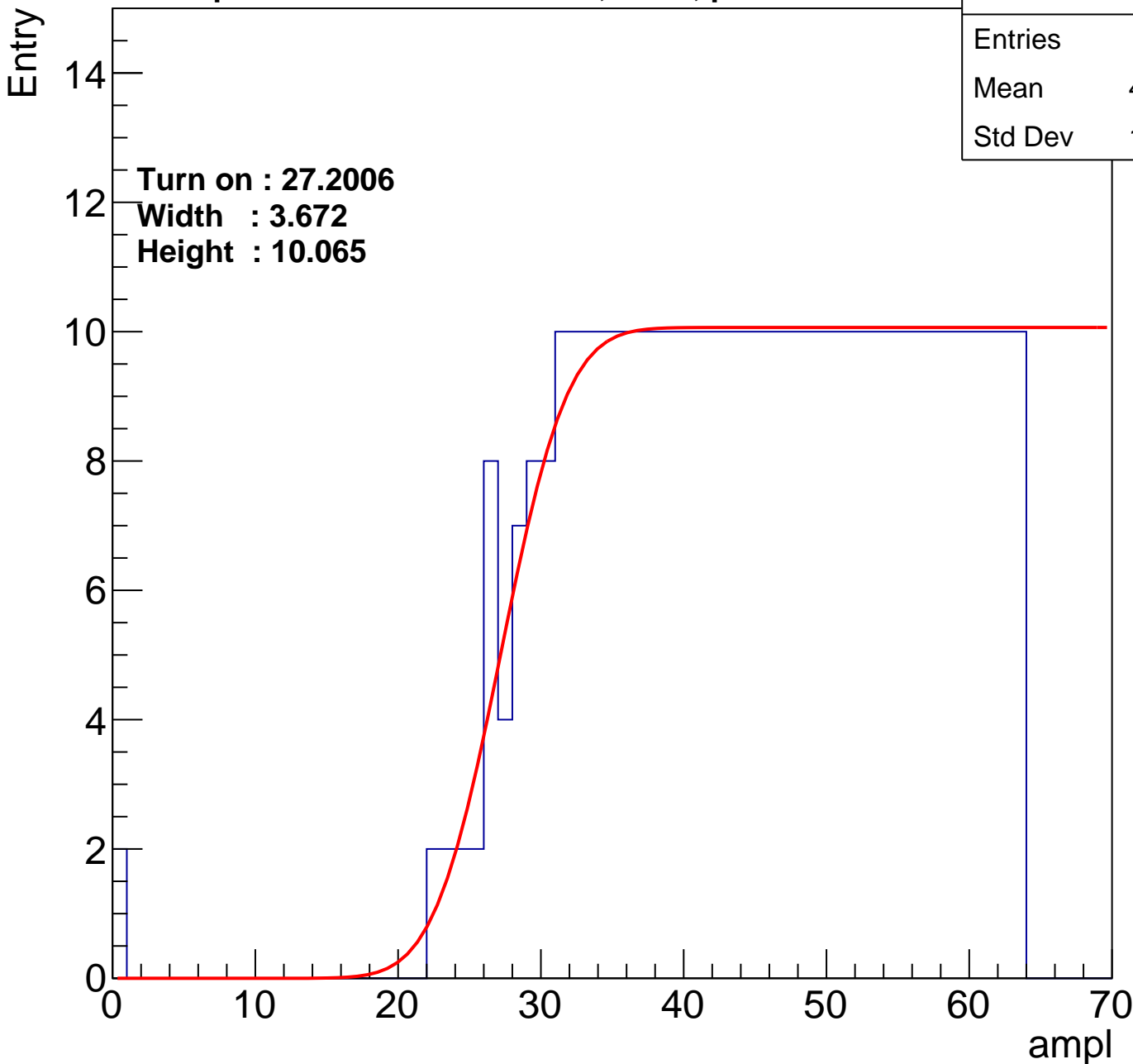
calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.49
Std Dev	11.43

Turn on : 27.2006

Width : 3.672

Height : 10.065



B1L000S, U3-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.63

Turn on : 25.3493

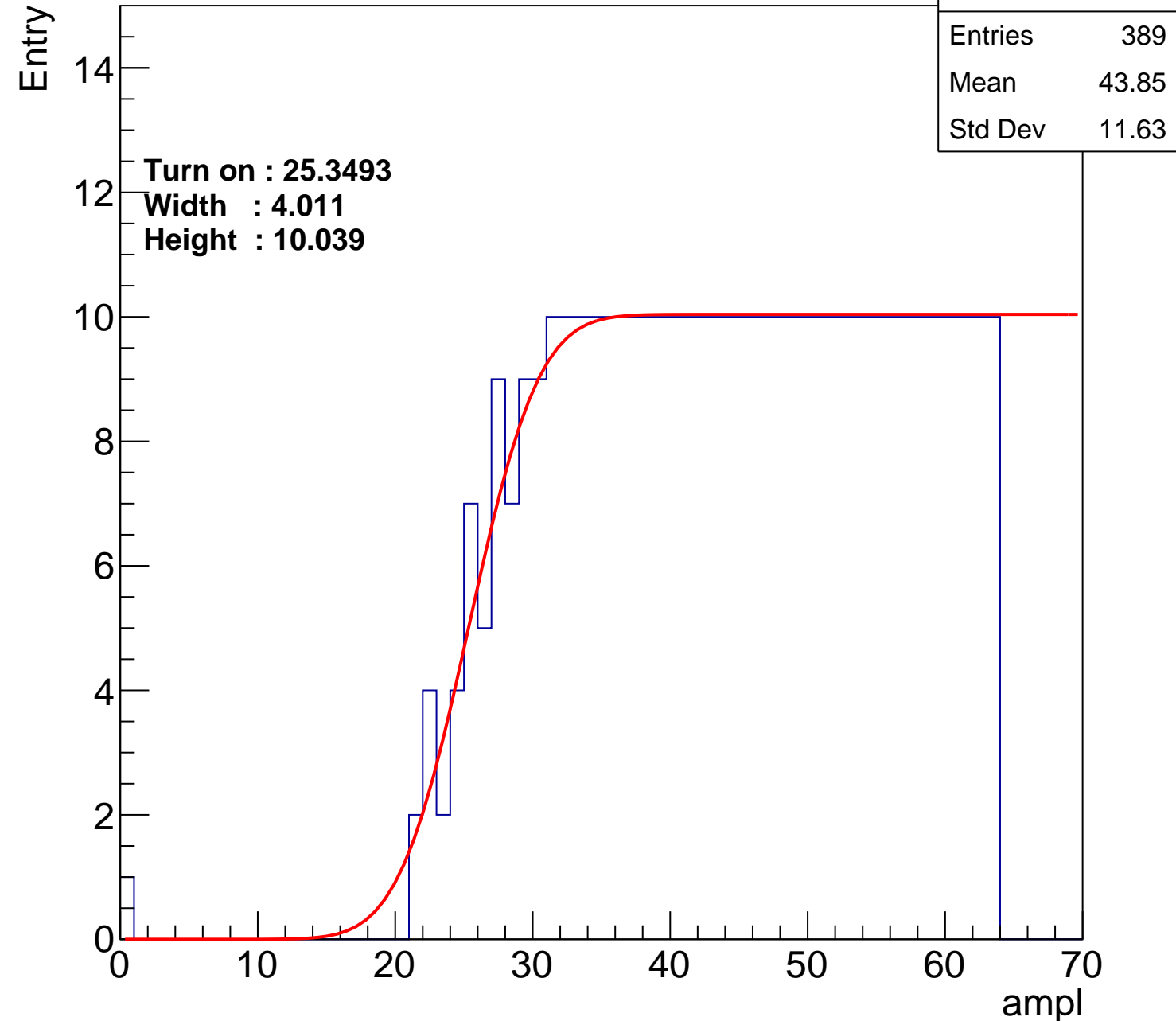
Width : 4.011

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.68
Std Dev	12.21

Turn on : 25.9588

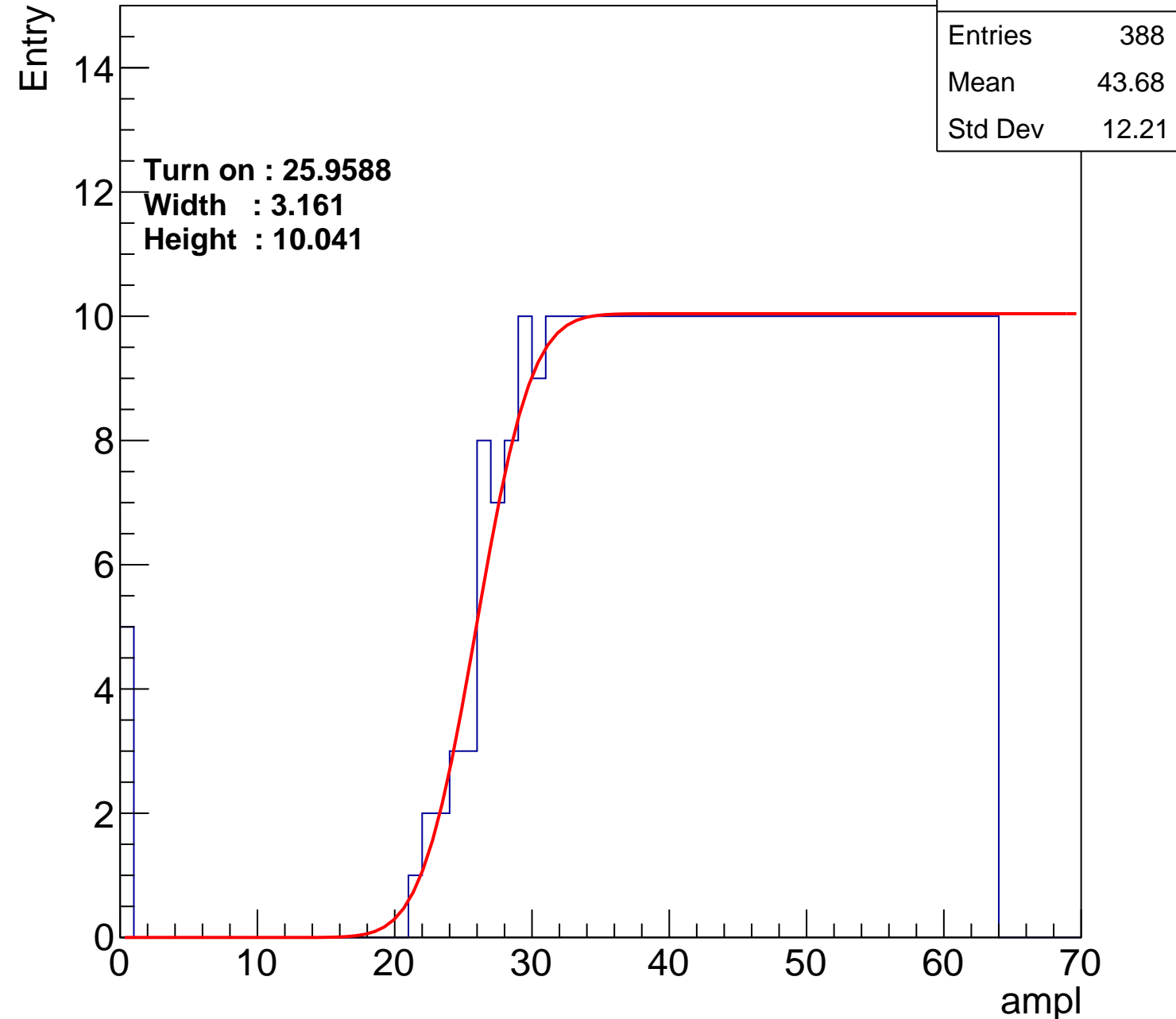
Width : 3.161

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 25.9055

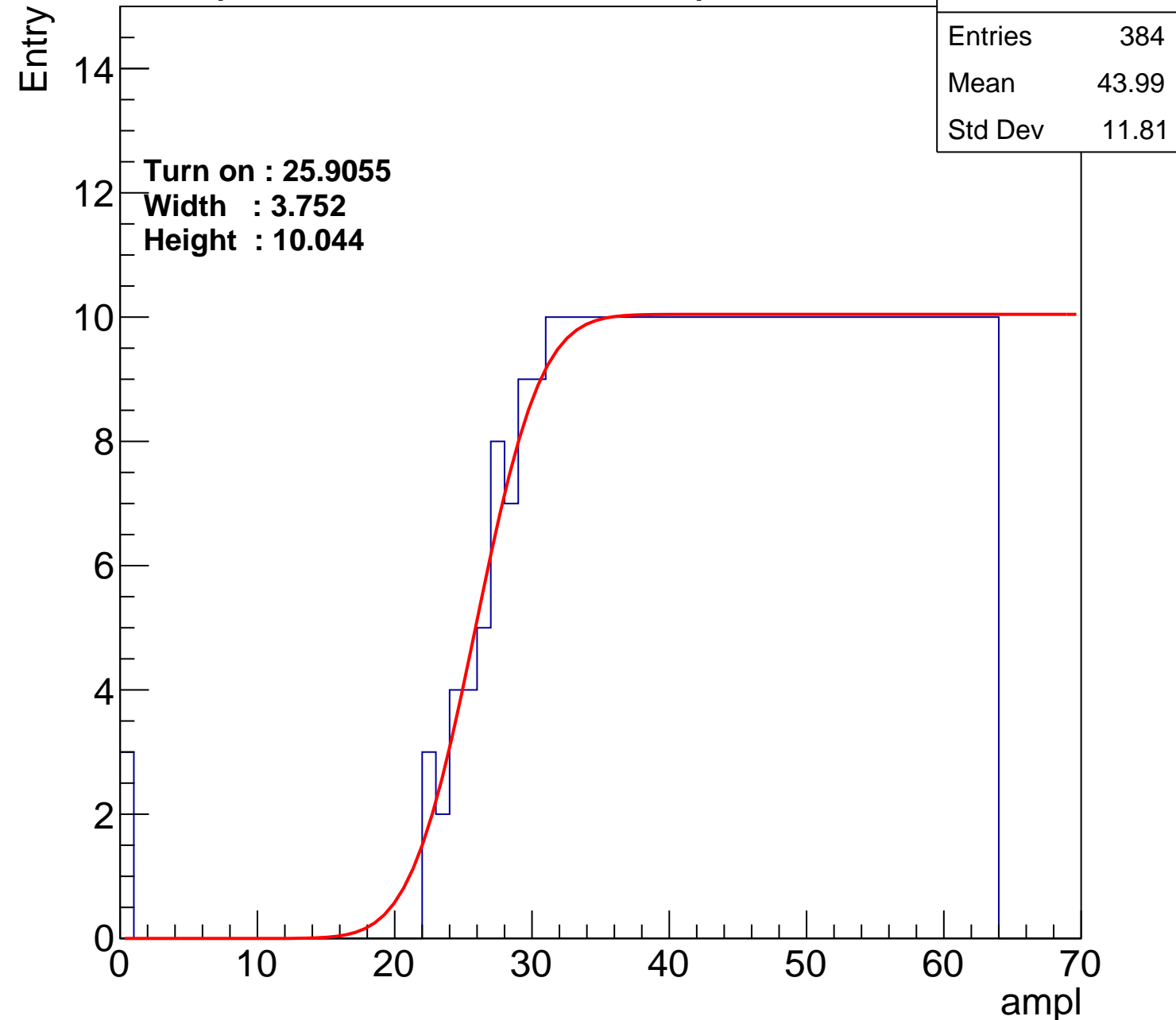
Width : 3.752

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.3847

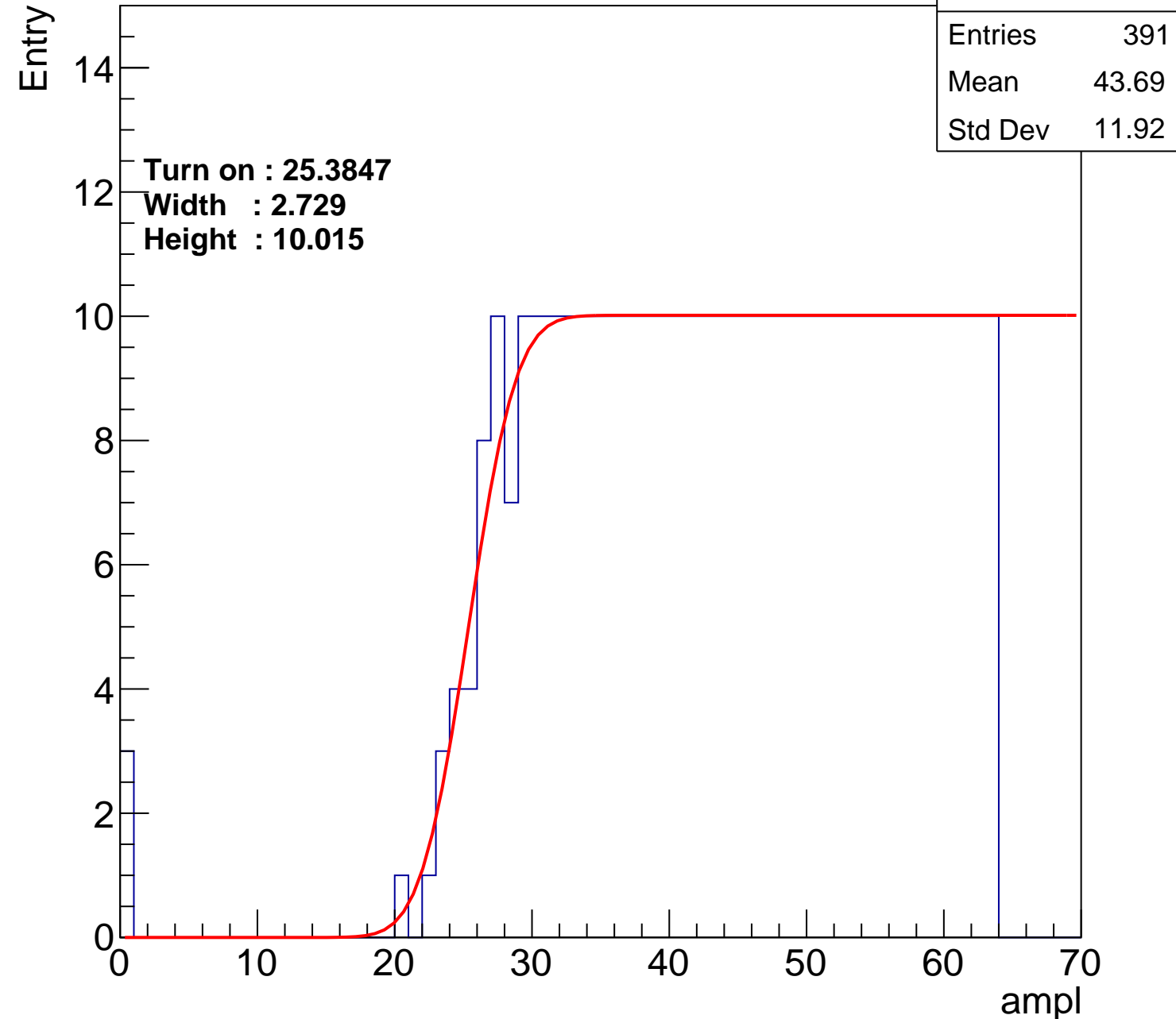
Width : 2.729

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.75
Std Dev	12.28

Turn on : 26.3448

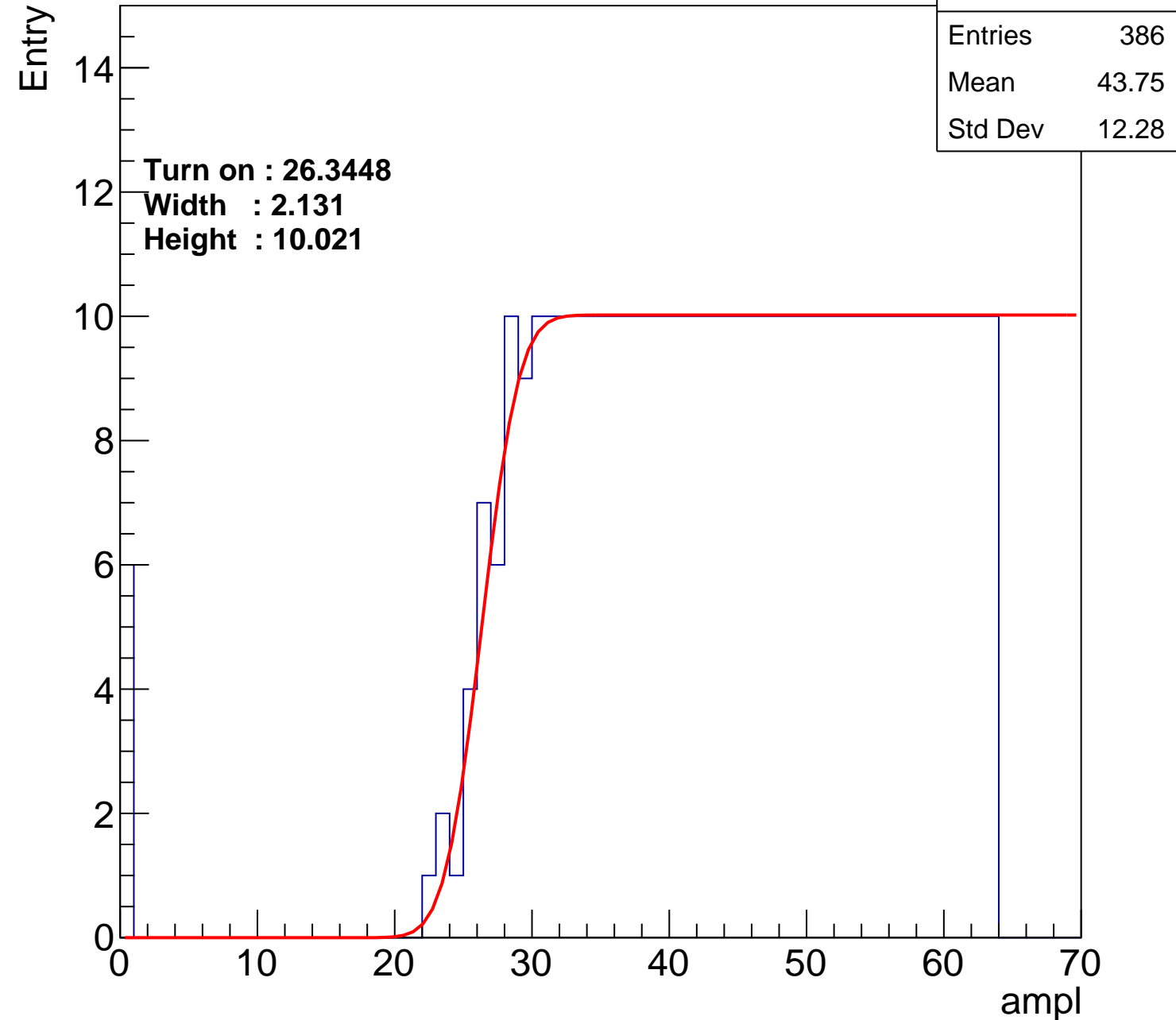
Width : 2.131

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch67

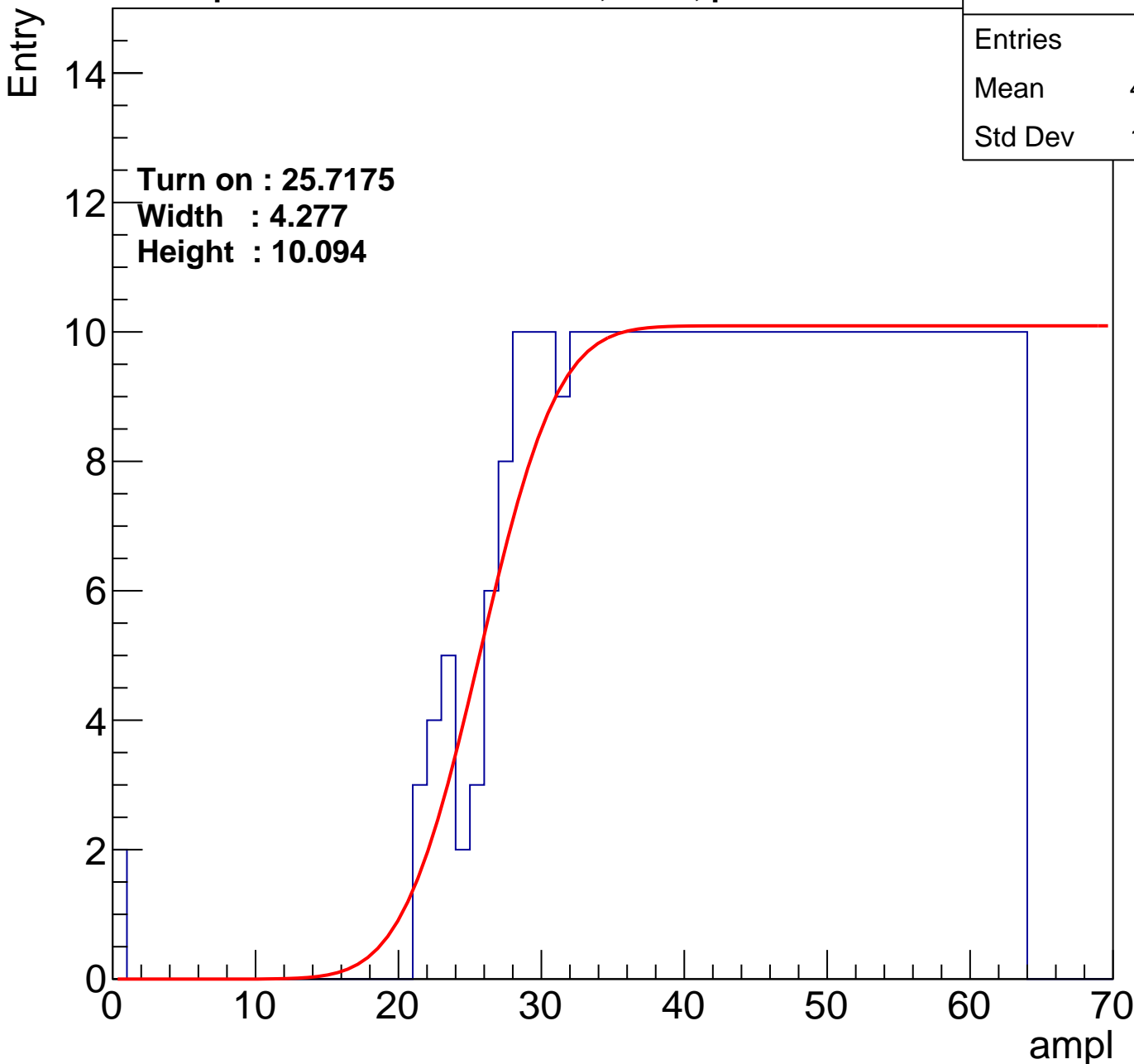
calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.7175

Width : 4.277

Height : 10.094



B1L000S, U3-ch68

calib_packv5_042523_0143.root, FC#3, port B3

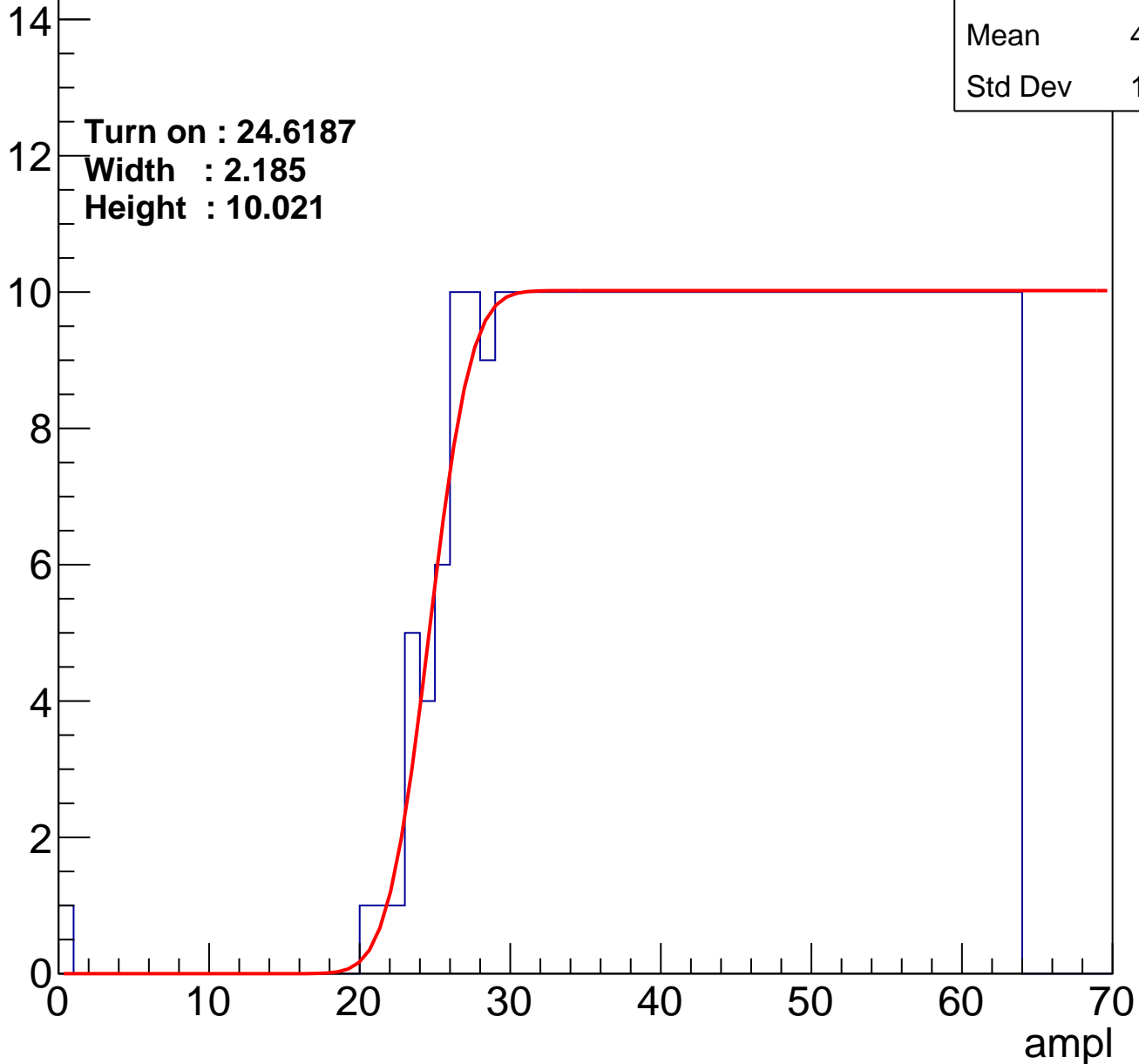
Entries	398
Mean	43.48
Std Dev	11.75

Turn on : 24.6187

Width : 2.185

Height : 10.021

Entry



B1L000S, U3-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.52
Std Dev	12.14

Turn on : 24.9443

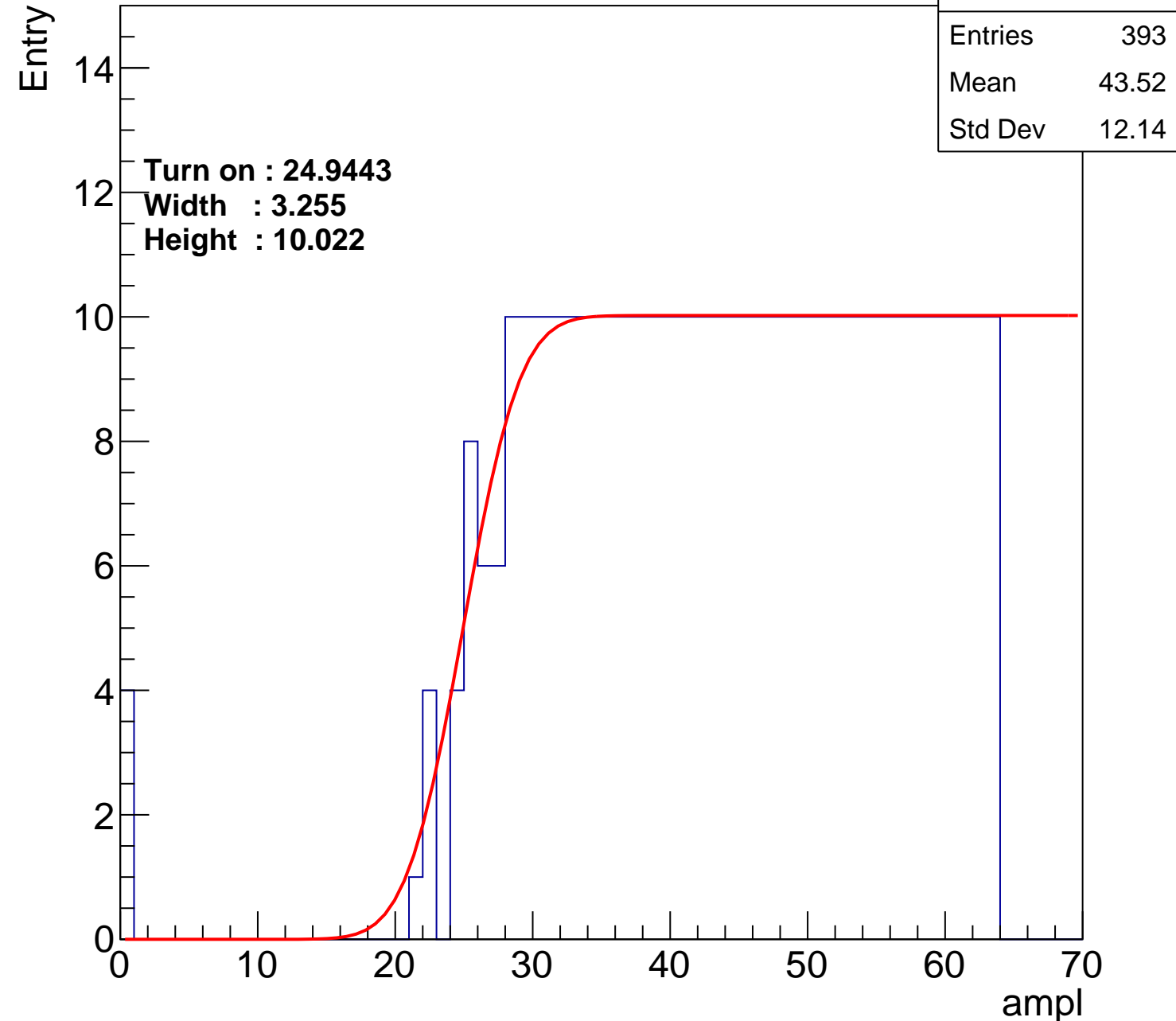
Width : 3.255

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.33
Std Dev	12.39

Turn on : 25.2141

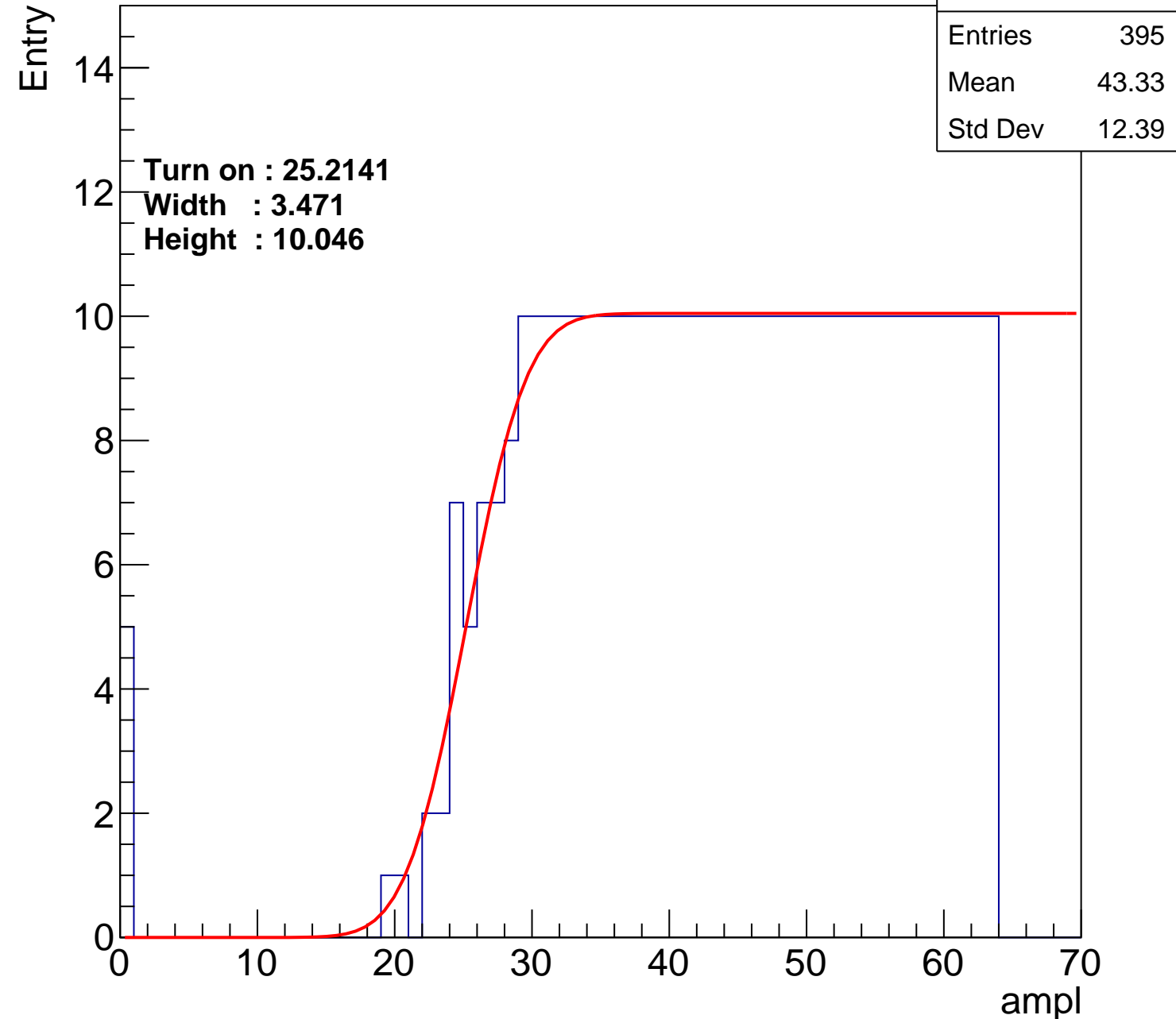
Width : 3.471

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch71

calib_packv5_042523_0143.root, FC#3, port B3

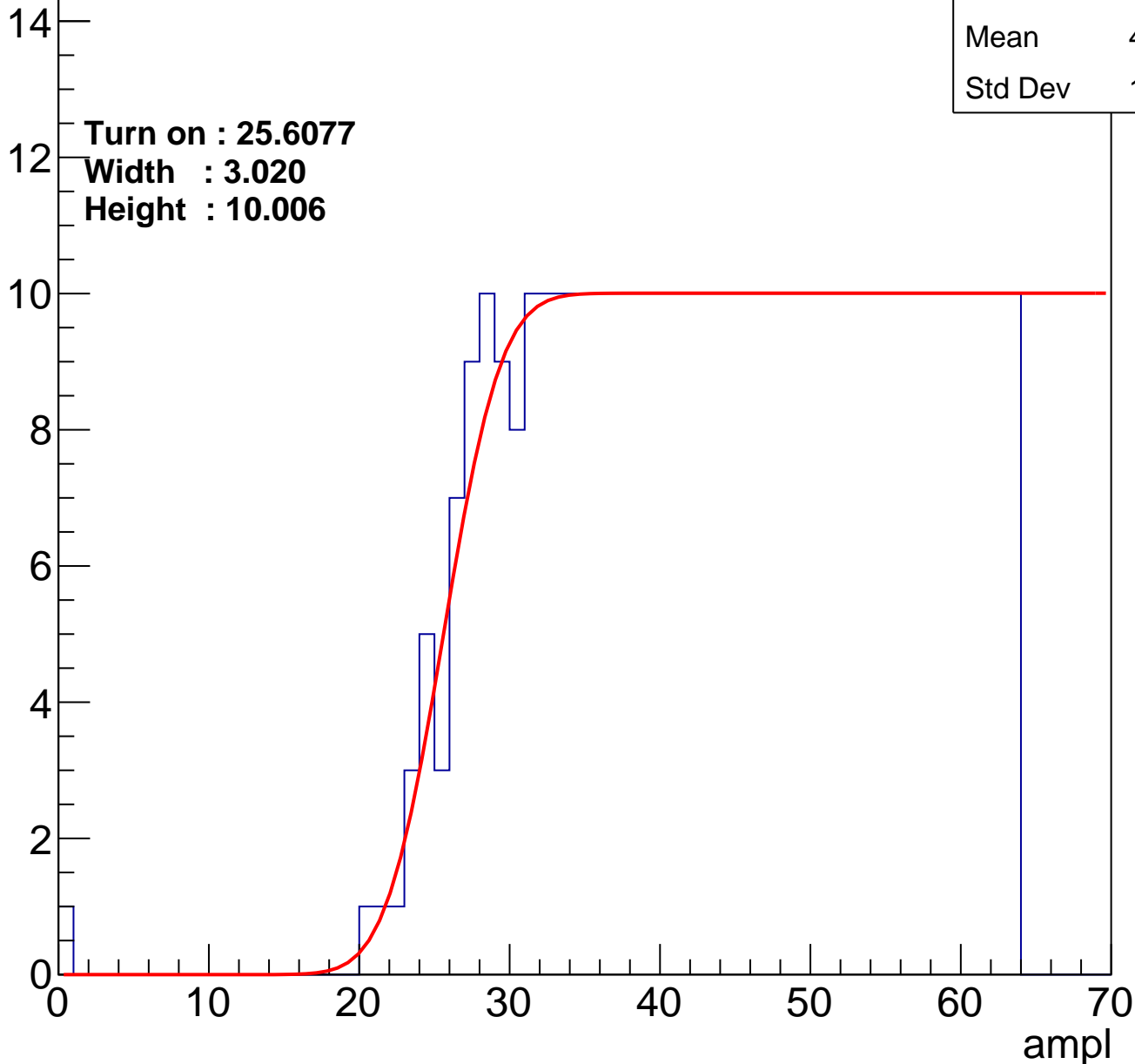
Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 25.6077

Width : 3.020

Height : 10.006

Entry



B1L000S, U3-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.06
Std Dev	11.92

Turn on : 26.2804

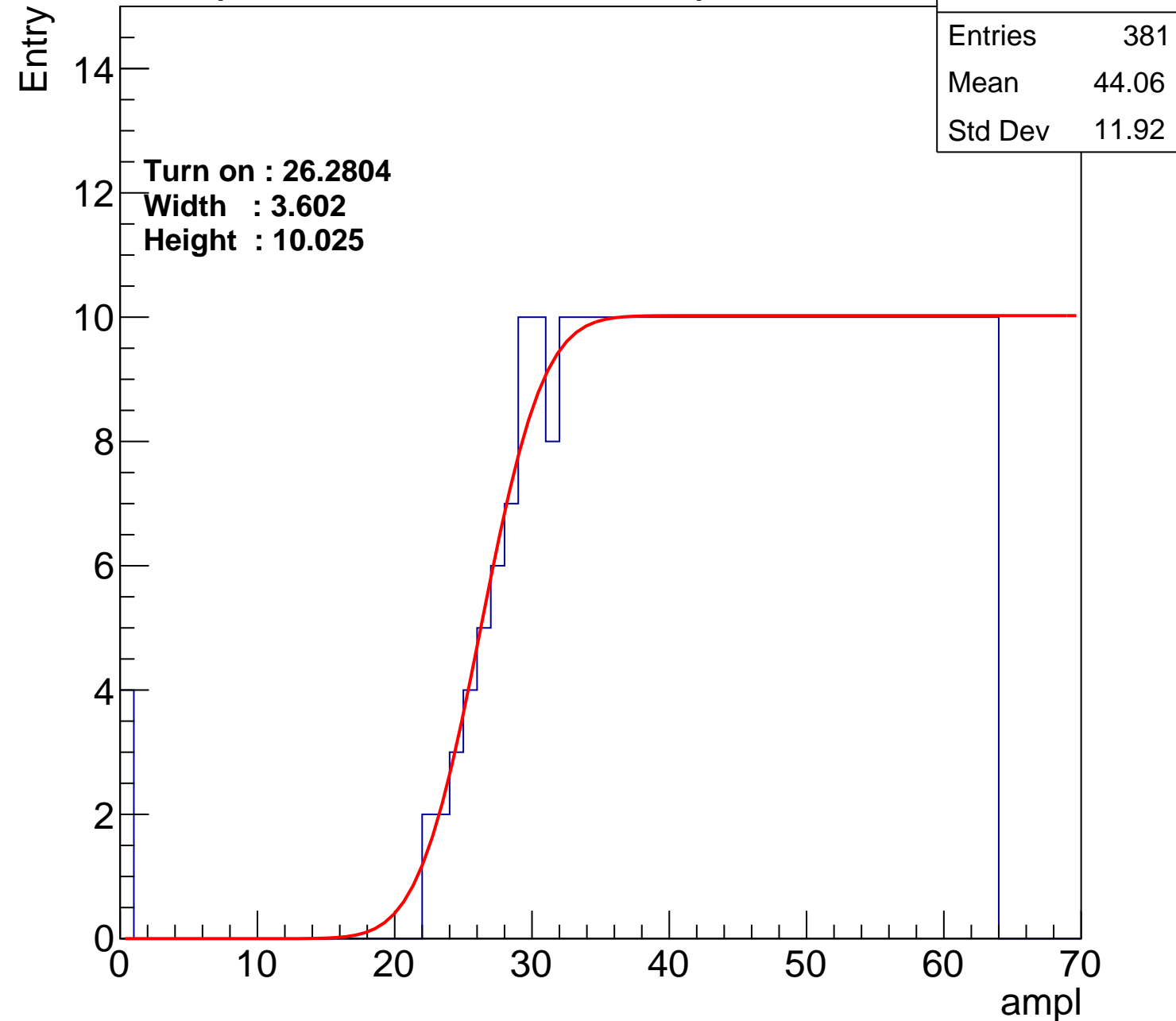
Width : 3.602

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch73

calib_packv5_042523_0143.root, FC#3, port B3

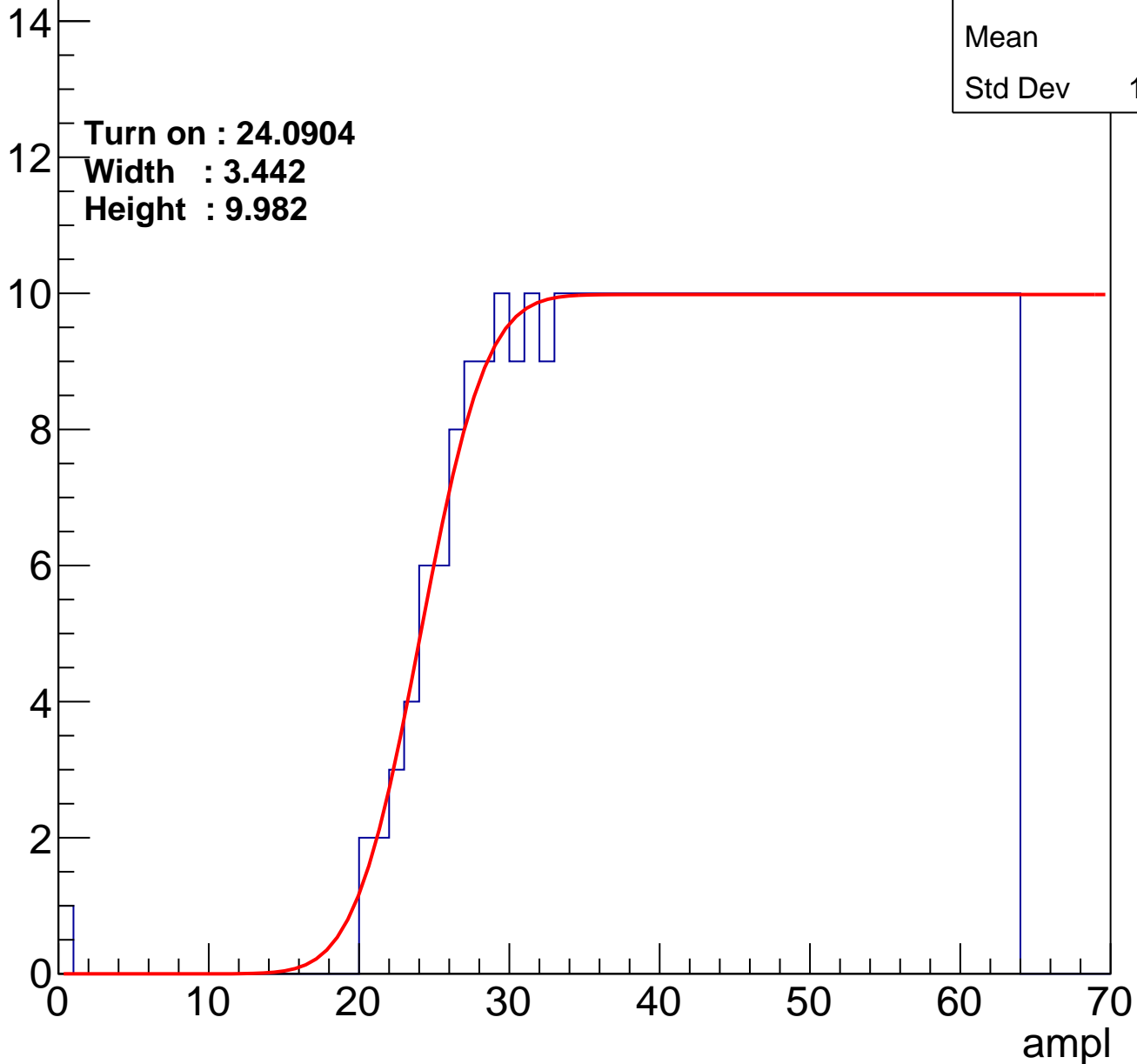
Entries	398
Mean	43.4
Std Dev	11.87

Turn on : 24.0904

Width : 3.442

Height : 9.982

Entry



B1L000S, U3-ch74

calib_packv5_042523_0143.root, FC#3, port B3

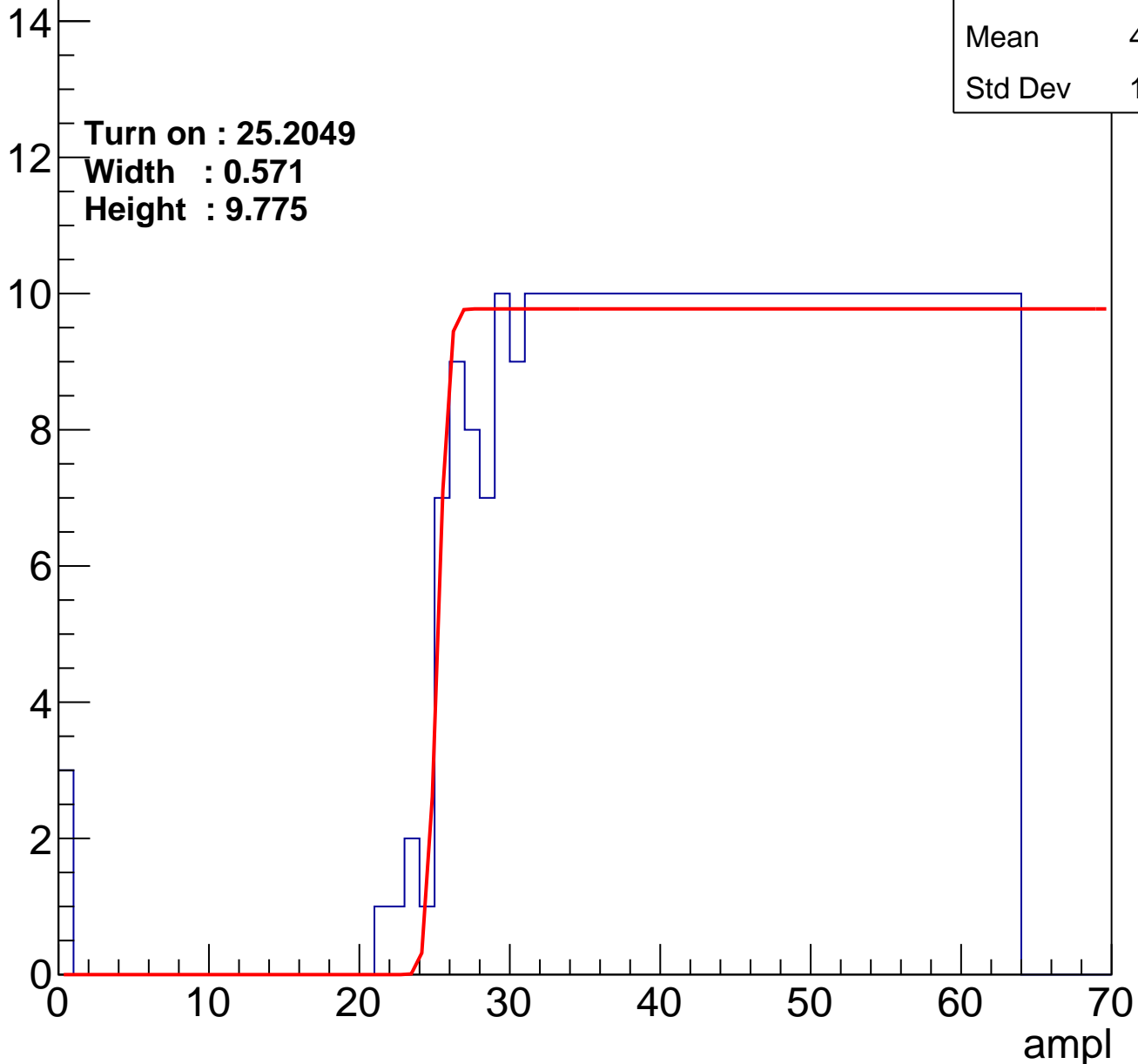
Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 25.2049

Width : 0.571

Height : 9.775

Entry



B1L000S, U3-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.06
Std Dev	12.28

Turn on : 24.5133

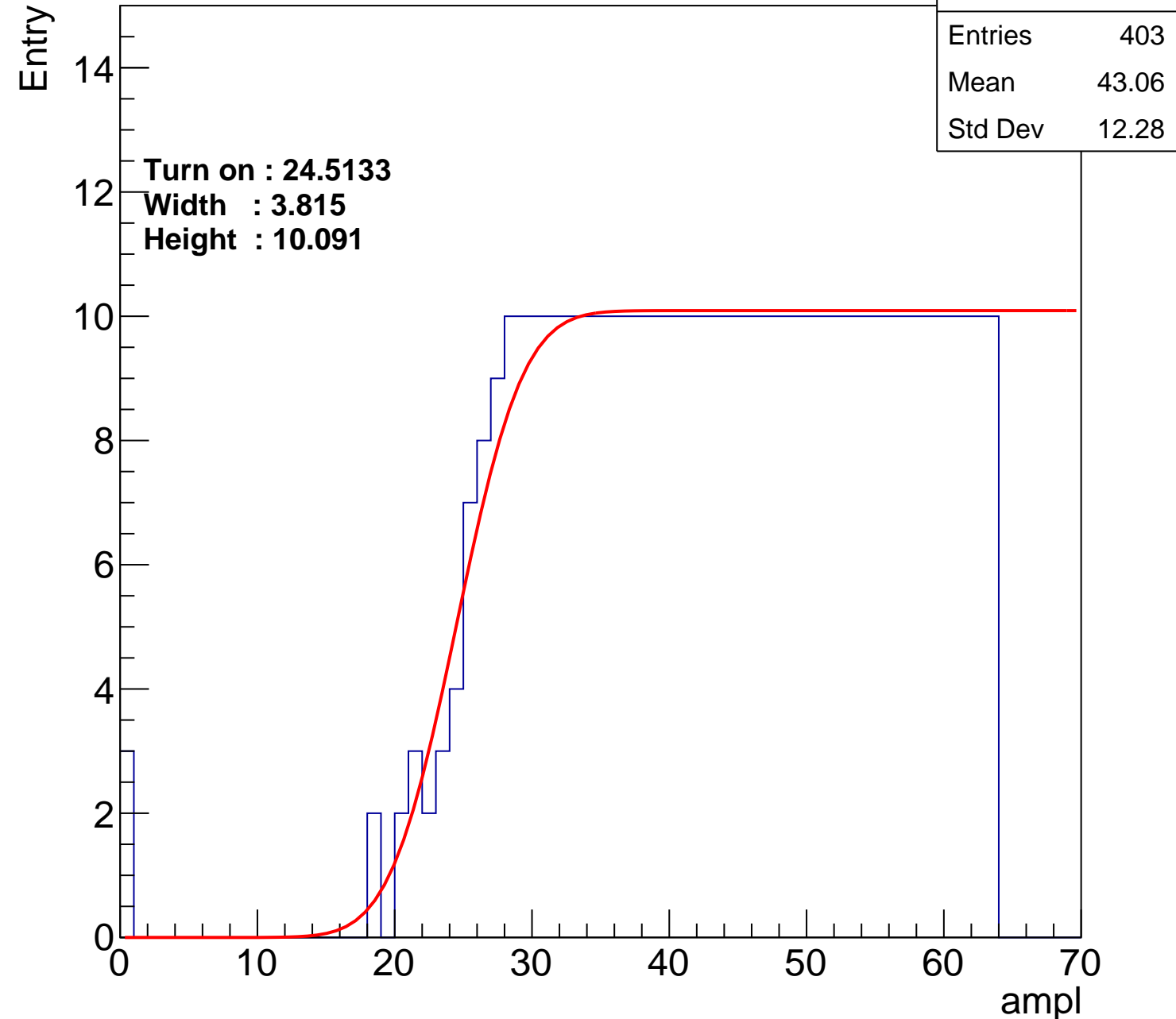
Width : 3.815

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch76

calib_packv5_042523_0143.root, FC#3, port B3

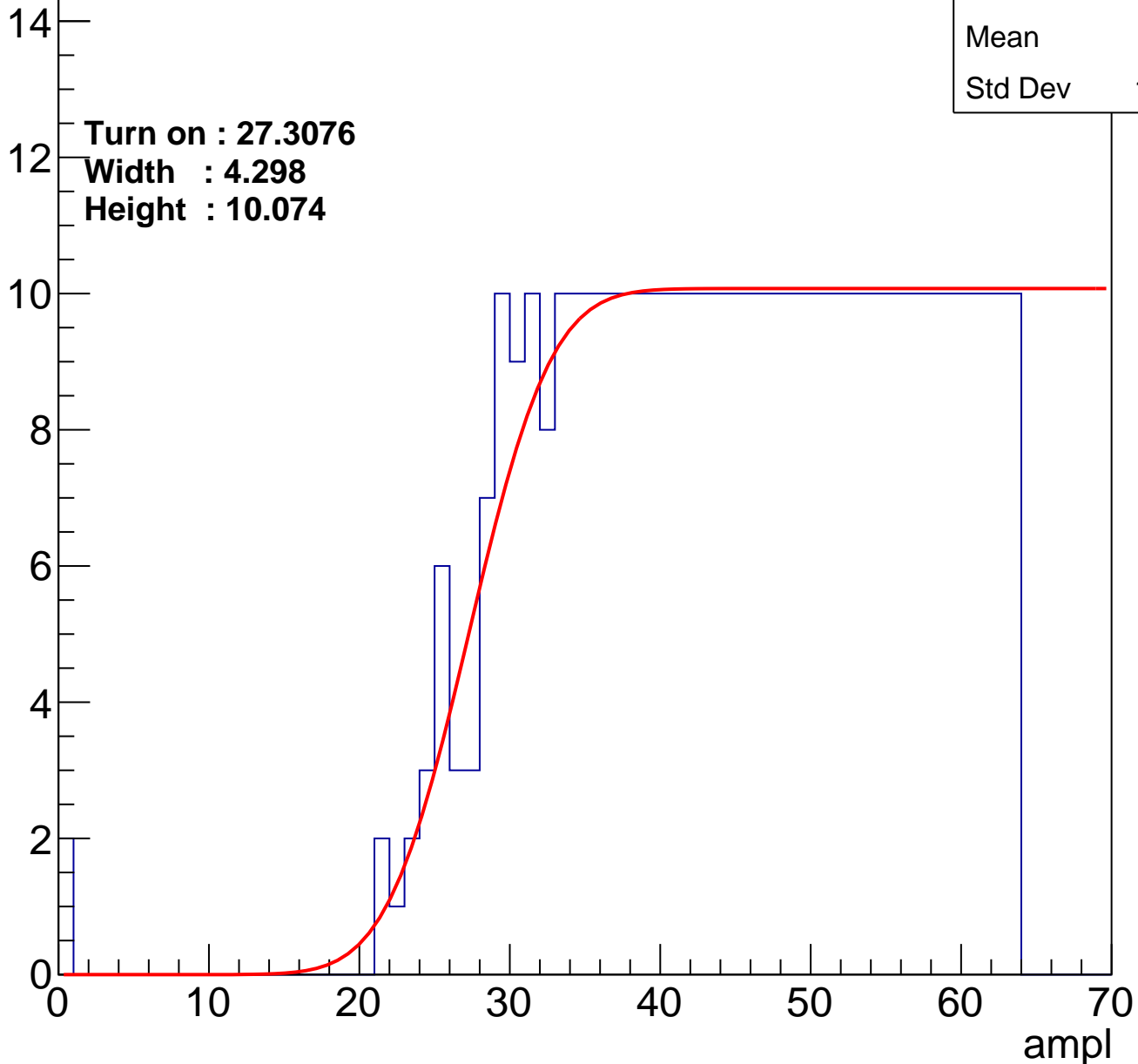
Entries	376
Mean	44.4
Std Dev	11.51

Turn on : 27.3076

Width : 4.298

Height : 10.074

Entry



B1L000S, U3-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.94
Std Dev	11.67

Turn on : 25.8383

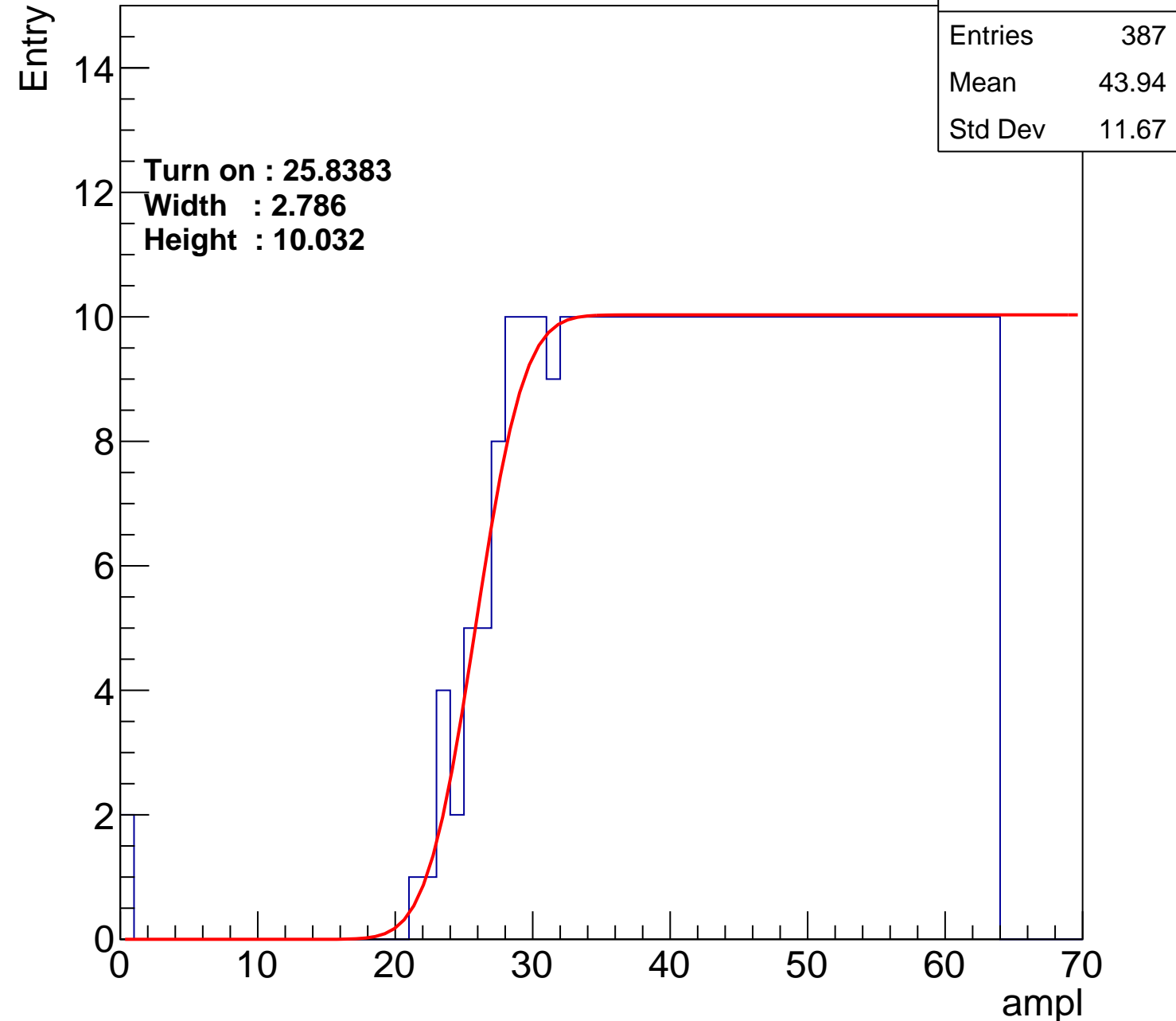
Width : 2.786

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch78

calib_packv5_042523_0143.root, FC#3, port B3

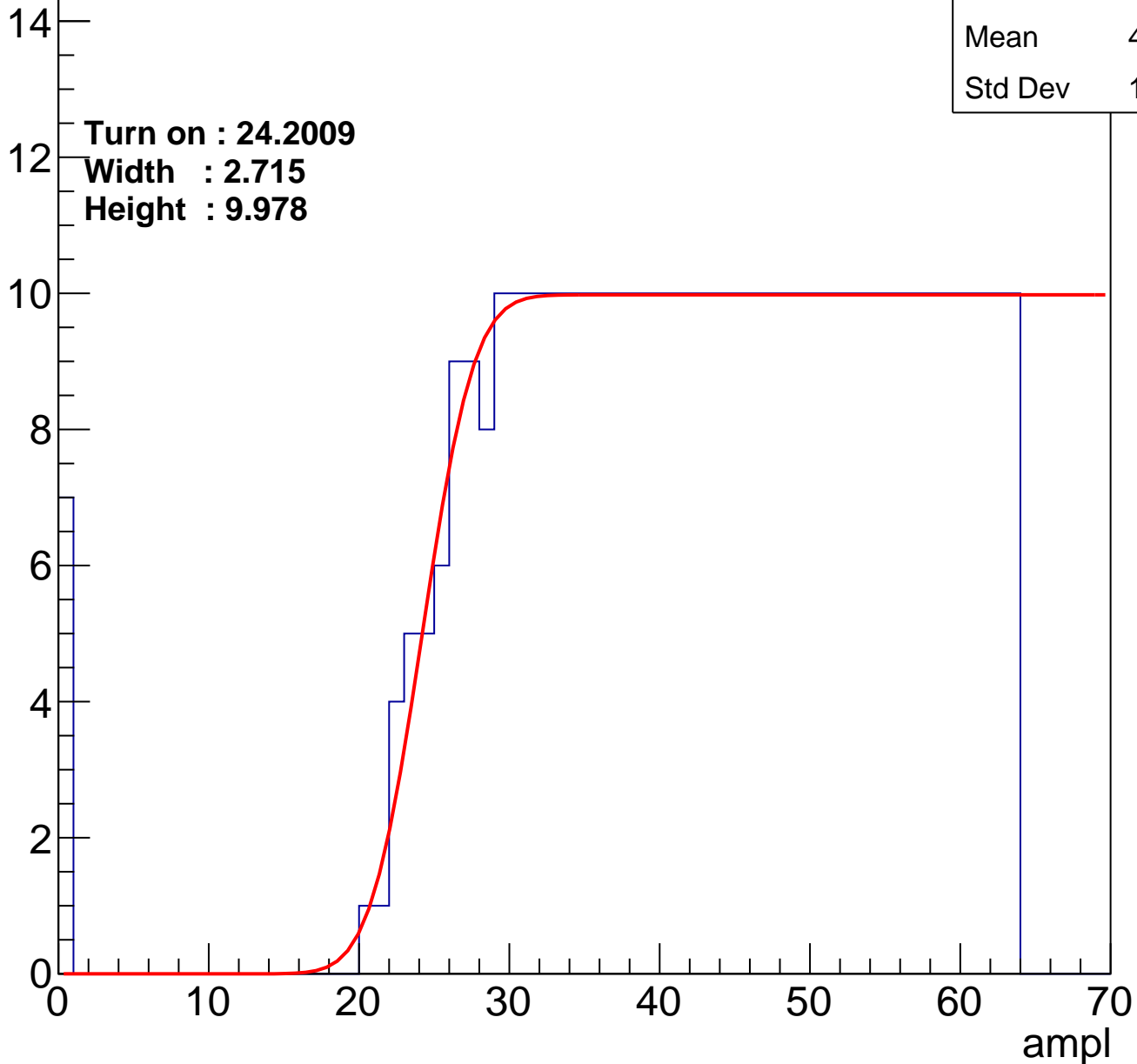
Entries	405
Mean	42.75
Std Dev	12.86

Turn on : 24.2009

Width : 2.715

Height : 9.978

Entry



B1L000S, U3-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.84
Std Dev	12.25

Turn on : 26.2695

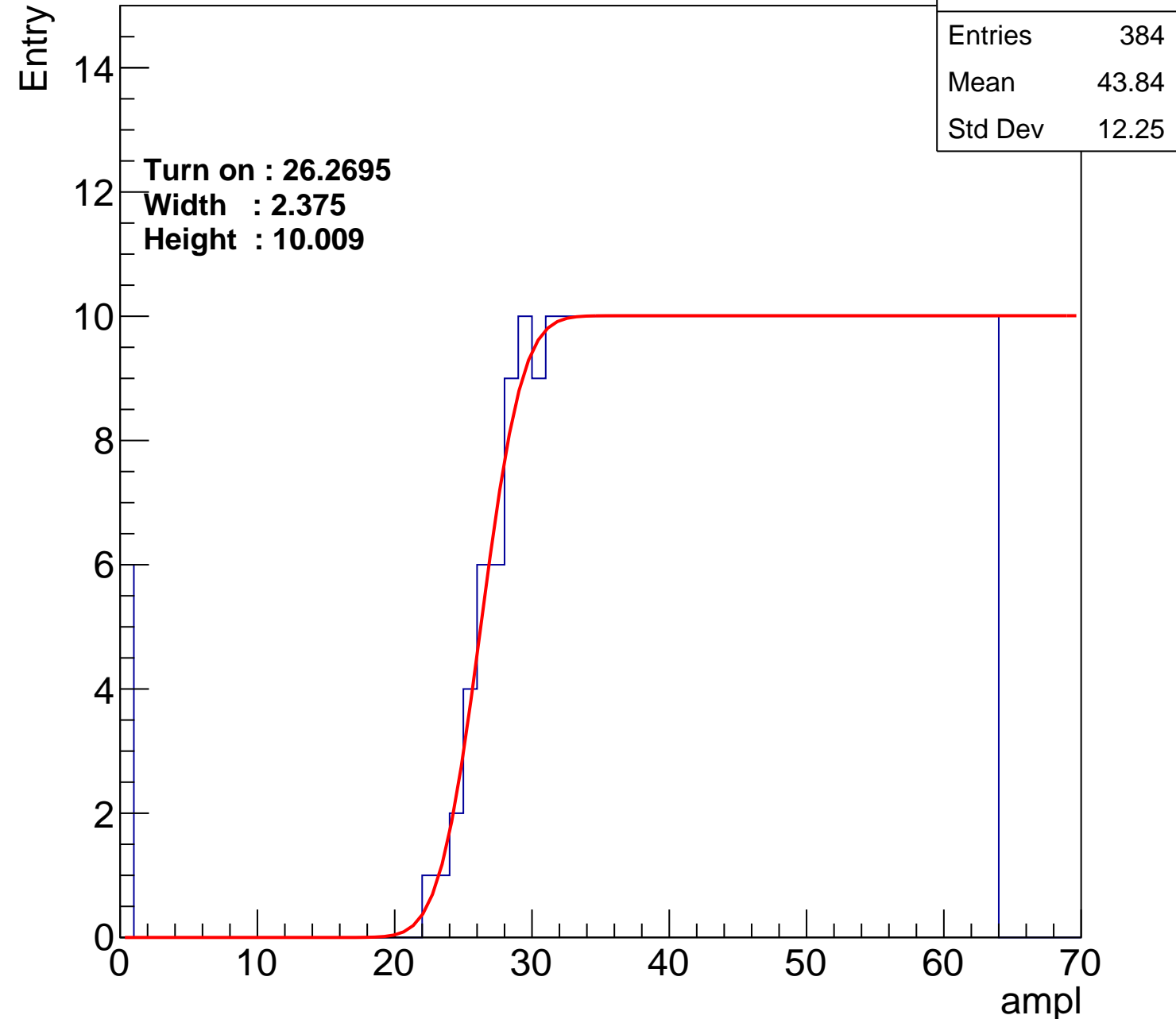
Width : 2.375

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch80

calib_packv5_042523_0143.root, FC#3, port B3

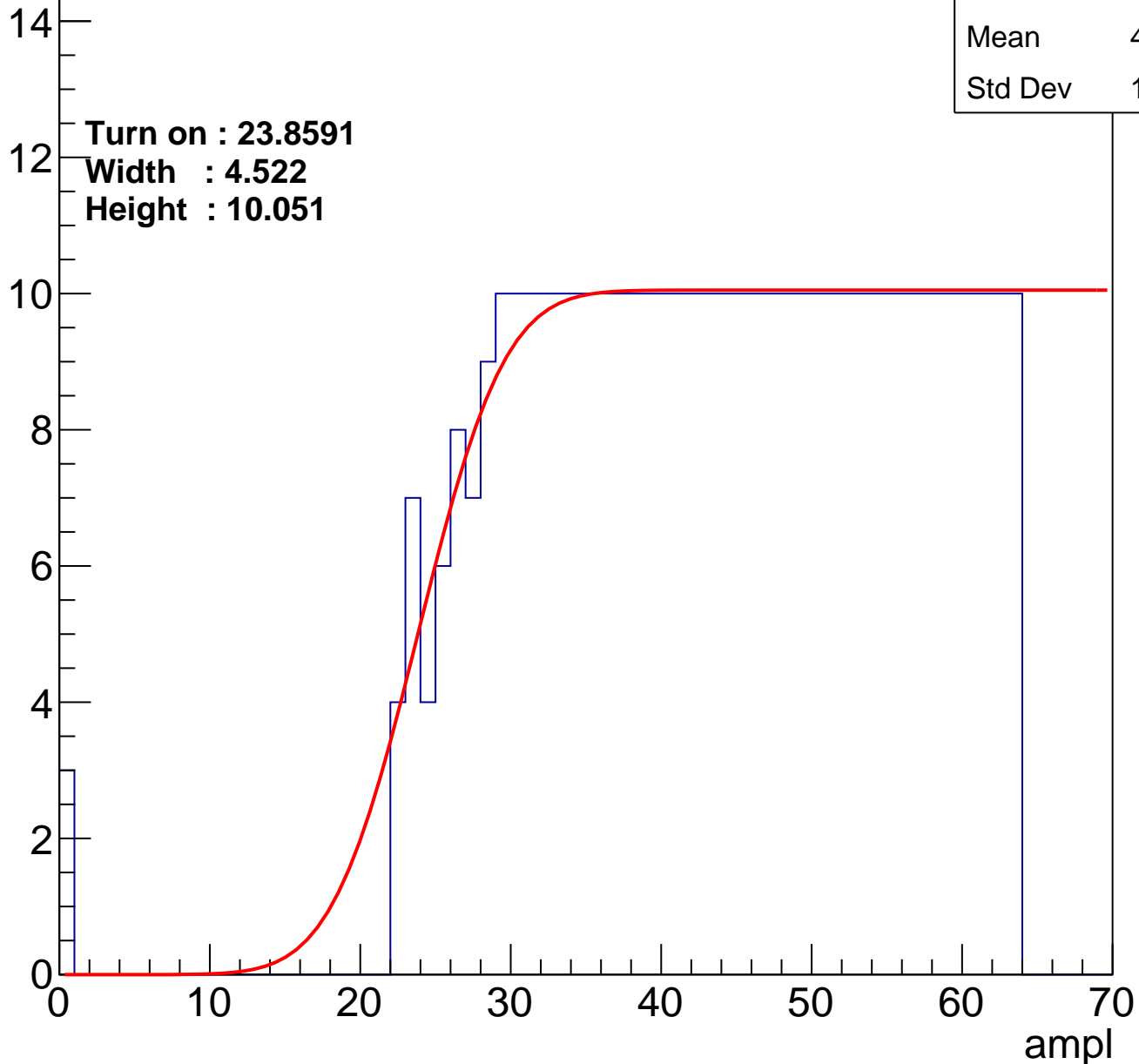
Entries	398
Mean	43.33
Std Dev	12.12

Turn on : 23.8591

Width : 4.522

Height : 10.051

Entry



B1L000S, U3-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	42.84
Std Dev	12.98

Turn on : 24.4224

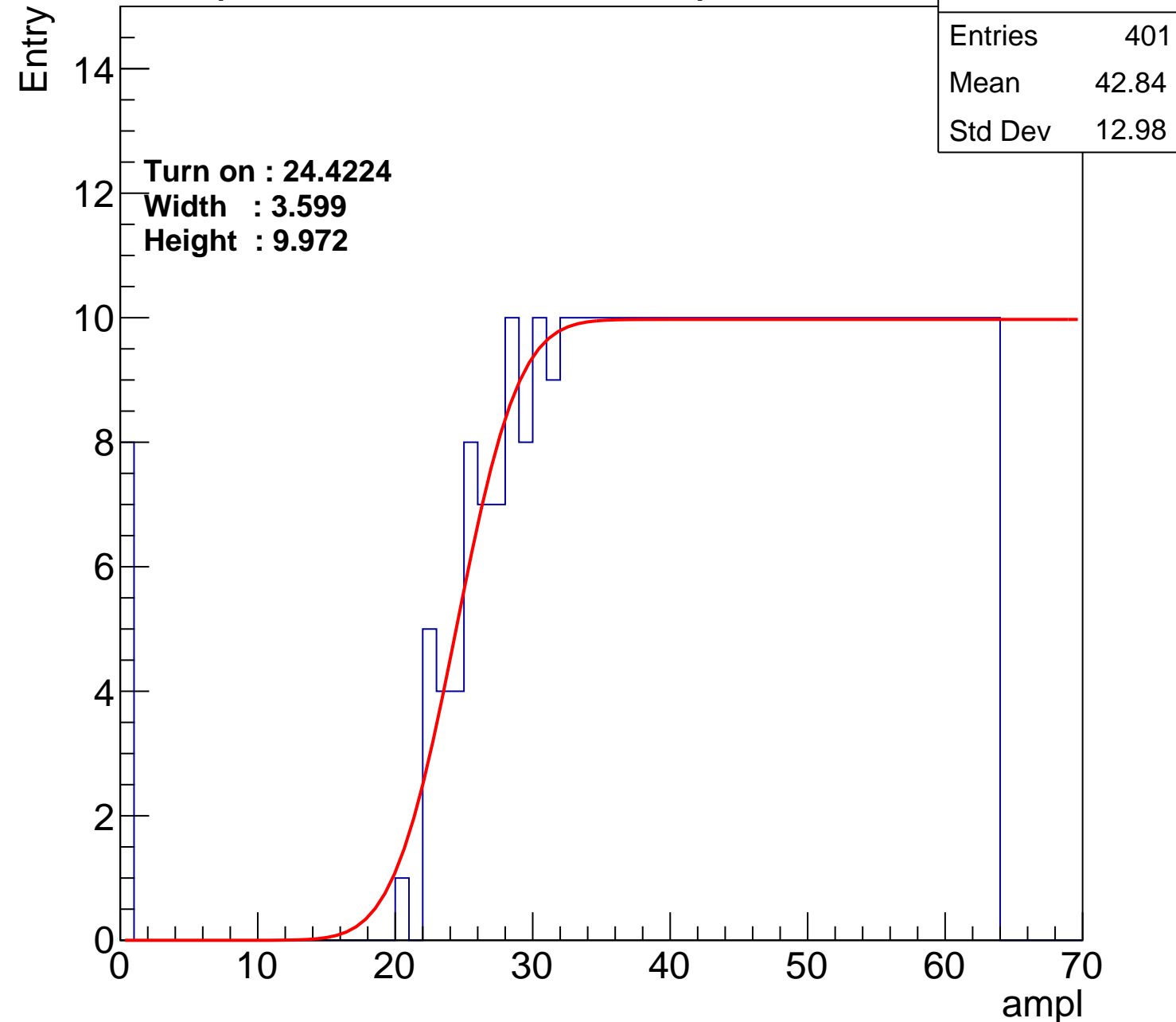
Width : 3.599

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.47
Std Dev	11.76

Turn on : 24.5429

Width : 2.052

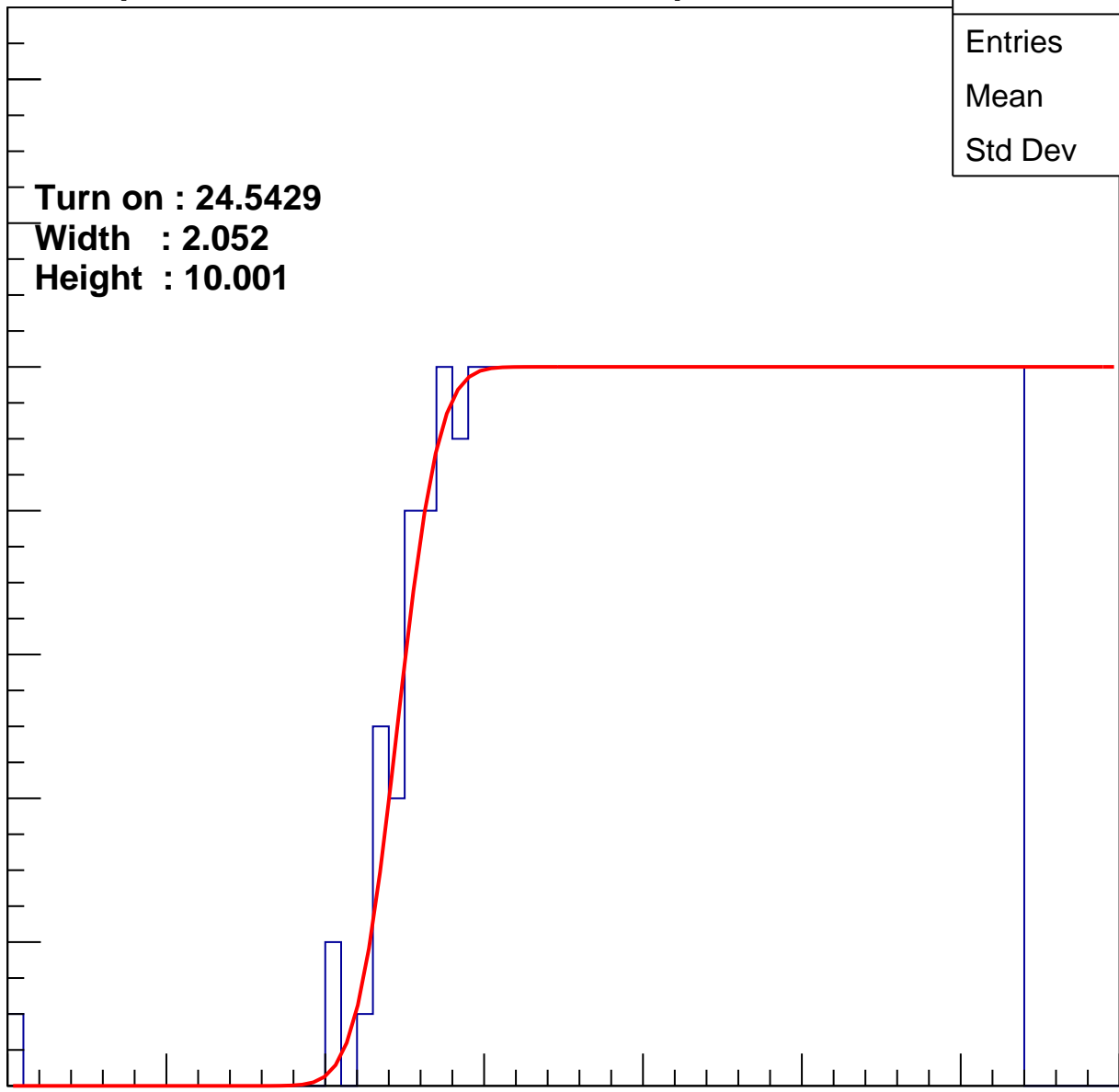
Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U3-ch83

calib_packv5_042523_0143.root, FC#3, port B3

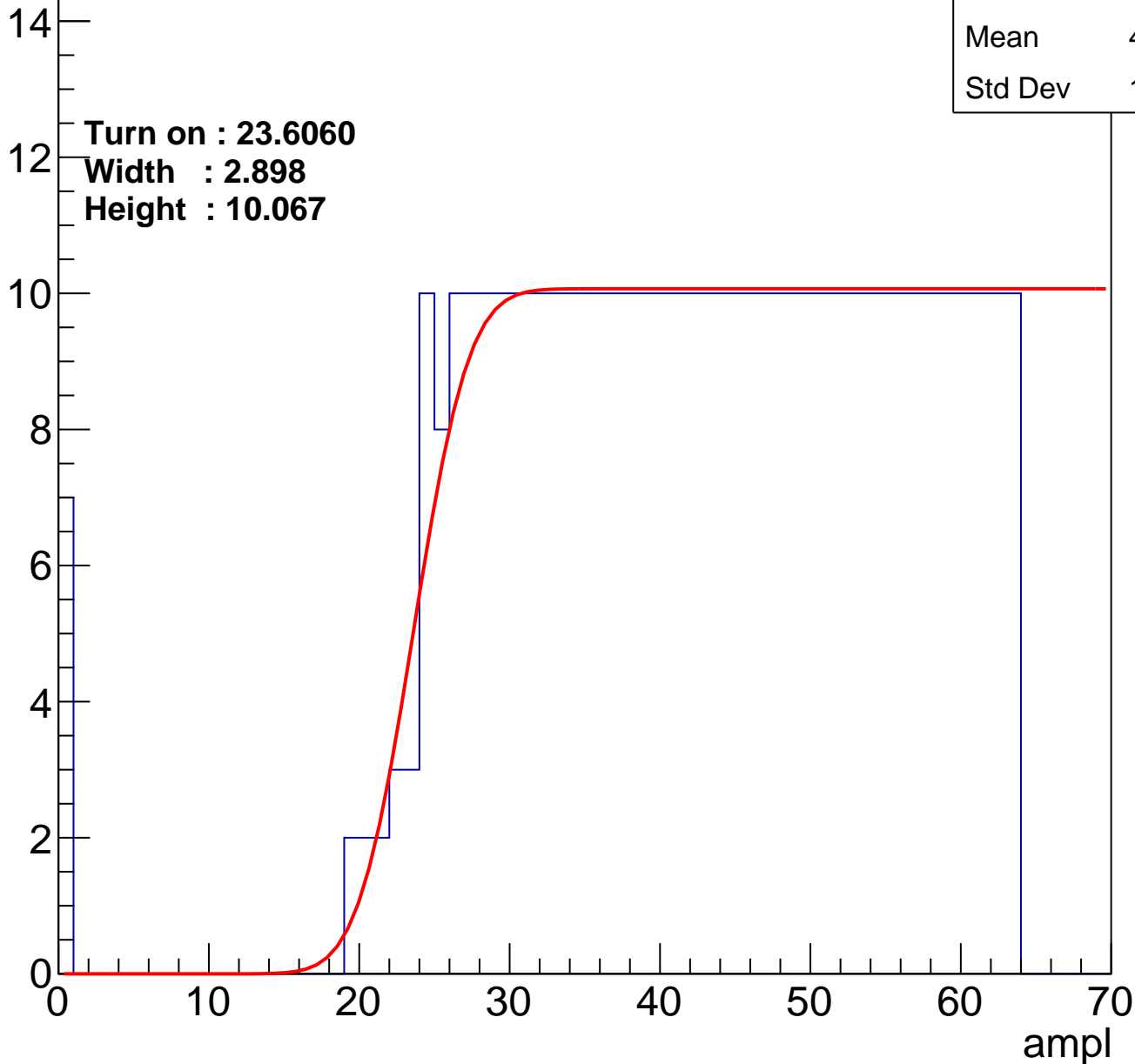
Entry

Entries	417
Mean	42.22
Std Dev	13.06

Turn on : 23.6060

Width : 2.898

Height : 10.067



B1L000S, U3-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.27
Std Dev	11.99

Turn on : 24.4118

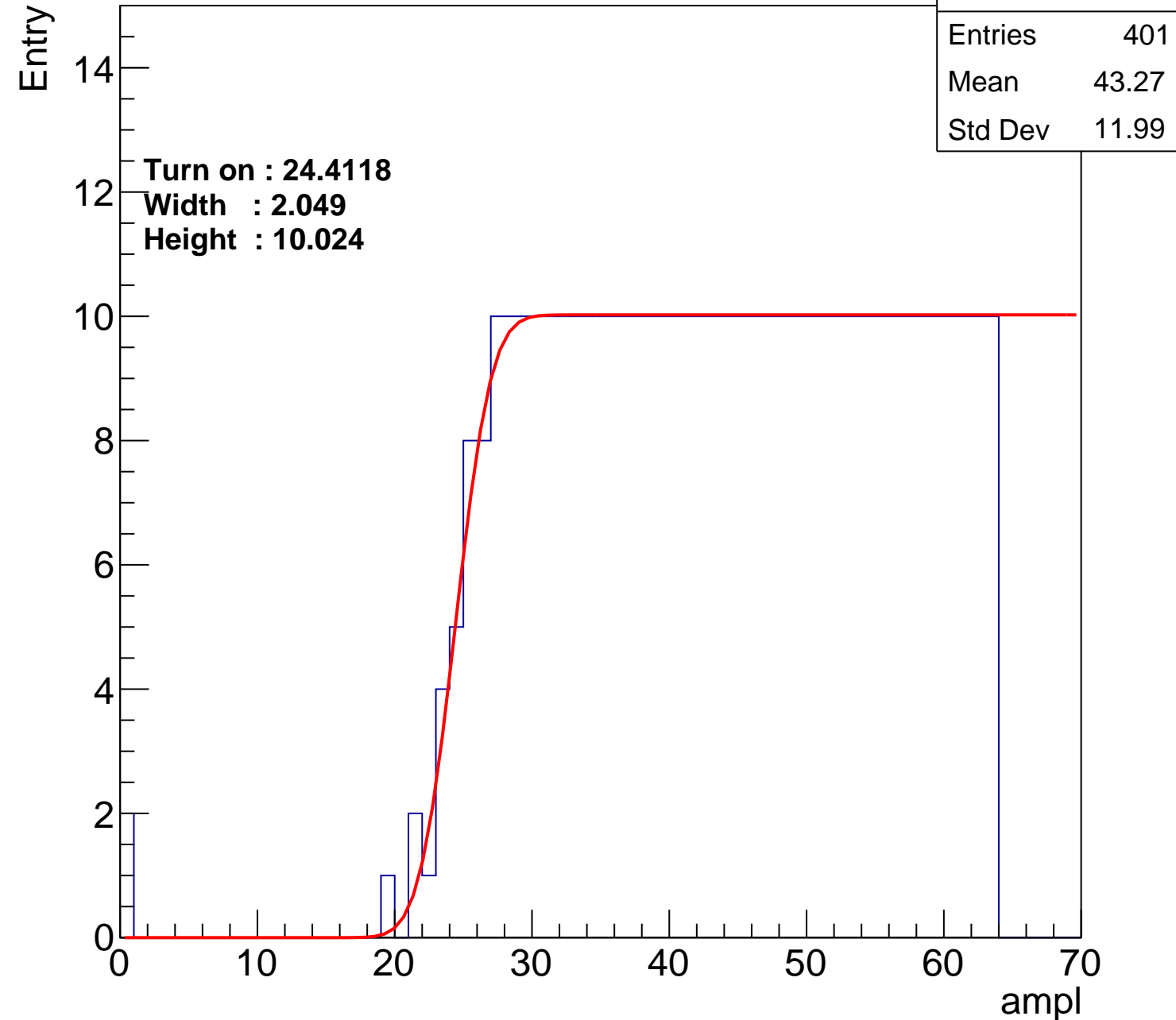
Width : 2.049

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 26.0611

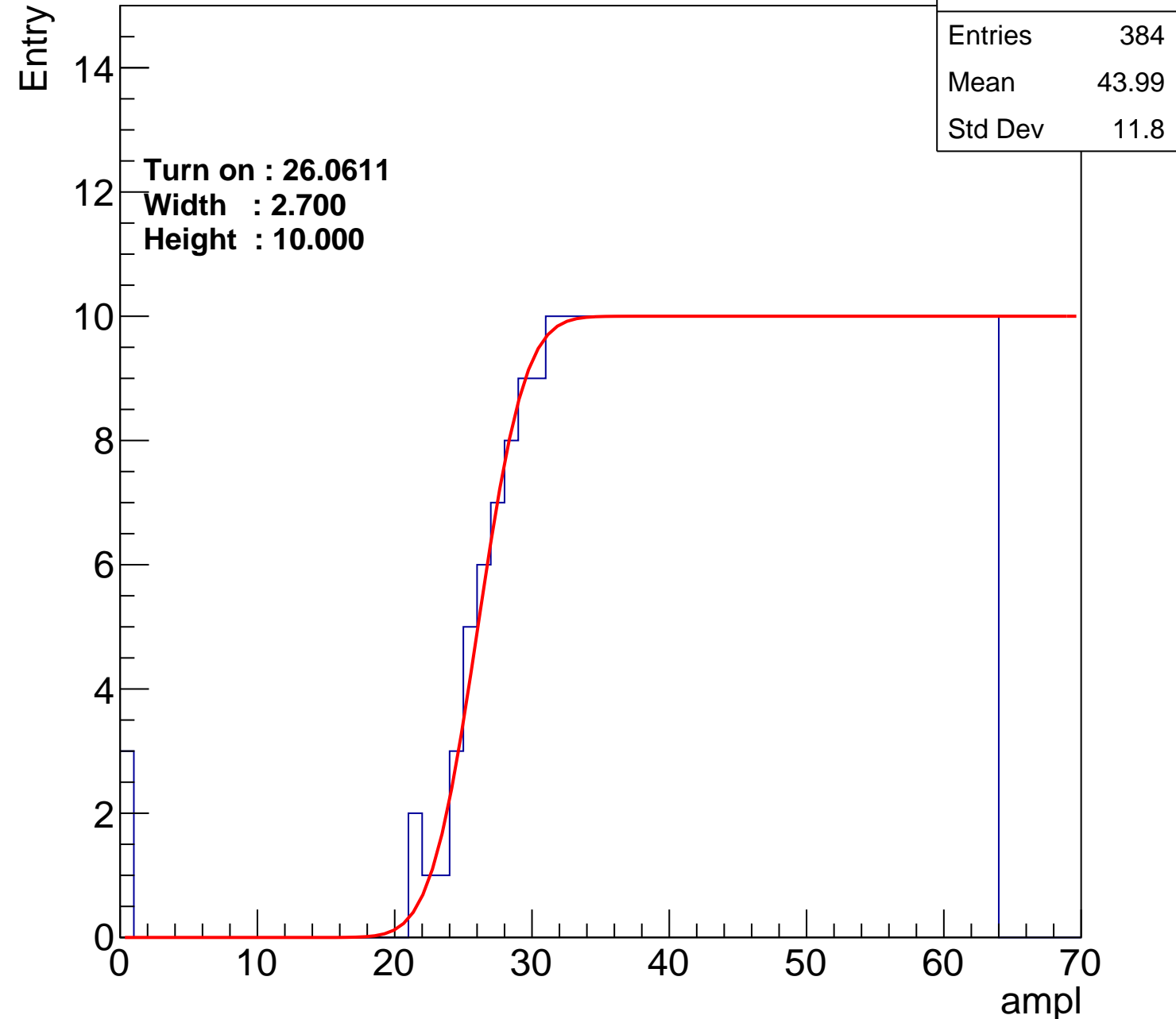
Width : 2.700

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.86
Std Dev	11.98

Turn on : 25.8980

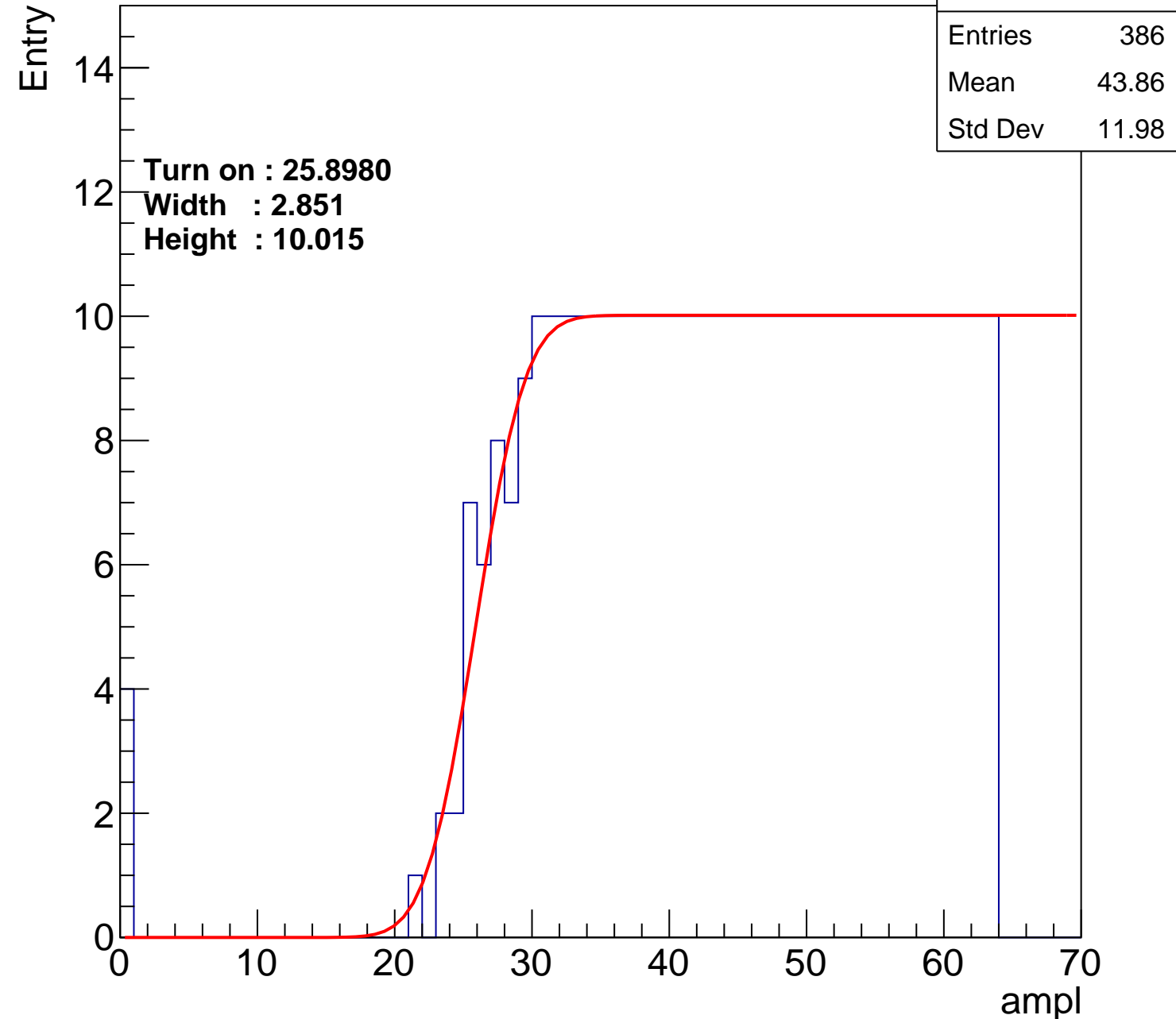
Width : 2.851

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.12
Std Dev	12.34

Turn on : 24.0702

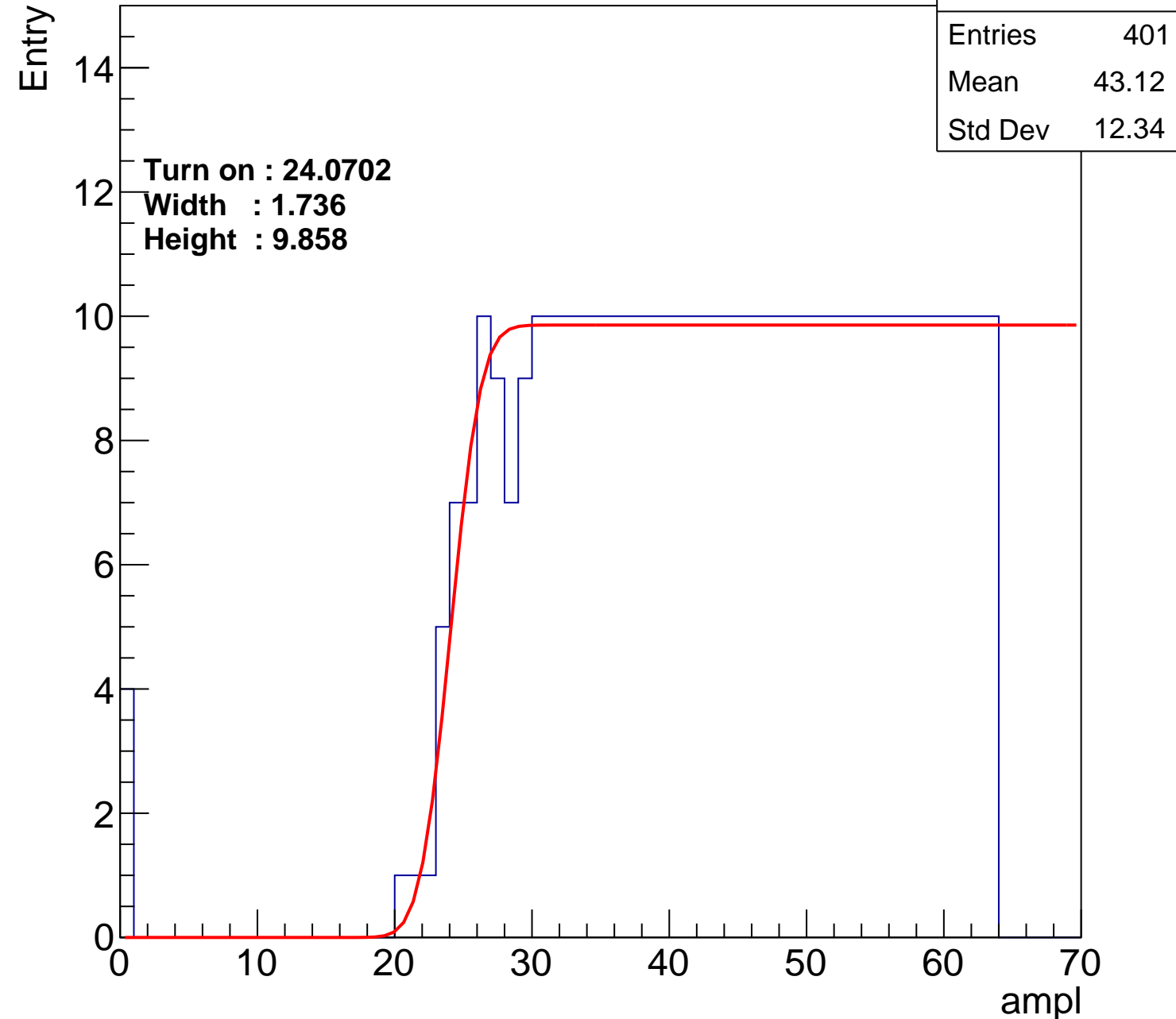
Width : 1.736

Height : 9.858

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.39
Std Dev	11.9

Turn on : 24.6194

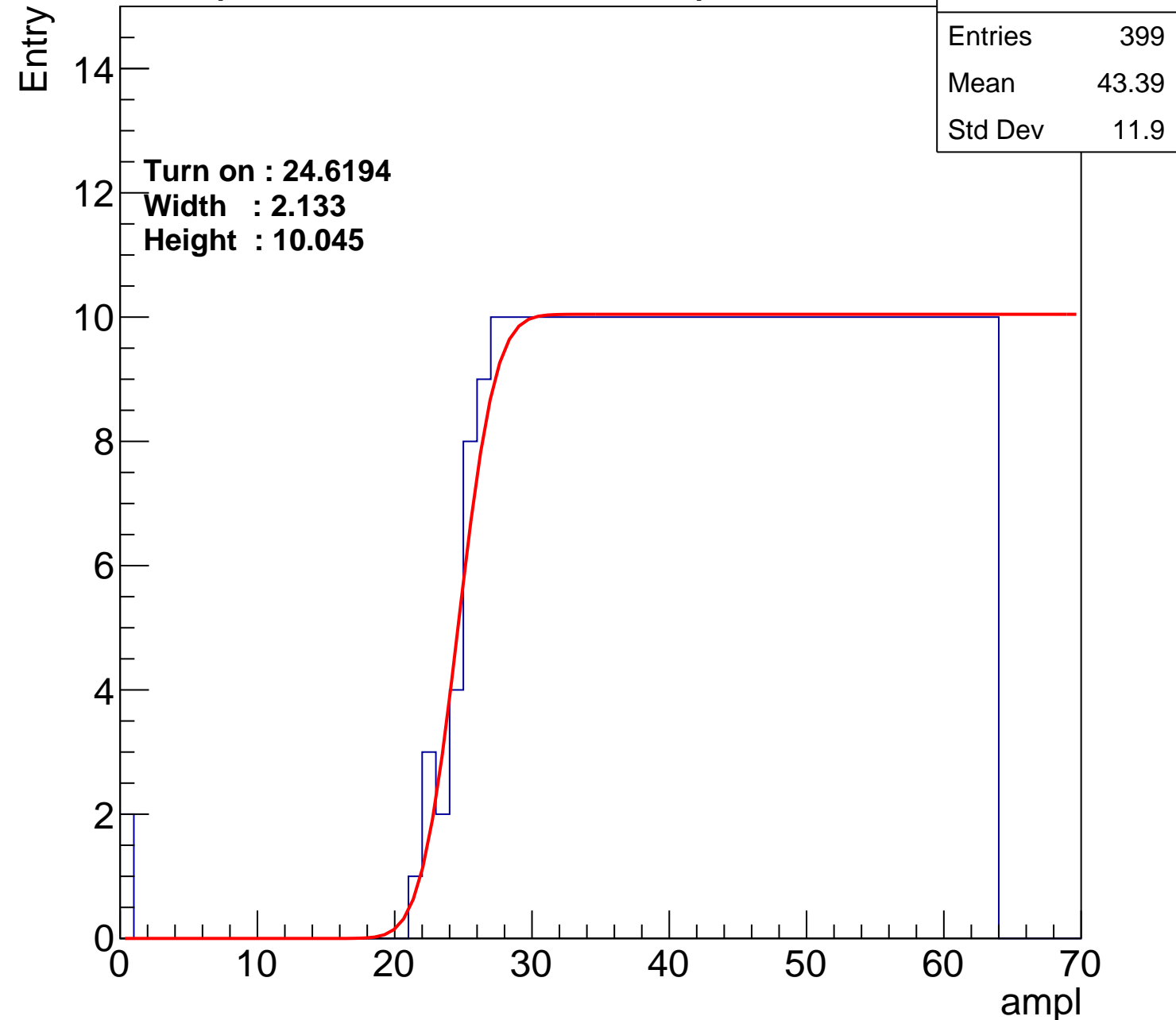
Width : 2.133

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.68
Std Dev	11.68

Turn on : 25.1788

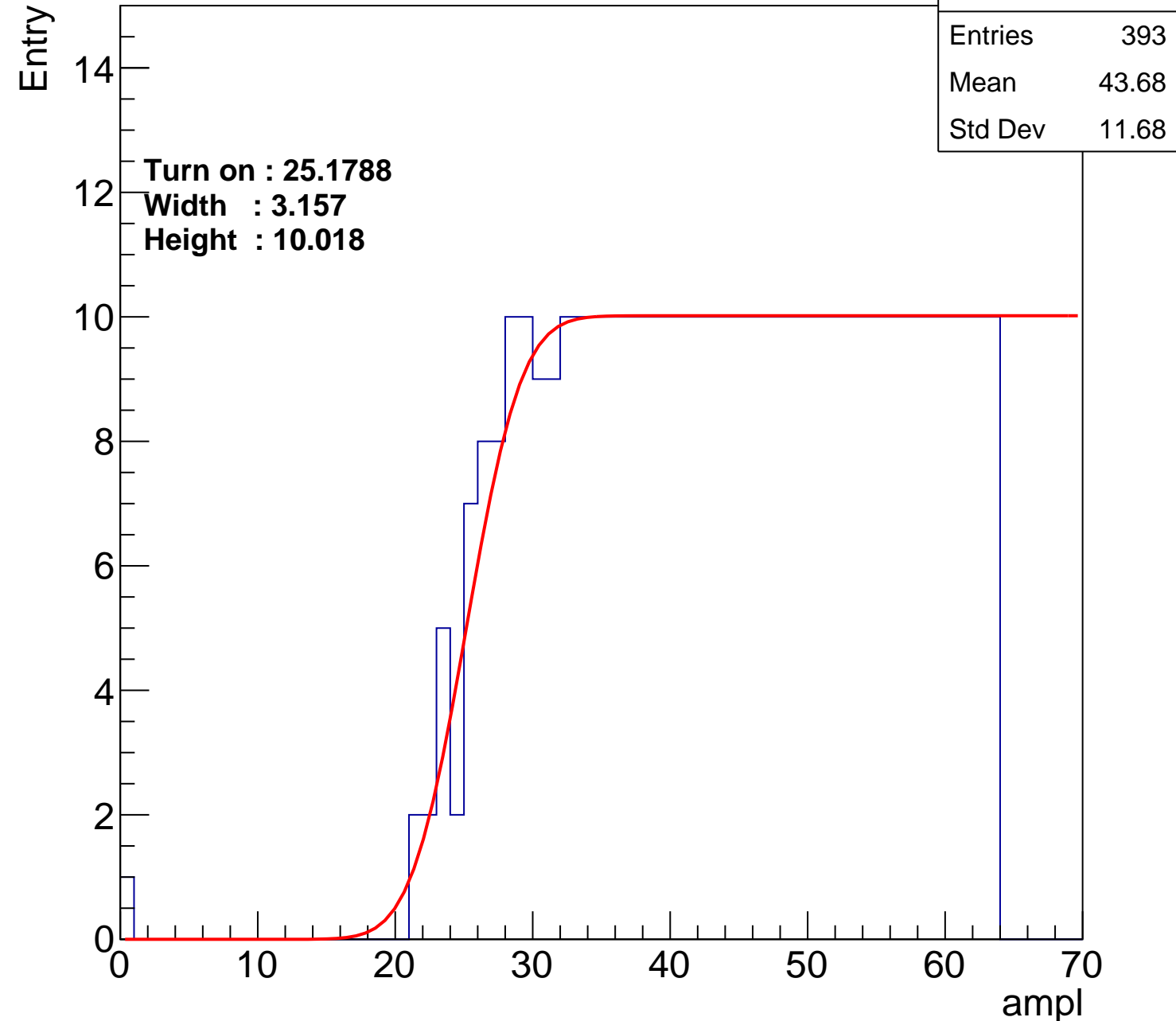
Width : 3.157

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	414
Mean	42.54
Std Dev	12.53

Turn on : 23.2361

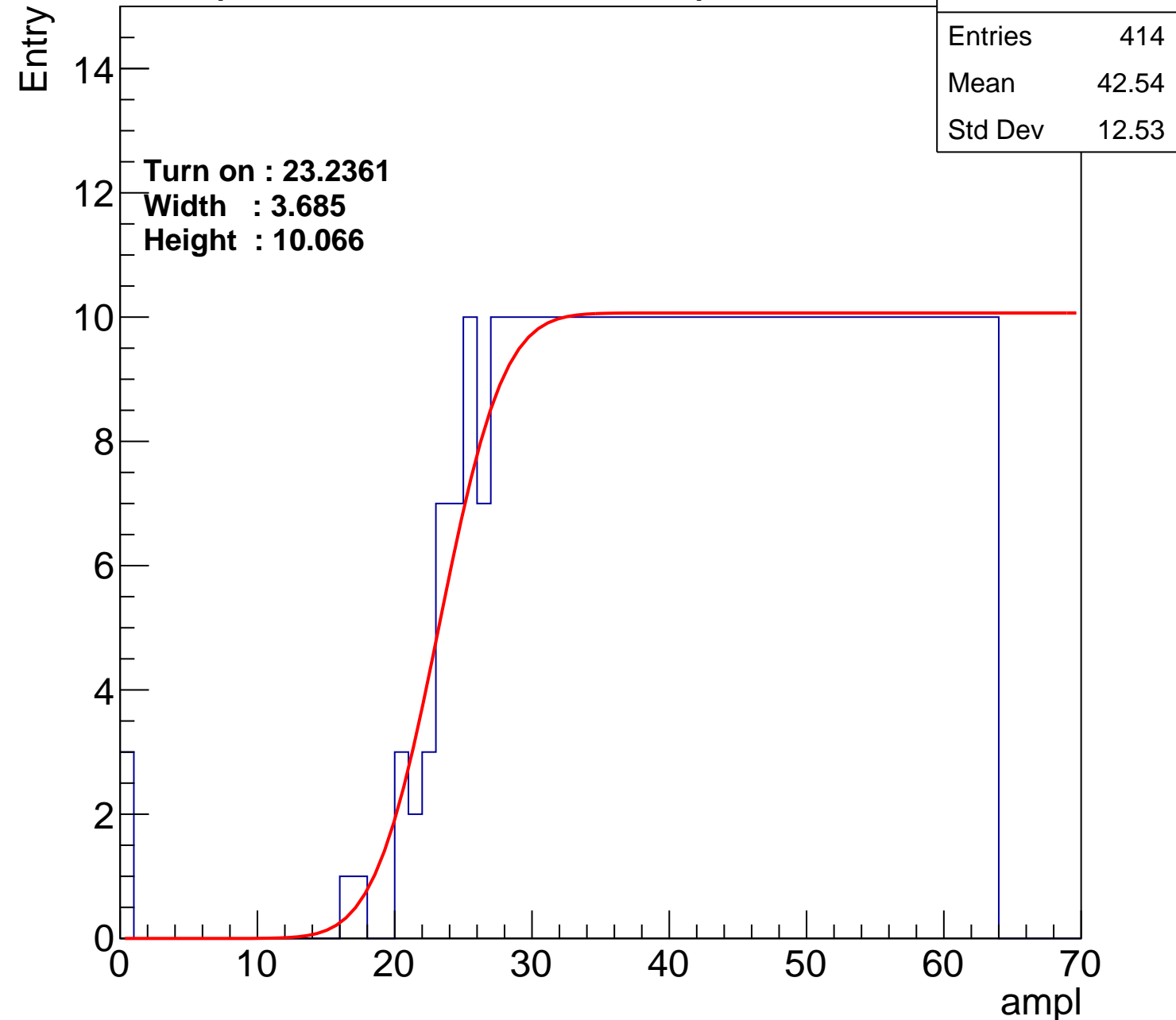
Width : 3.685

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.77
Std Dev	11.48

Turn on : 27.7663

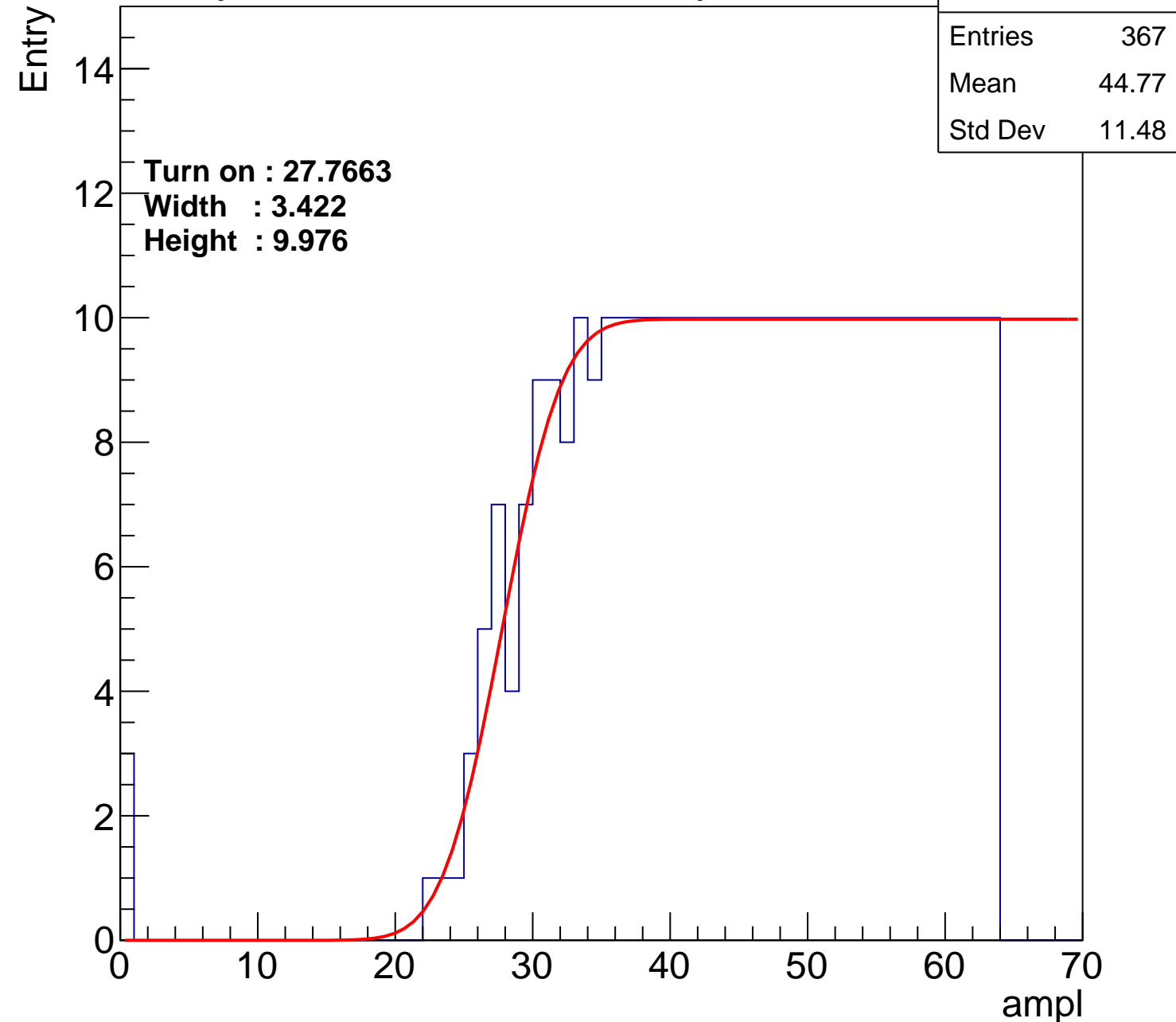
Width : 3.422

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 24.7425

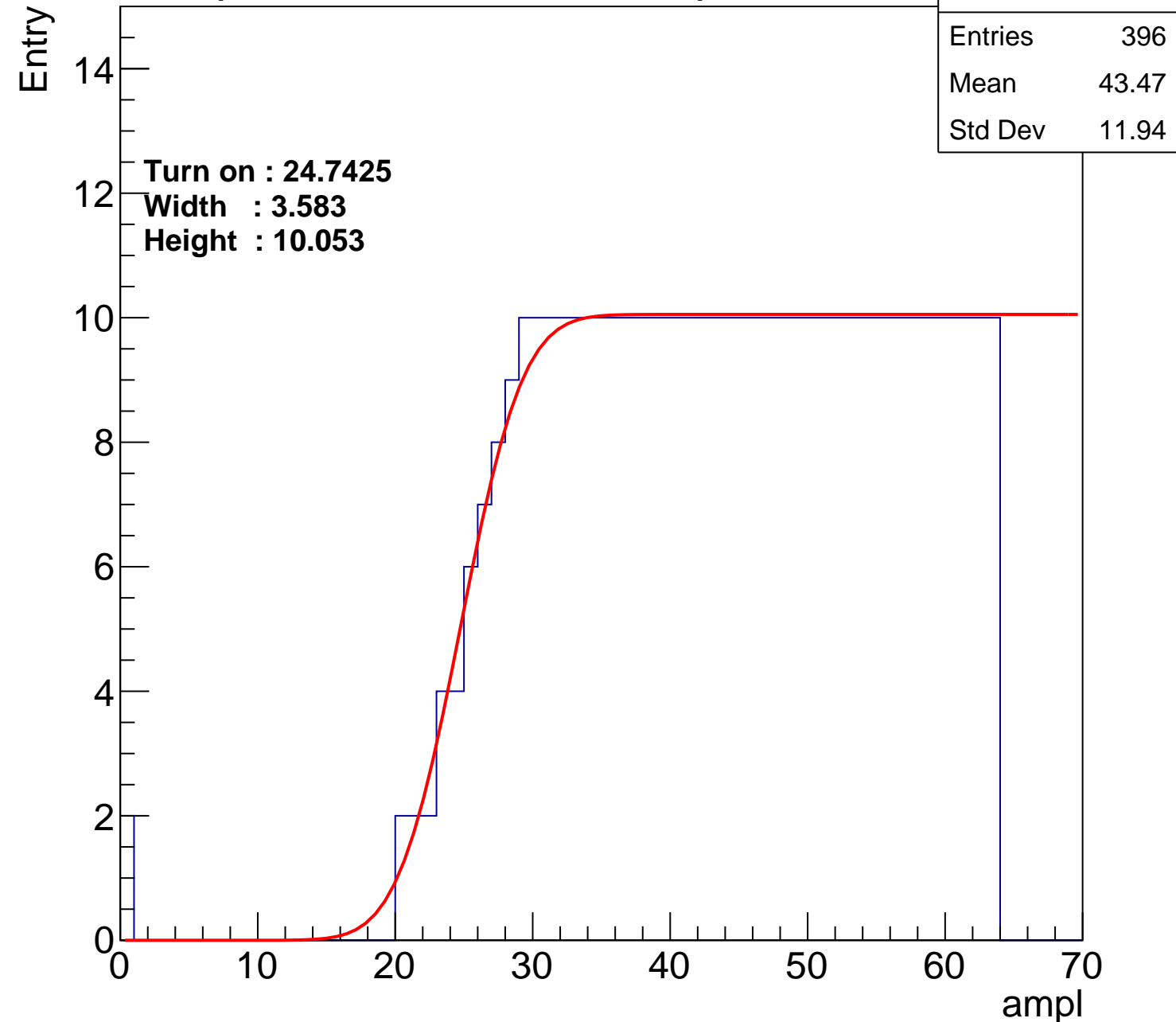
Width : 3.583

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 26.8533

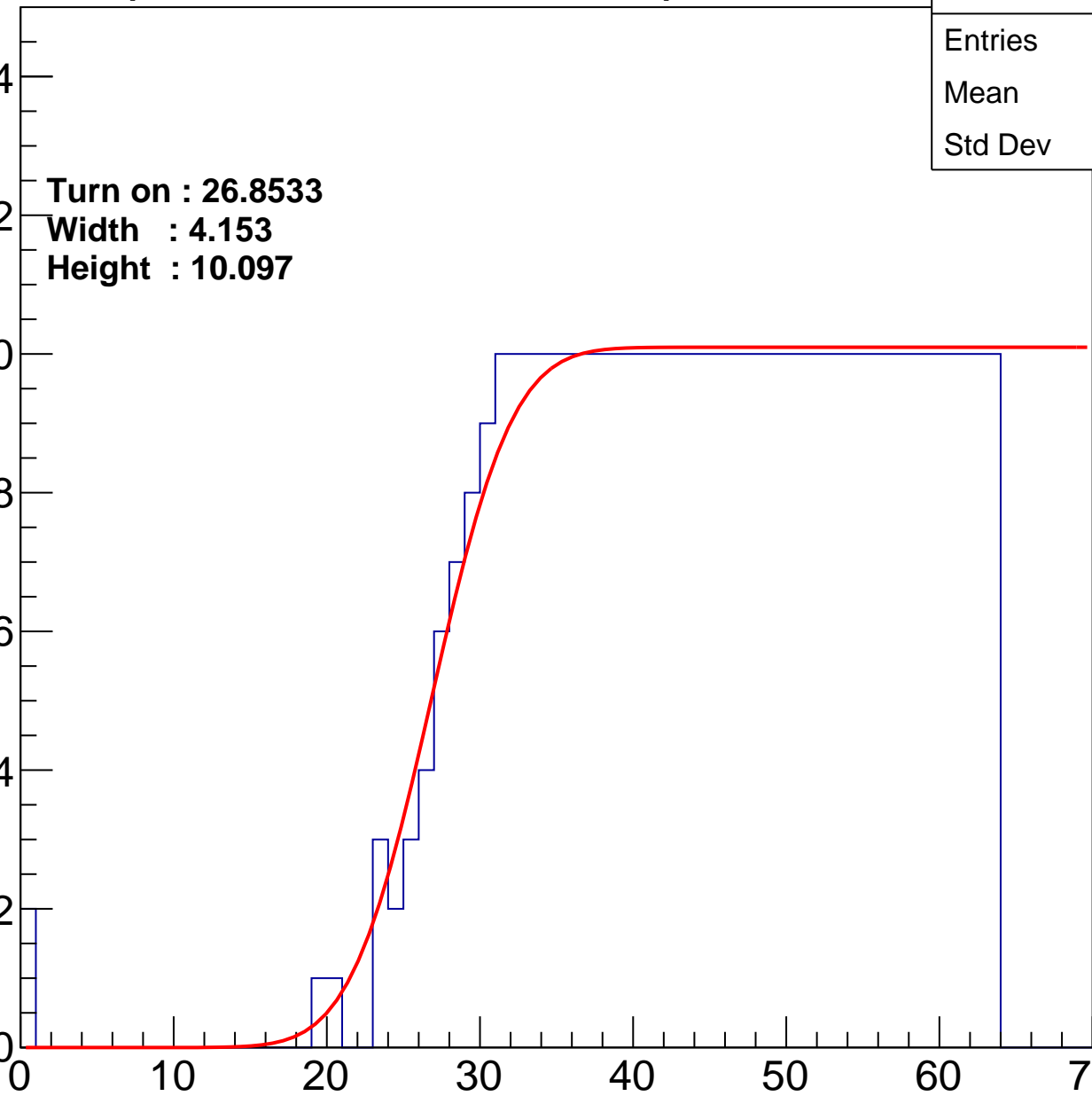
Width : 4.153

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.42
Std Dev	11.55

Turn on : 27.0339

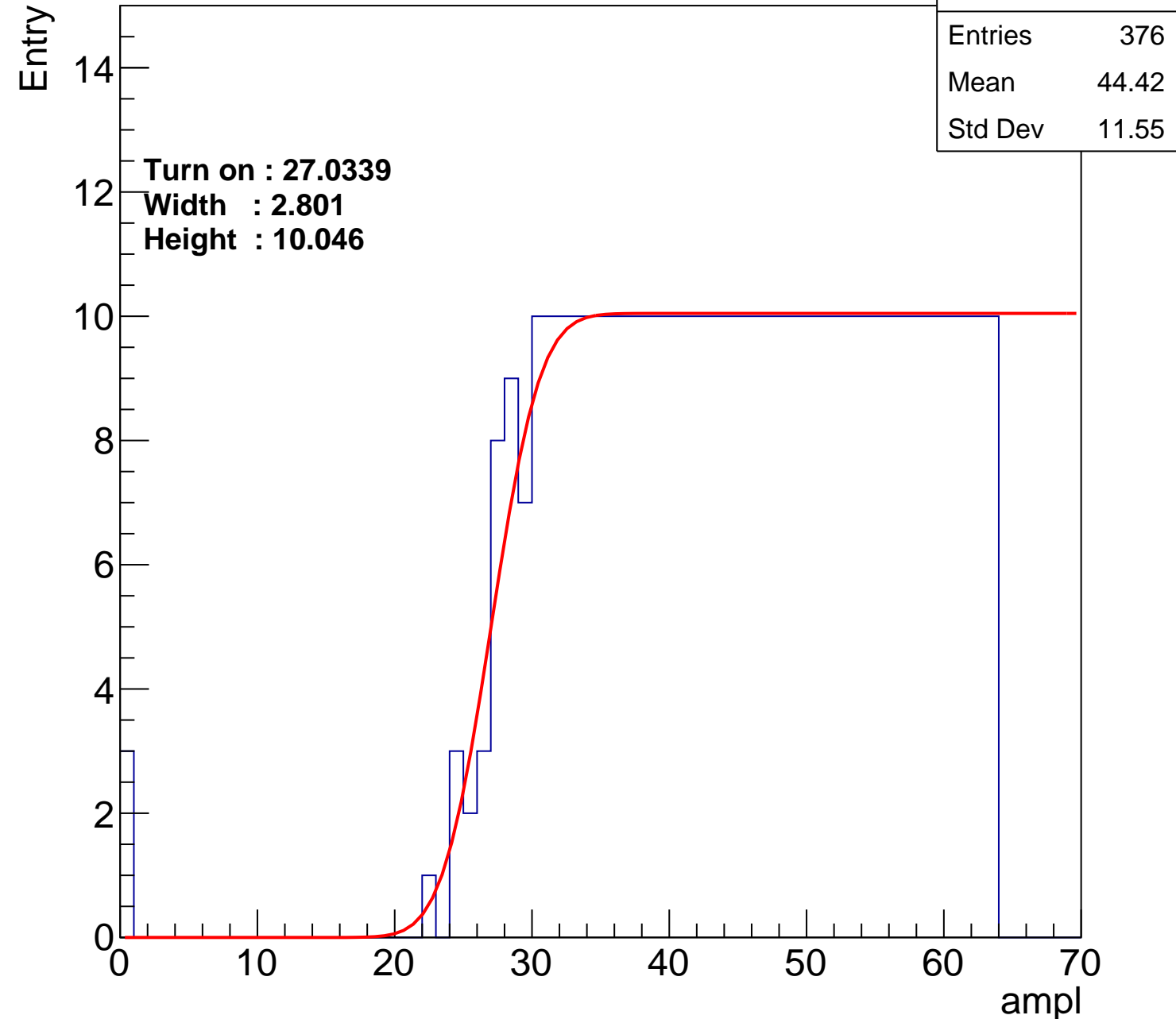
Width : 2.801

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch95

calib_packv5_042523_0143.root, FC#3, port B3

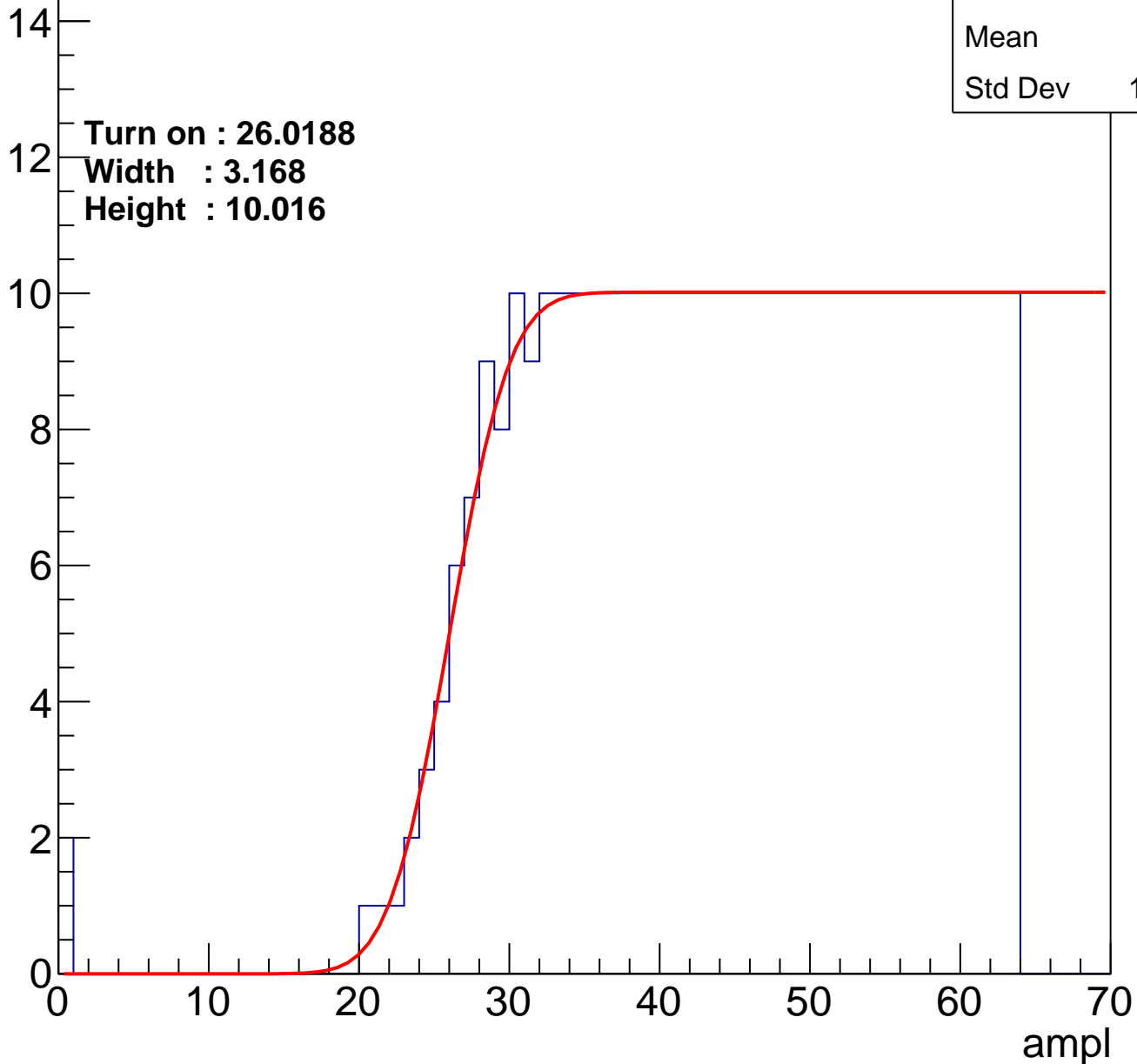
Entries	383
Mean	44.1
Std Dev	11.62

Turn on : 26.0188

Width : 3.168

Height : 10.016

Entry



B1L000S, U3-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.29
Std Dev	12.17

Turn on : 24.3264

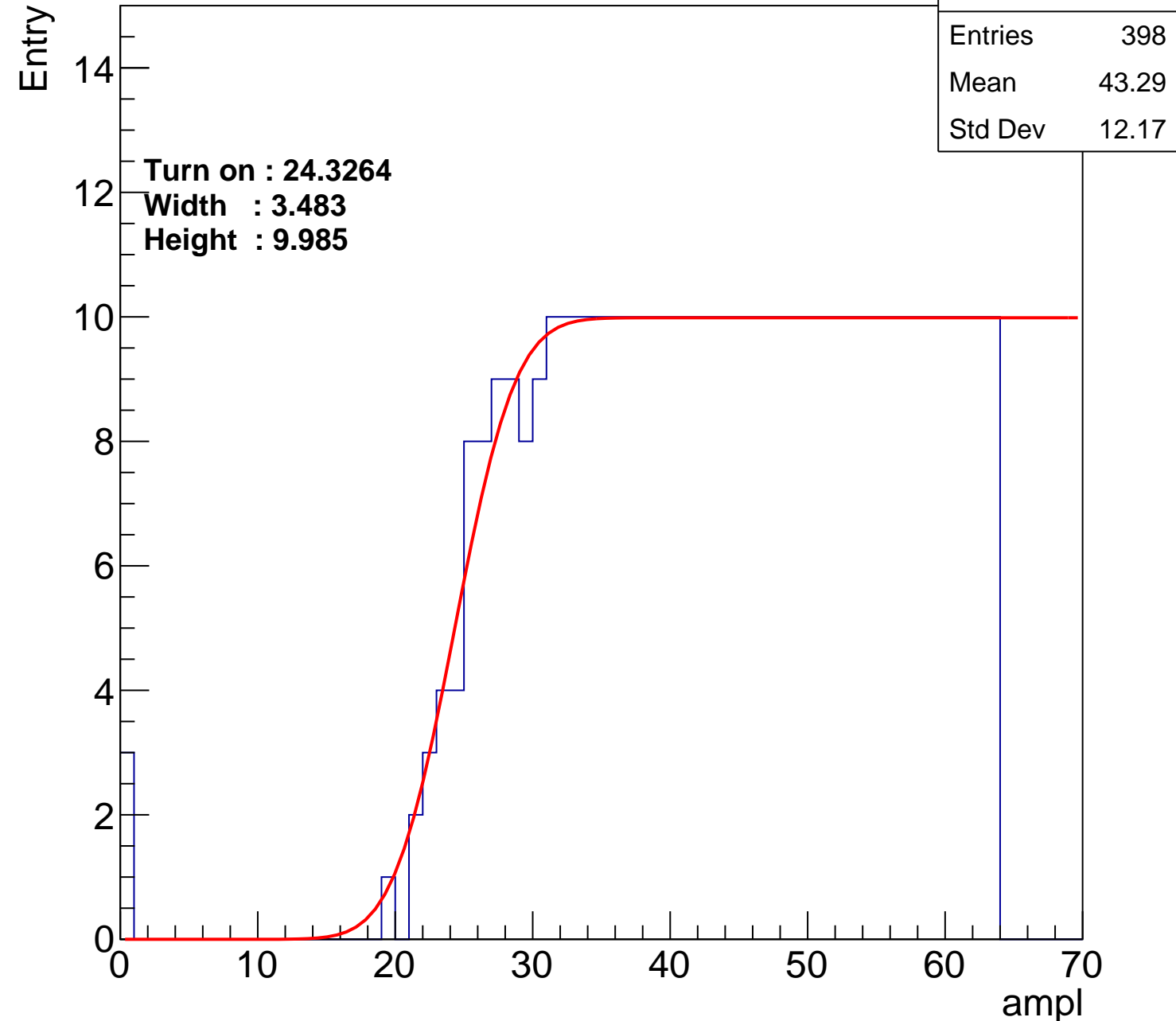
Width : 3.483

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
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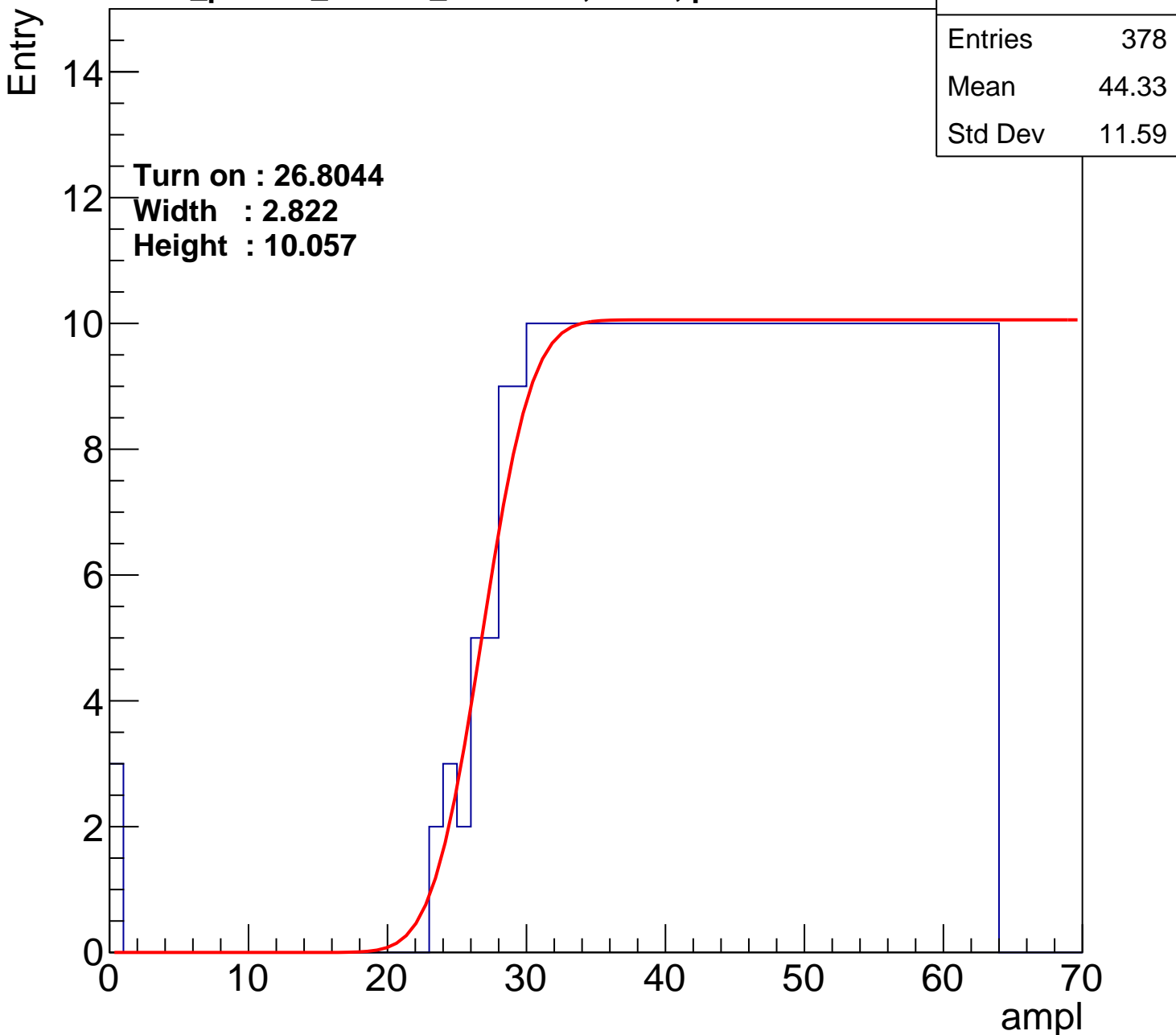
Mean	44.33
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Std Dev	11.59
---------	-------

Turn on : 26.8044

Width : 2.822

Height : 10.057



B1L000S, U3-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.6456

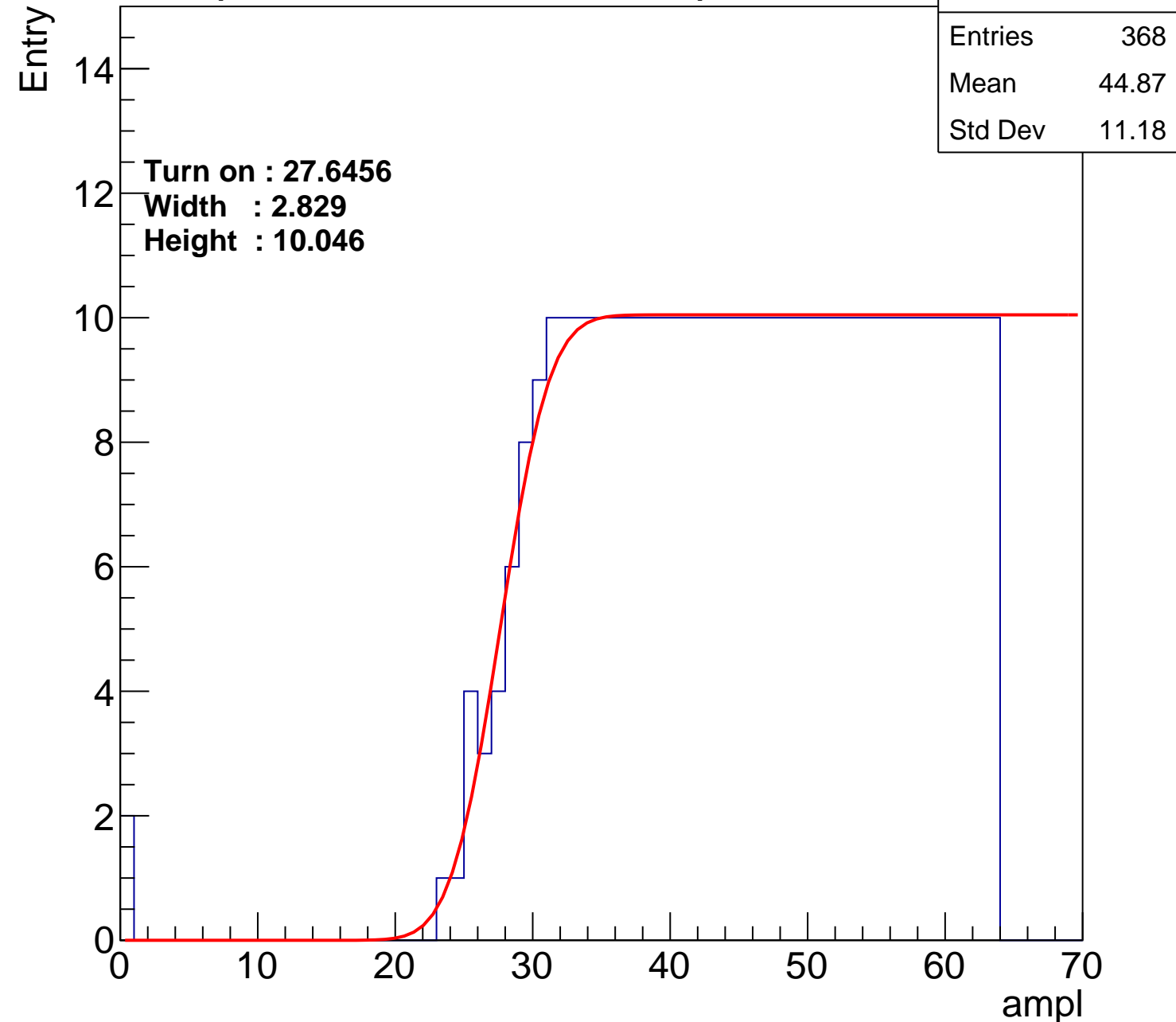
Width : 2.829

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch99

calib_packv5_042523_0143.root, FC#3, port B3

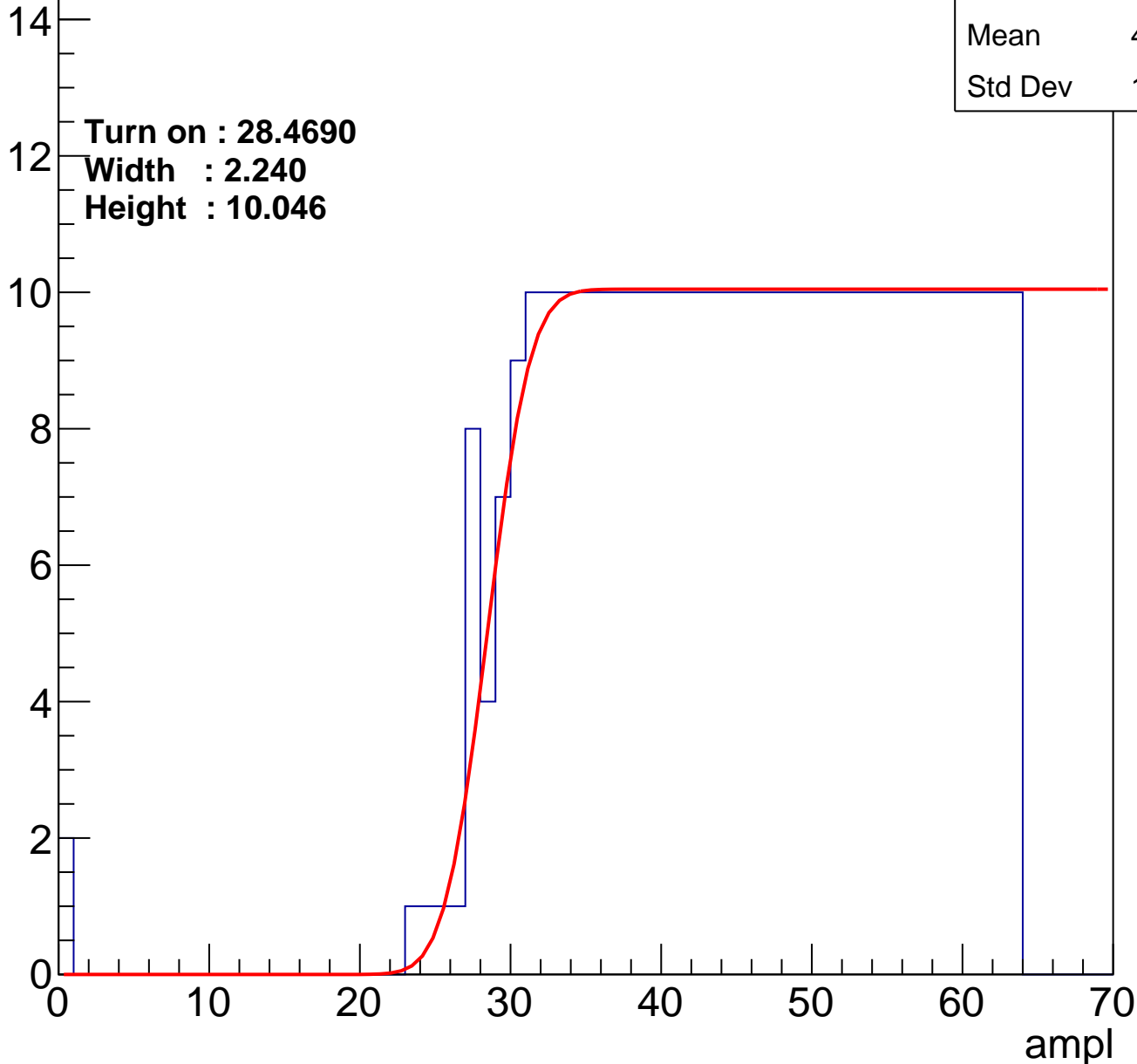
Entry

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 28.4690

Width : 2.240

Height : 10.046



B1L000S, U3-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.96
Std Dev	11.98

Turn on : 26.5308

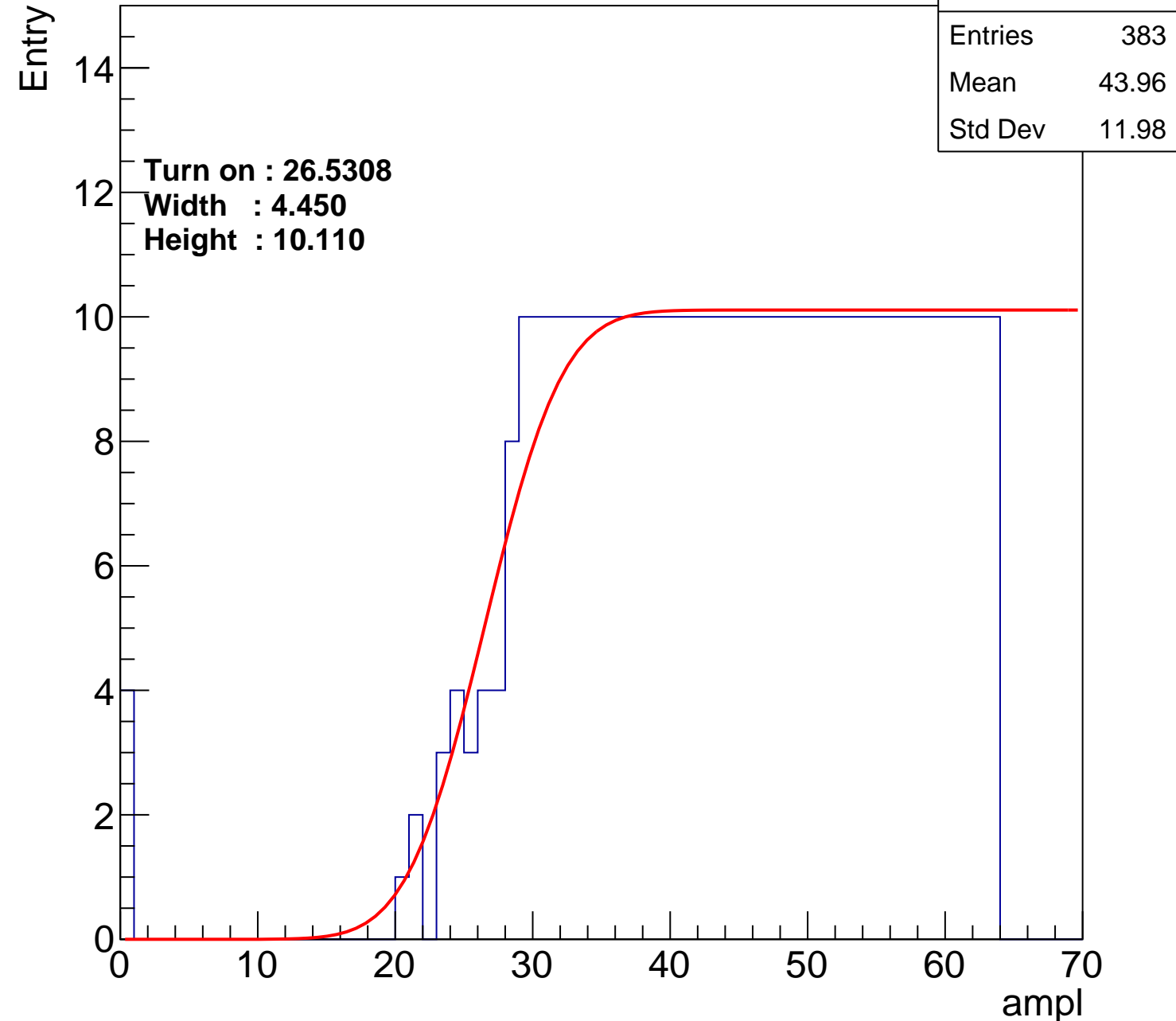
Width : 4.450

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.55
Std Dev	11.33

Turn on : 27.1502

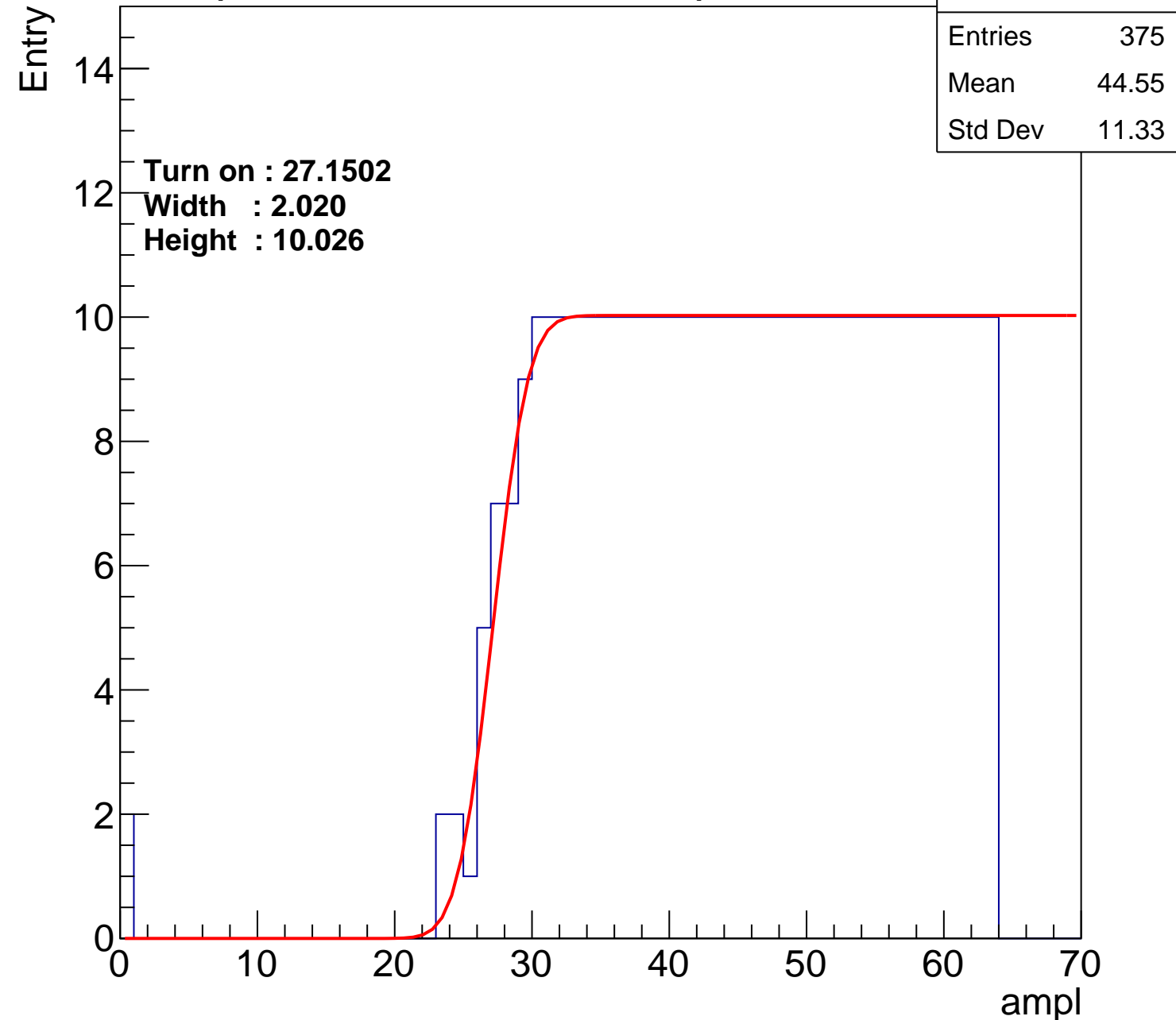
Width : 2.020

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.43
Std Dev	12.22

Turn on : 25.2266

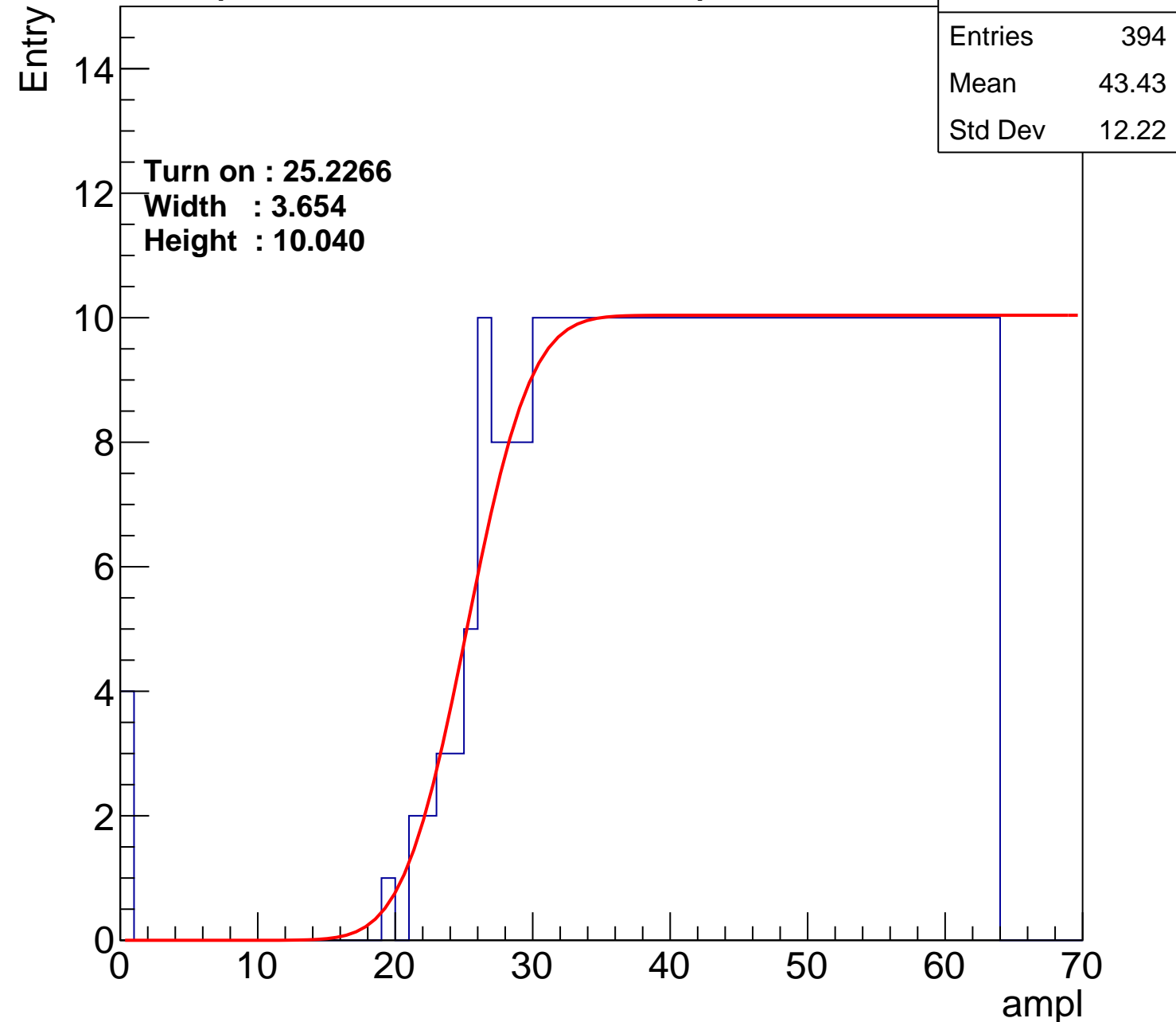
Width : 3.654

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch103

calib_packv5_042523_0143.root, FC#3, port B3

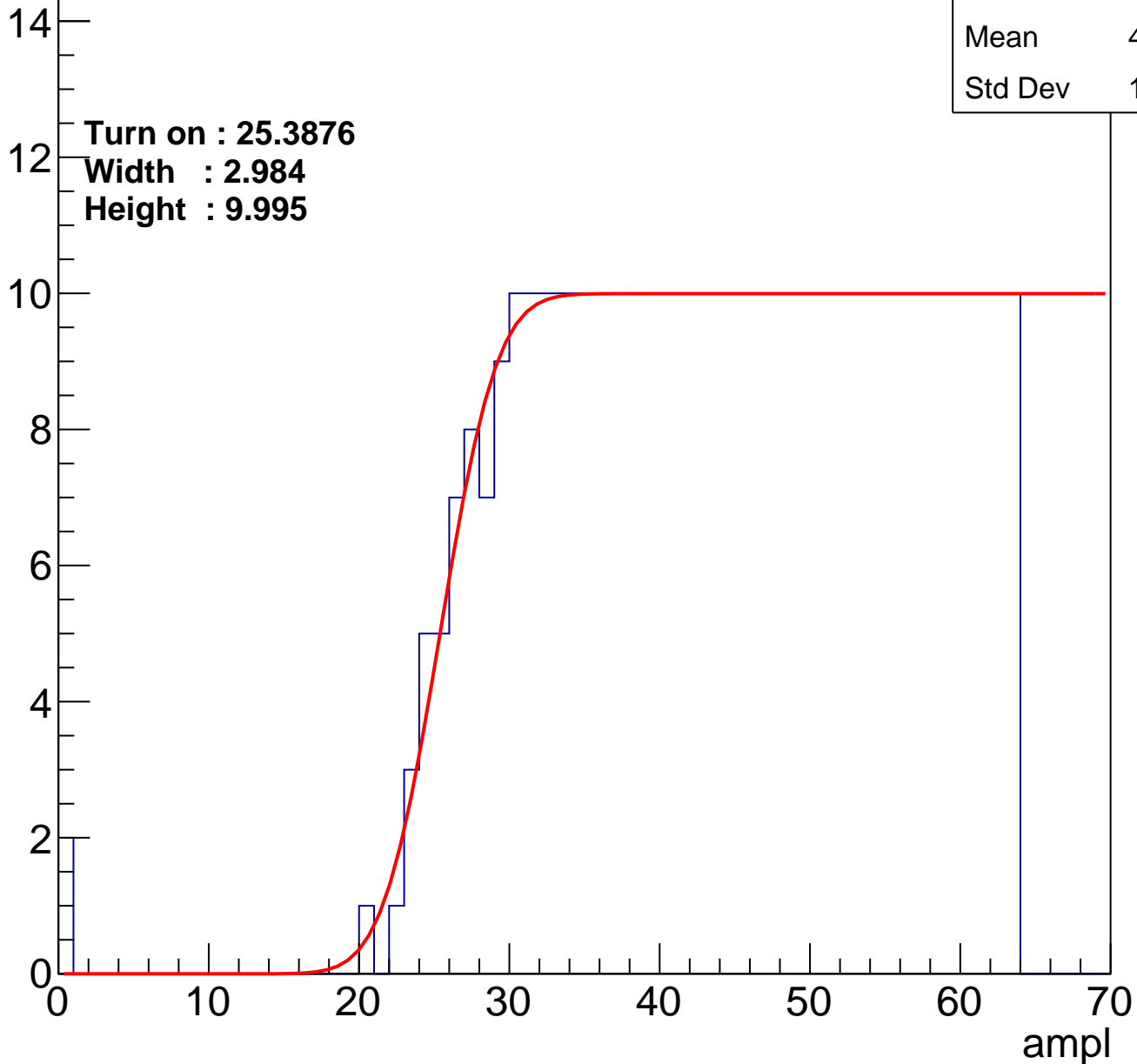
Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.3876

Width : 2.984

Height : 9.995

Entry



B1L000S, U3-ch104

calib_packv5_042523_0143.root, FC#3, port B3

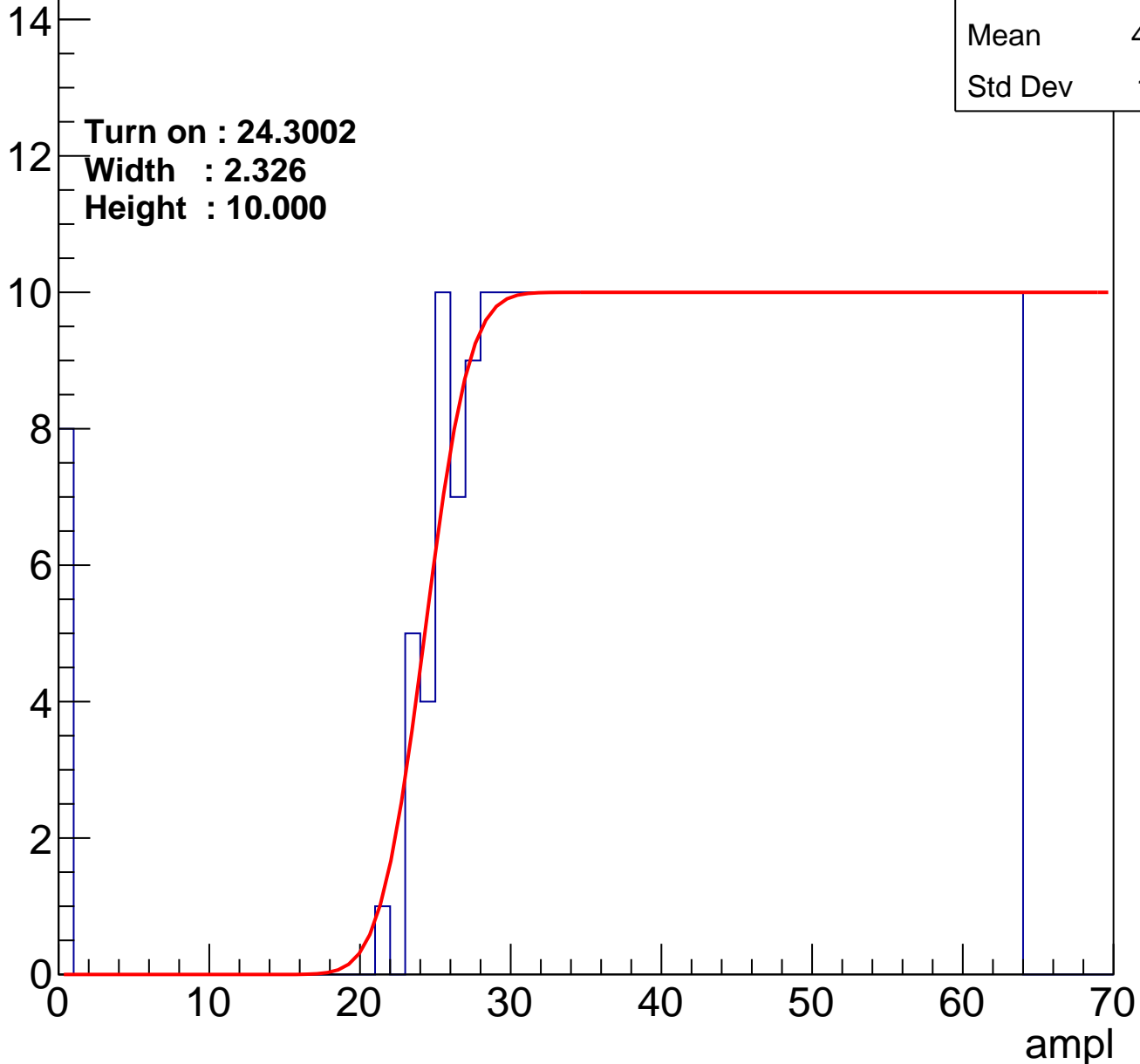
Entries	404
Mean	42.79
Std Dev	12.91

Turn on : 24.3002

Width : 2.326

Height : 10.000

Entry



B1L000S, U3-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.62
Std Dev	11.13

Turn on : 26.5157

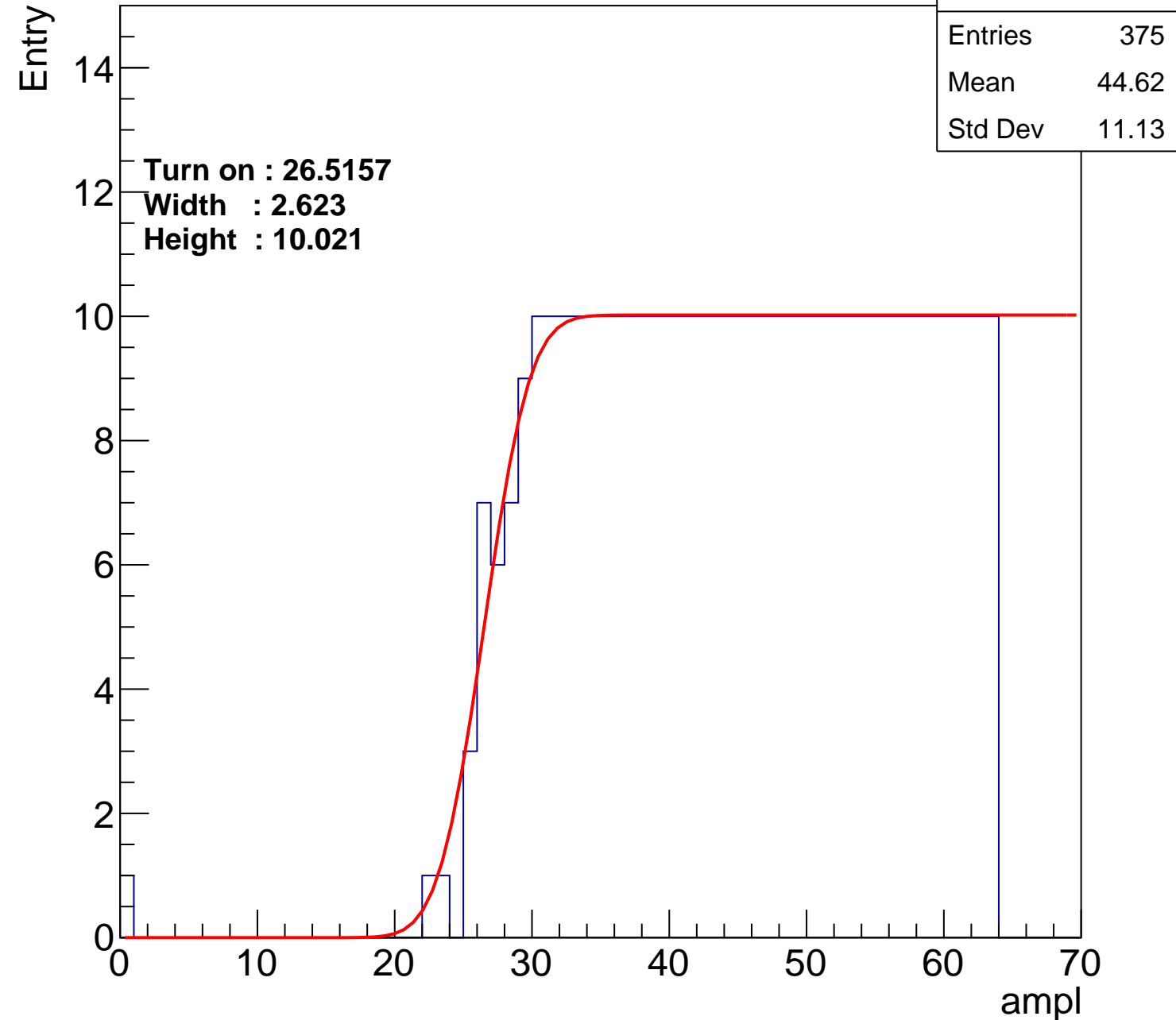
Width : 2.623

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch106

calib_packv5_042523_0143.root, FC#3, port B3

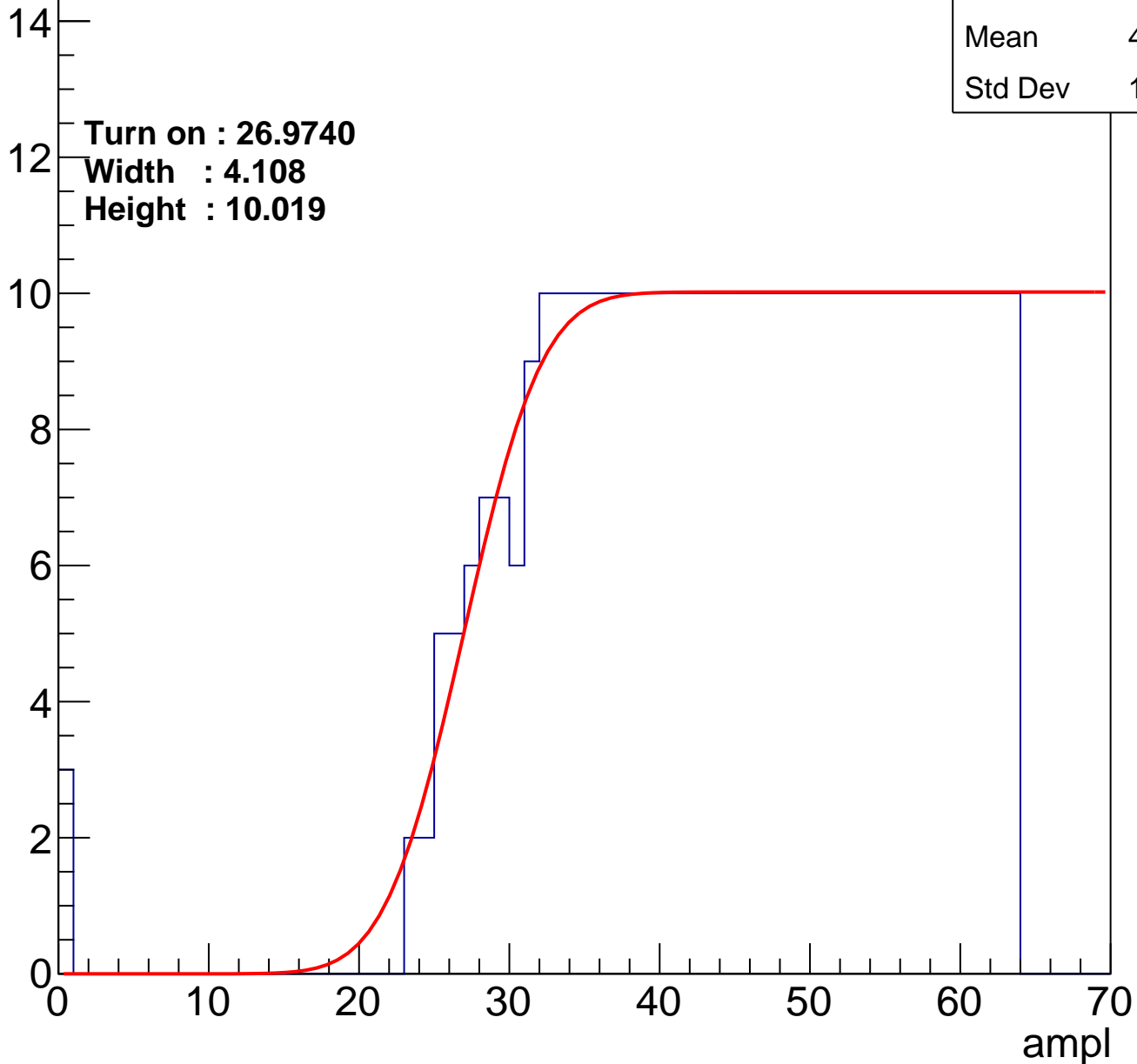
Entries	372
Mean	44.54
Std Dev	11.57

Turn on : 26.9740

Width : 4.108

Height : 10.019

Entry



B1L000S, U3-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 27.7243

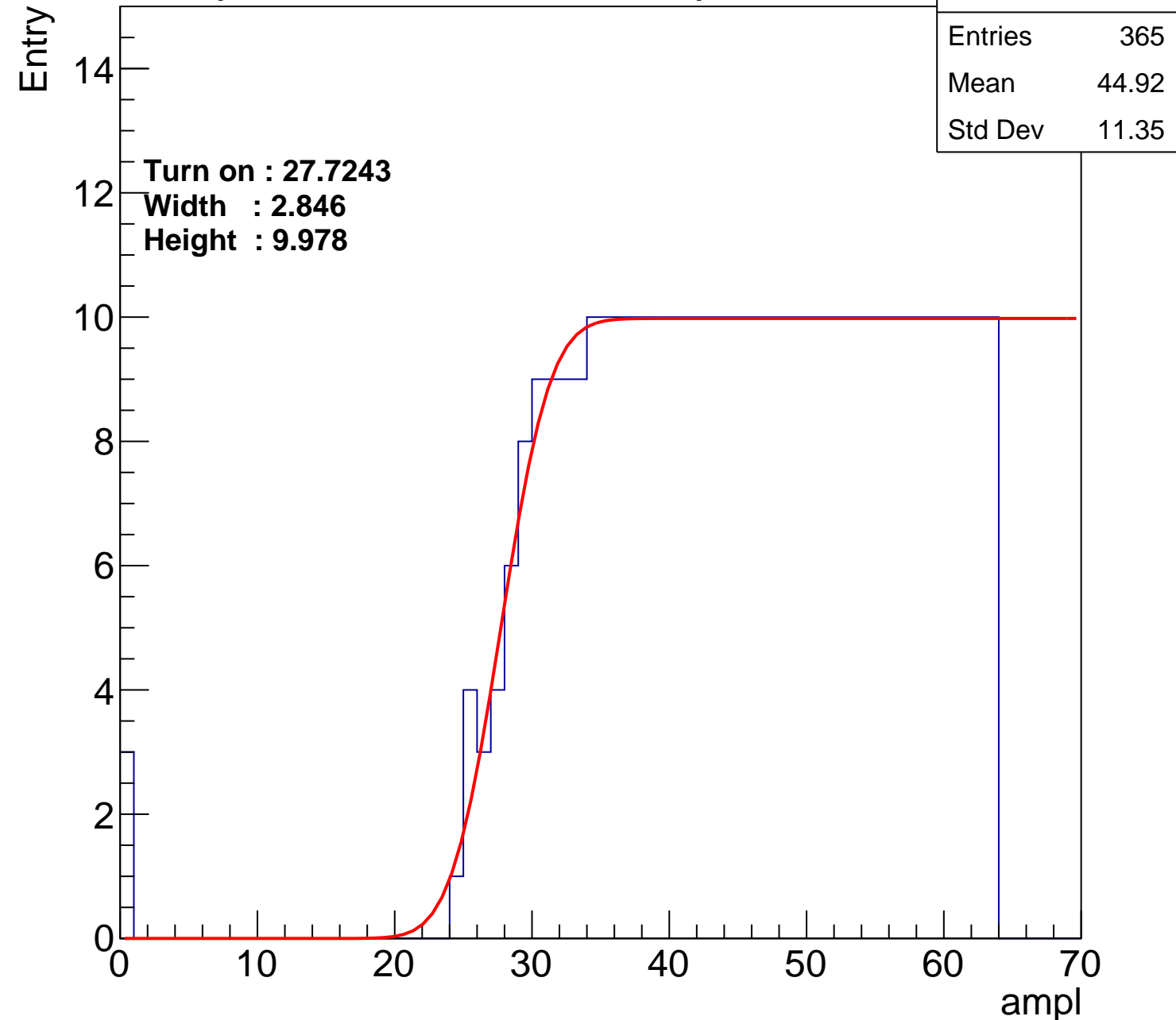
Width : 2.846

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.32
Std Dev	10.74

Turn on : 28.3003

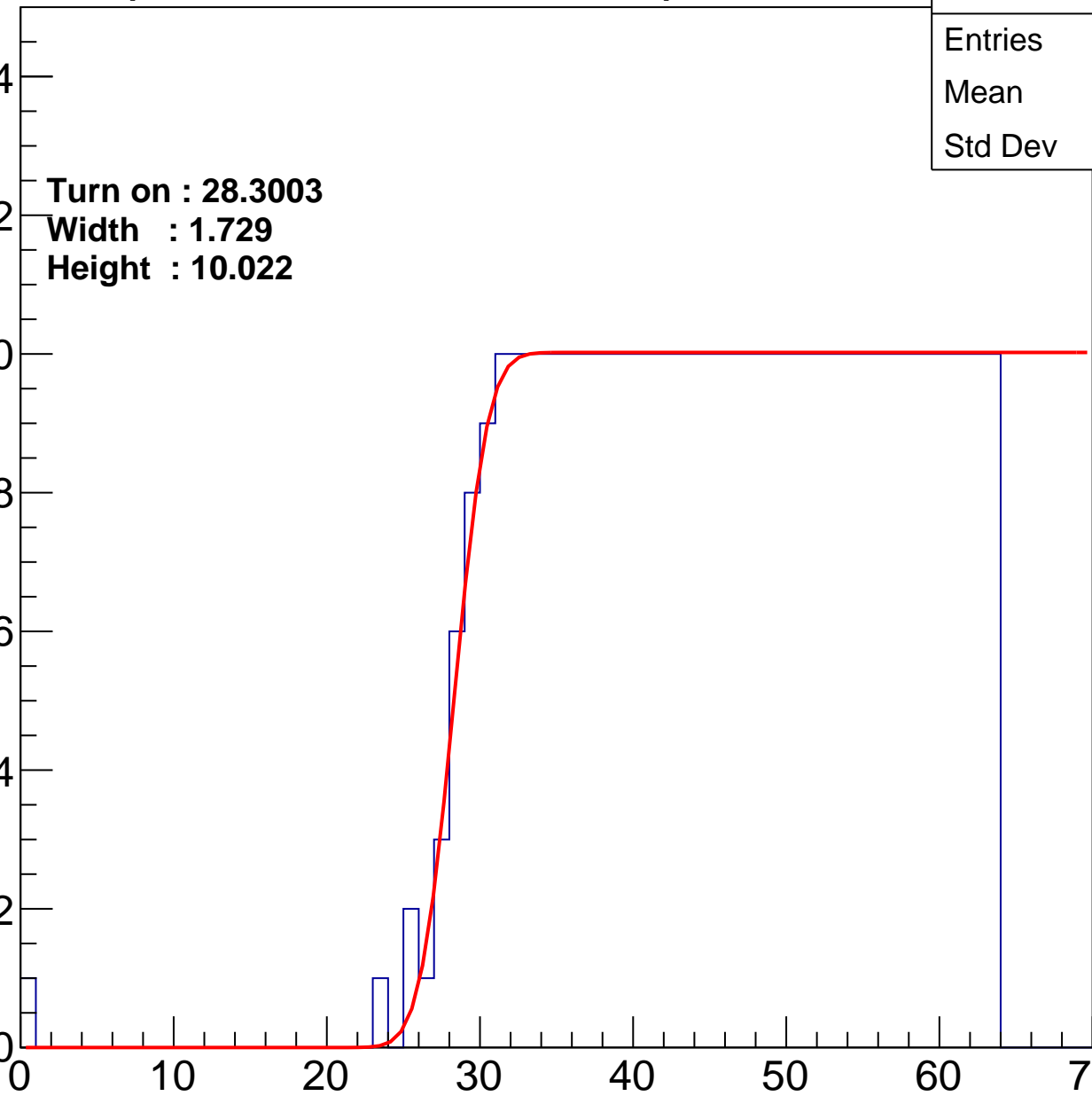
Width : 1.729

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.7
Std Dev	11.36

Turn on : 27.6838

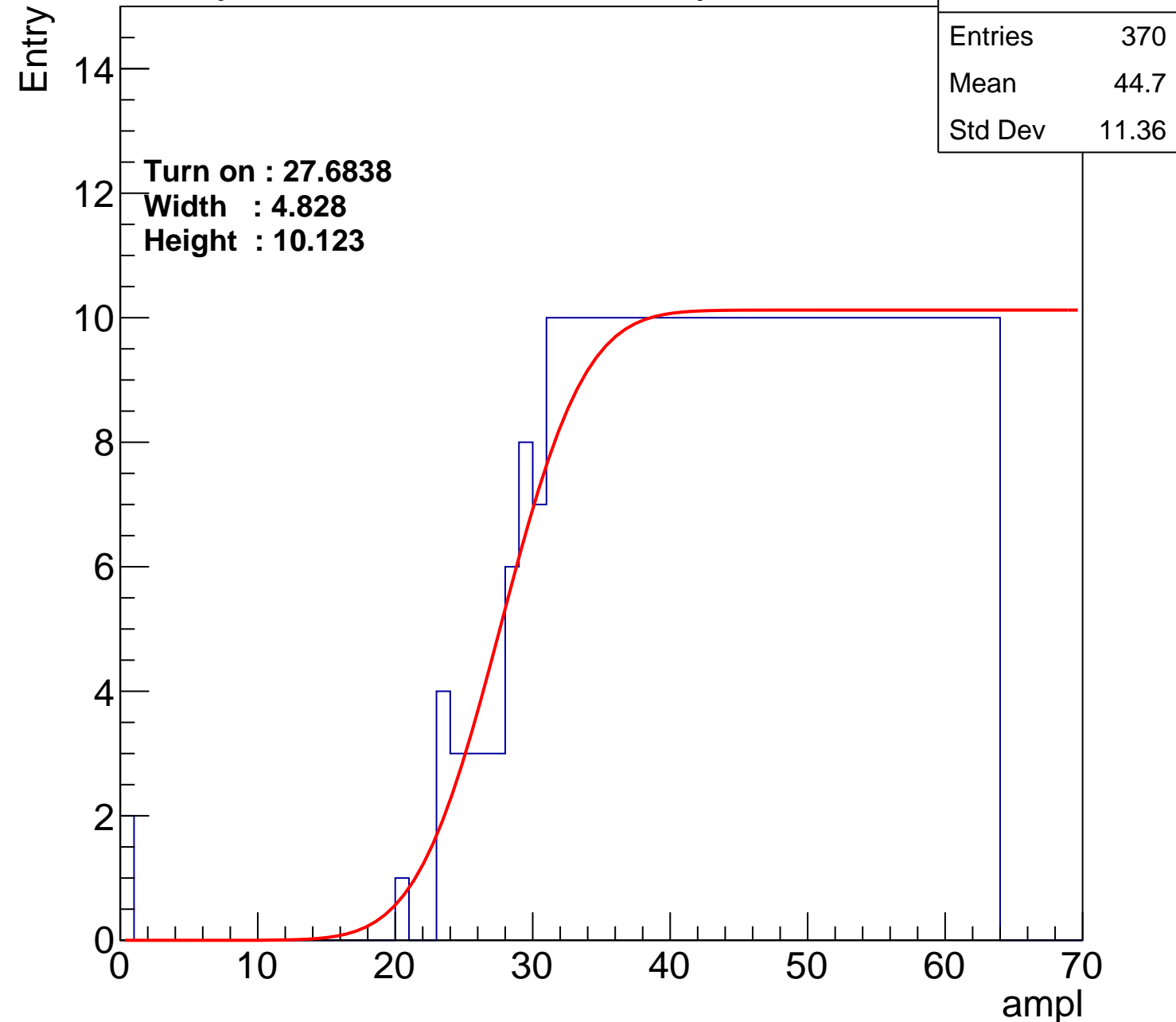
Width : 4.828

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.84
Std Dev	11.88

Turn on : 25.6714

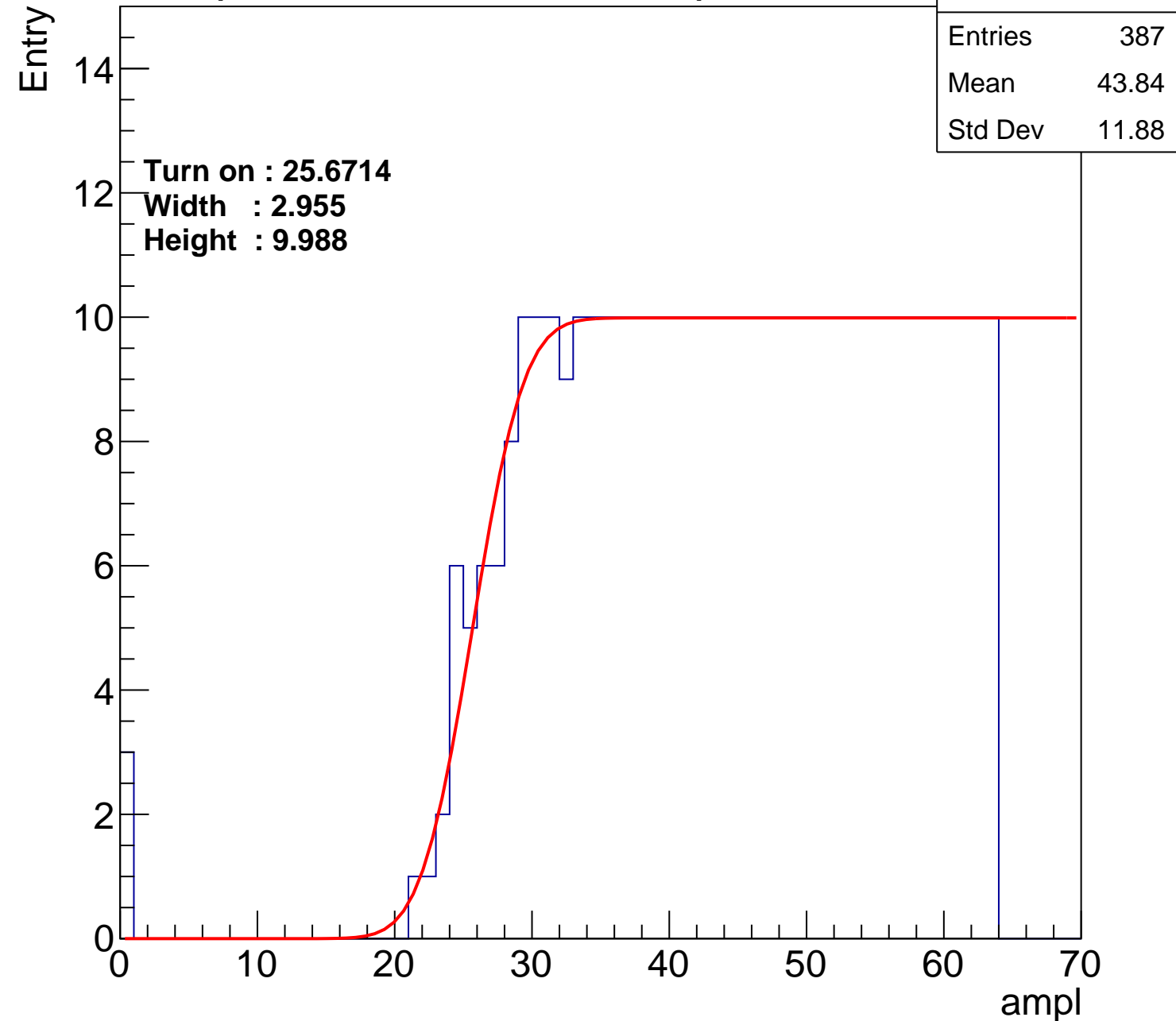
Width : 2.955

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch111

calib_packv5_042523_0143.root, FC#3, port B3

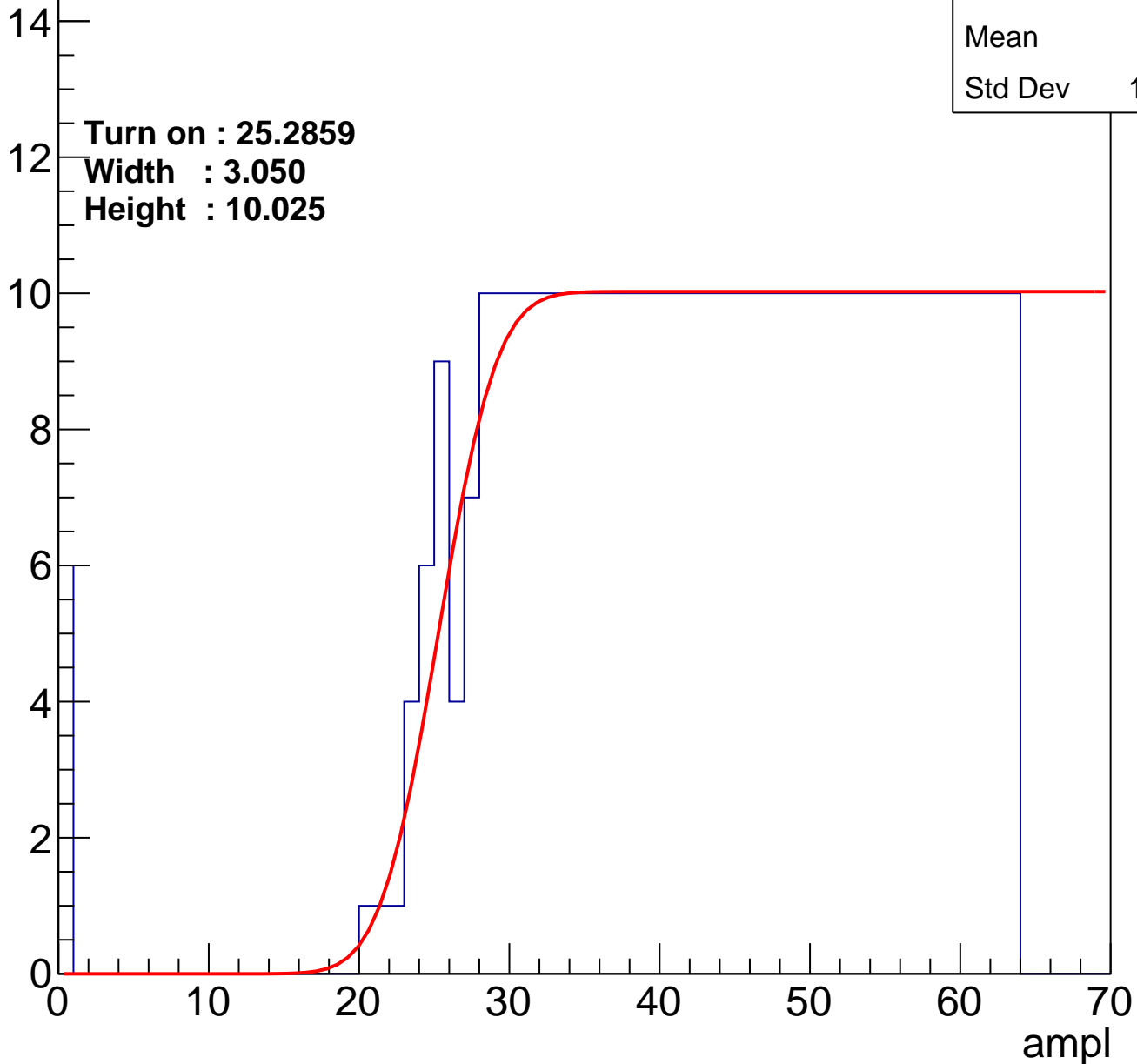
Entries	399
Mean	43.1
Std Dev	12.59

Turn on : 25.2859

Width : 3.050

Height : 10.025

Entry



B1L000S, U3-ch112

calib_packv5_042523_0143.root, FC#3, port B3

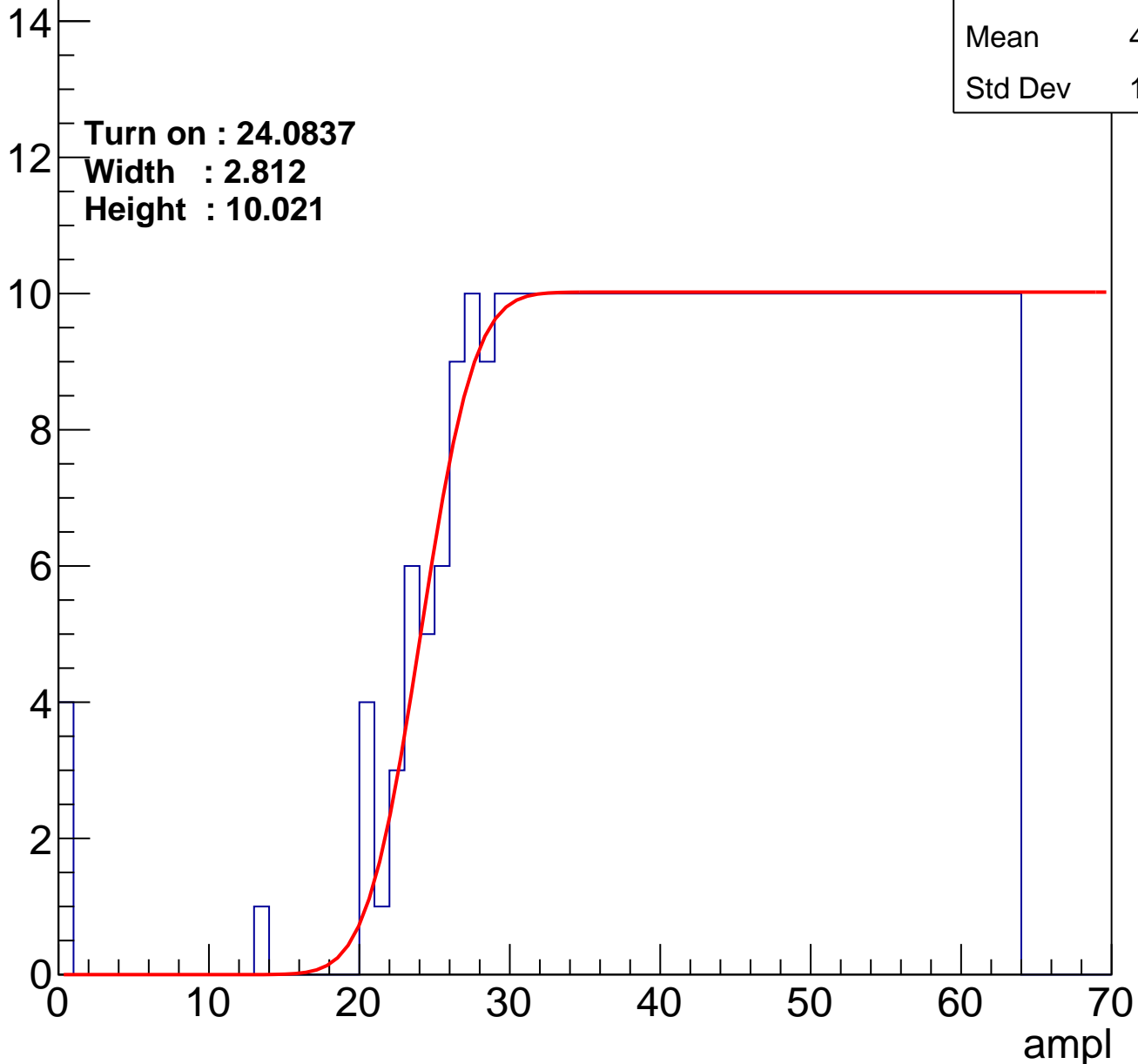
Entries	408
Mean	42.75
Std Dev	12.56

Turn on : 24.0837

Width : 2.812

Height : 10.021

Entry



B1L000S, U3-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 27.0308

Width : 2.177

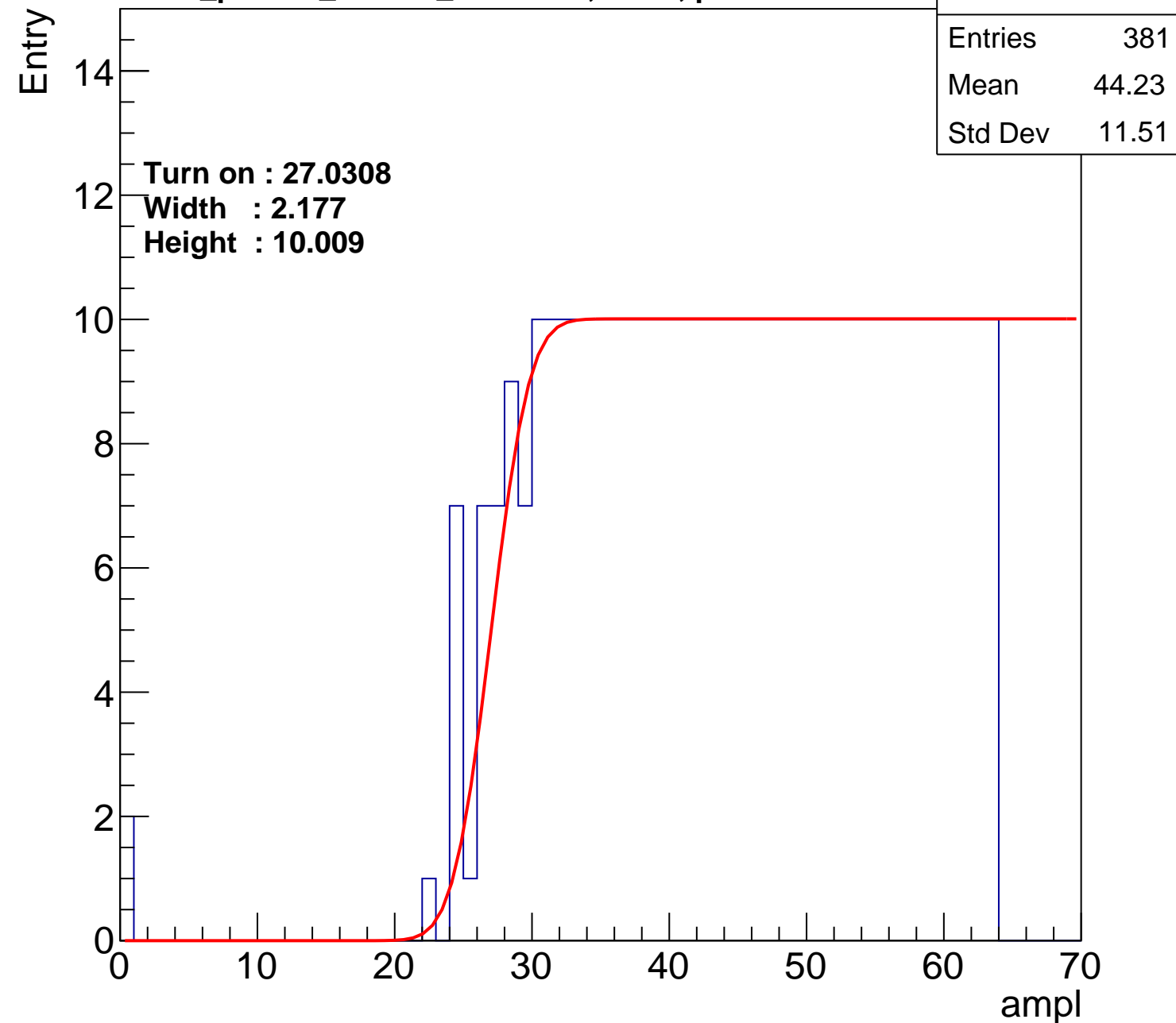
Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U3-ch114

calib_packv5_042523_0143.root, FC#3, port B3

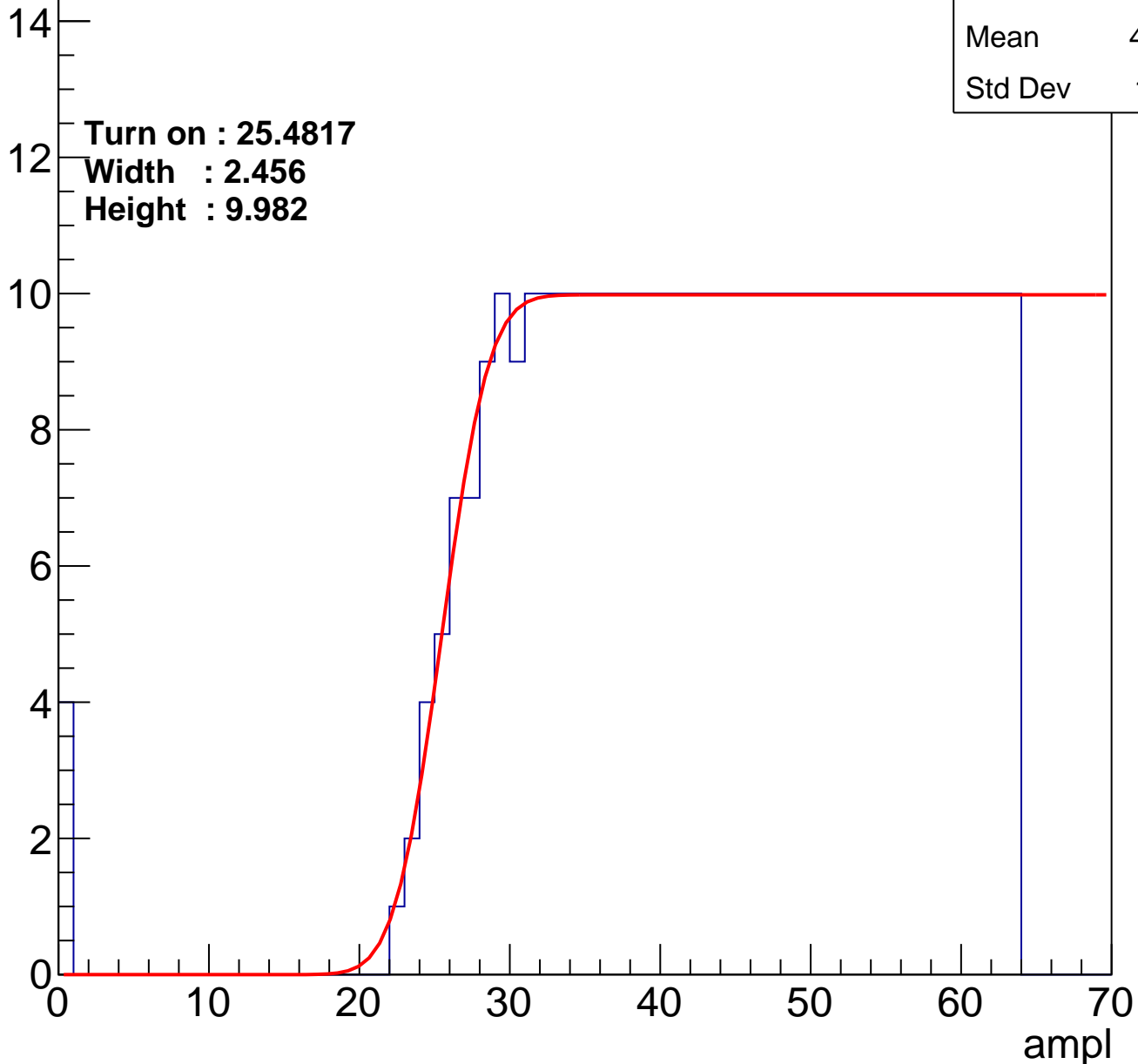
Entries	388
Mean	43.77
Std Dev	12.01

Turn on : 25.4817

Width : 2.456

Height : 9.982

Entry



B1L000S, U3-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.29
Std Dev	11.79

Turn on : 26.8829

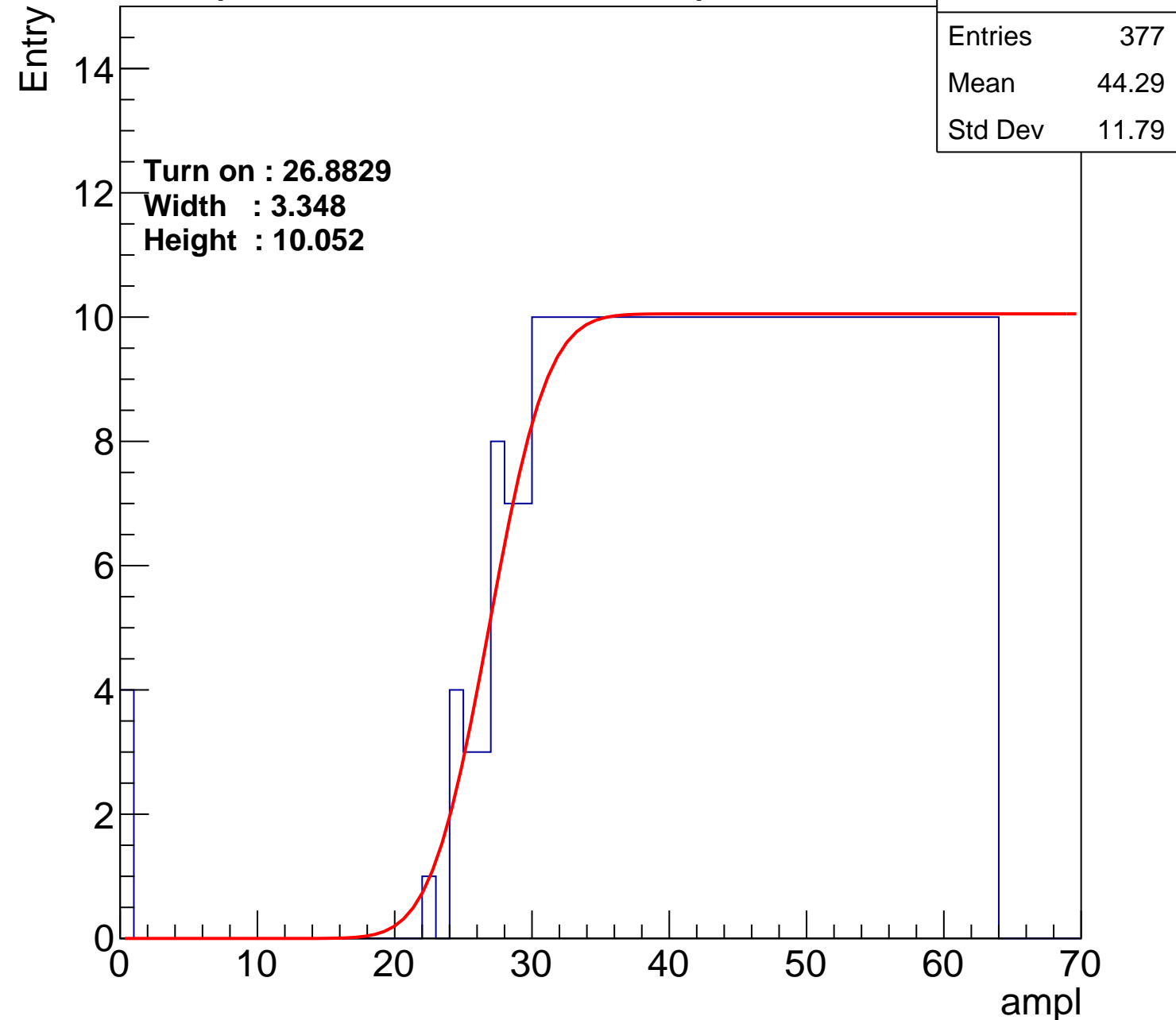
Width : 3.348

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch116

calib_packv5_042523_0143.root, FC#3, port B3

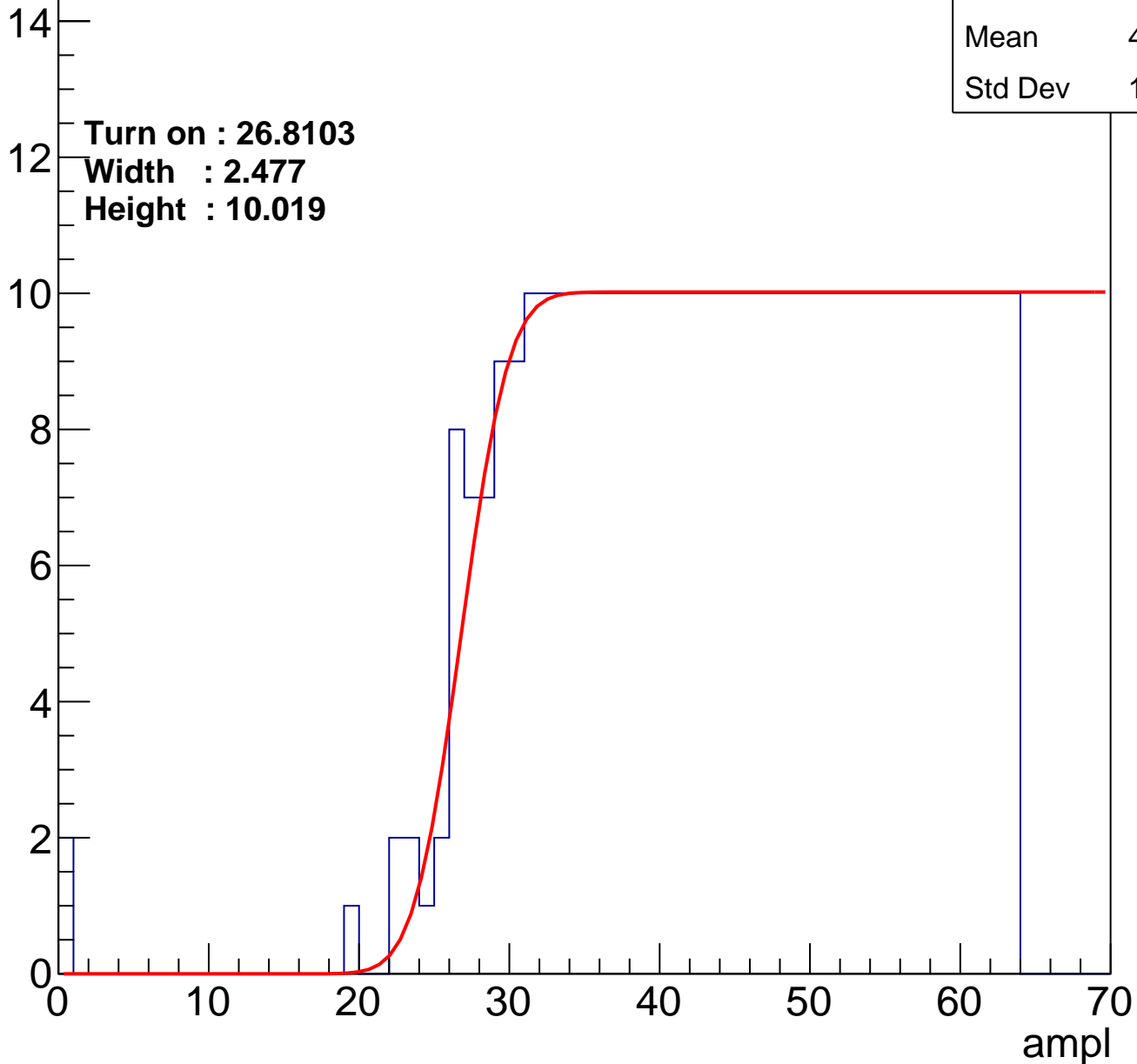
Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.8103

Width : 2.477

Height : 10.019

Entry



B1L000S, U3-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.38
Std Dev	12.25

Turn on : 25.7991

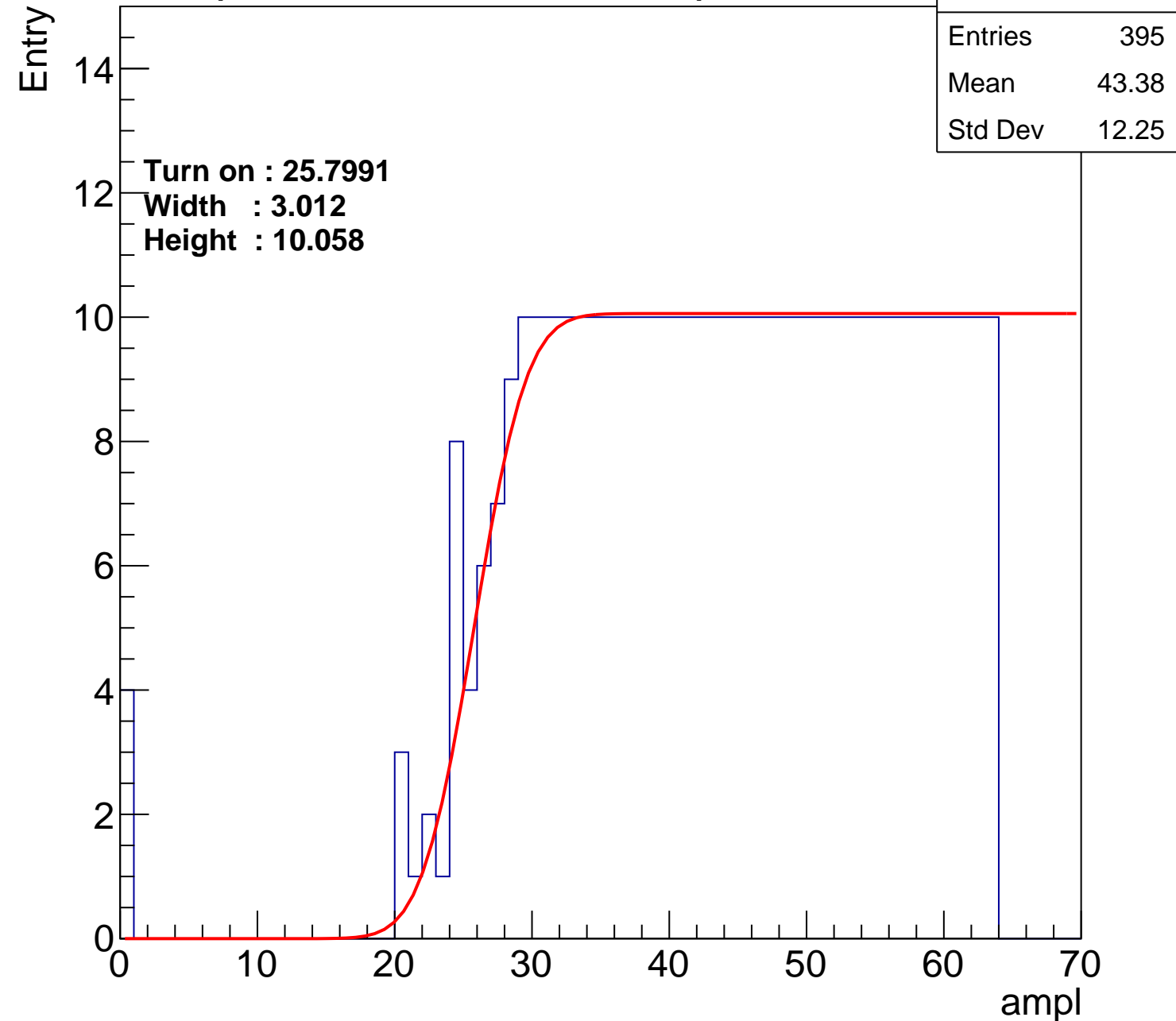
Width : 3.012

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.47
Std Dev	11.52

Turn on : 26.7131

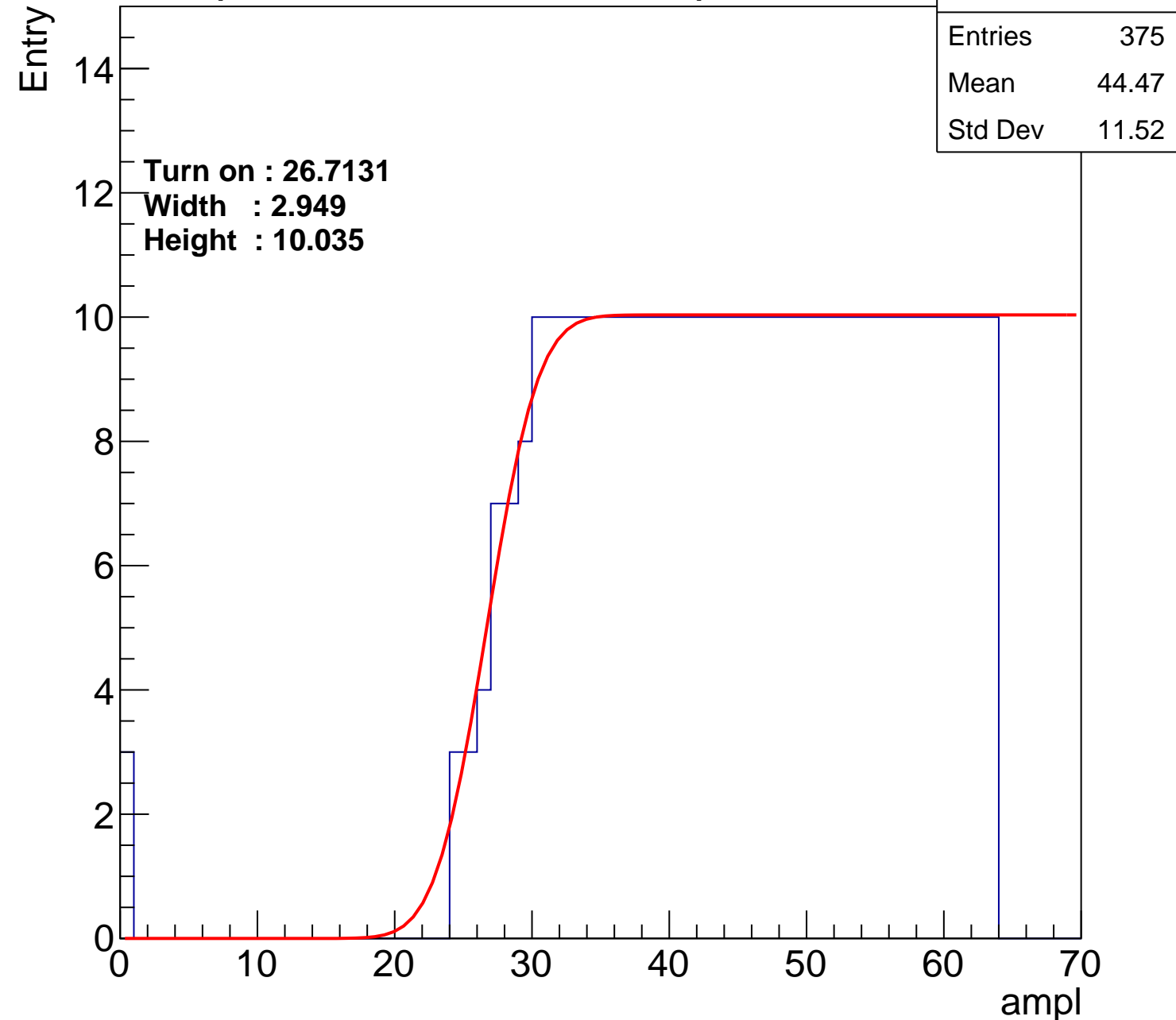
Width : 2.949

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.93
Std Dev	11.81

Turn on : 25.9148

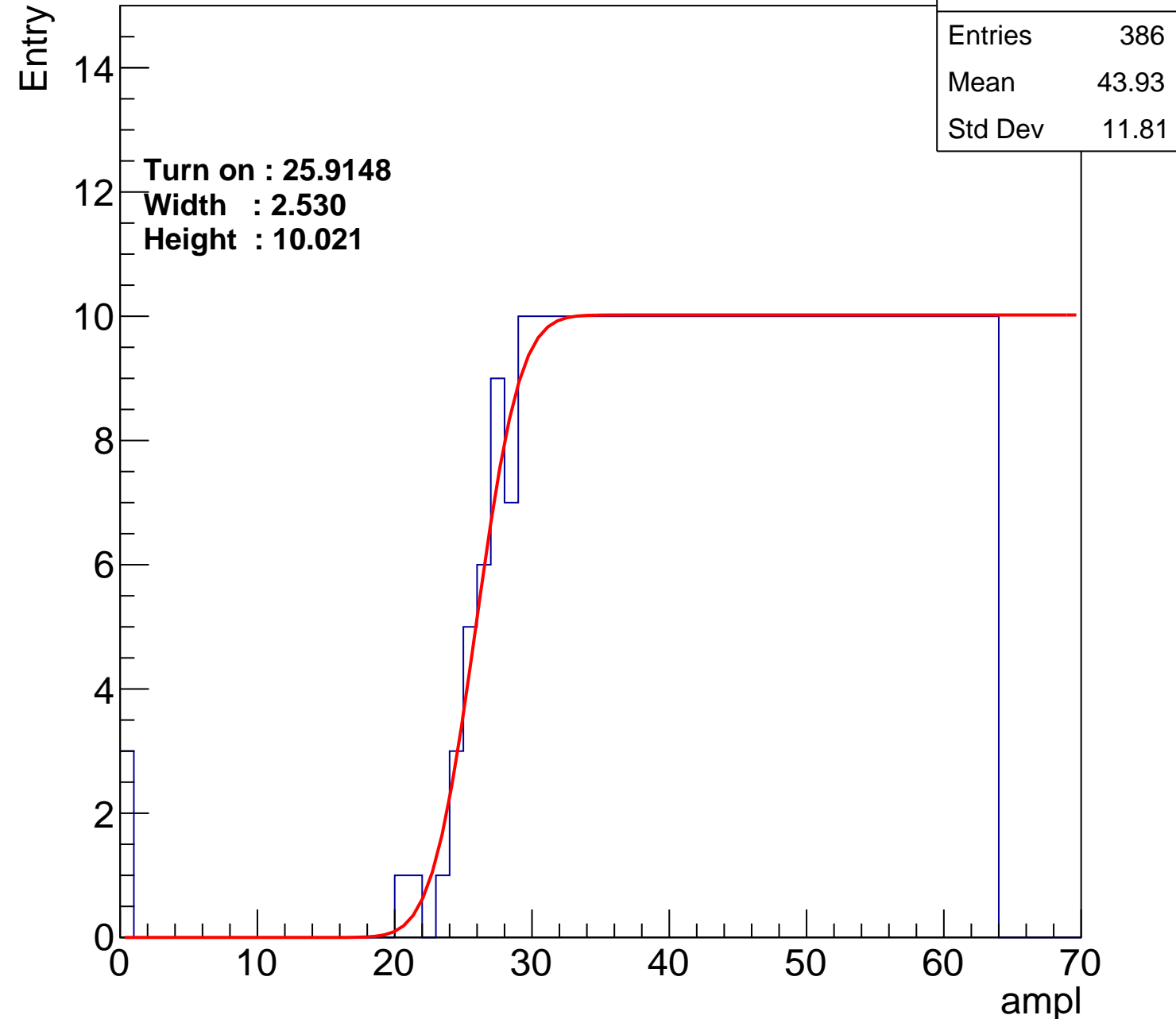
Width : 2.530

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch120

calib_packv5_042523_0143.root, FC#3, port B3

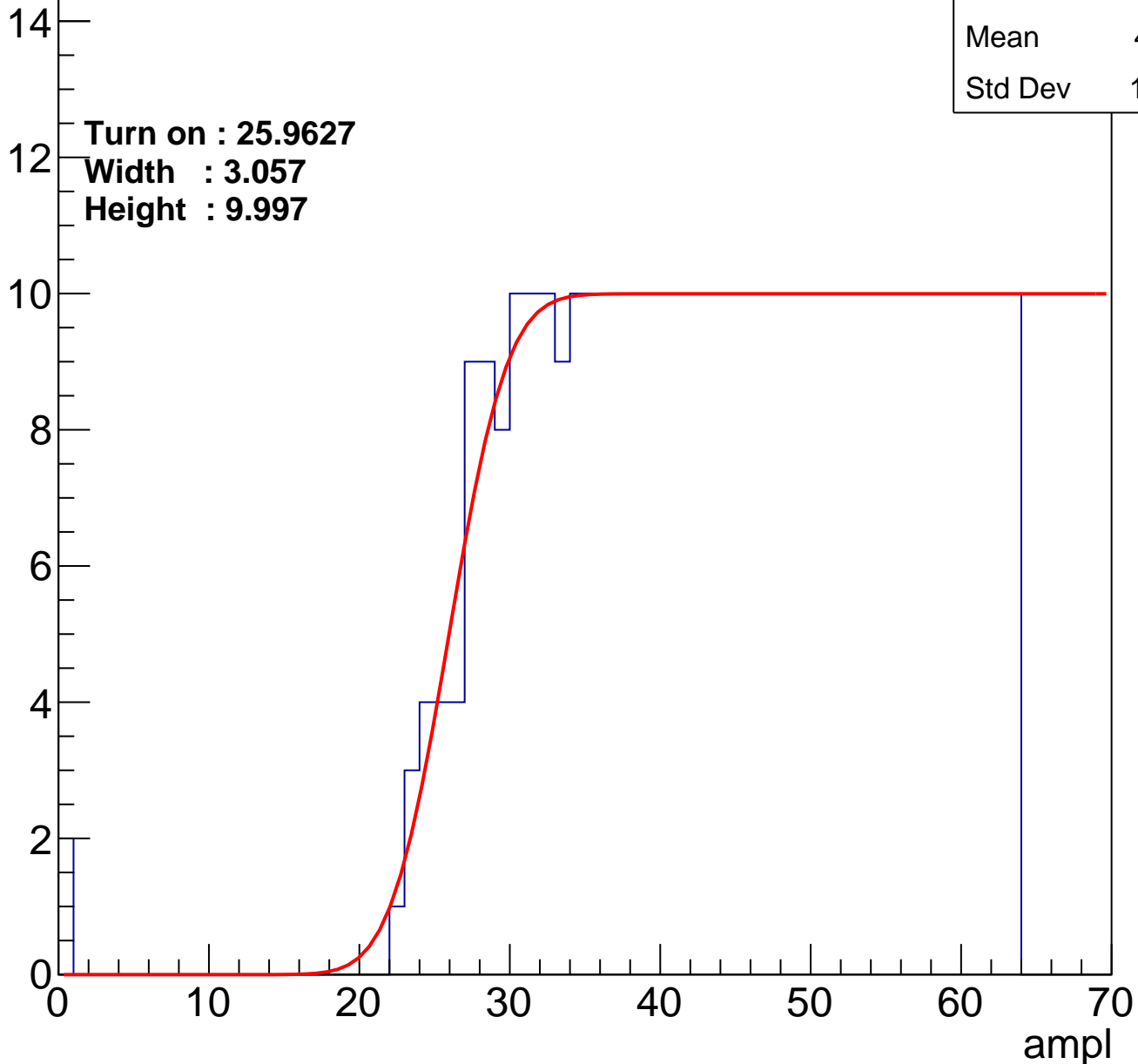
Entries	383
Mean	44.11
Std Dev	11.59

Turn on : 25.9627

Width : 3.057

Height : 9.997

Entry



B1L000S, U3-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	44.88
Std Dev	11.55

Turn on : 28.1449

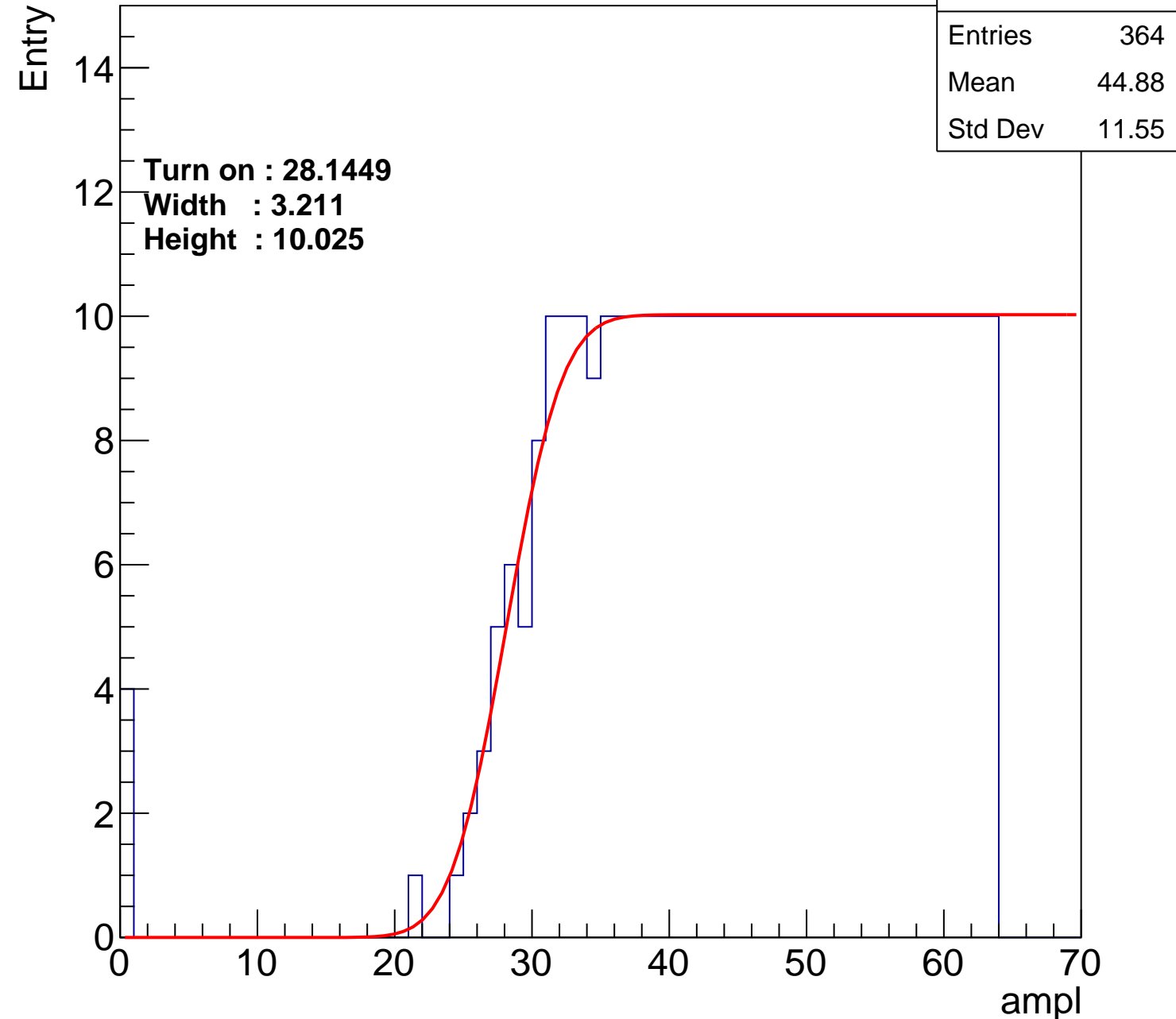
Width : 3.211

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch122

calib_packv5_042523_0143.root, FC#3, port B3

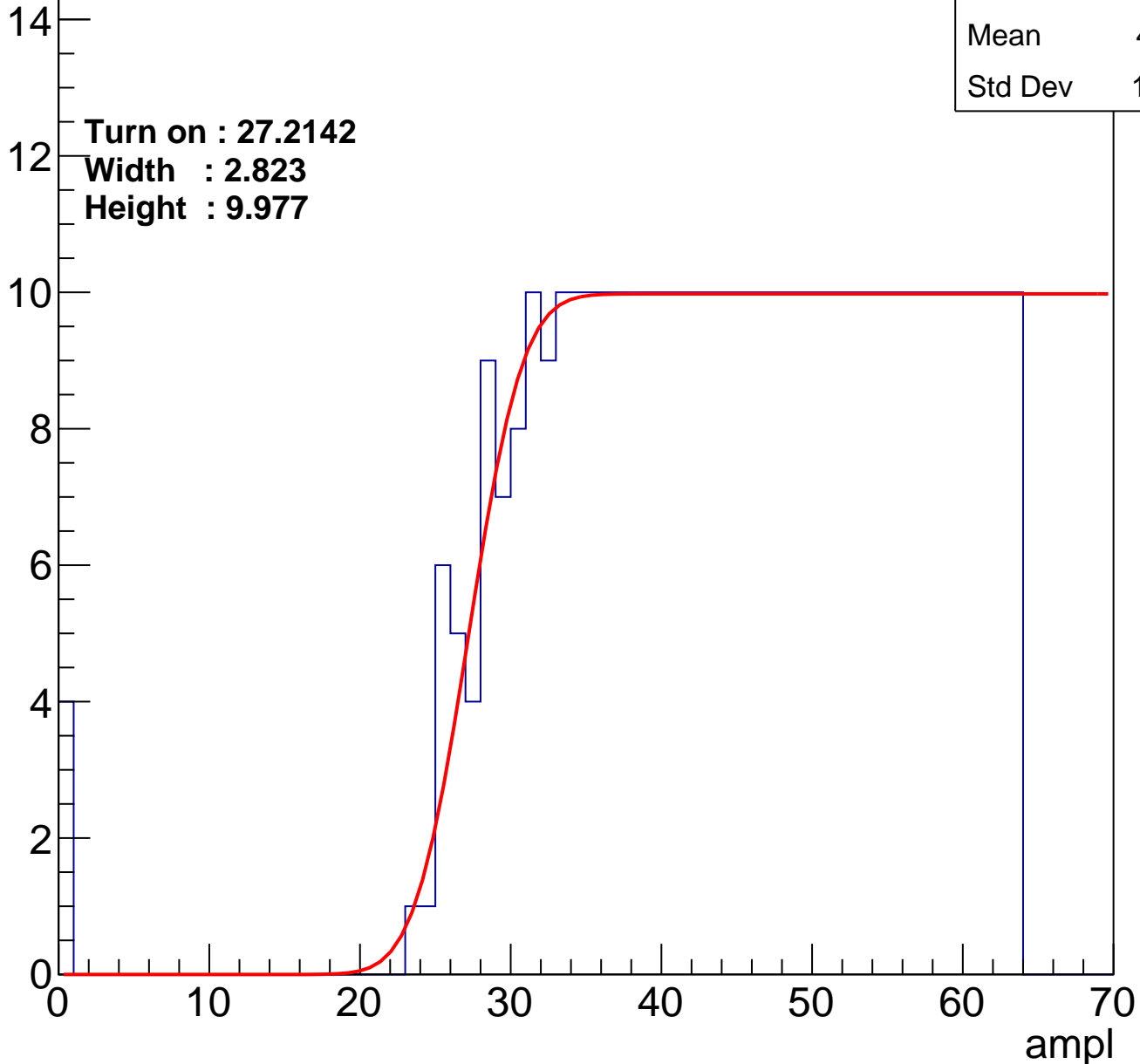
Entries	374
Mean	44.41
Std Dev	11.75

Turn on : 27.2142

Width : 2.823

Height : 9.977

Entry



B1L000S, U3-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.13
Std Dev	11.7

Turn on : 26.6590

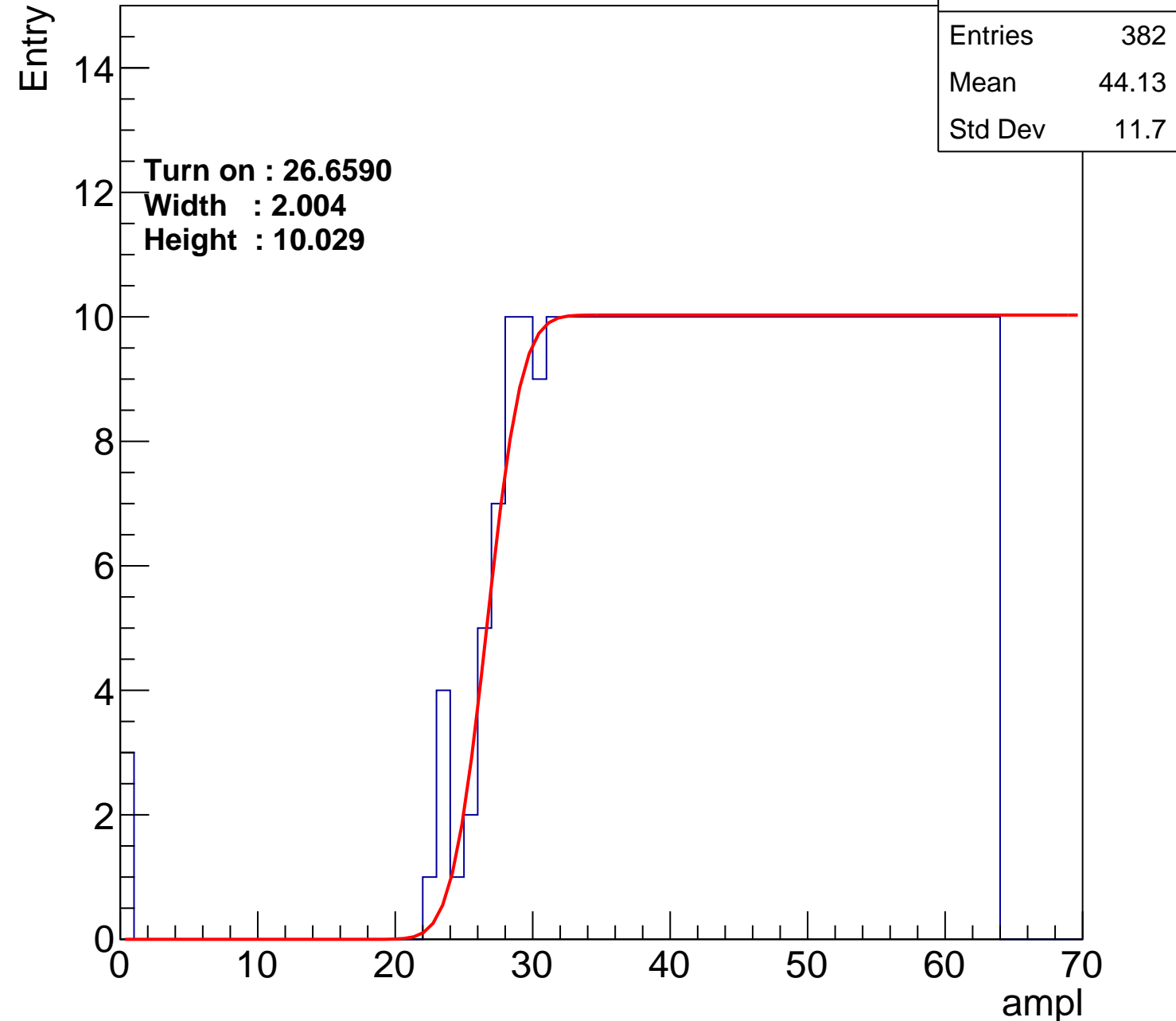
Width : 2.004

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch124

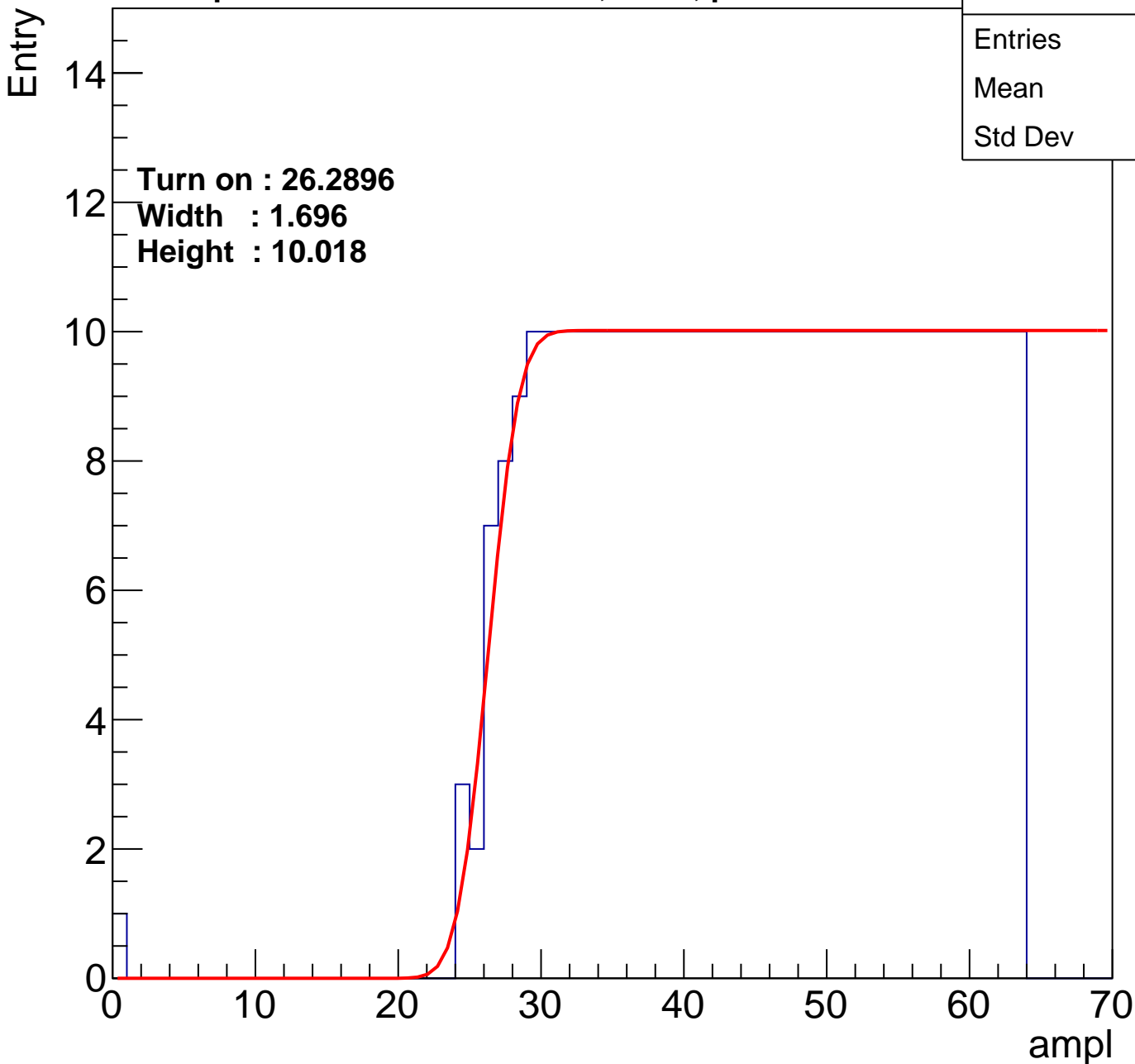
calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.4
Std Dev	11.21

Turn on : 26.2896

Width : 1.696

Height : 10.018



B1L000S, U3-ch125

calib_packv5_042523_0143.root, FC#3, port B3

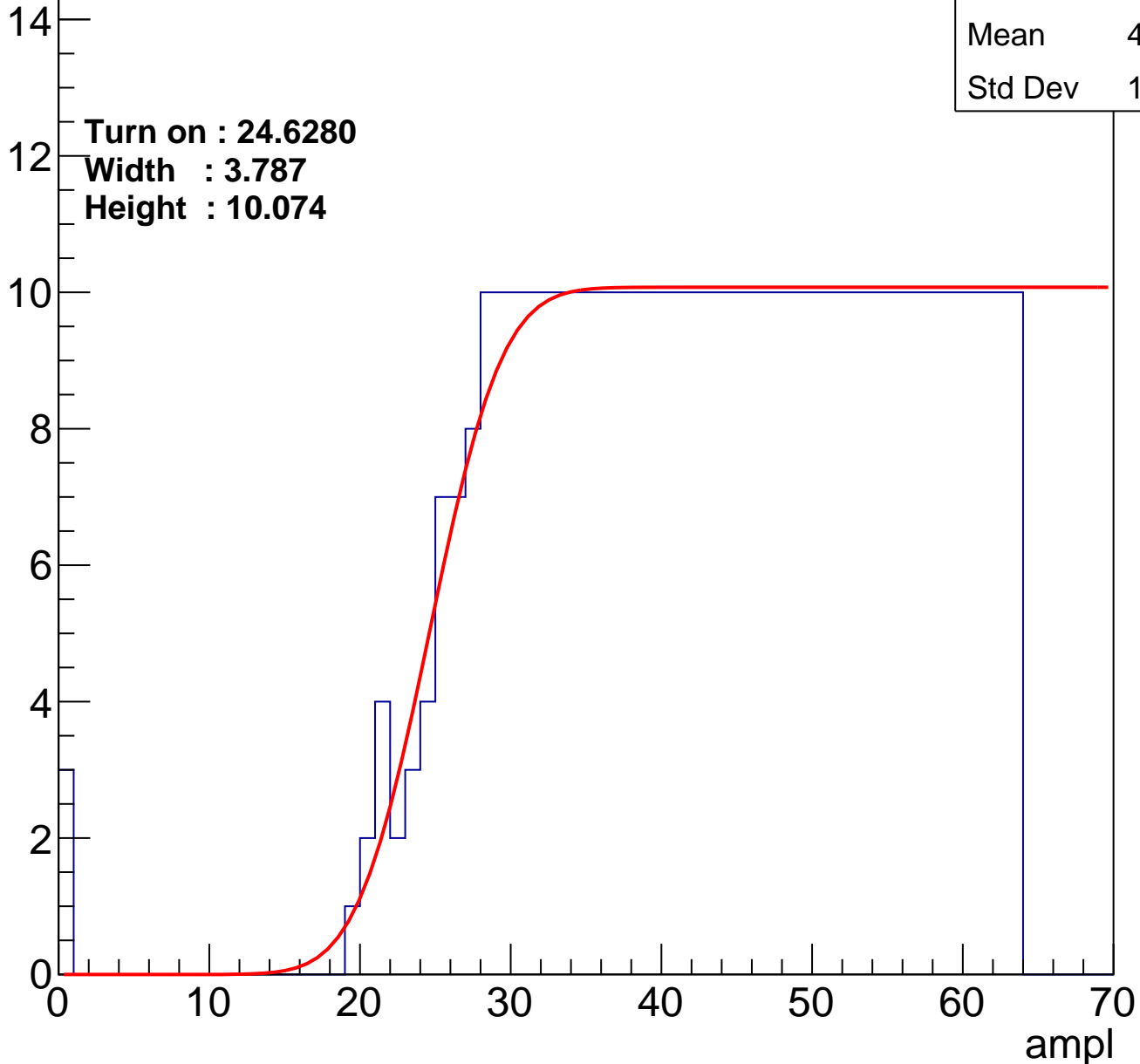
Entries	401
Mean	43.15
Std Dev	12.24

Turn on : 24.6280

Width : 3.787

Height : 10.074

Entry



B1L000S, U3-ch126

calib_packv5_042523_0143.root, FC#3, port B3

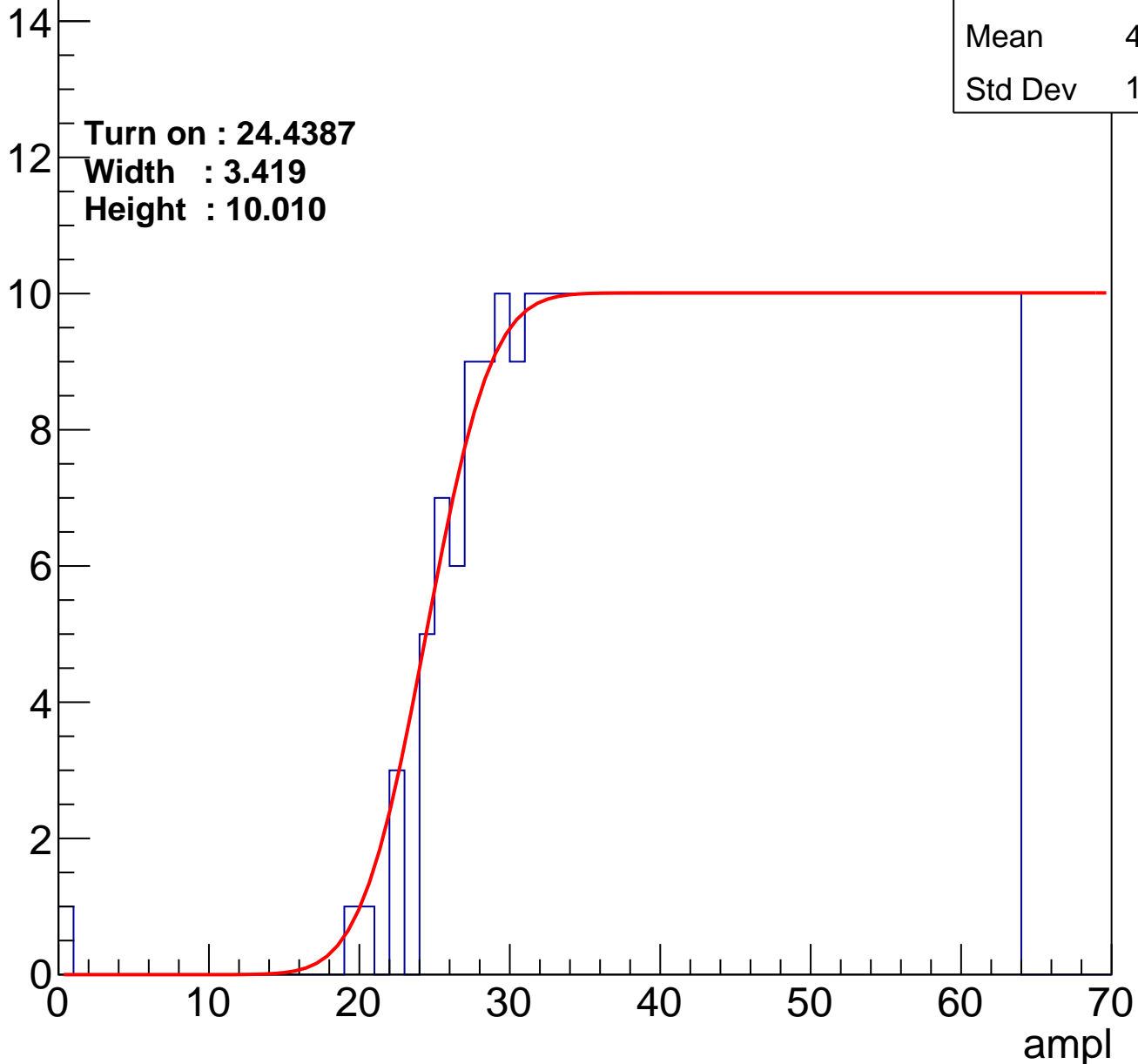
Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 24.4387

Width : 3.419

Height : 10.010

Entry



B1L000S, U3-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.3130

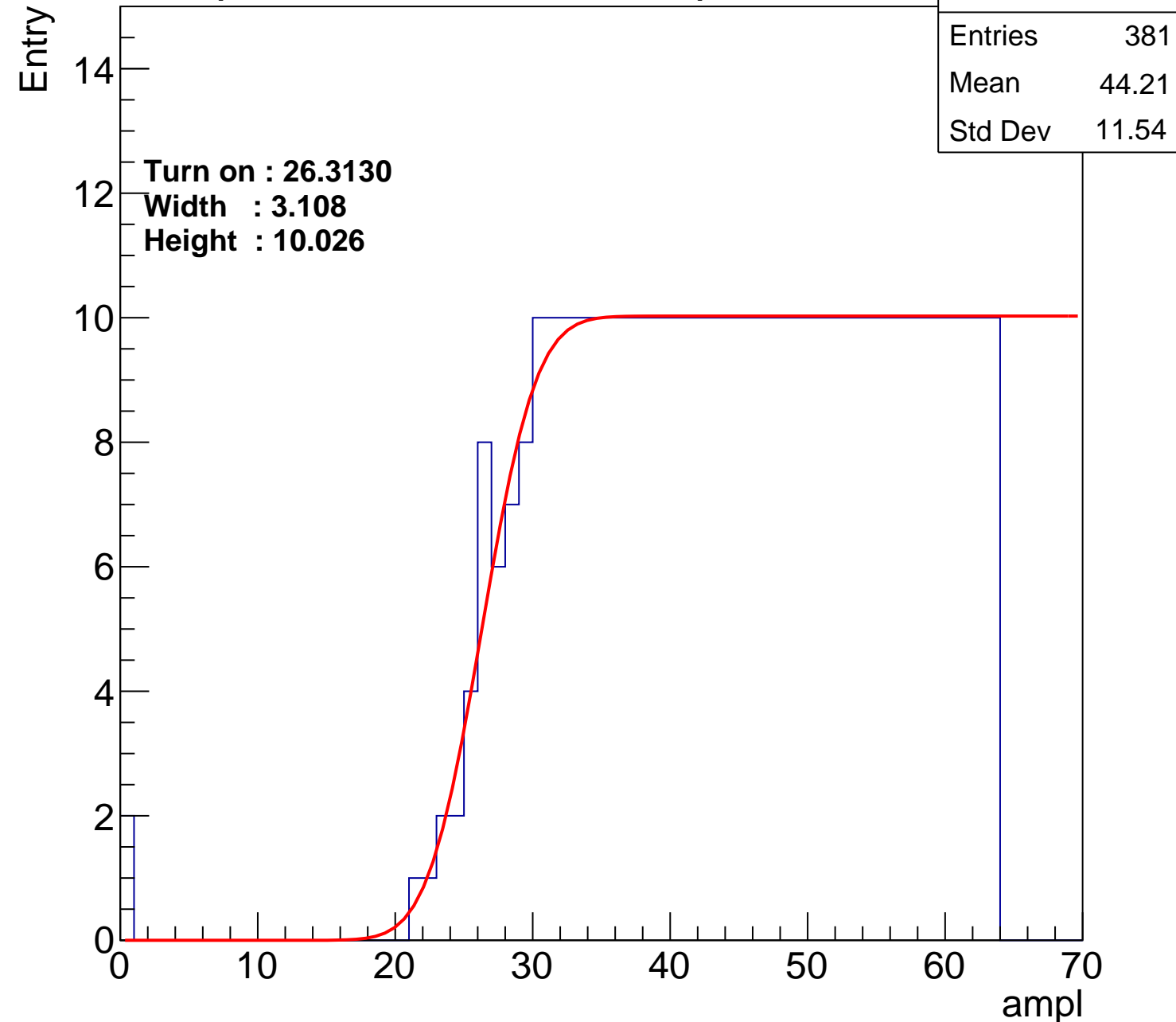
Width : 3.108

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U3-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.3130

Width : 3.108

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl

