

B1L001S, U26-ch0, adc0

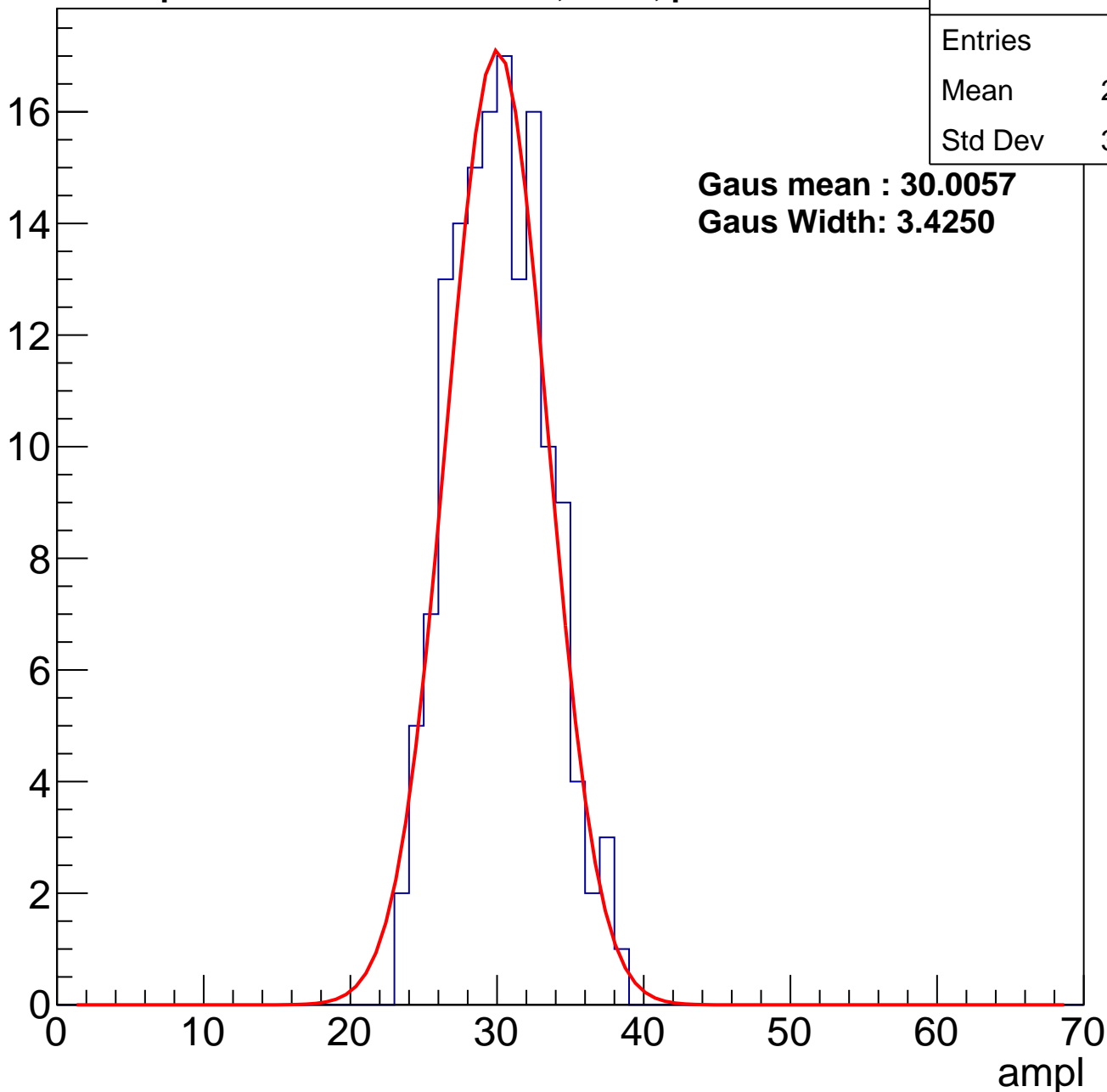
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 147 |
| Mean | 29.68 |
| Std Dev | 3.239 |

Gaus mean : 30.0057

Gaus Width: 3.4250

Entry



B1L001S, U26-ch0, adc1

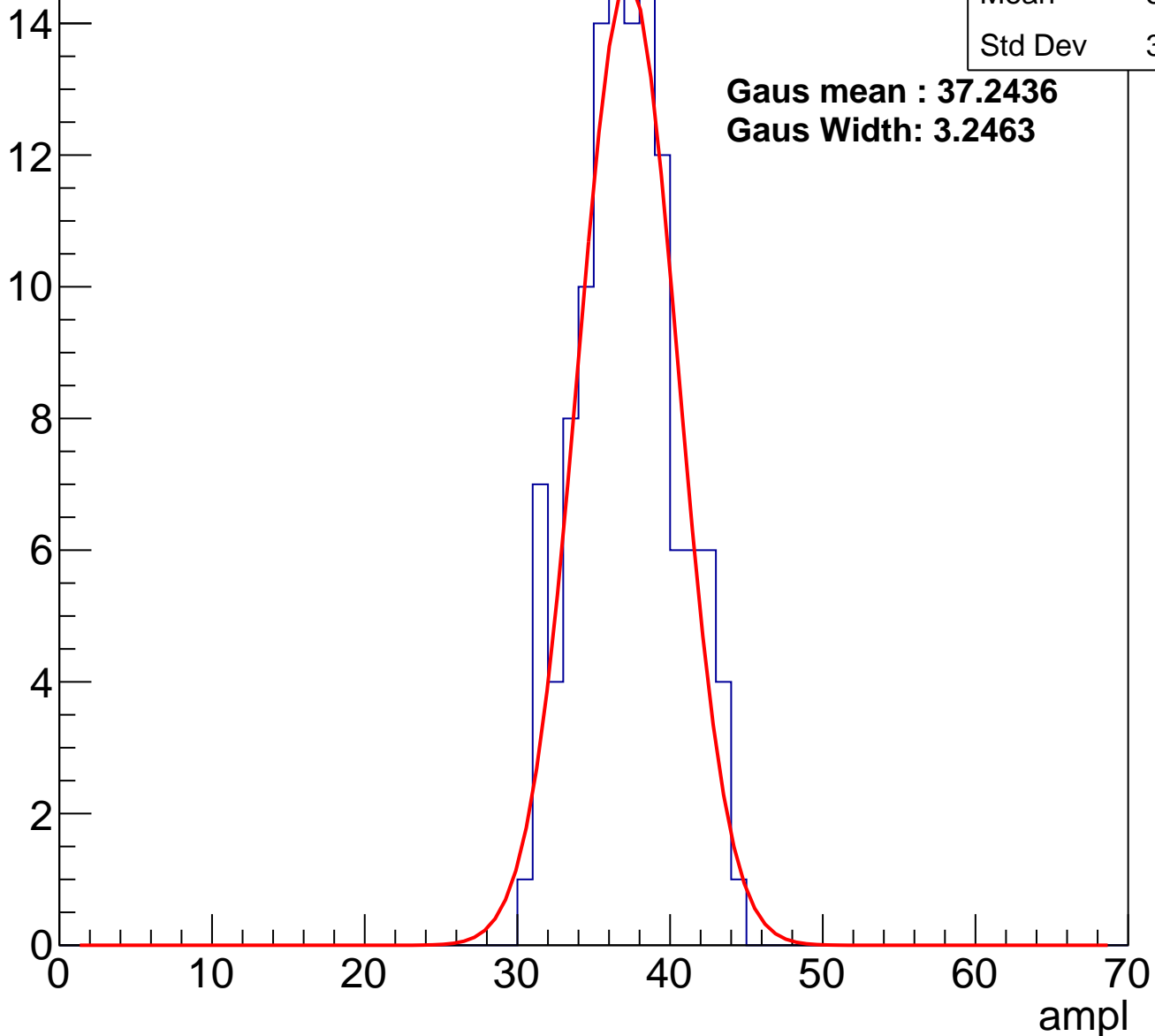
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 36.74 |
| Std Dev | 3.172 |

Gaus mean : 37.2436

Gaus Width: 3.2463

Entry



B1L001S, U26-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 156 |
| Mean | 43.37 |
| Std Dev | 3.647 |

Gaus mean : 43.9085

Gaus Width: 3.6552

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

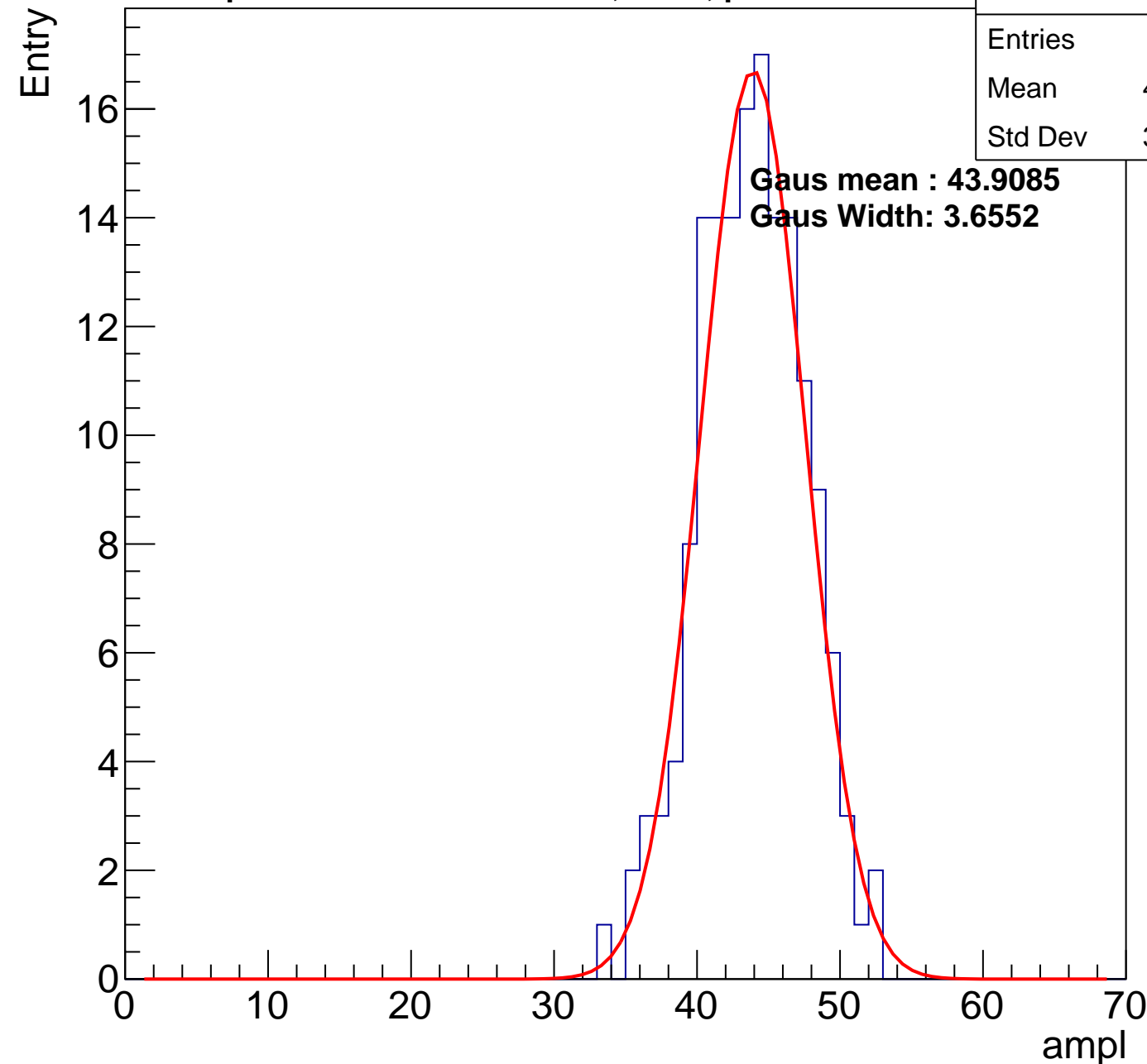
40

50

60

ampl

70



B1L001S, U26-ch0, adc3

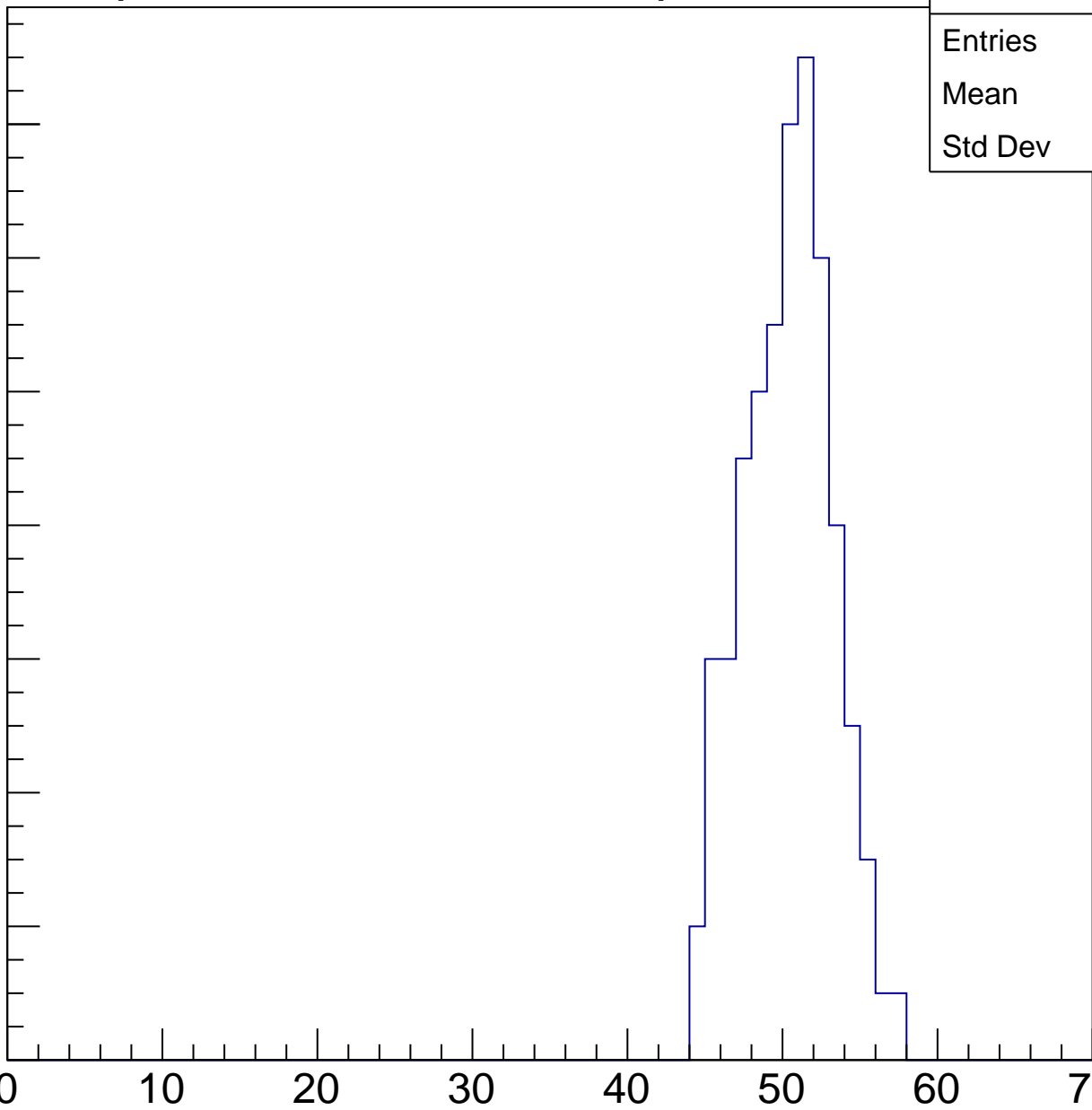
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 103 |
| Mean | 49.87 |
| Std Dev | 2.827 |

ampl



B1L001S, U26-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 55.63 |
| Std Dev | 3.261 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

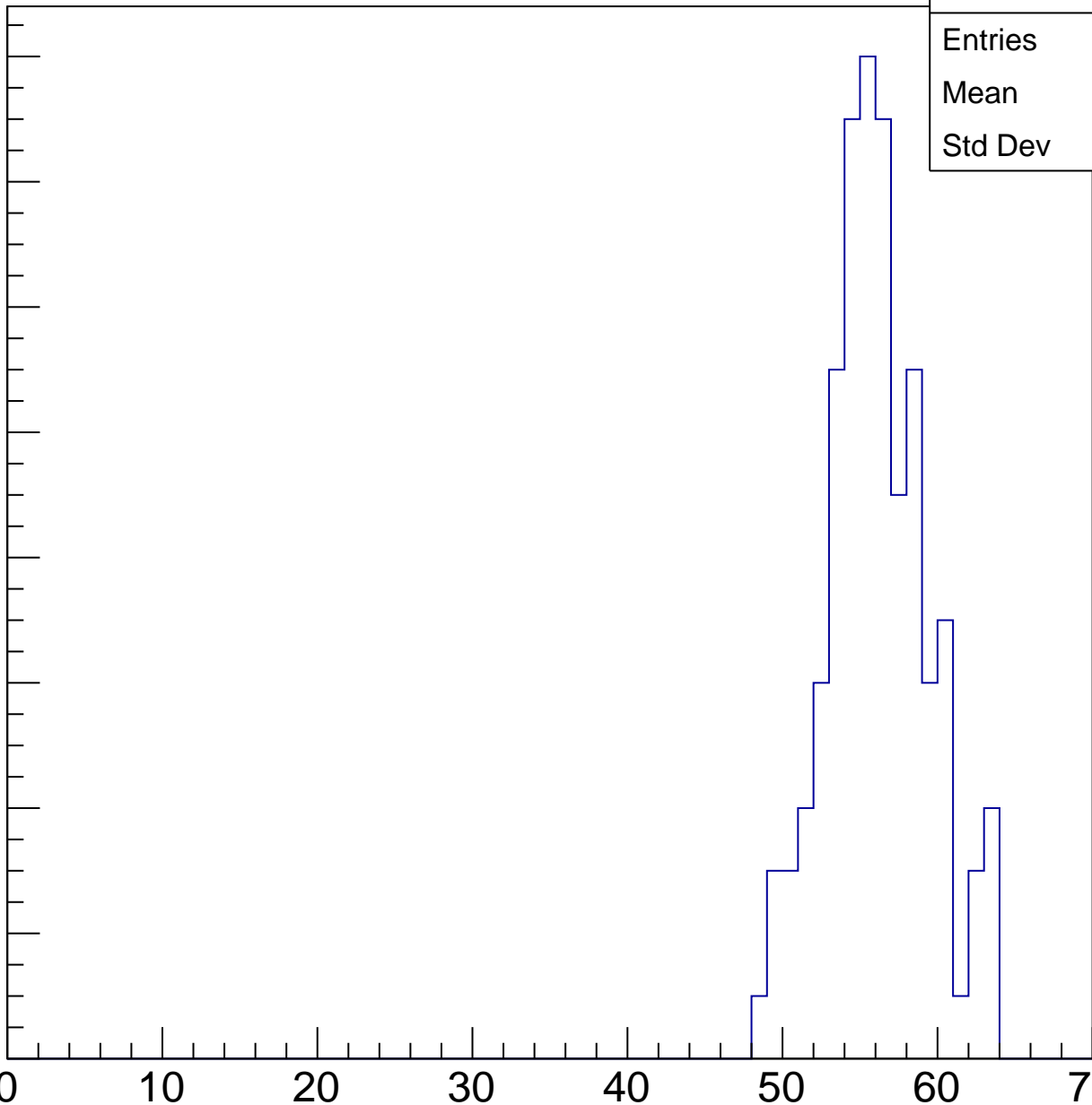
40

50

60

70

ampl



B1L001S, U26-ch0, adc5

calib_packv5_042523_0143.root, FC#2, port C2

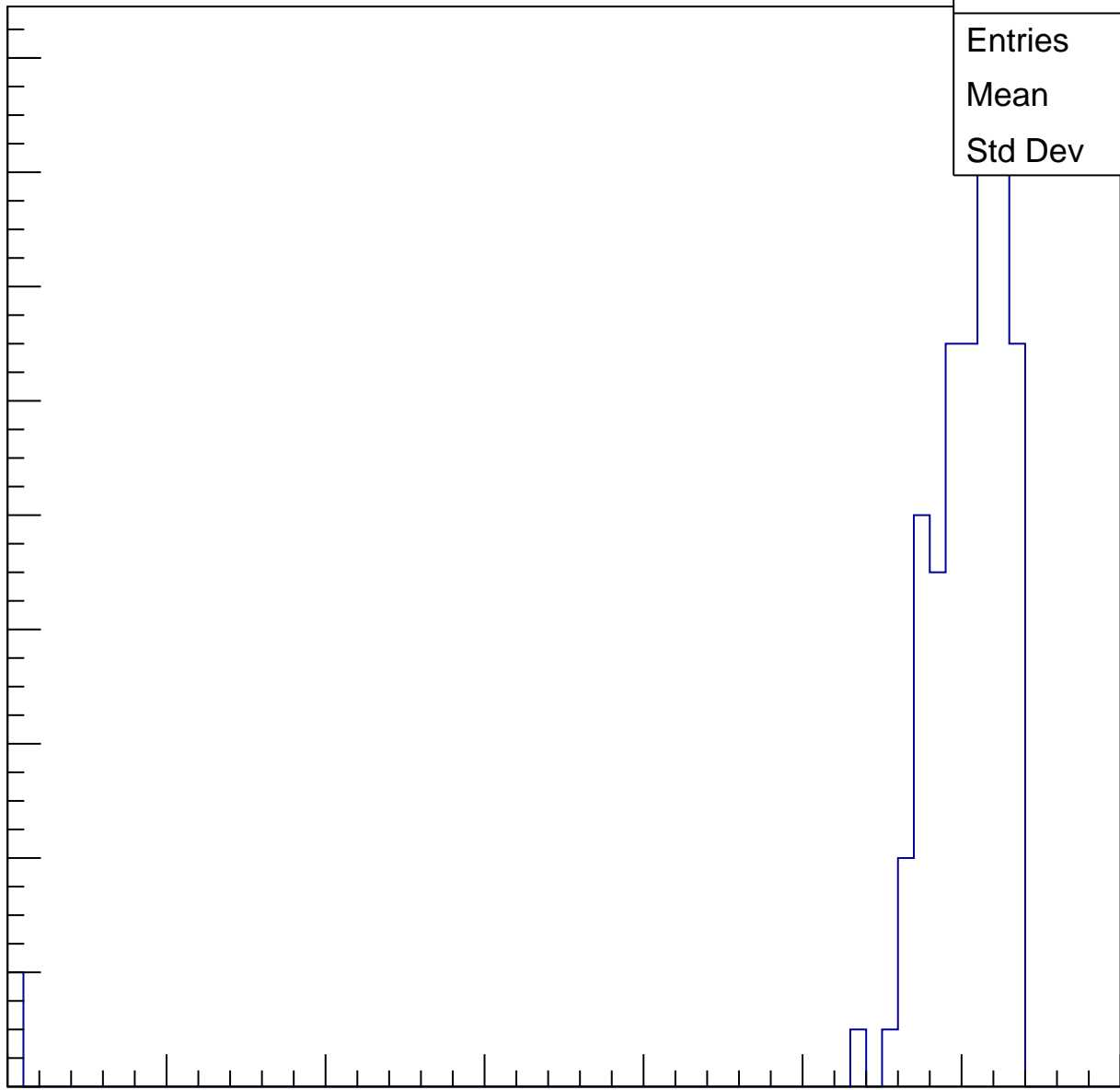
| | |
|---------|-------|
| Entries | 101 |
| Mean | 58.83 |
| Std Dev | 8.641 |

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

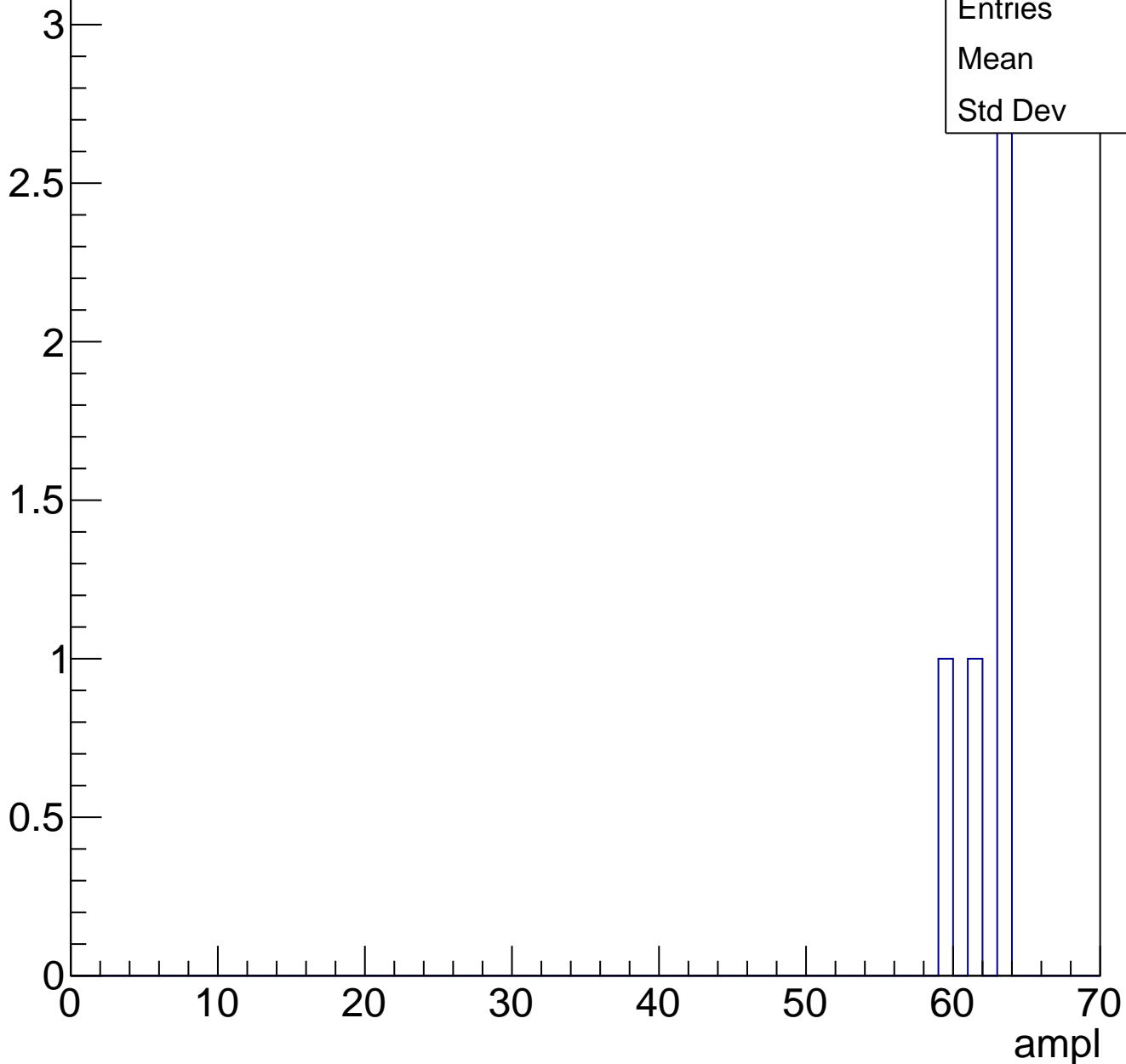
ampl



B1L001S, U26-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

ampl

B1L001S, U26-ch1, adc0

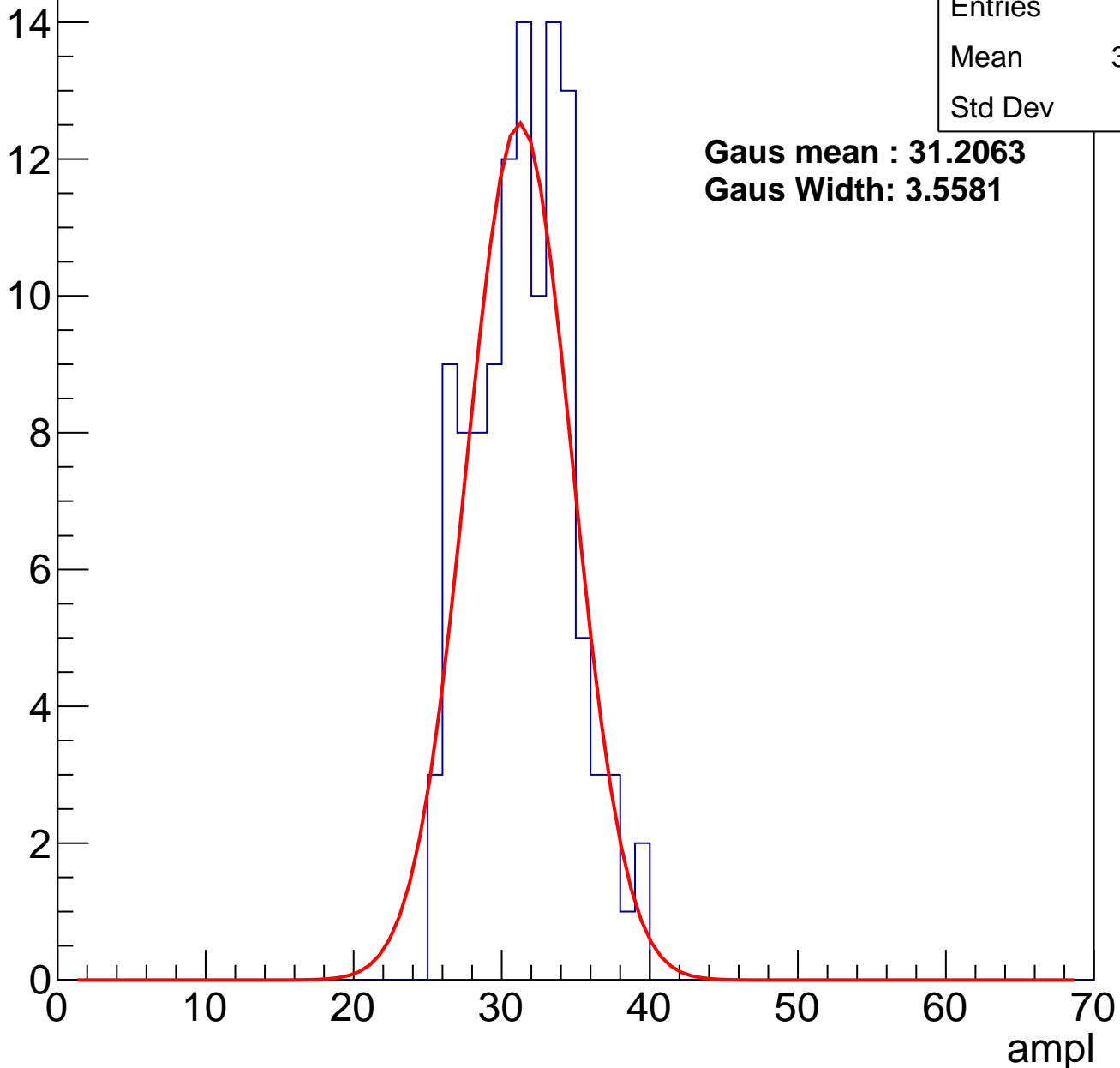
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 114 |
| Mean | 31.04 |
| Std Dev | 3.25 |

Gaus mean : 31.2063

Gaus Width: 3.5581

Entry



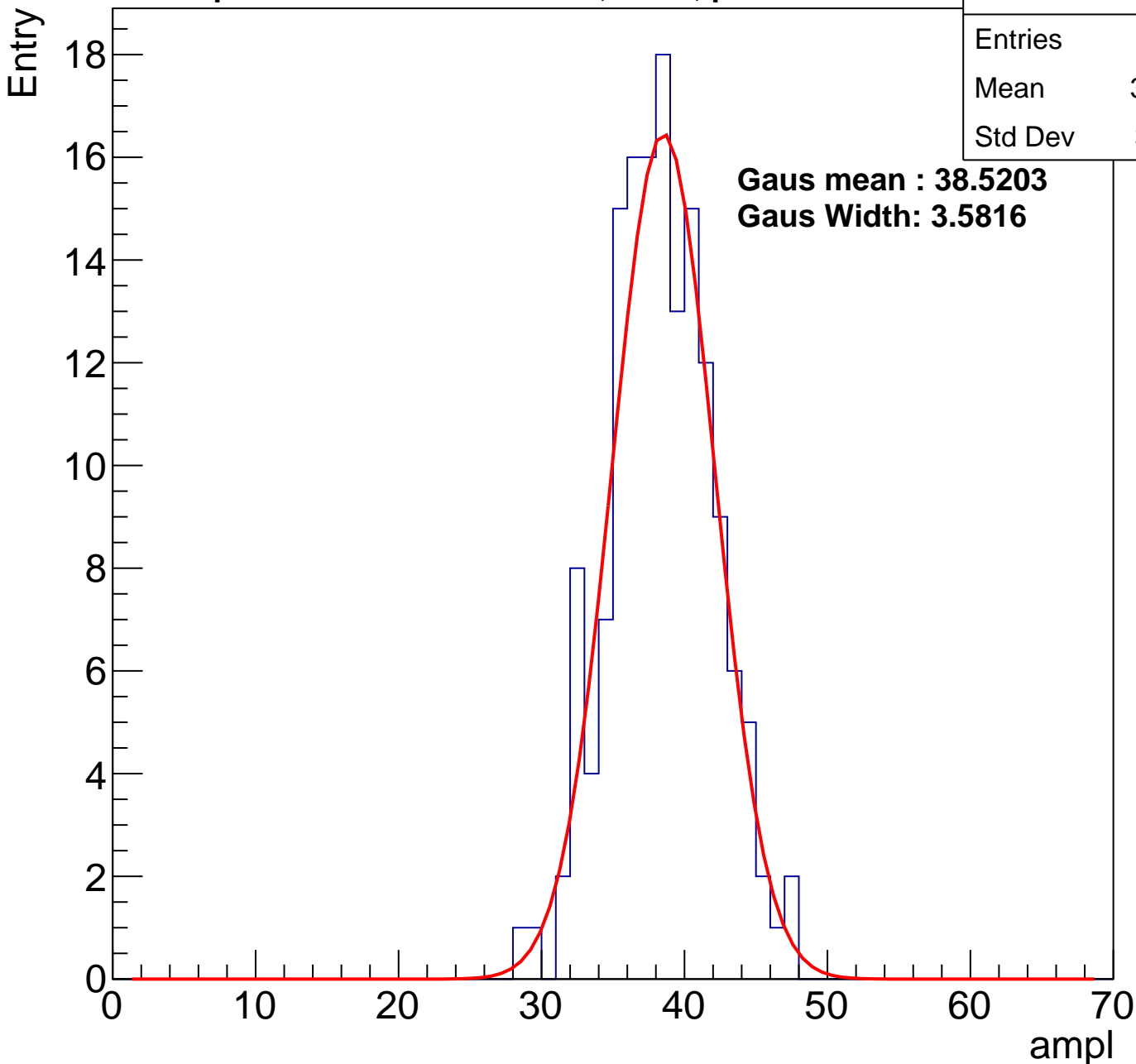
B1L001S, U26-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 37.95 |
| Std Dev | 3.591 |

Gaus mean : 38.5203

Gaus Width: 3.5816



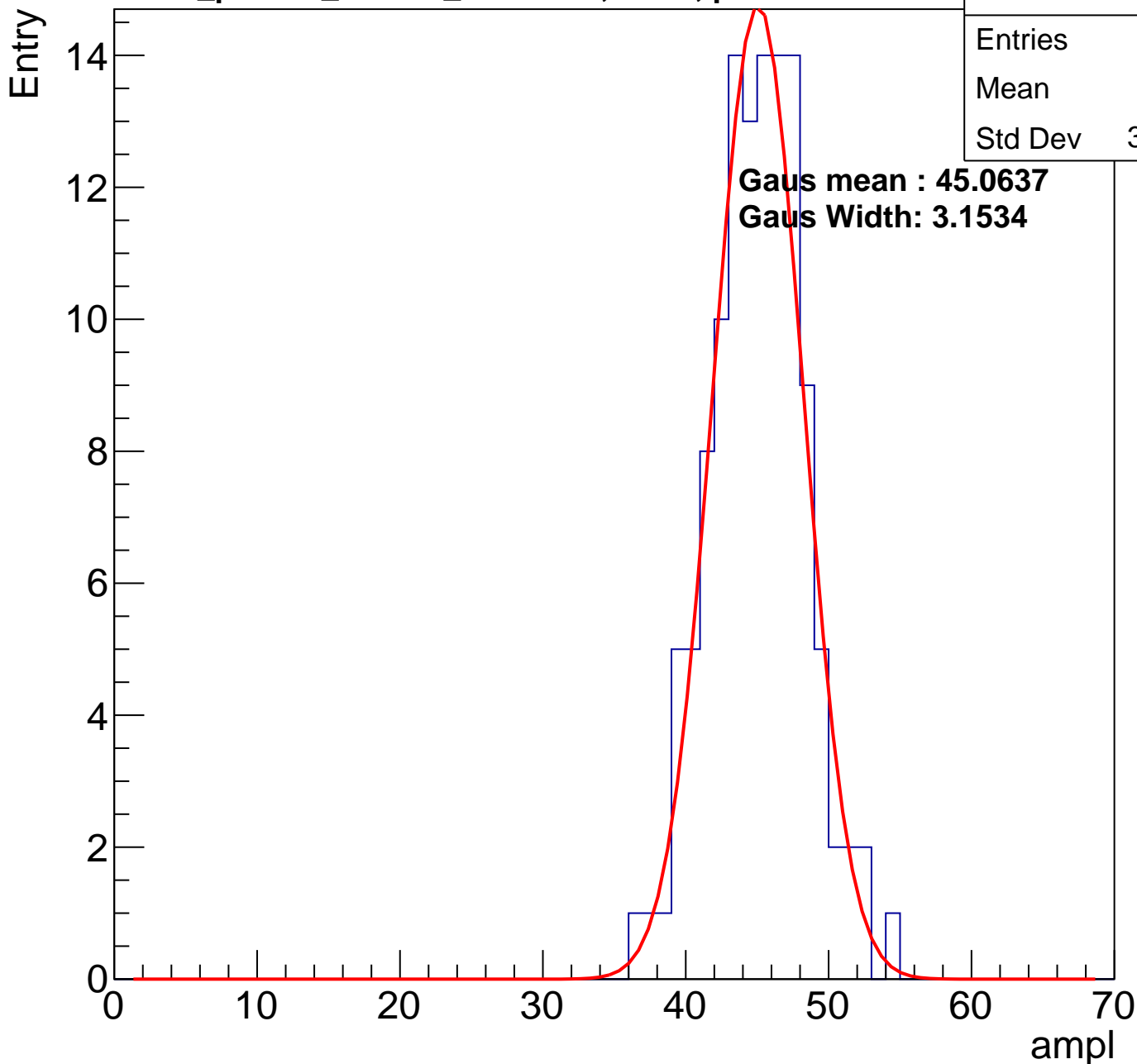
B1L001S, U26-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 44.6 |
| Std Dev | 3.276 |

Gaus mean : 45.0637

Gaus Width: 3.1534

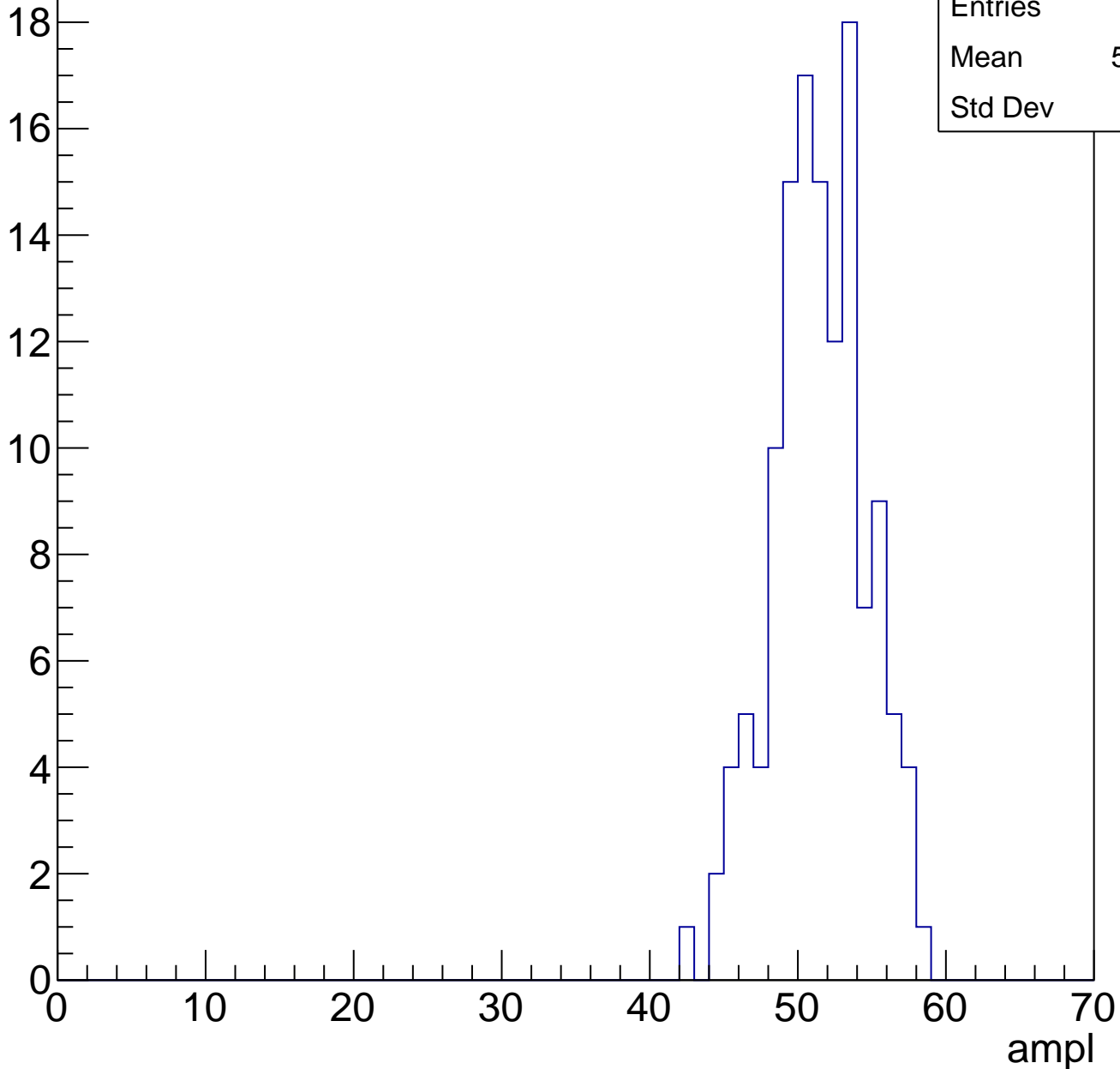


B1L001S, U26-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 50.97 |
| Std Dev | 3.16 |

Entry



B1L001S, U26-ch1, adc4

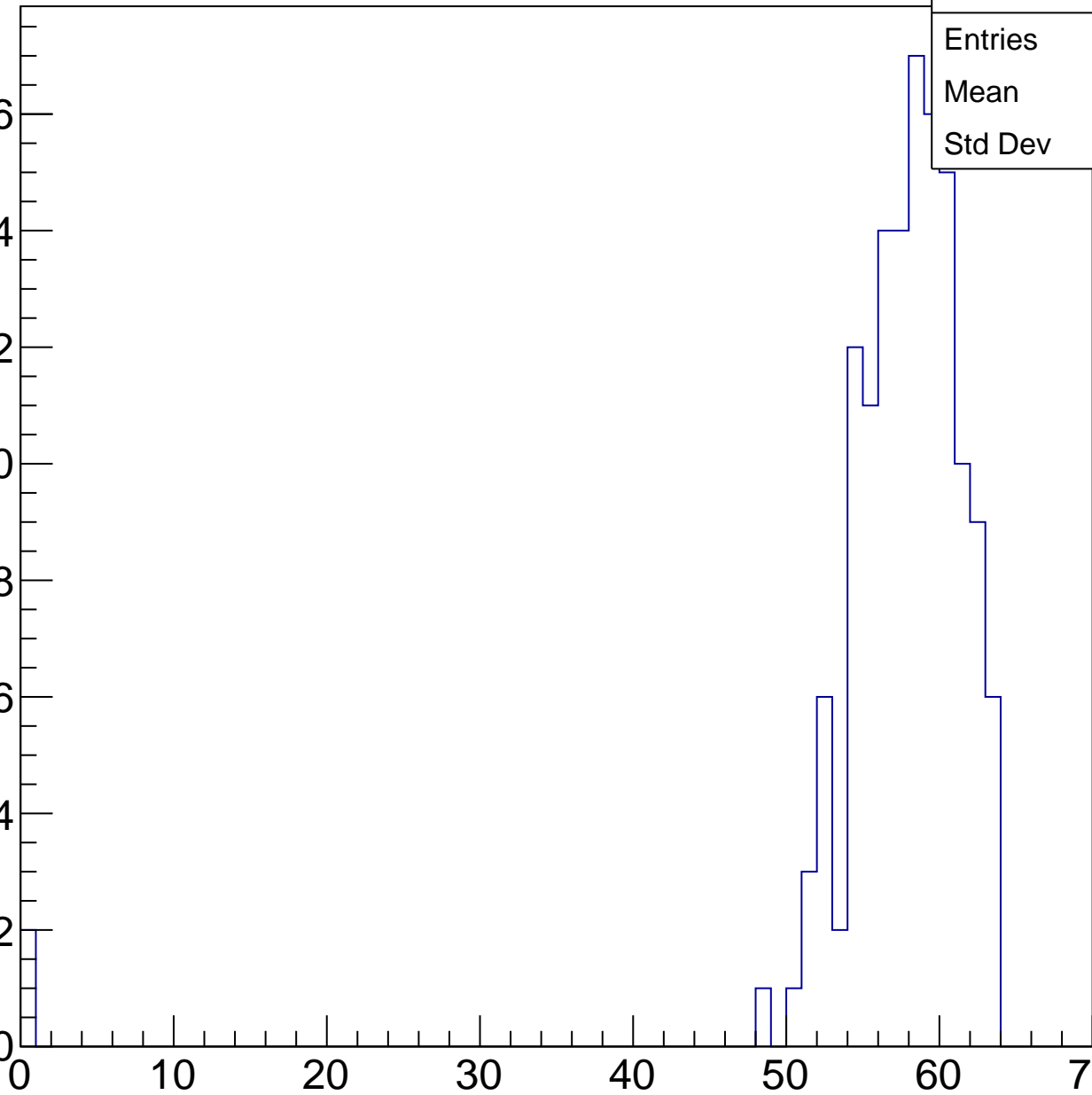
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 139 |
| Mean | 56.69 |
| Std Dev | 7.533 |

ampl



B1L001S, U26-ch1, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

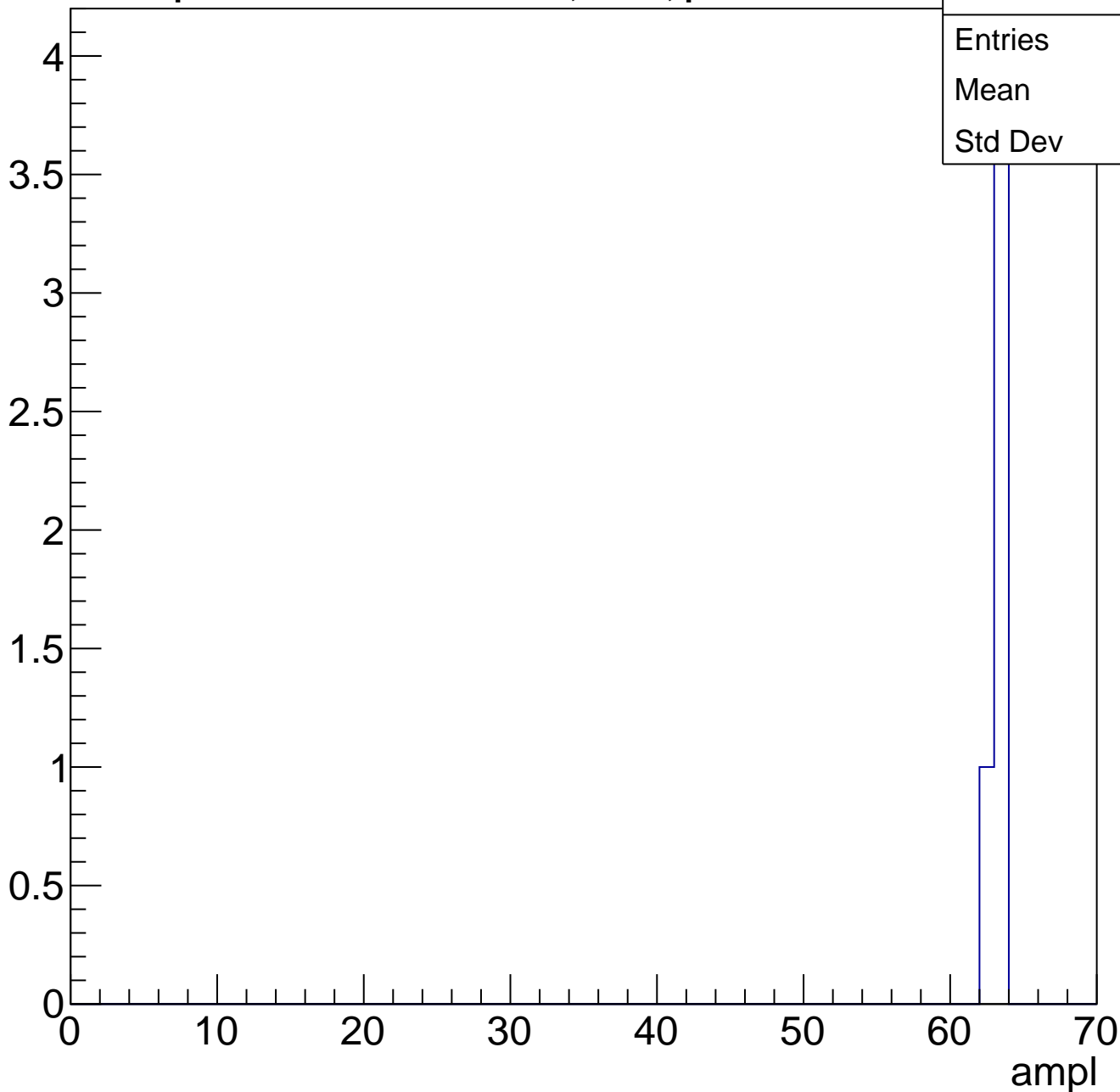
ampl

| | |
|---------|-------|
| Entries | 44 |
| Mean | 60.95 |
| Std Dev | 1.821 |

B1L001S, U26-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 25 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl

2

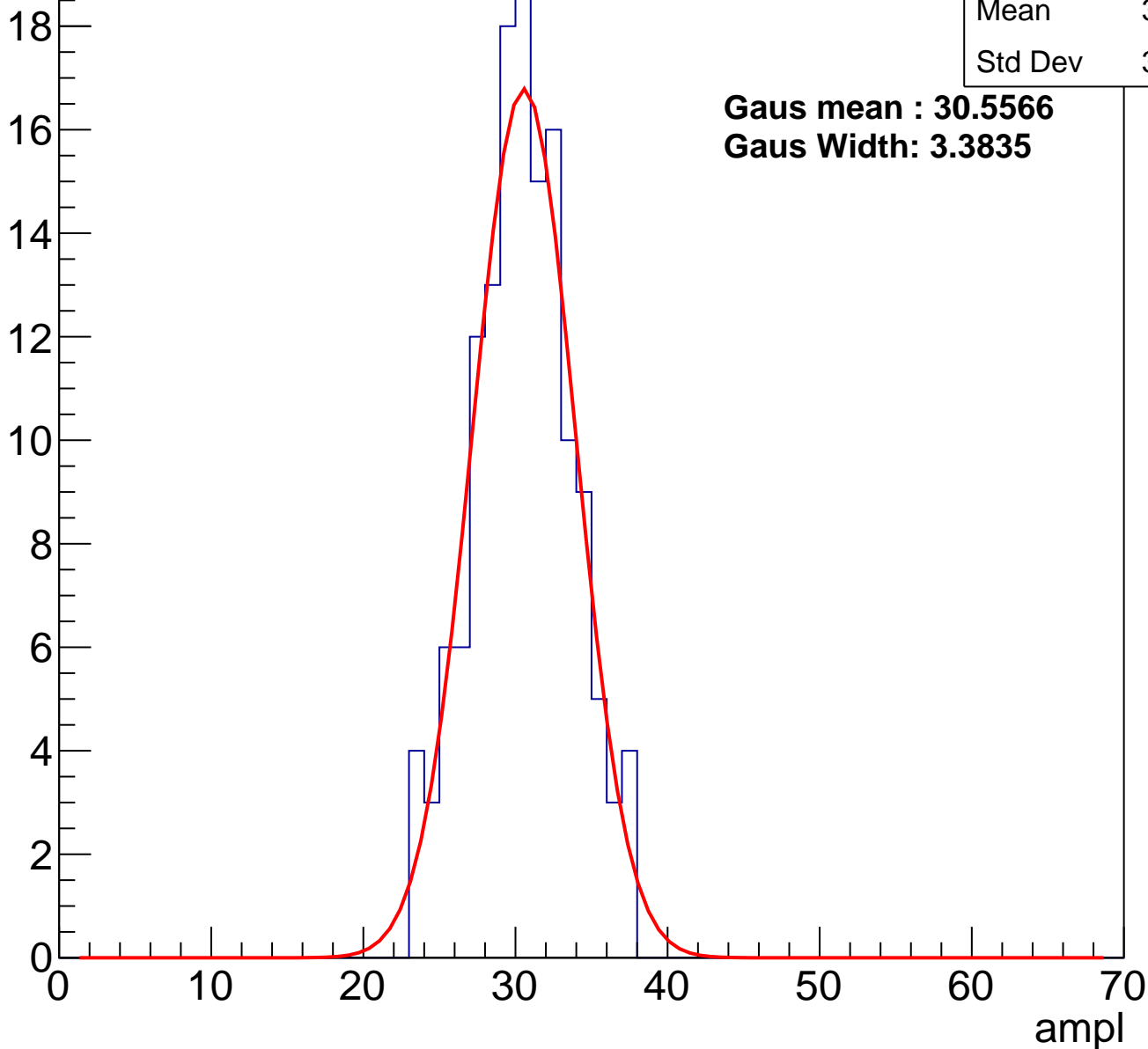
B1L001S, U26-ch2, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 30.03 |
| Std Dev | 3.206 |

Gaus mean : 30.5566
Gaus Width: 3.3835

Entry



B1L001S, U26-ch2, adc1

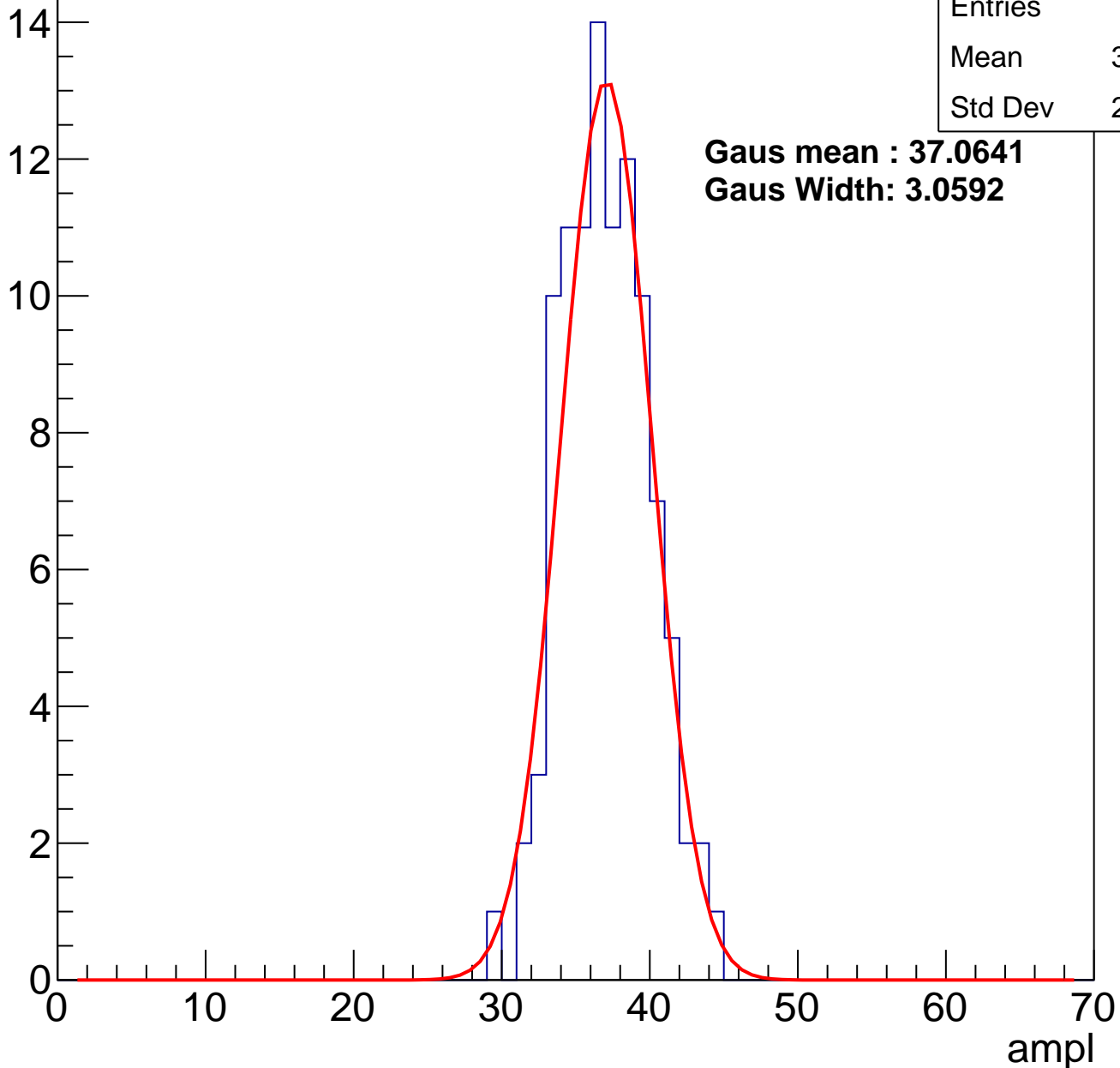
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 102 |
| Mean | 36.59 |
| Std Dev | 2.932 |

Gaus mean : 37.0641

Gaus Width: 3.0592

Entry



B1L001S, U26-ch2, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

42.17

Std Dev

3.346

Gaus mean : 42.5963

Gaus Width: 3.3511

0

10

20

30

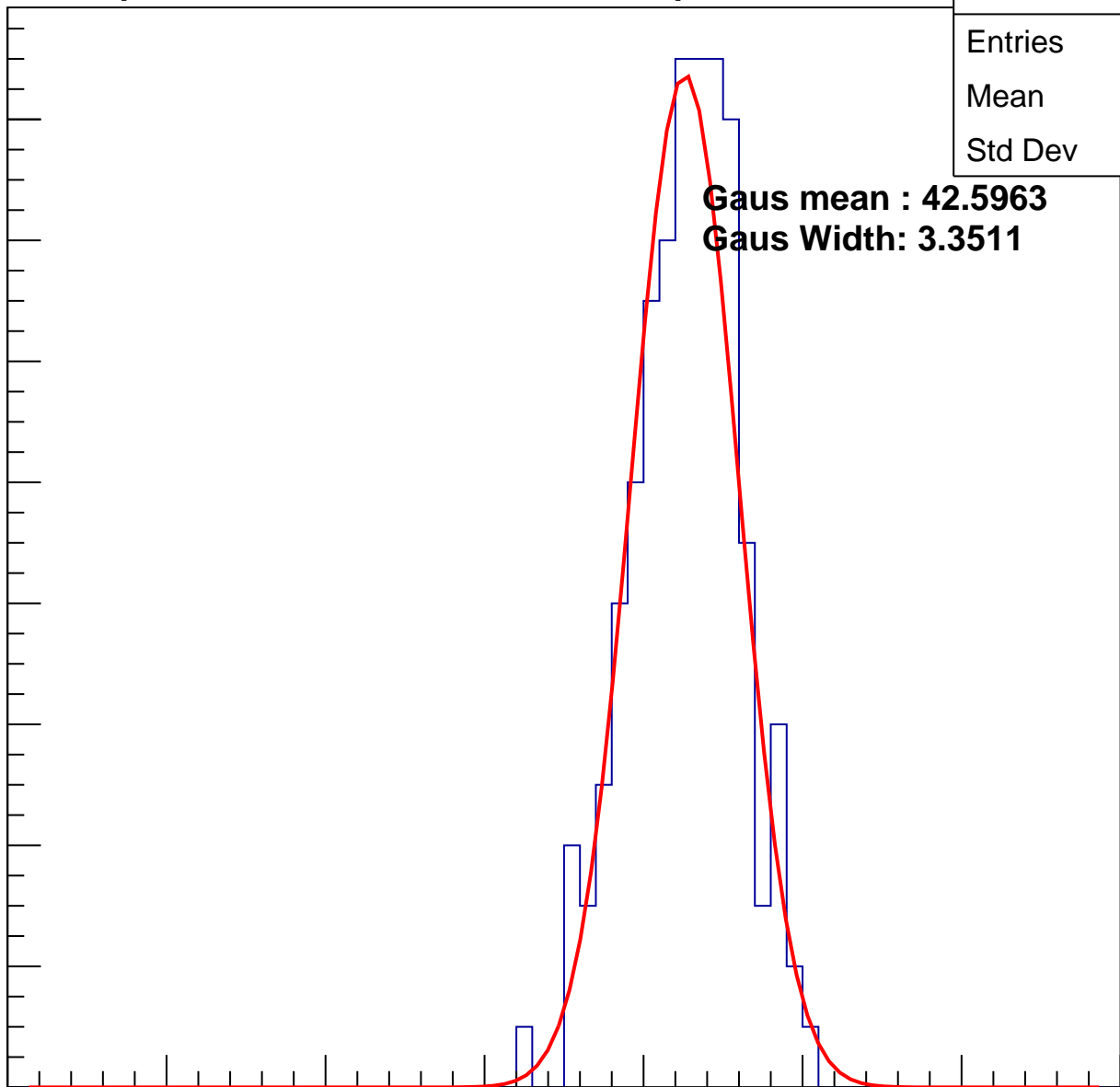
40

50

60

70

ampl



B1L001S, U26-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

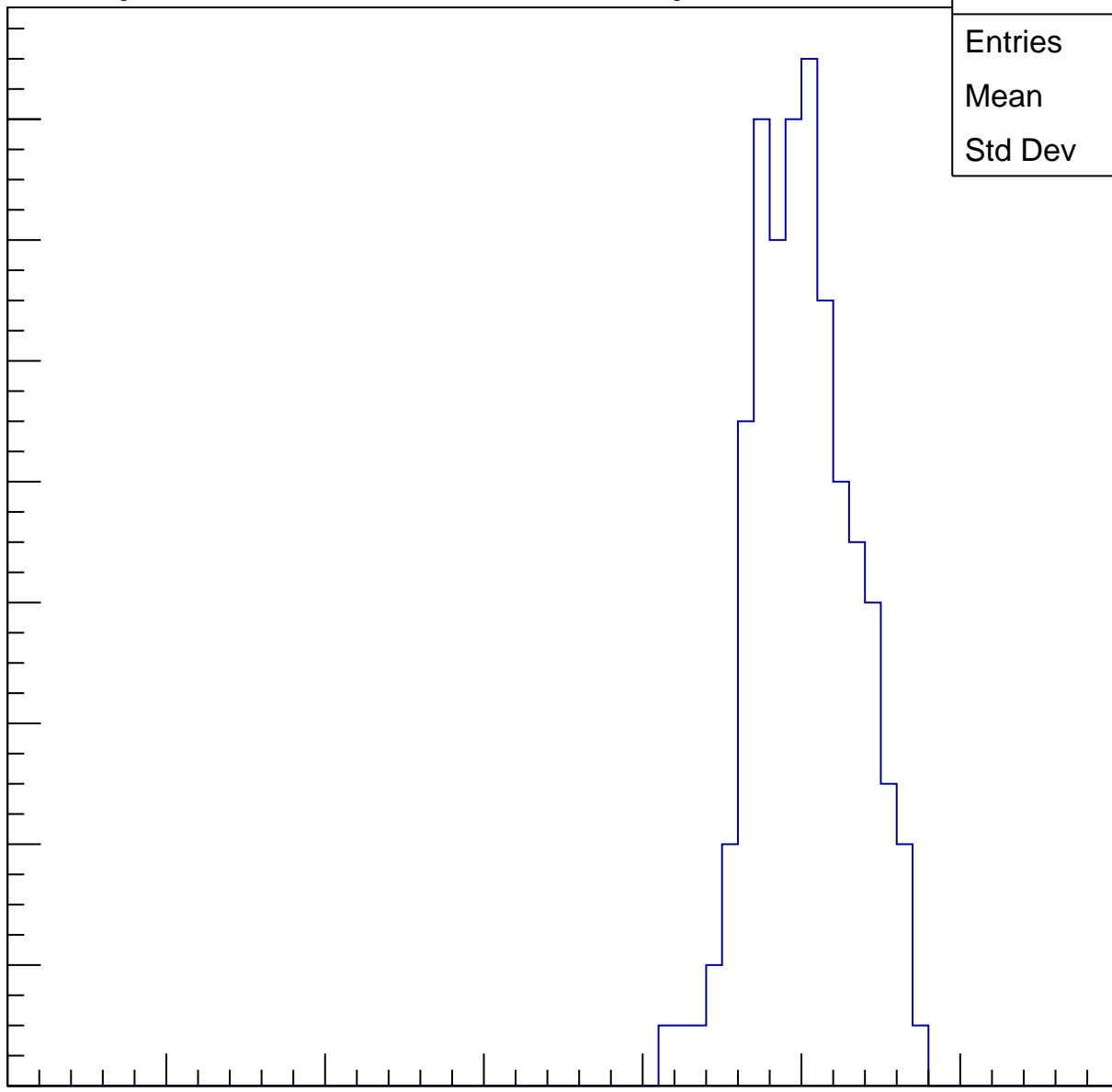
| | |
|---------|-------|
| Entries | 133 |
| Mean | 49.67 |
| Std Dev | 3.154 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

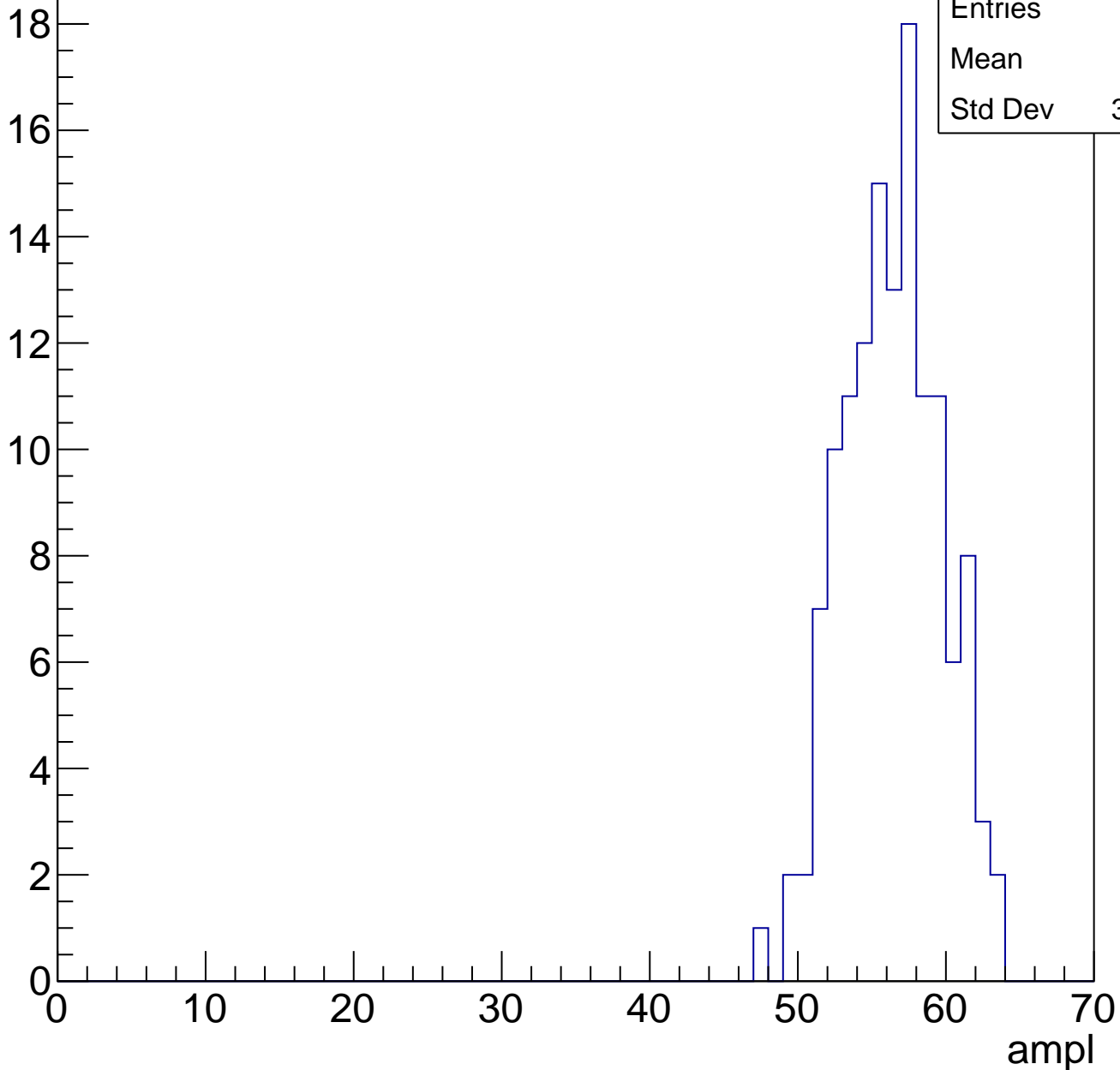


B1L001S, U26-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 55.9 |
| Std Dev | 3.268 |

Entry



B1L001S, U26-ch2, adc5

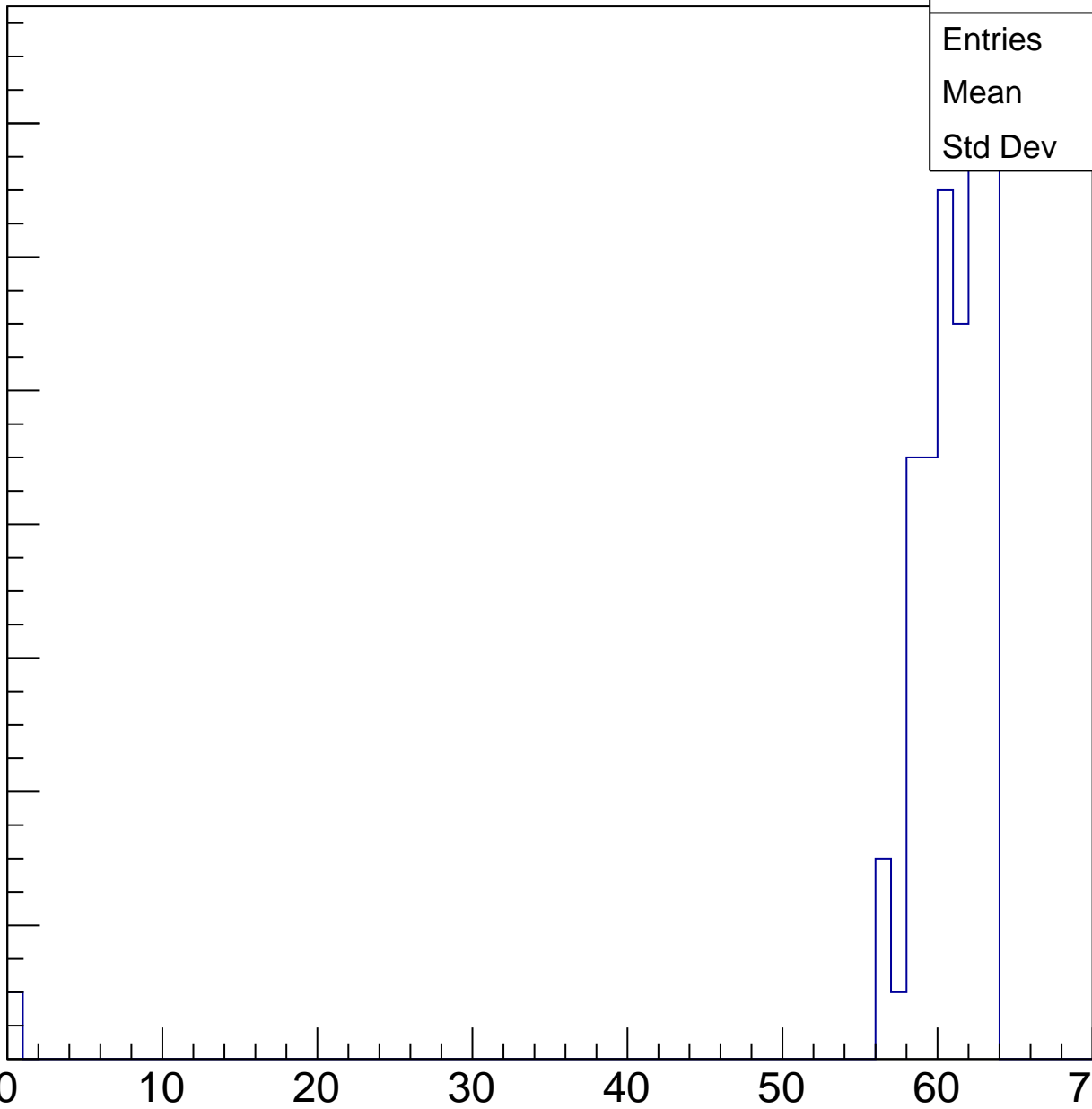
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 59.75 |
| Std Dev | 7.158 |

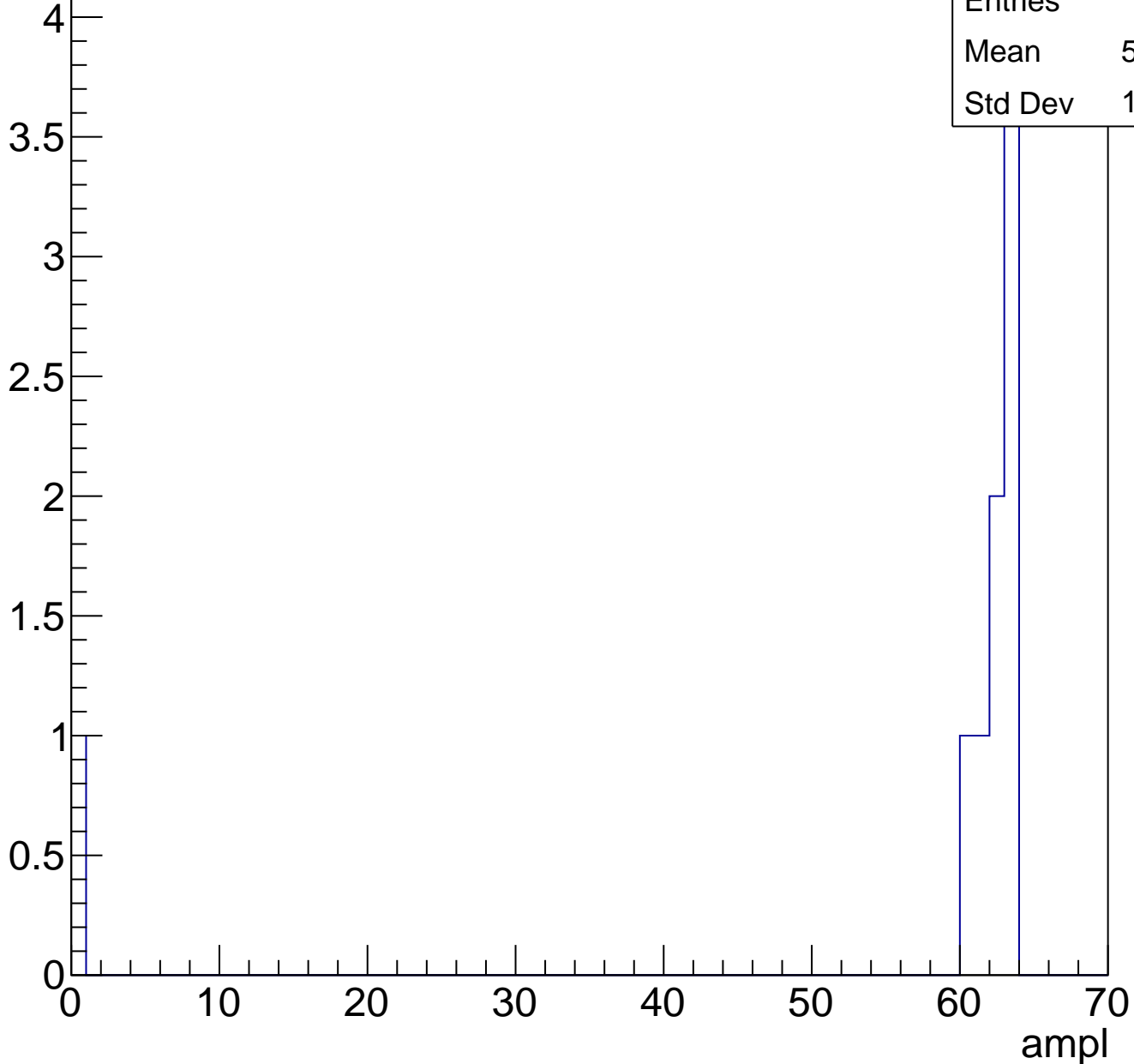
ampl



B1L001S, U26-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch3, adc0

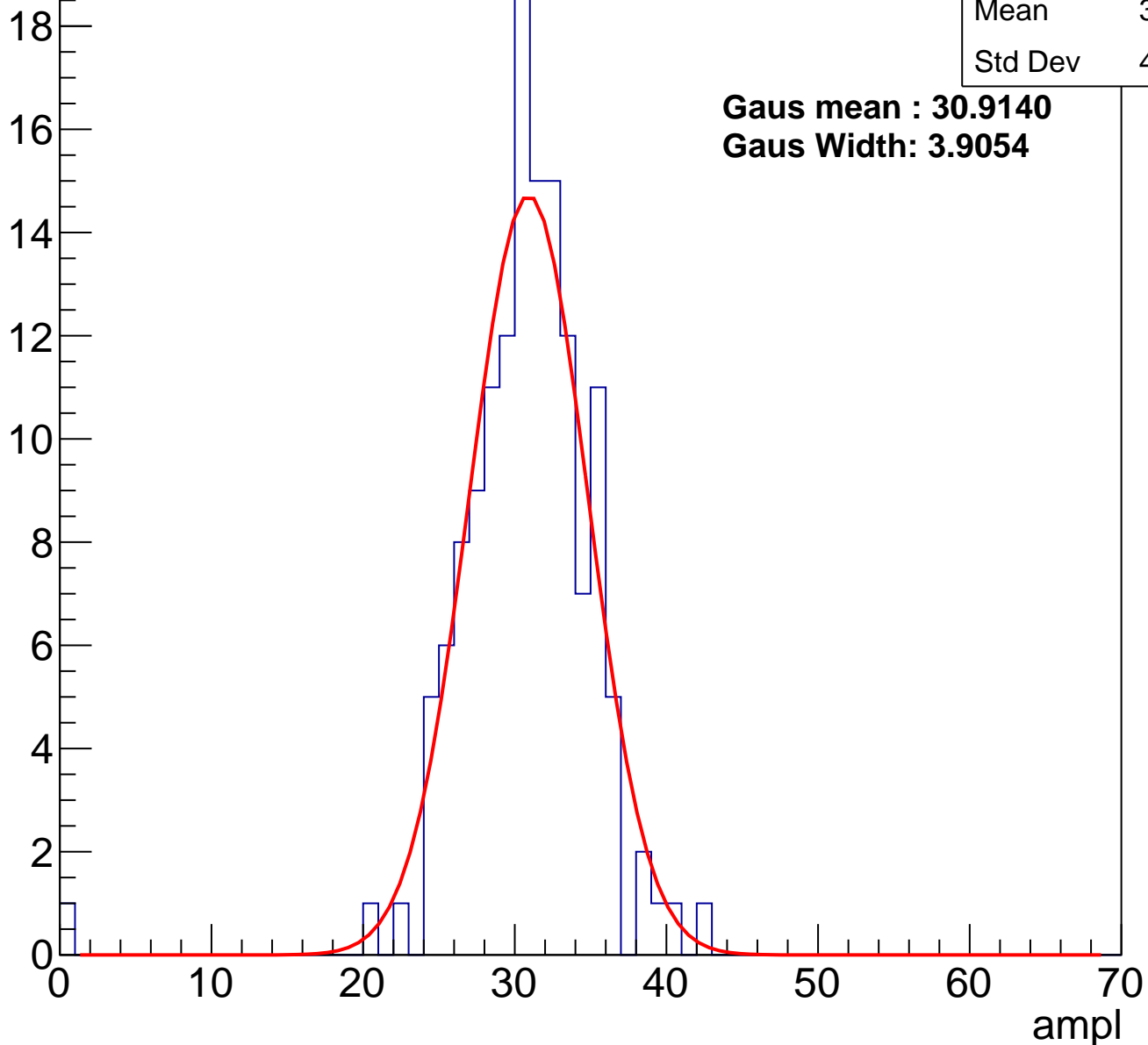
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 30.28 |
| Std Dev | 4.465 |

Gaus mean : 30.9140

Gaus Width: 3.9054

Entry



B1L001S, U26-ch3, adc1

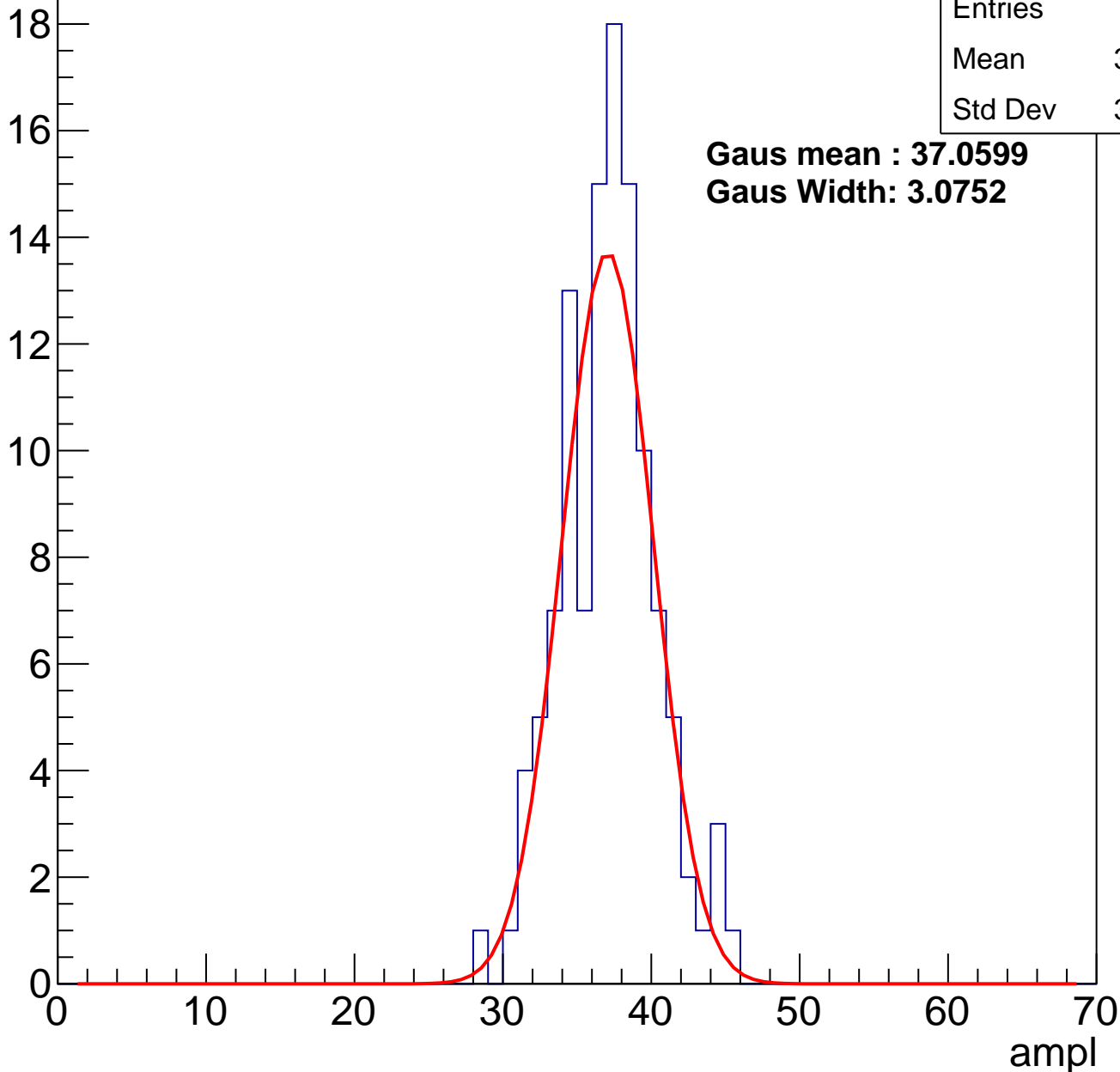
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 36.65 |
| Std Dev | 3.165 |

Gaus mean : 37.0599

Gaus Width: 3.0752

Entry



B1L001S, U26-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

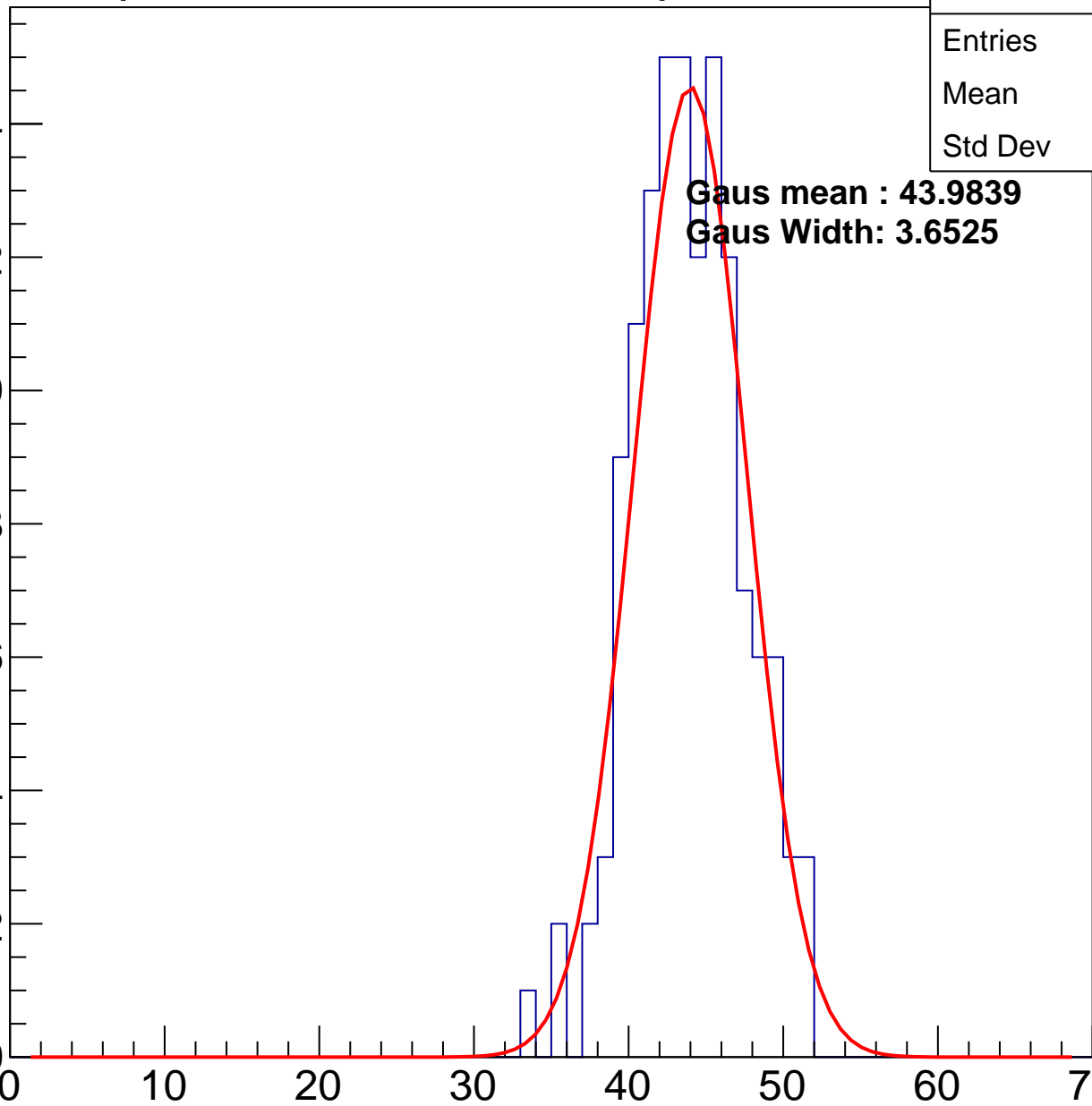
60

ampl

| | |
|---------|-------|
| Entries | 135 |
| Mean | 43.4 |
| Std Dev | 3.507 |

Gaus mean : 43.9839

Gaus Width: 3.6525



B1L001S, U26-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

156

Mean

50.05

Std Dev

3.843

ampl

0

10

20

30

40

50

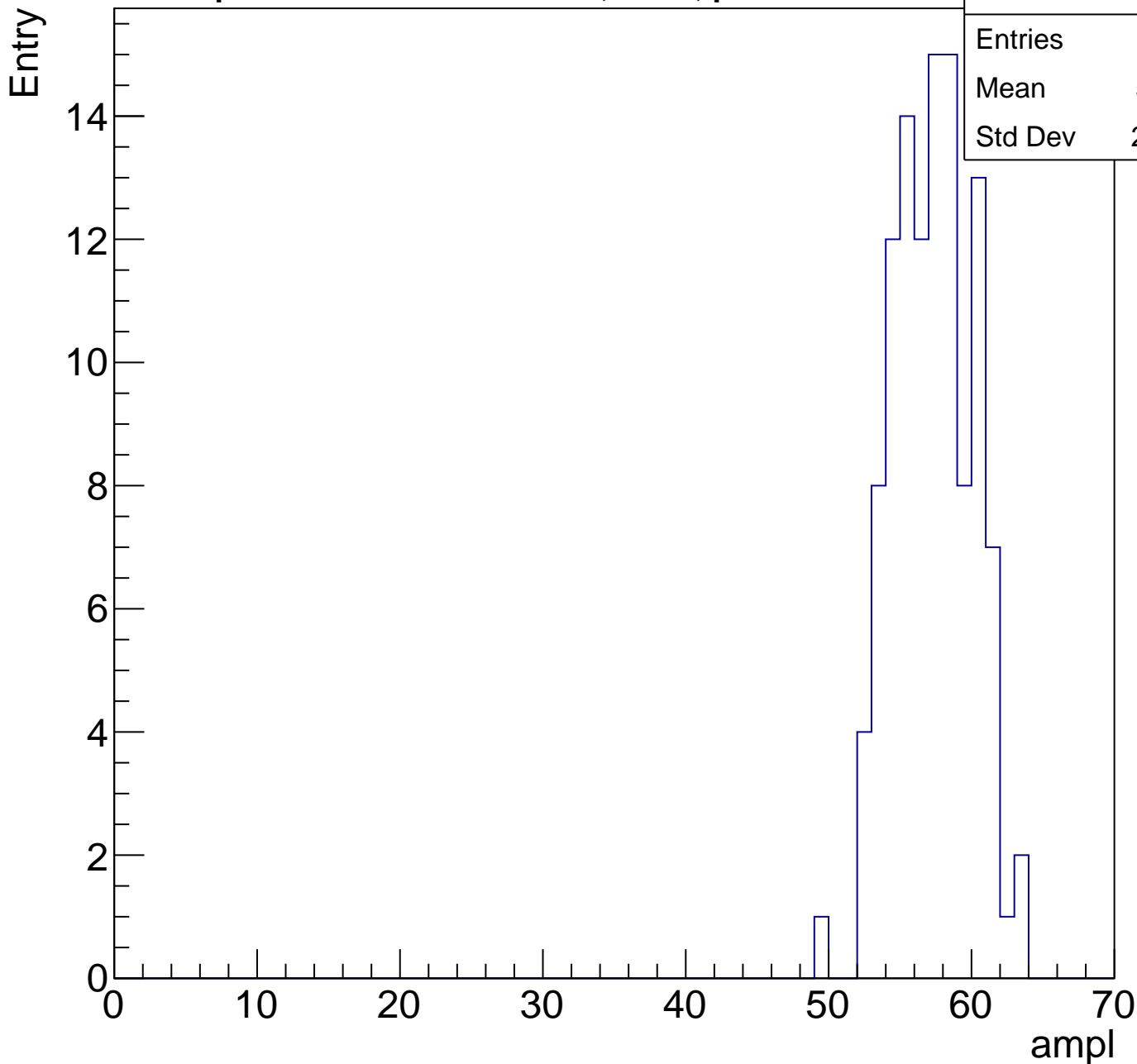
60

70

B1L001S, U26-ch3, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 56.81 |
| Std Dev | 2.734 |



B1L001S, U26-ch3, adc5

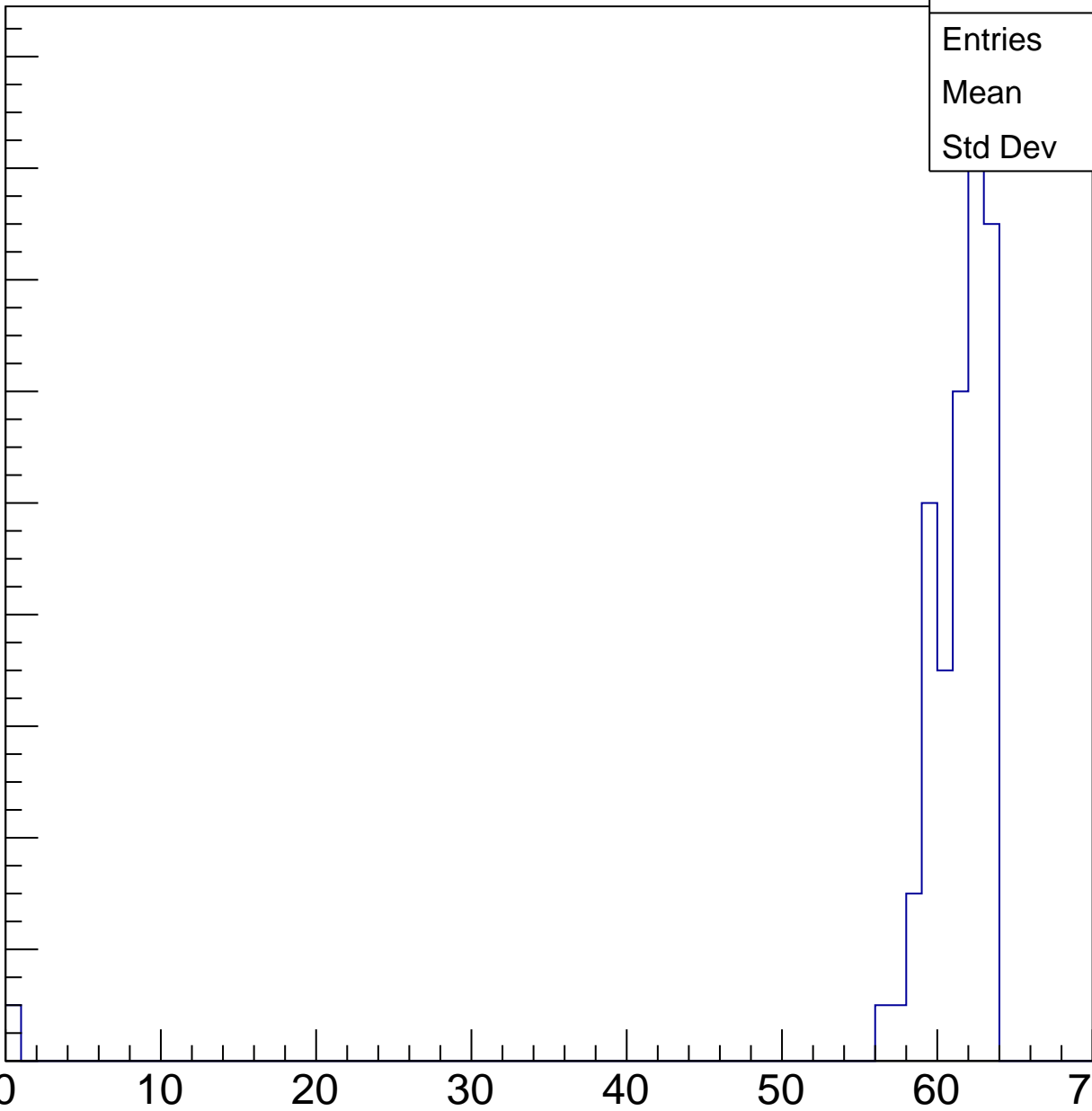
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 68 |
| Mean | 60.15 |
| Std Dev | 7.539 |

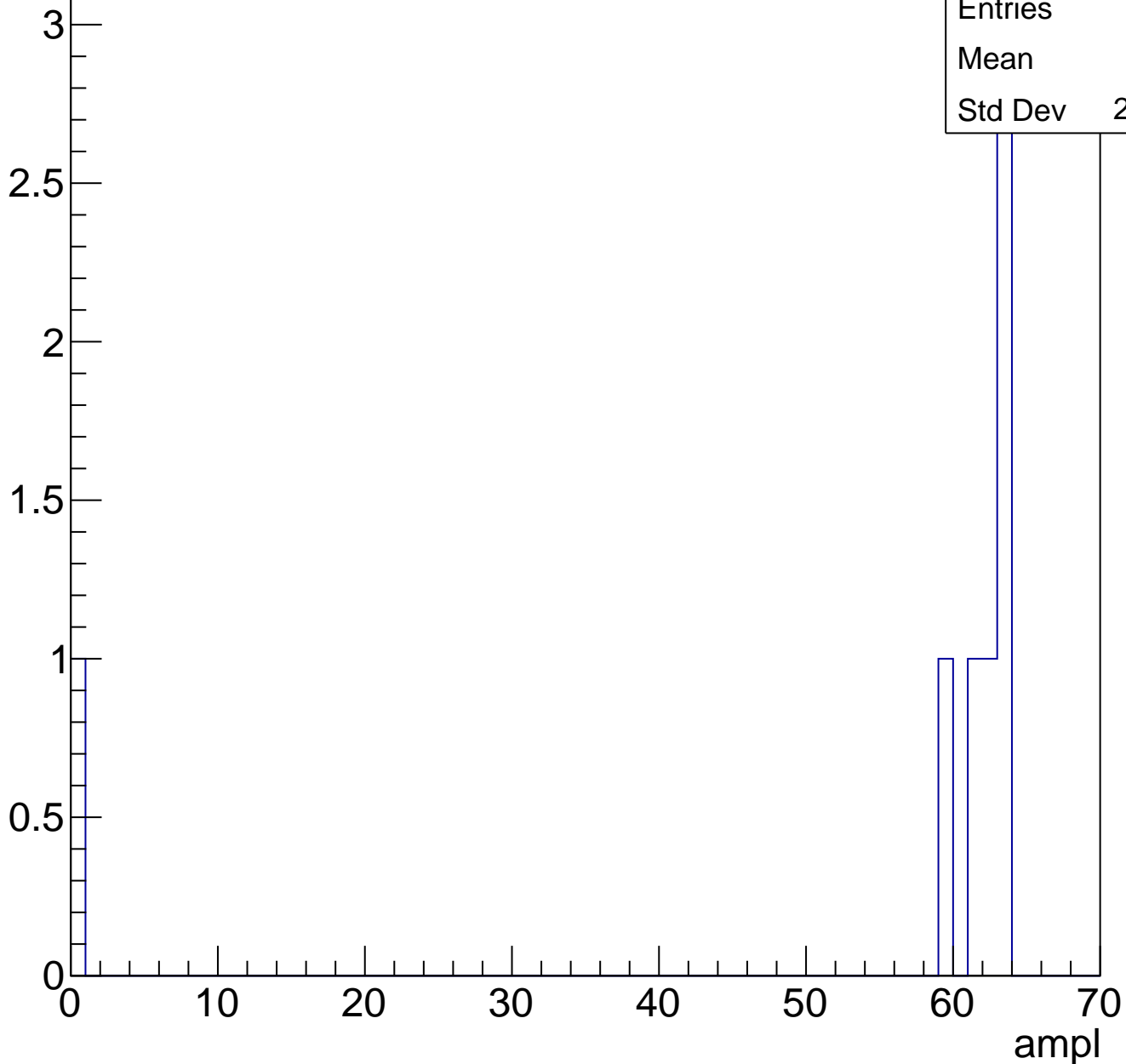
ampl



B1L001S, U26-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

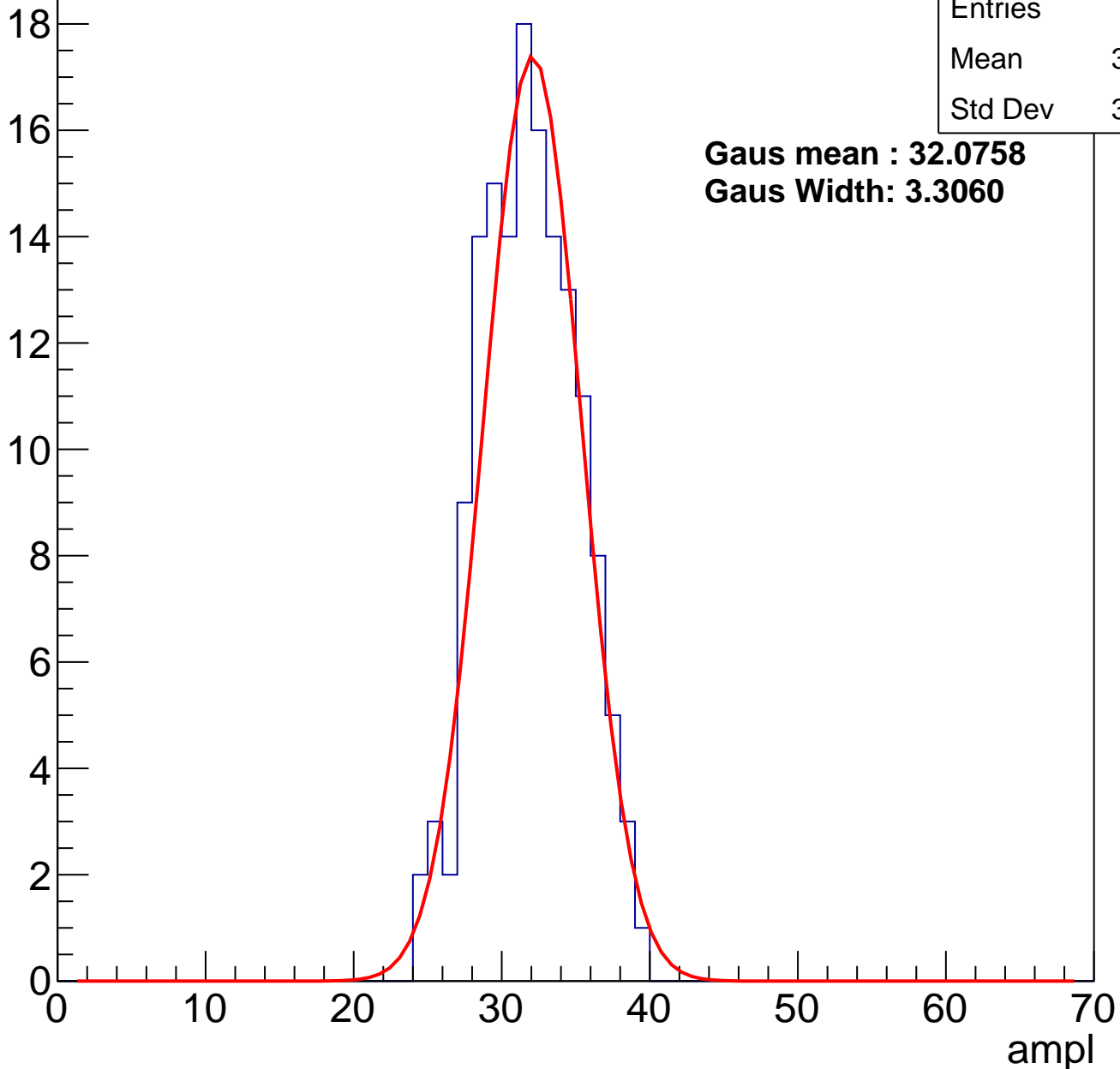
Entry



B1L001S, U26-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch4, adc1

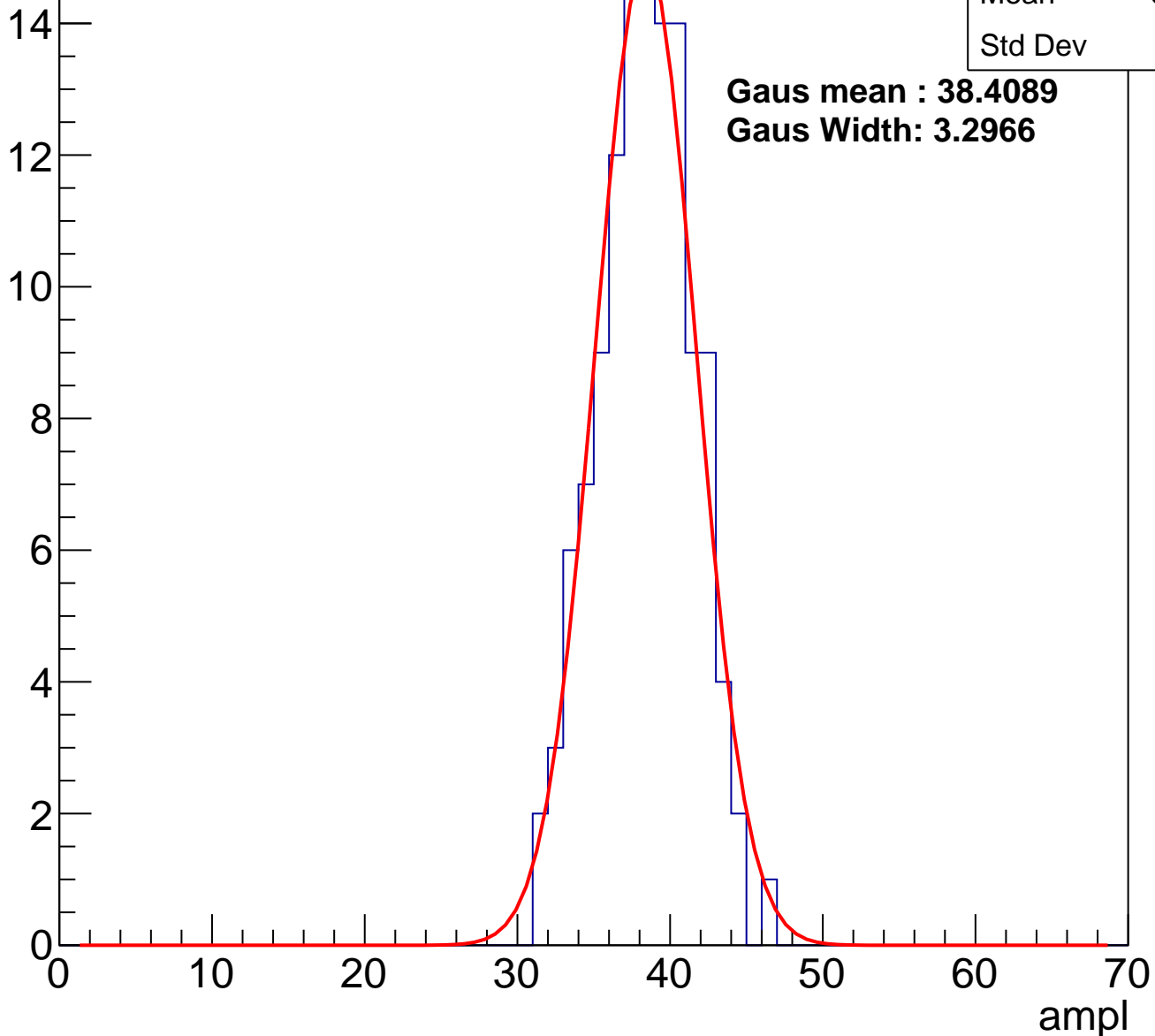
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 37.91 |
| Std Dev | 3.06 |

Gaus mean : 38.4089

Gaus Width: 3.2966

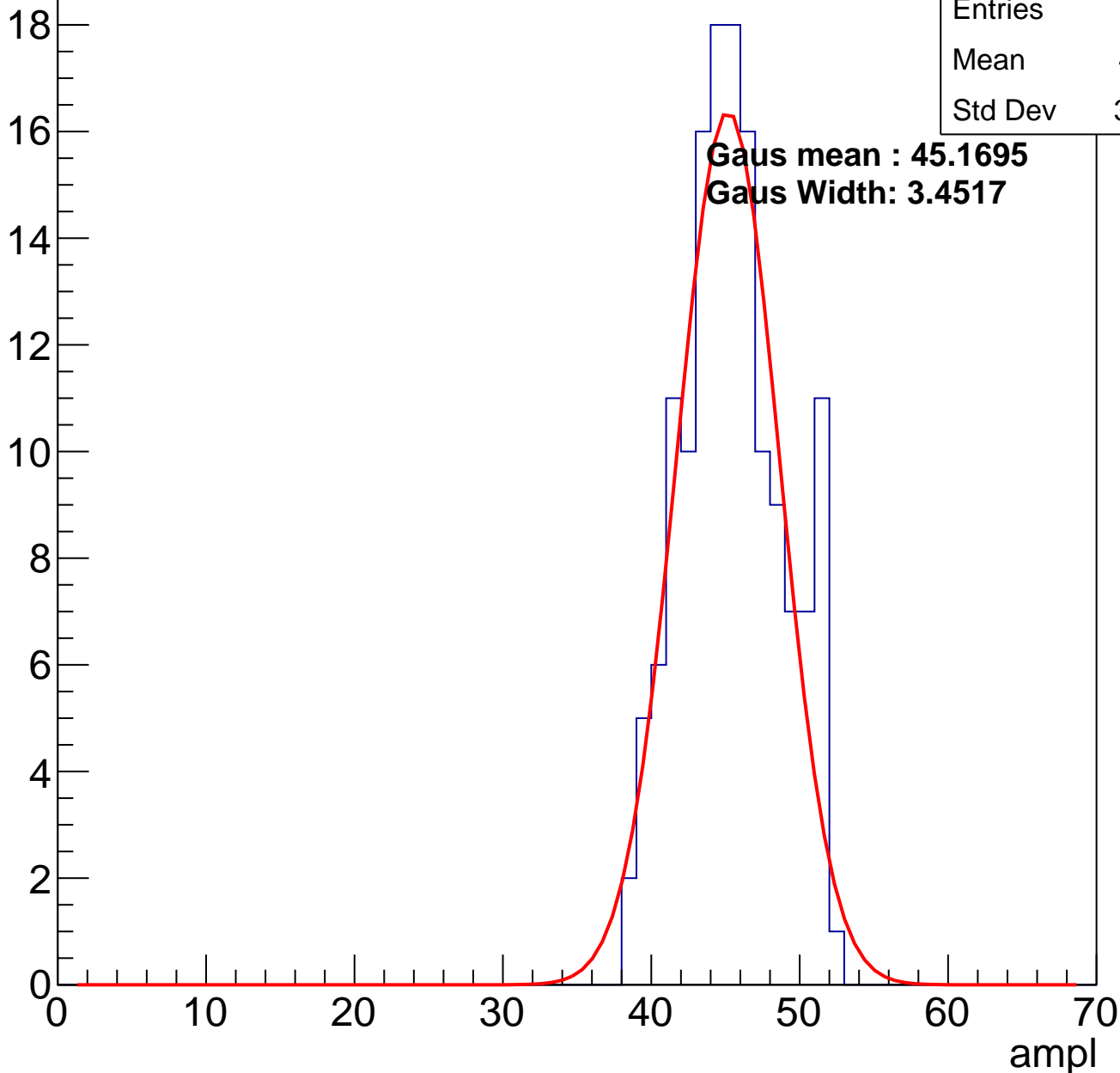
Entry



B1L001S, U26-ch4, adc2

calib_packv5_042523_0143.root, FC#2, port C2

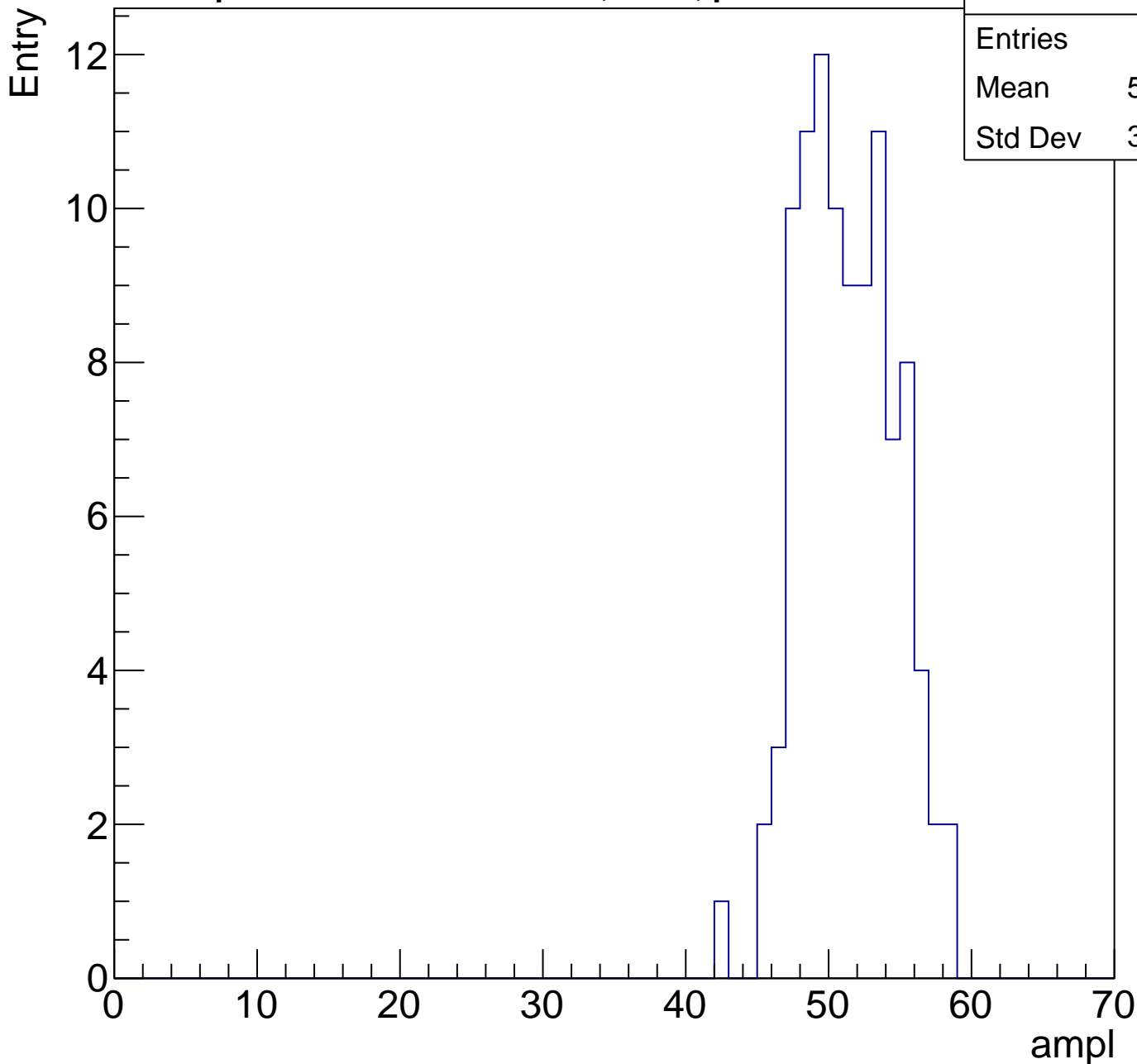
Entry



B1L001S, U26-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 101 |
| Mean | 50.87 |
| Std Dev | 3.236 |



B1L001S, U26-ch4, adc4

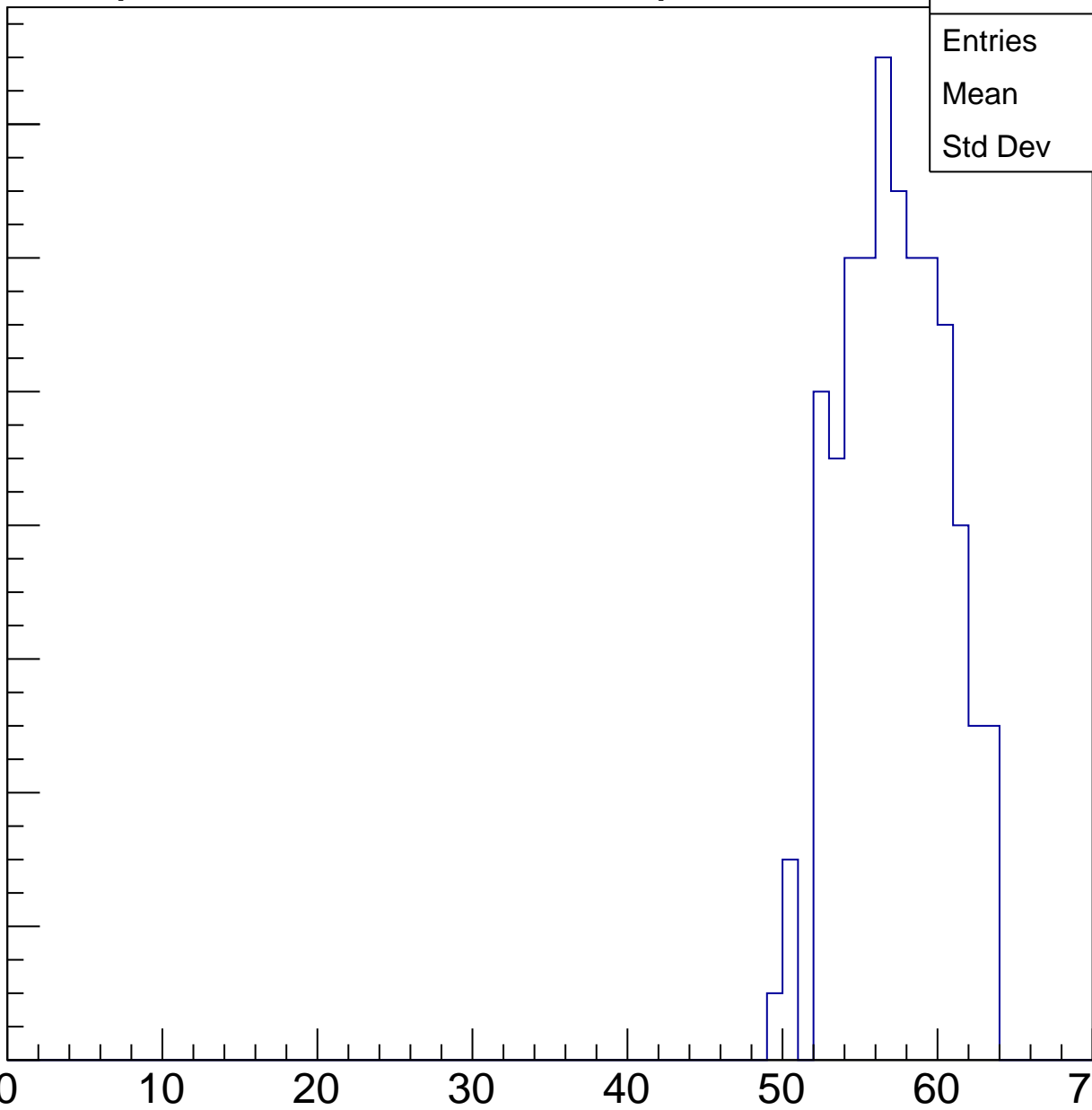
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 128 |
| Mean | 56.73 |
| Std Dev | 3.258 |

ampl



B1L001S, U26-ch4, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

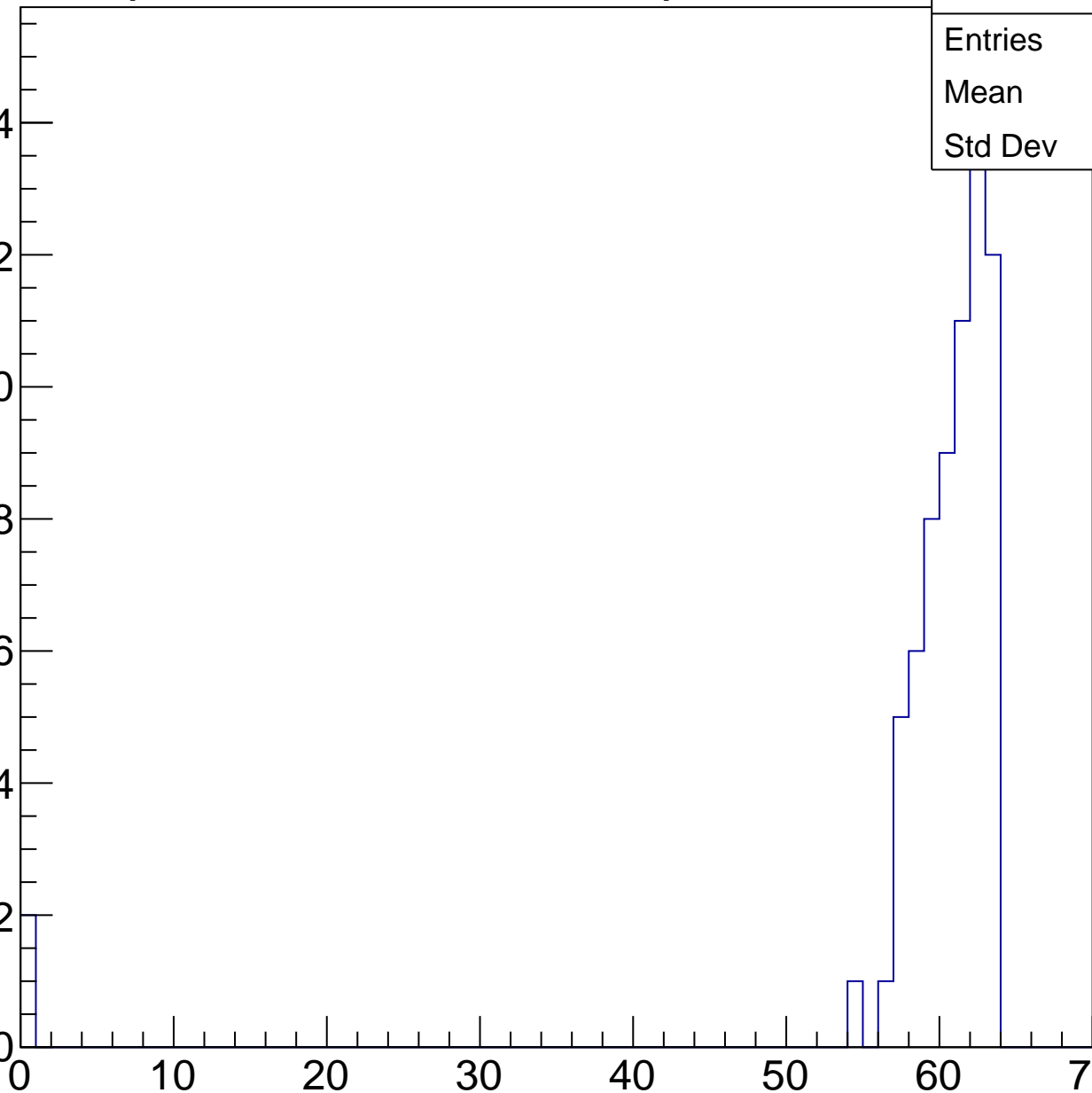
4

2

0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.74 |
| Std Dev | 10.28 |

ampl



B1L001S, U26-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

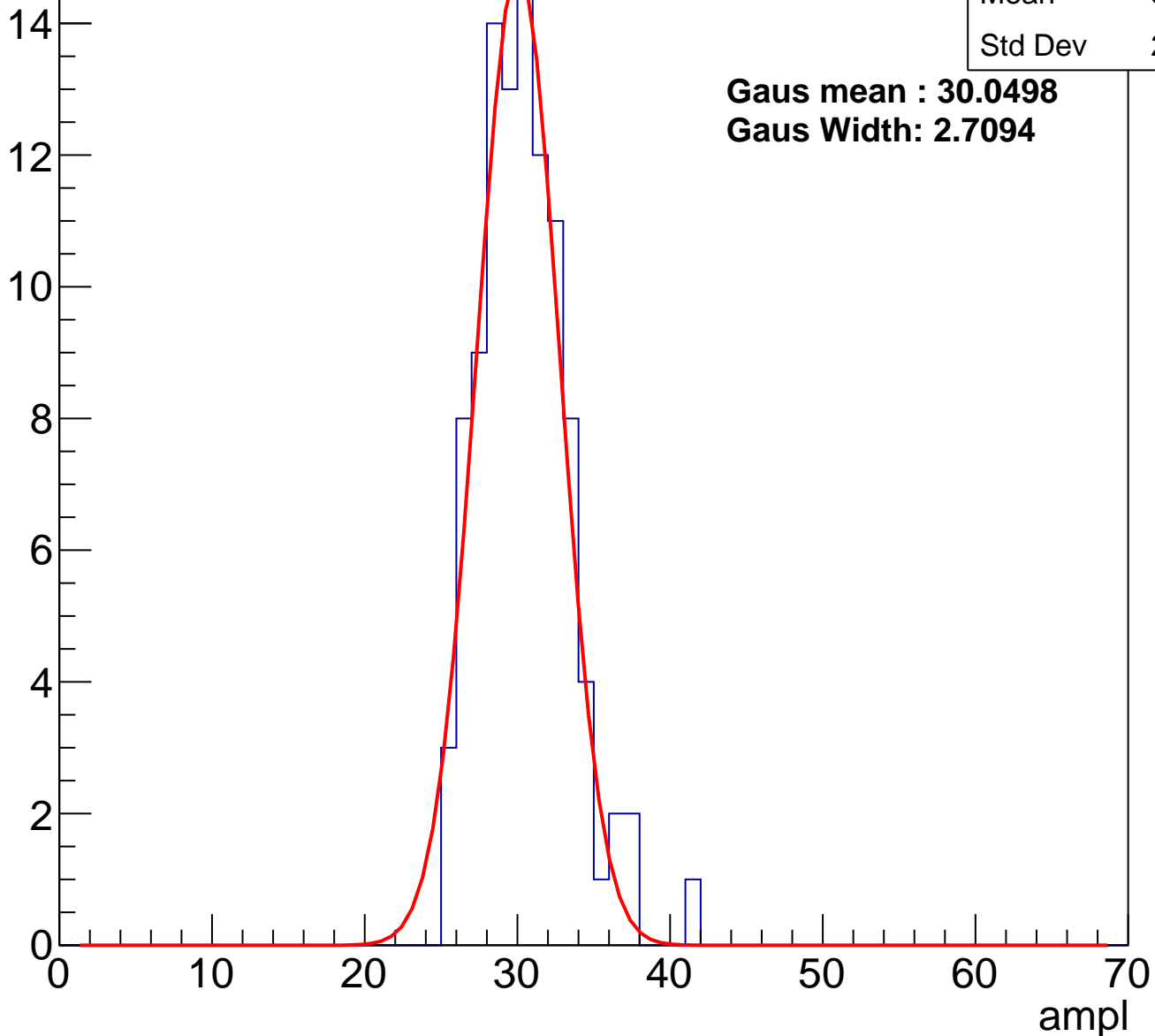
Entry



B1L001S, U26-ch5, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch5, adc1

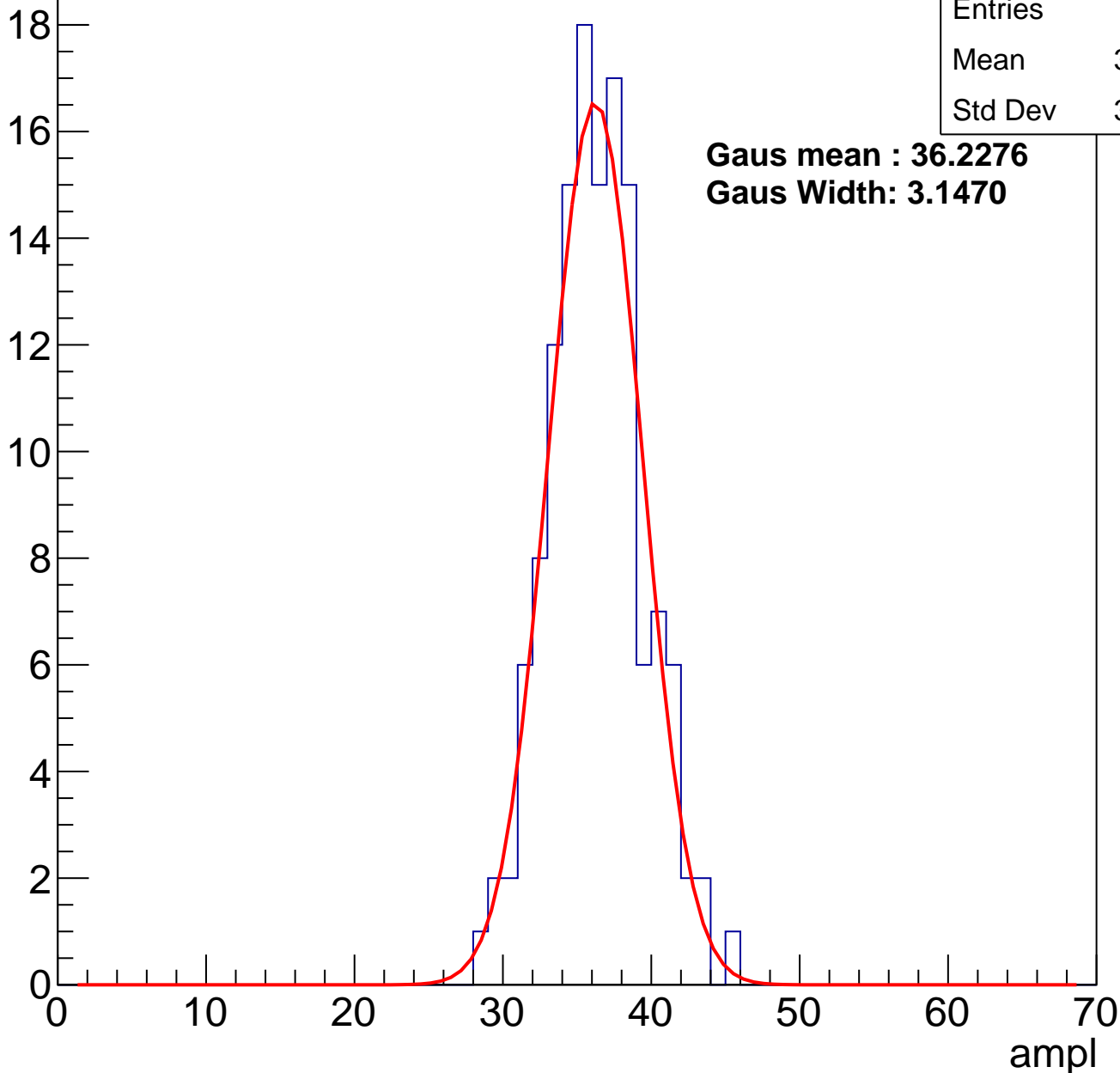
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 35.84 |
| Std Dev | 3.149 |

Gaus mean : 36.2276

Gaus Width: 3.1470

Entry



B1L001S, U26-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

157

Mean

43.2

Std Dev

3.598

Gaus mean : 43.7770

Gaus Width: 3.2526

0

10

20

30

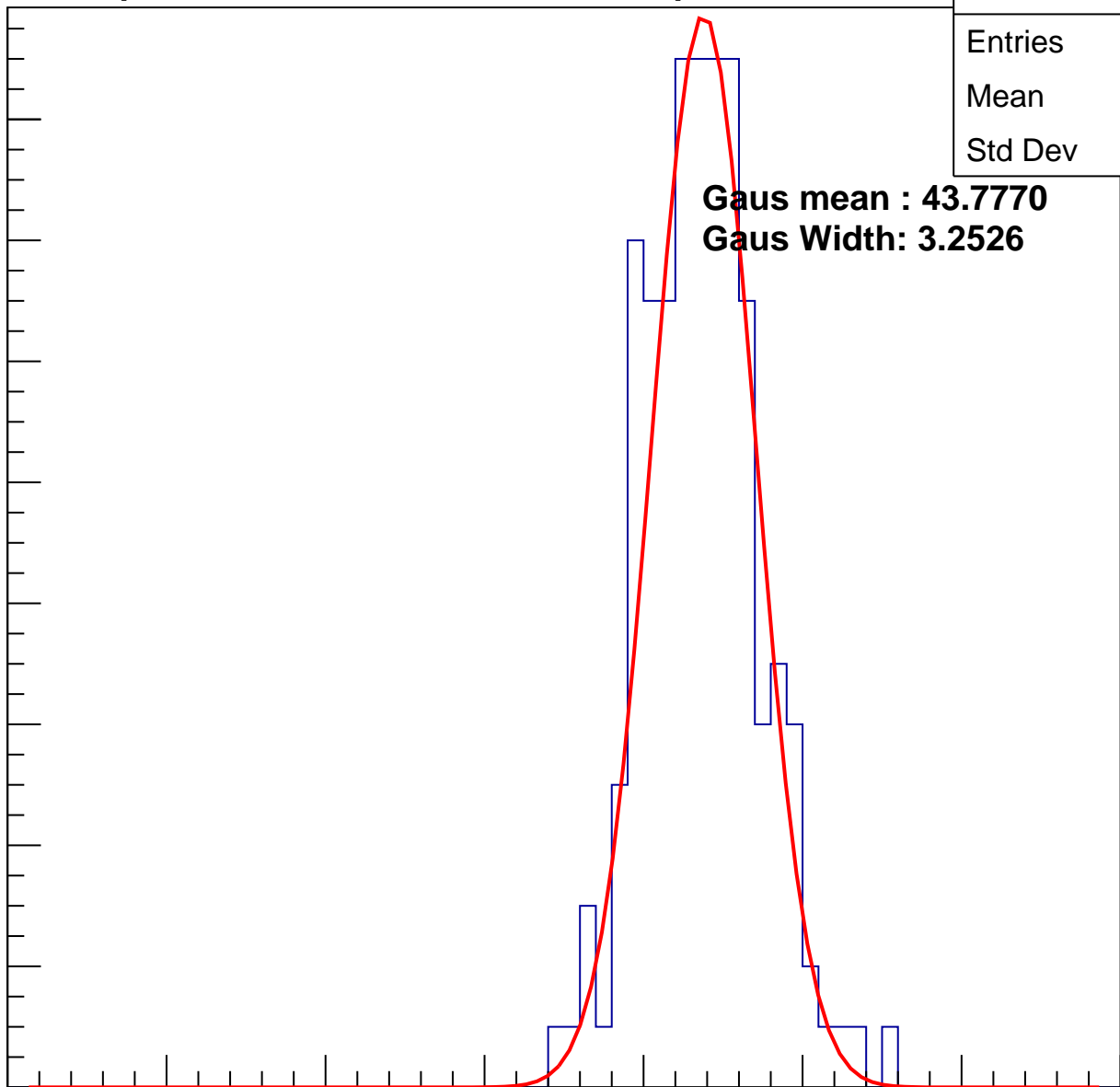
40

50

60

70

ampl



B1L001S, U26-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

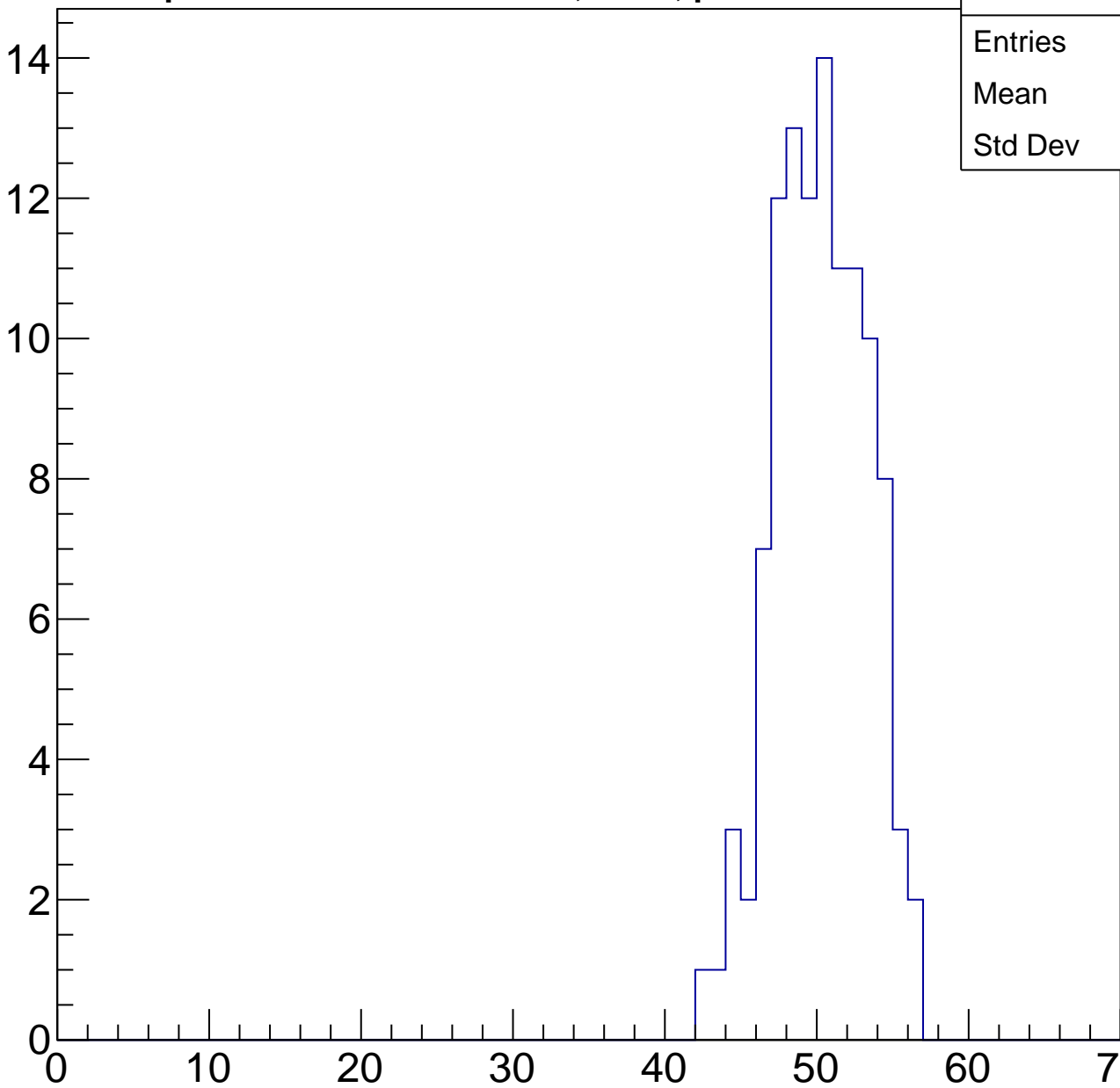
| | |
|---------|-------|
| Entries | 110 |
| Mean | 49.79 |
| Std Dev | 2.97 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch5, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

143

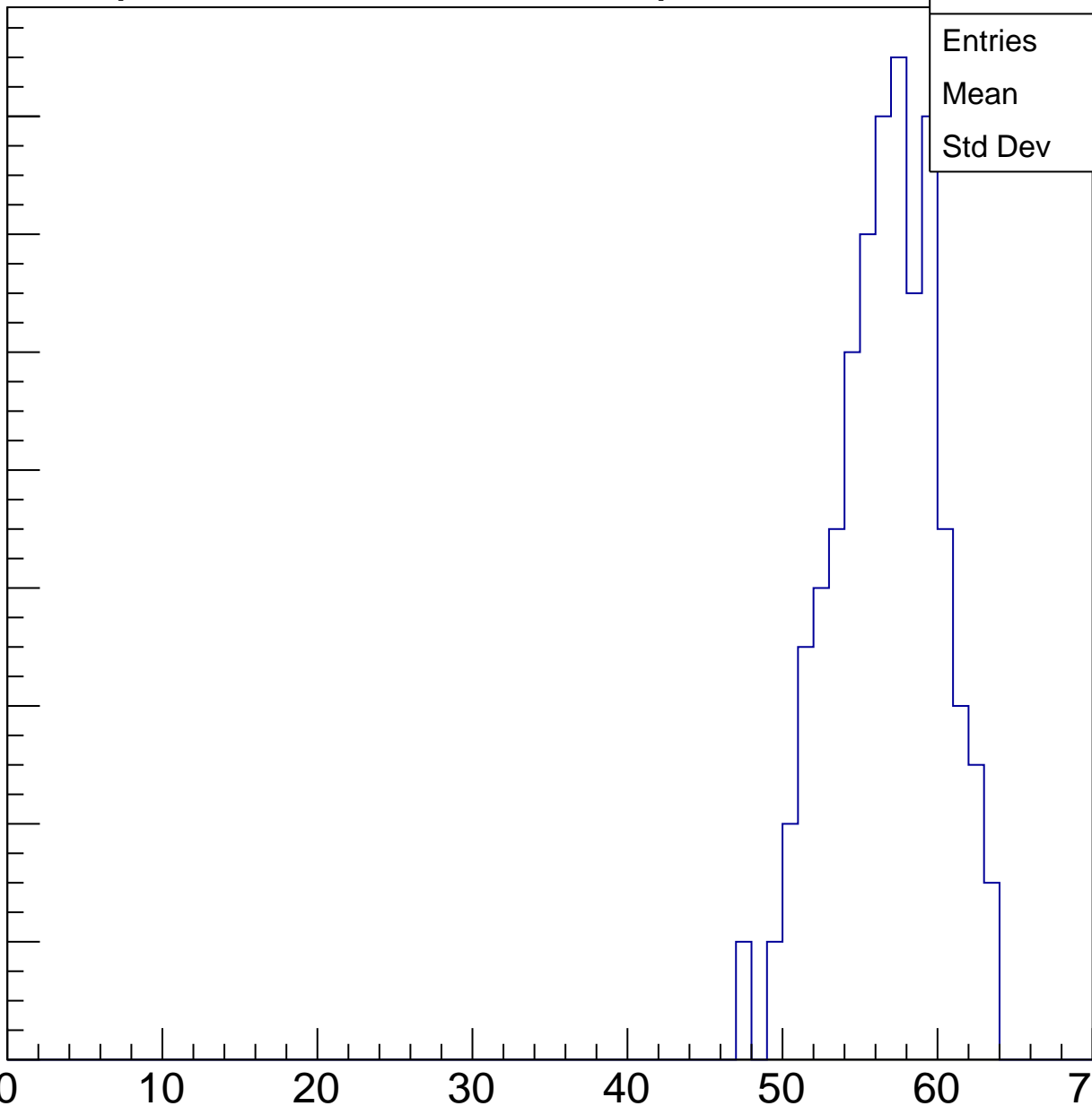
Mean

56.14

Std Dev

3.429

ampl



B1L001S, U26-ch5, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

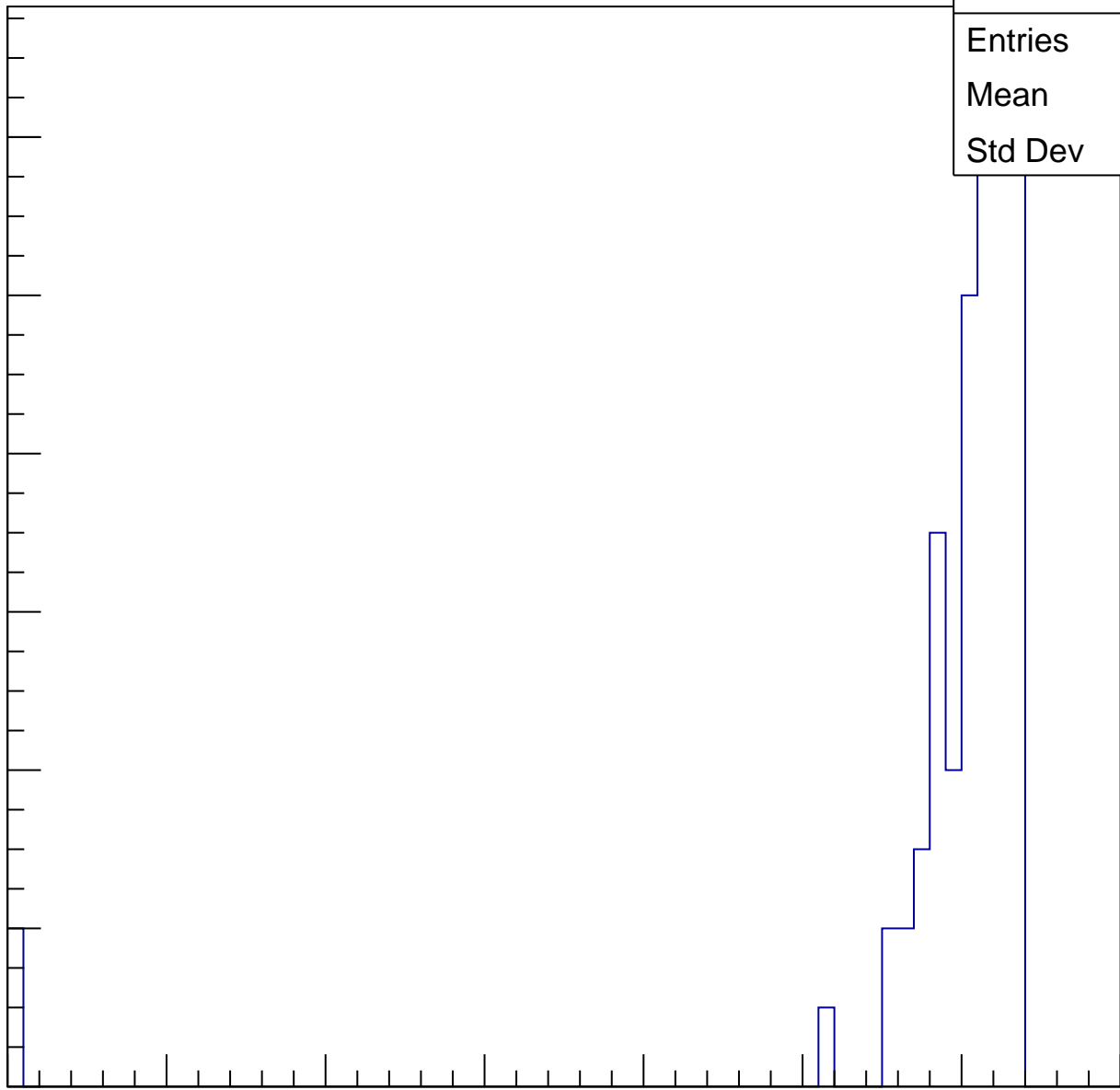
2

0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.63 |
| Std Dev | 10.33 |

ampl

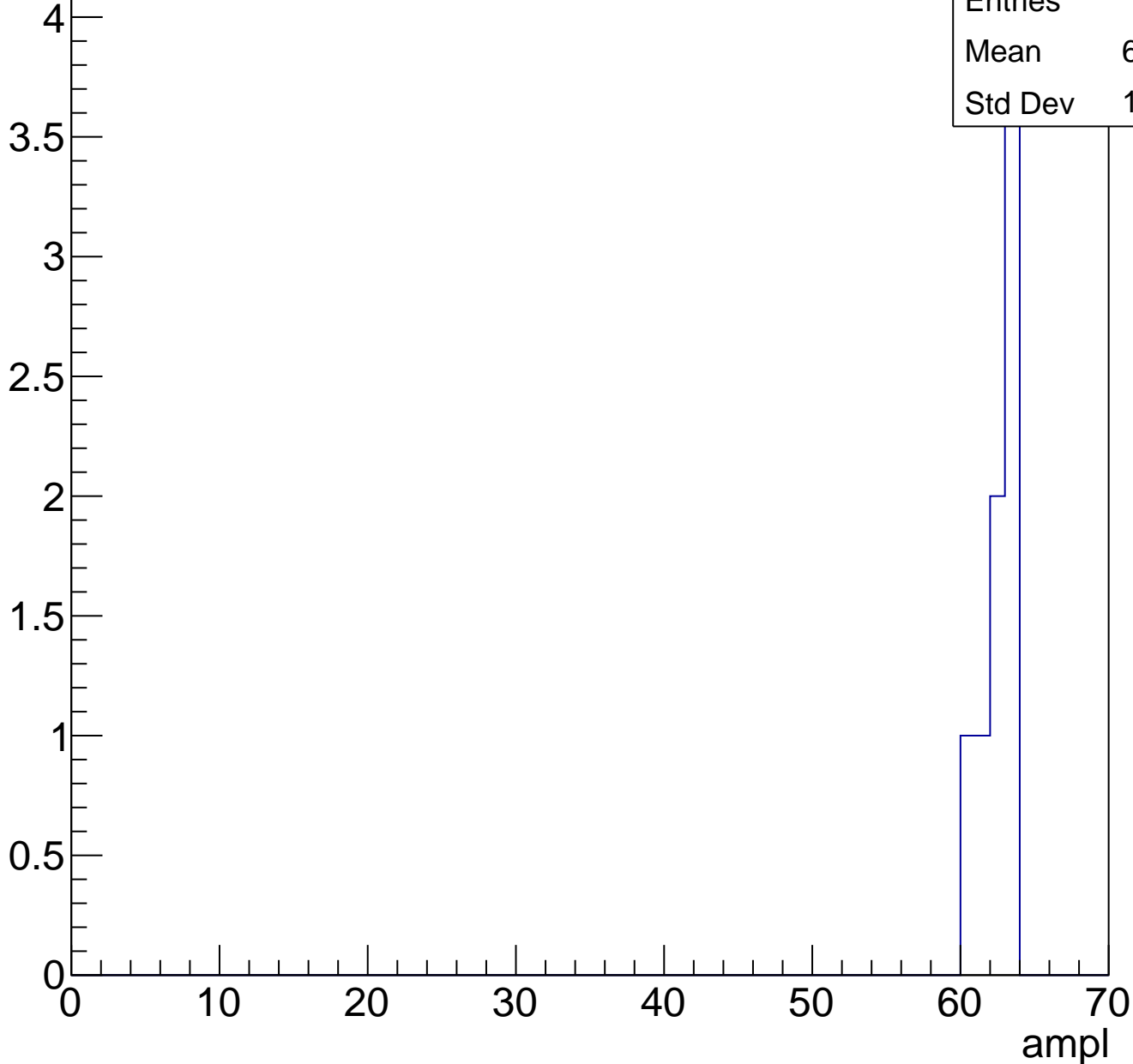
0 10 20 30 40 50 60 70



B1L001S, U26-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

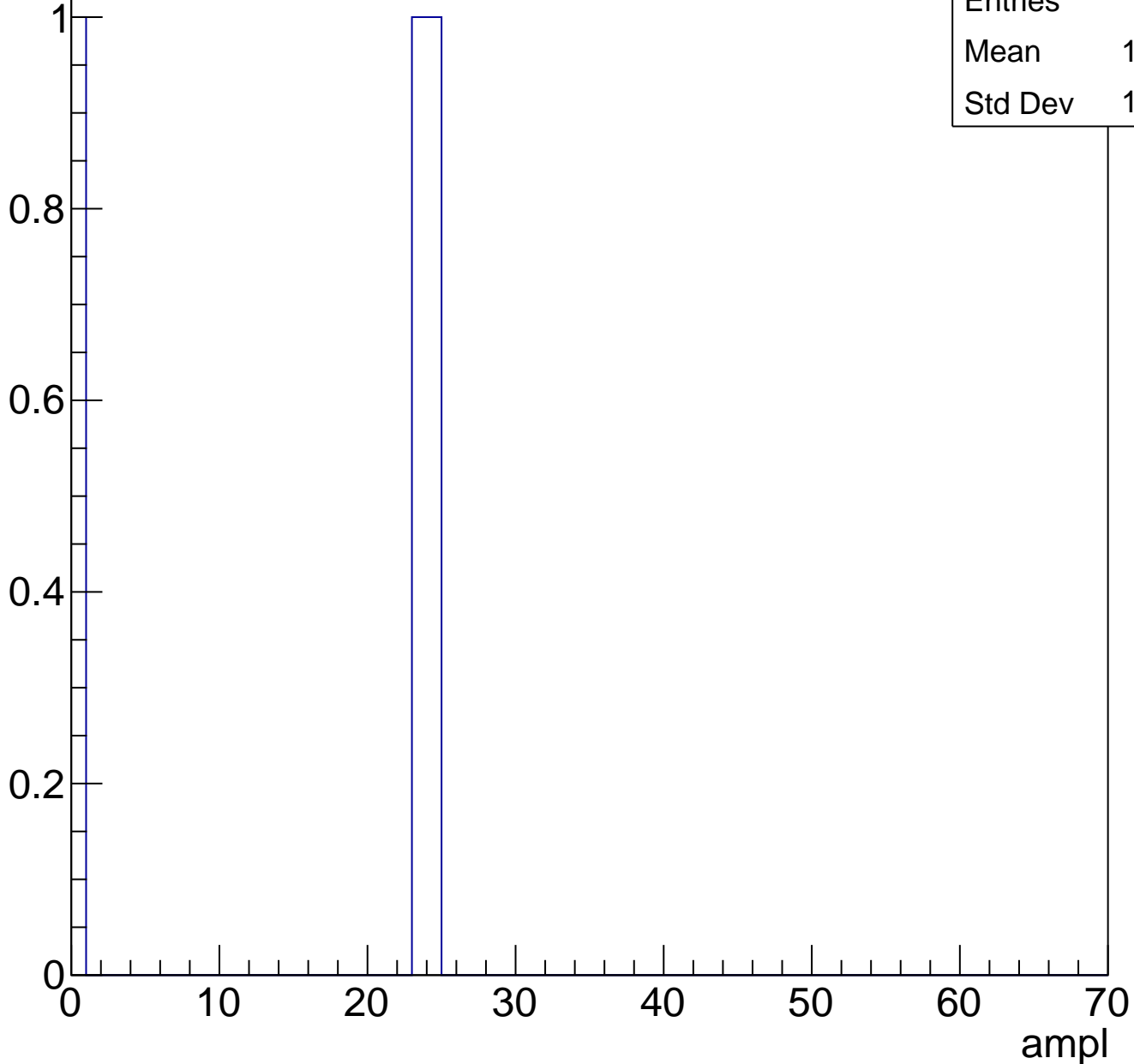
Entry



B1L001S, U26-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 15.67 |
| Std Dev | 11.09 |

B1L001S, U26-ch6, adc0

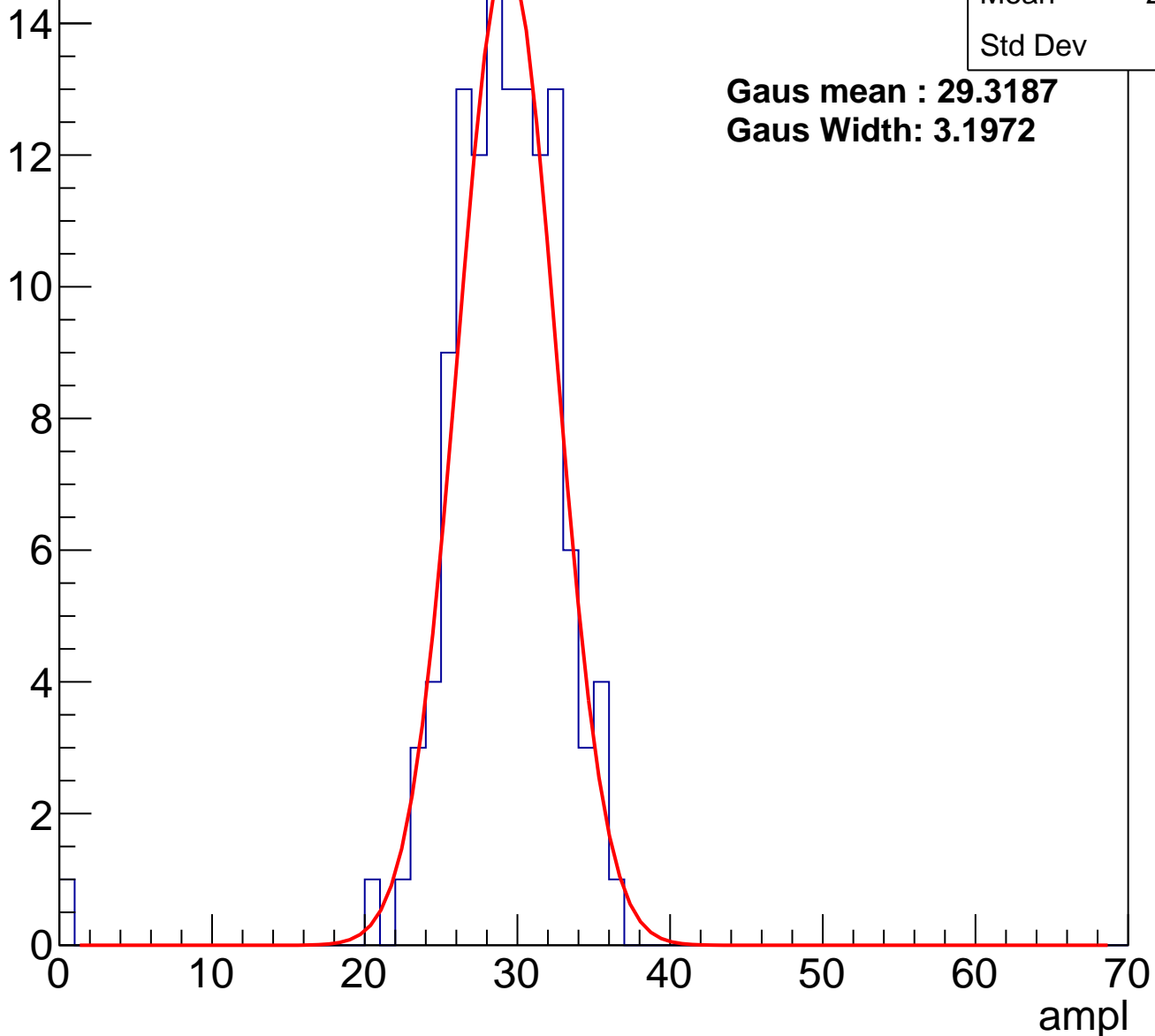
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 28.59 |
| Std Dev | 4.04 |

Gaus mean : 29.3187

Gaus Width: 3.1972

Entry



B1L001S, U26-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 35.37 |
| Std Dev | 3.517 |

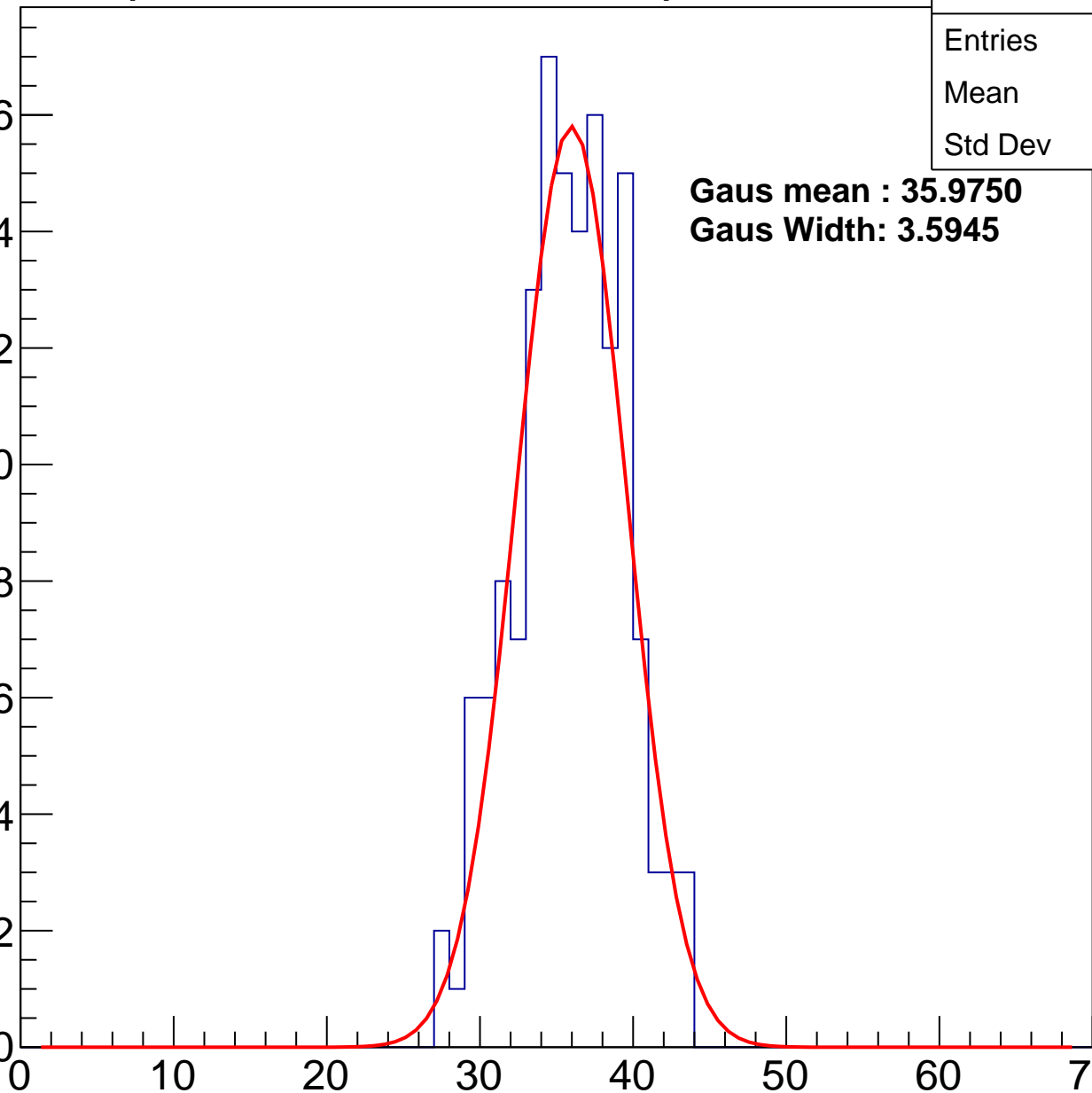
Gaus mean : 35.9750

Gaus Width: 3.5945

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch6, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

104

Mean

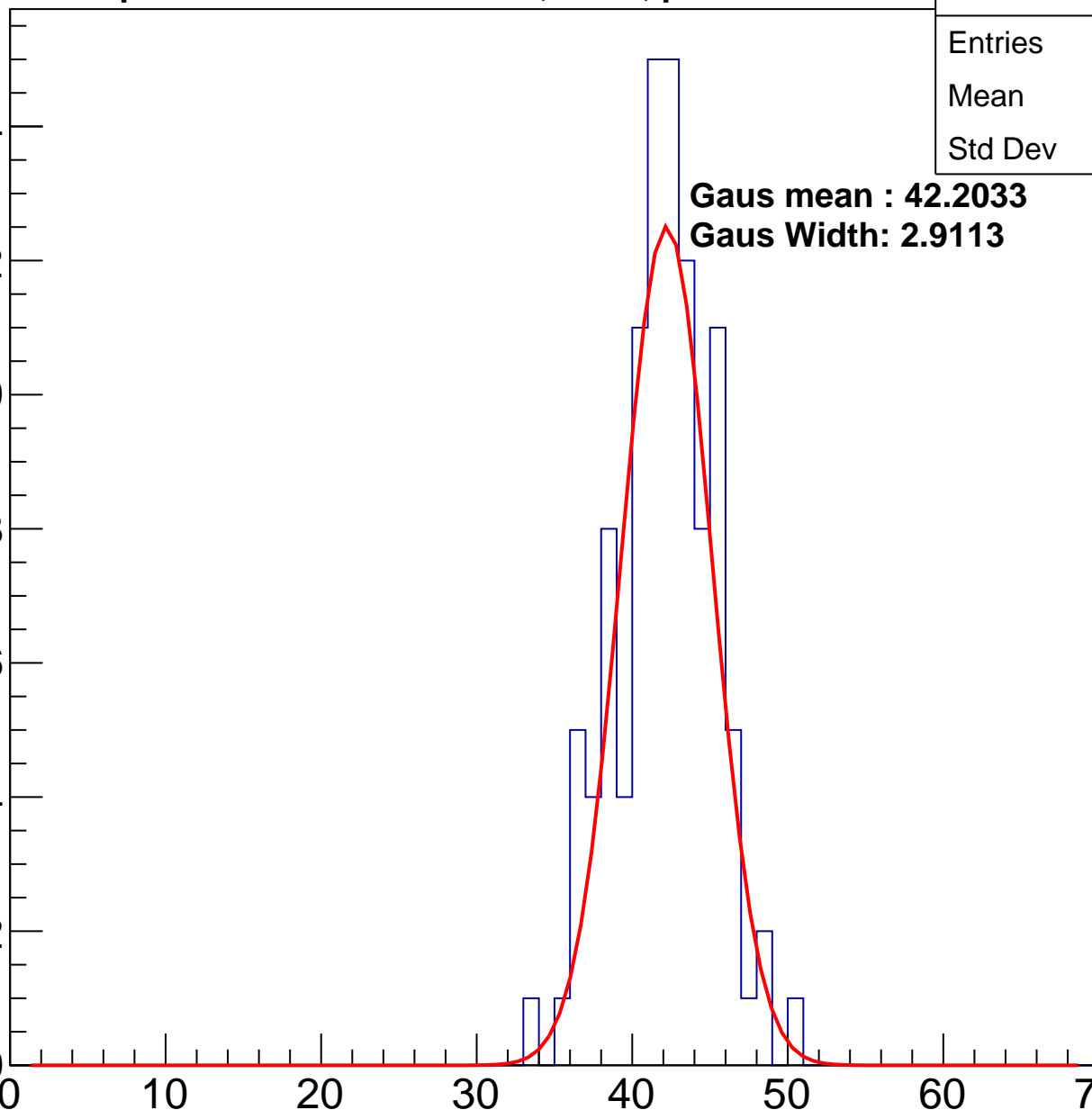
41.61

Std Dev

3.102

Gaus mean : 42.2033

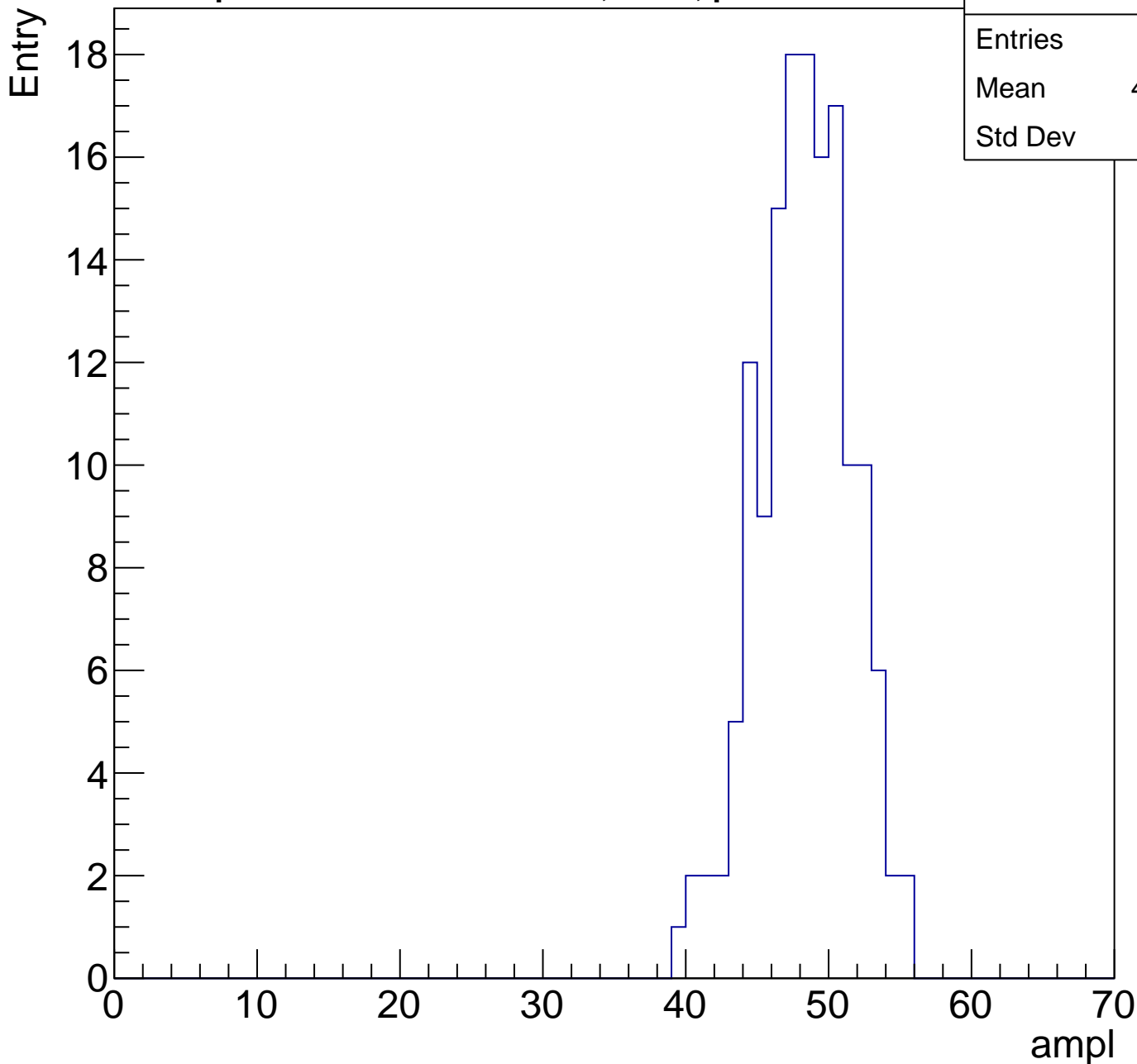
Gaus Width: 2.9113



B1L001S, U26-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

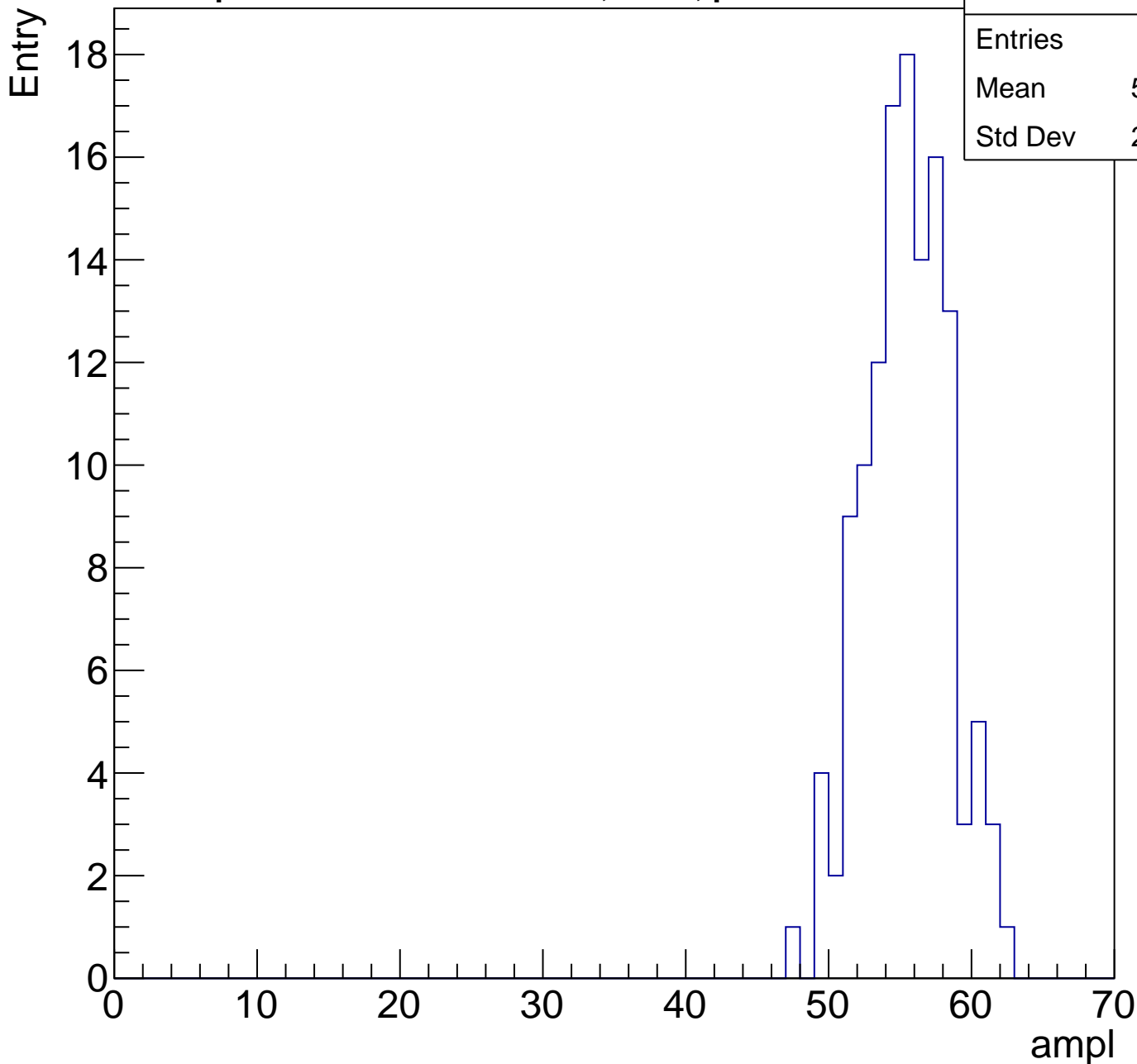
| | |
|---------|-------|
| Entries | 147 |
| Mean | 47.84 |
| Std Dev | 3.2 |



B1L001S, U26-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 54.98 |
| Std Dev | 2.907 |



B1L001S, U26-ch6, adc5

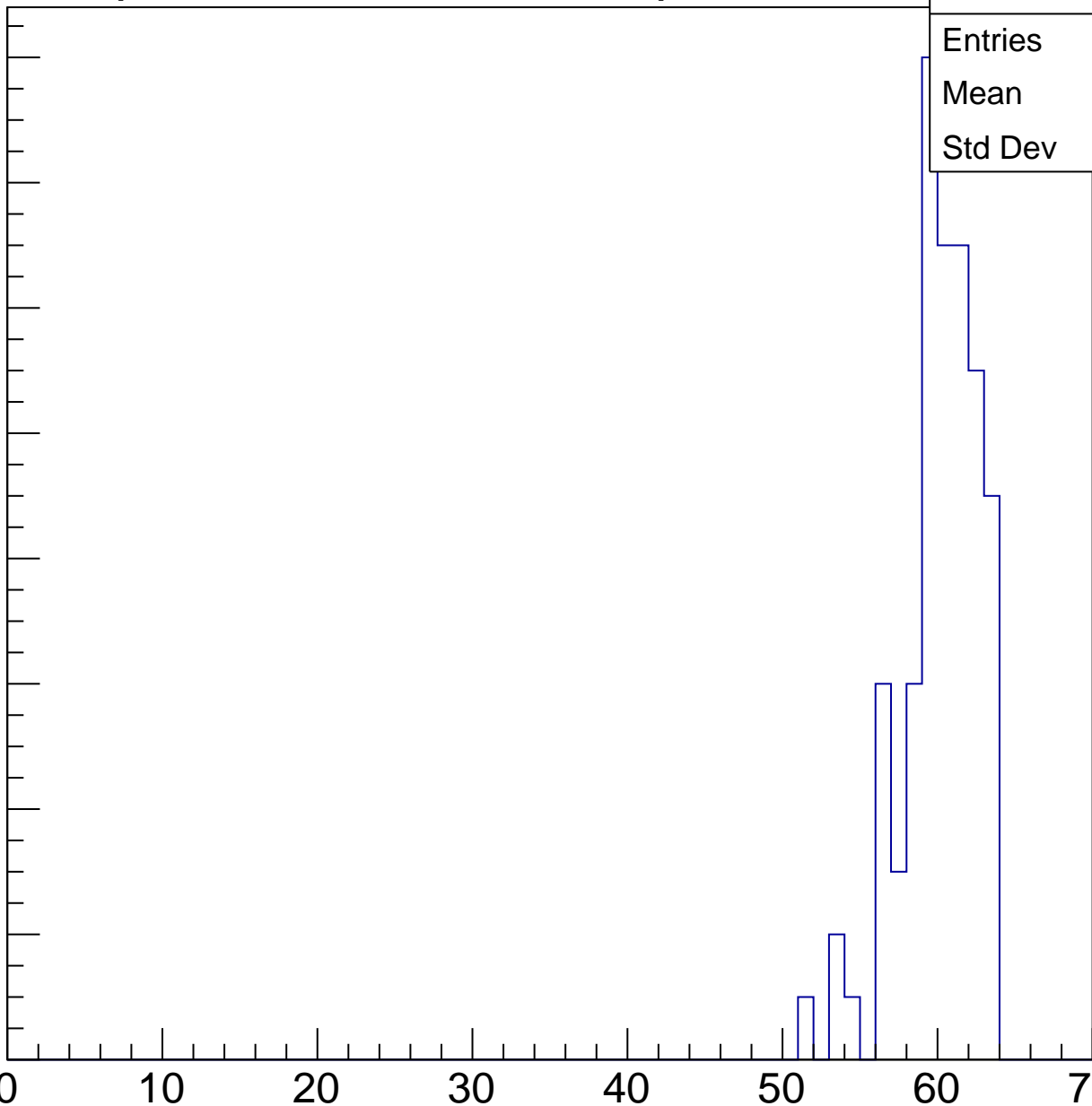
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 81 |
| Mean | 59.65 |
| Std Dev | 2.5 |

ampl



B1L001S, U26-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

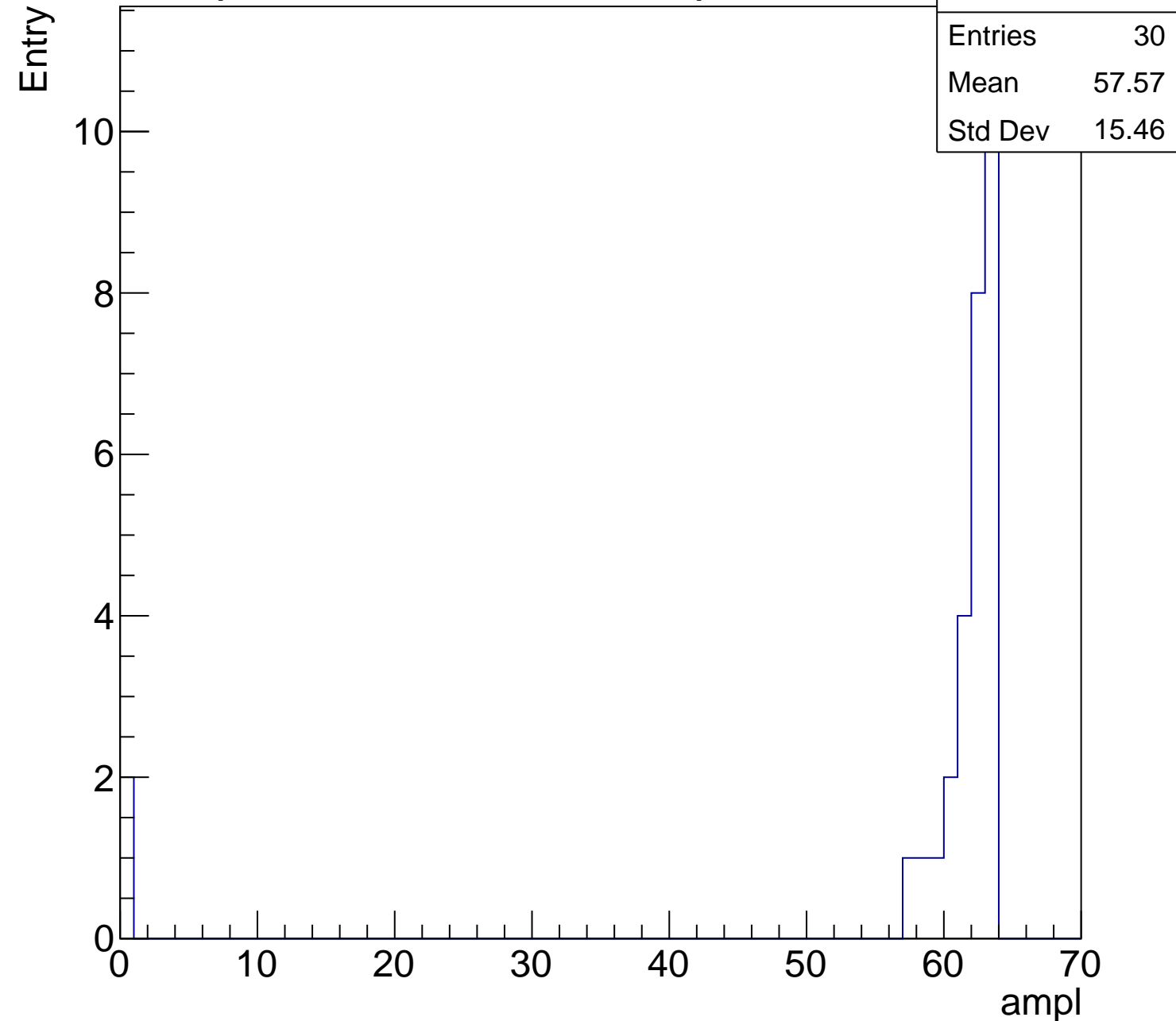
40

50

60

ampl

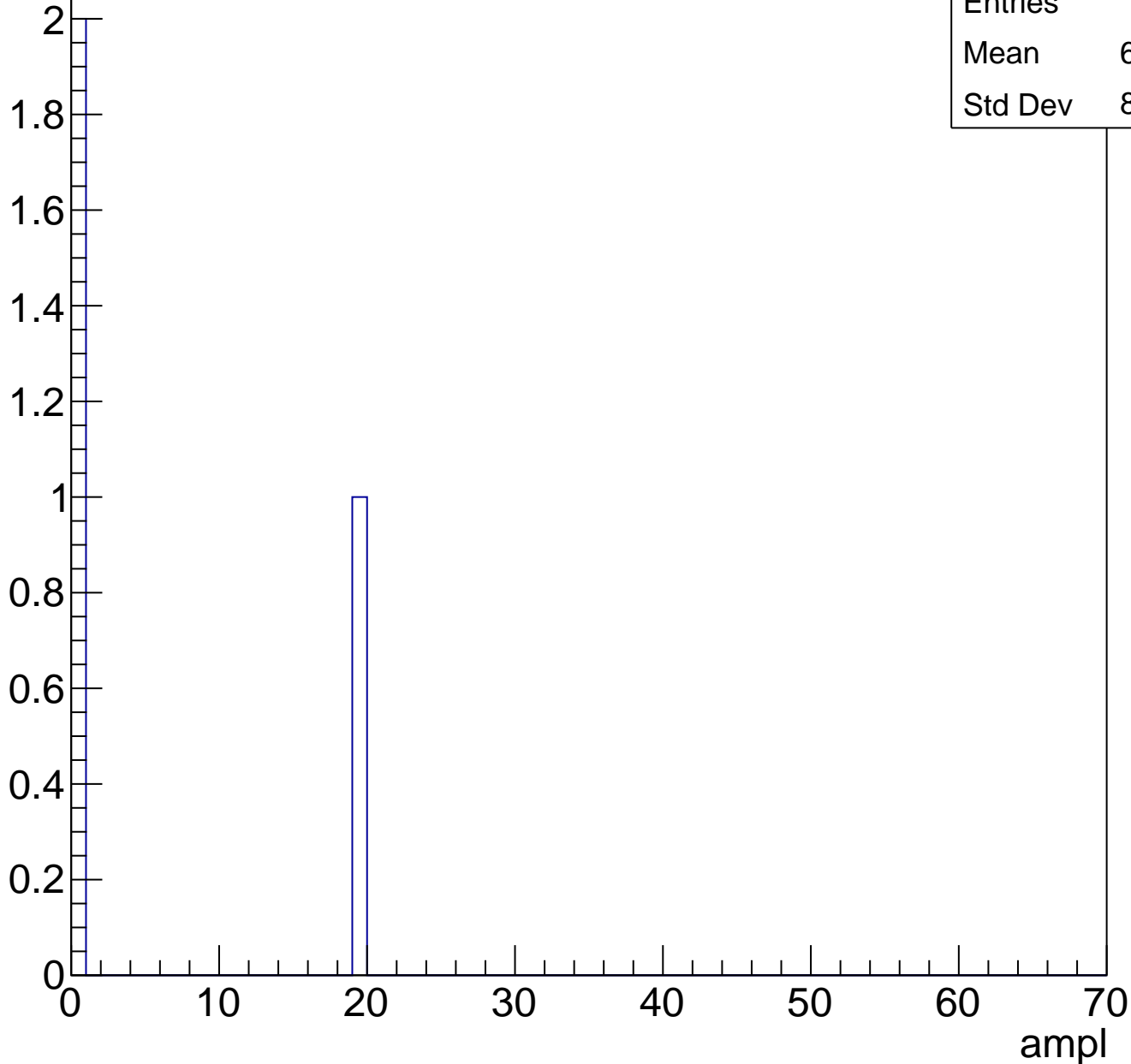
| | |
|---------|-------|
| Entries | 30 |
| Mean | 57.57 |
| Std Dev | 15.46 |



B1L001S, U26-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch7, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

122

Mean

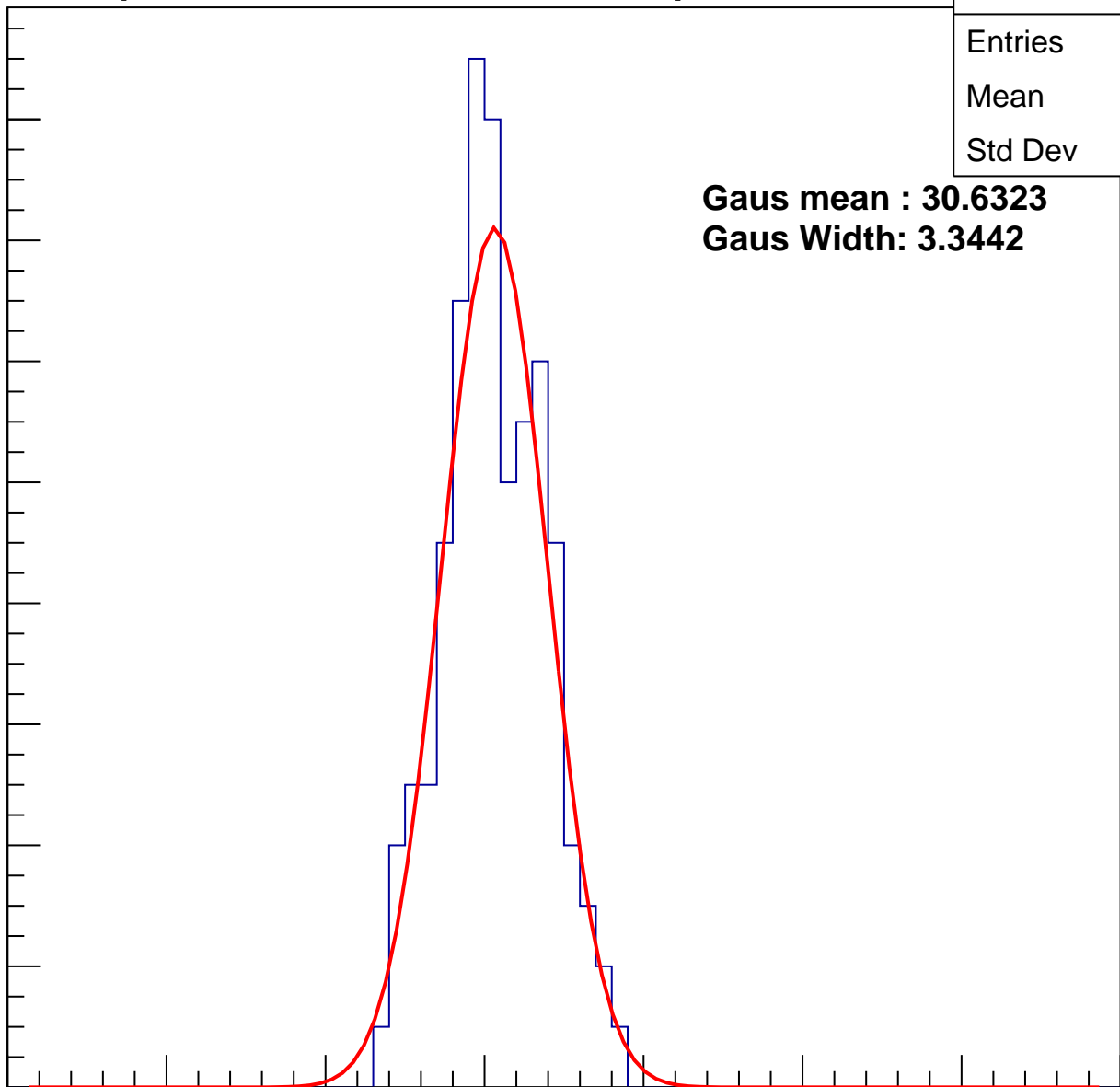
30.15

Std Dev

3.164

Gaus mean : 30.6323

Gaus Width: 3.3442



B1L001S, U26-ch7, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 36.22 |
| Std Dev | 3.453 |

Gaus mean : 37.0078

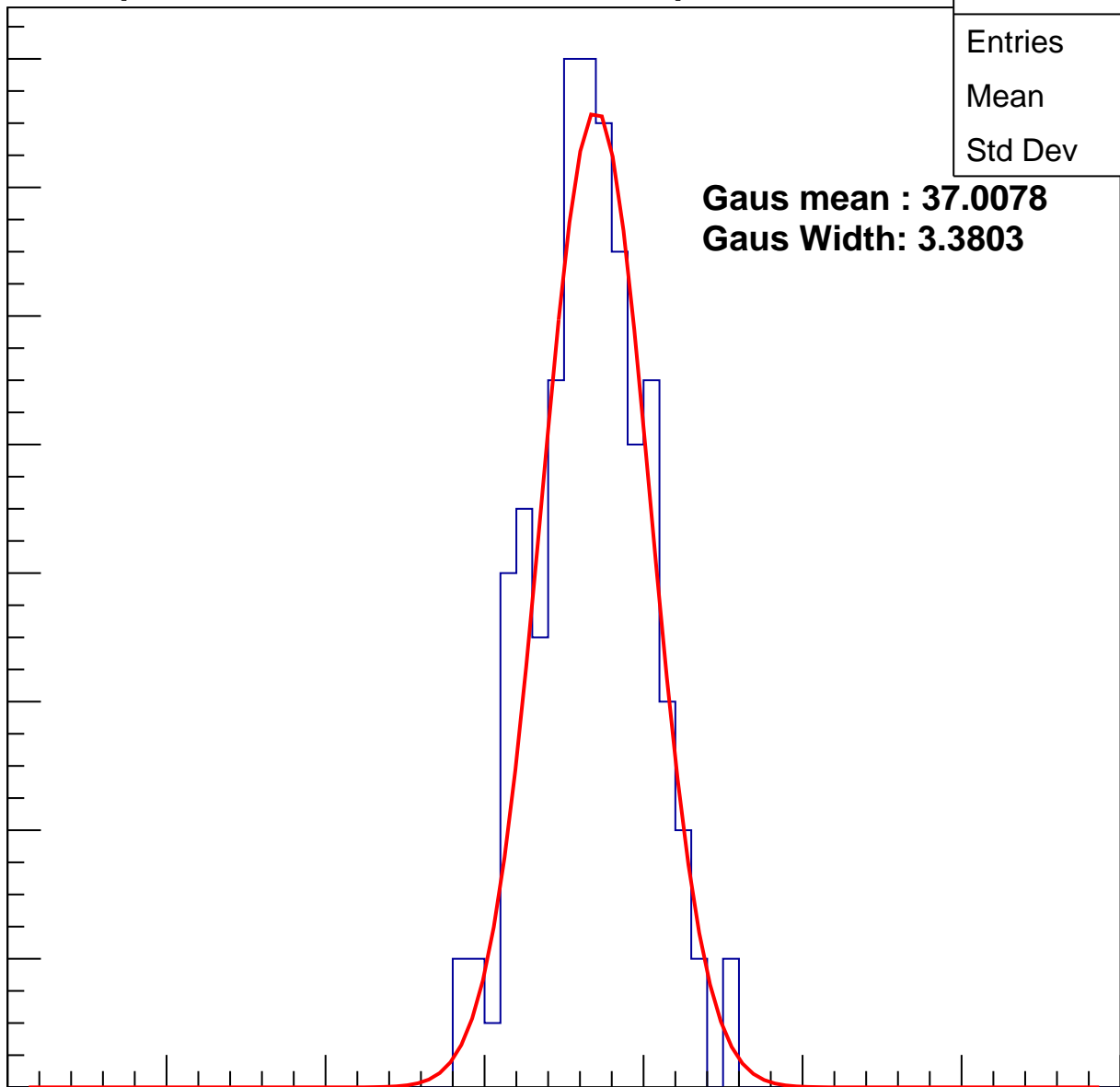
Gaus Width: 3.3803

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch7, adc2

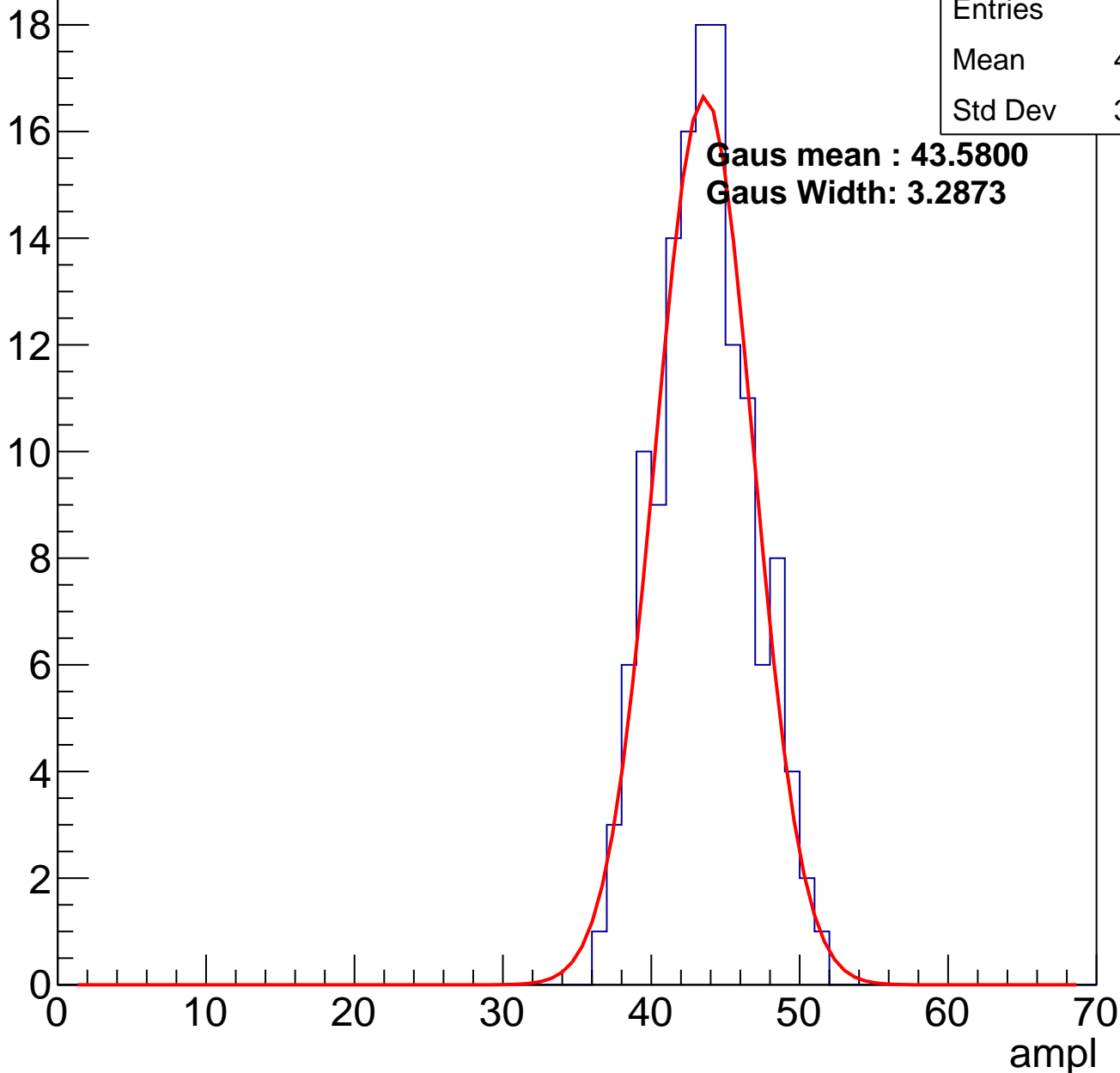
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 43.14 |
| Std Dev | 3.156 |

Gaus mean : 43.5800

Gaus Width: 3.2873

Entry



B1L001S, U26-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 50.07 |
| Std Dev | 3.025 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

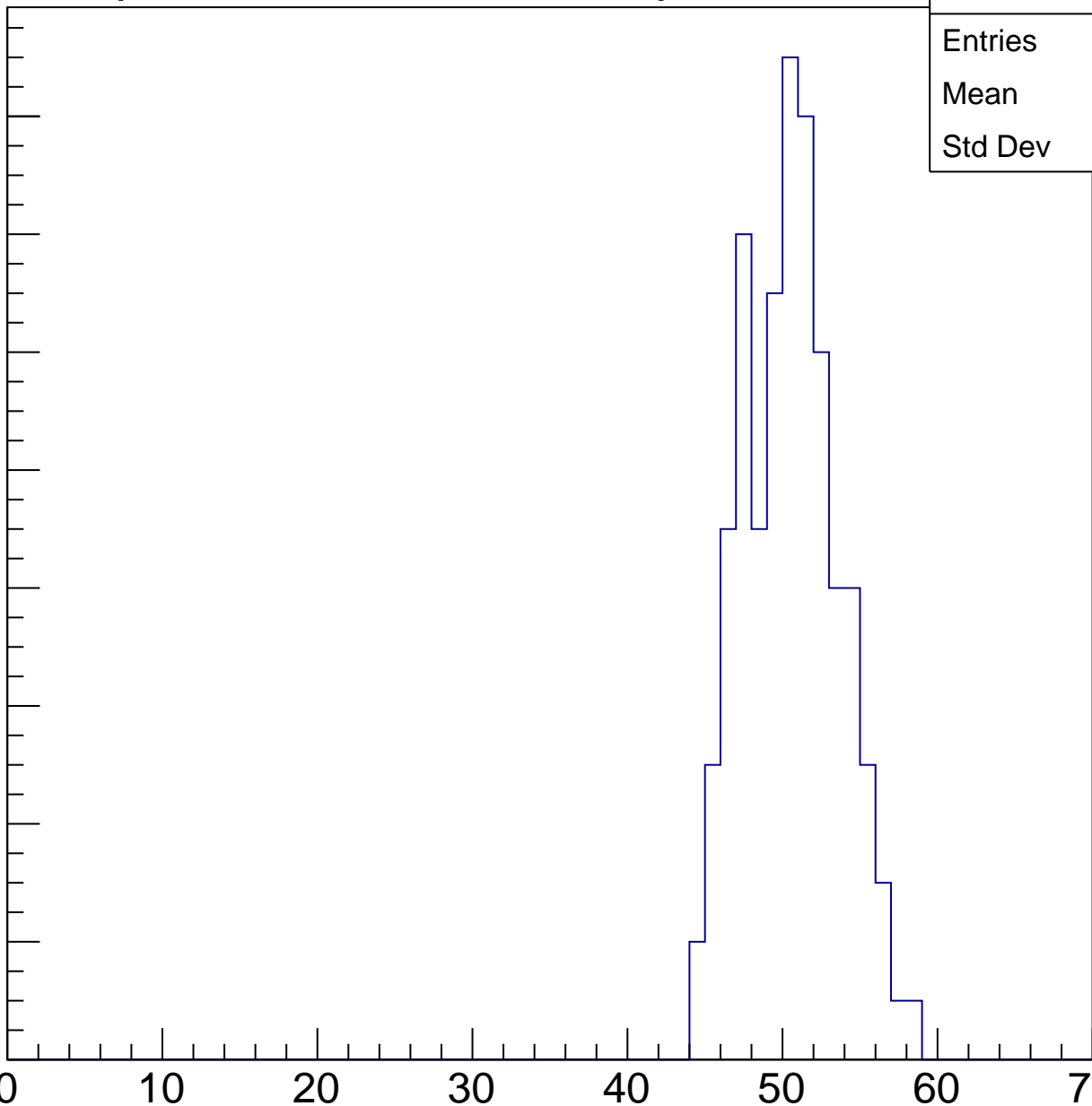
30

40

50

60

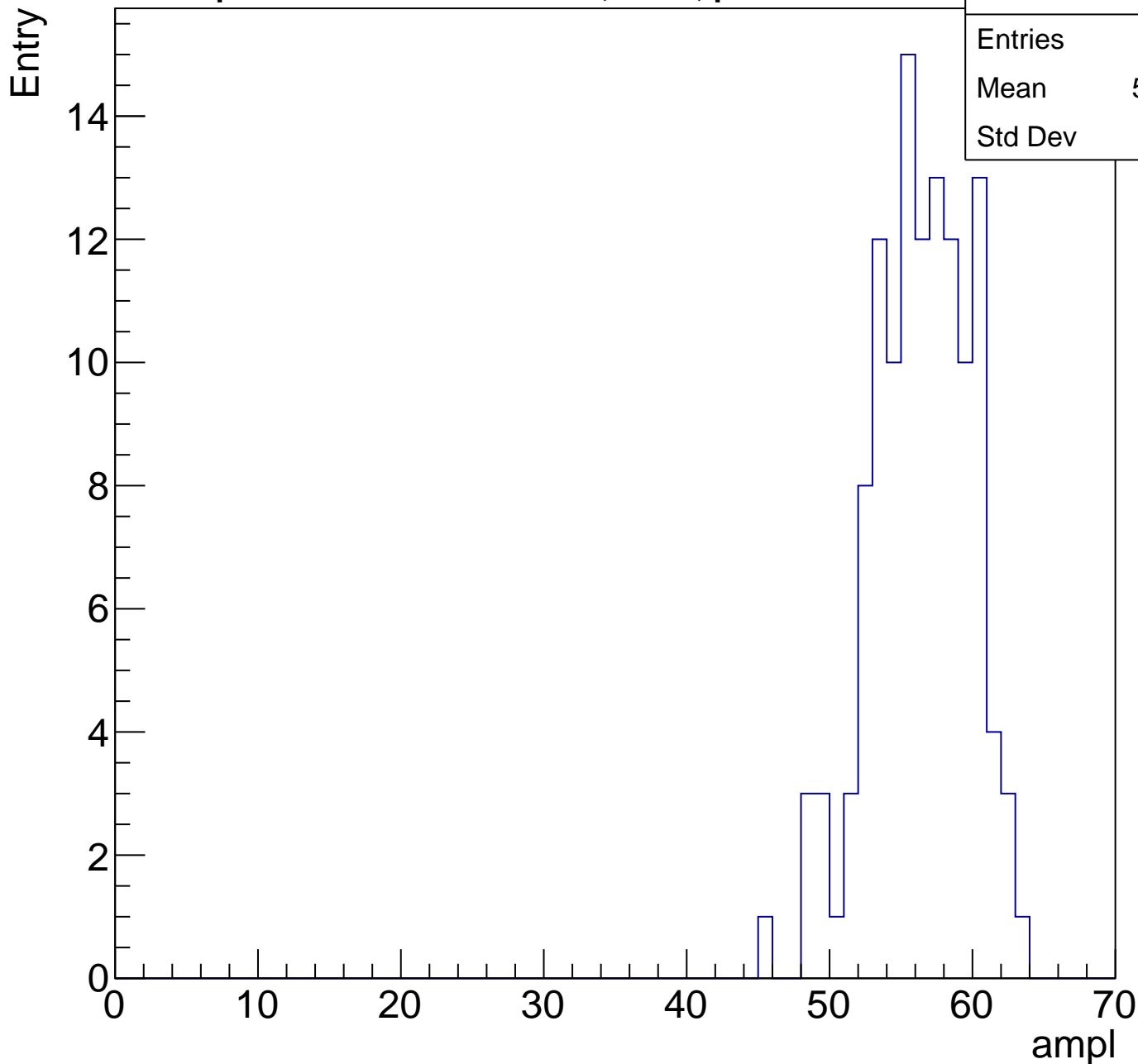
ampl



B1L001S, U26-ch7, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 55.87 |
| Std Dev | 3.45 |



B1L001S, U26-ch7, adc5

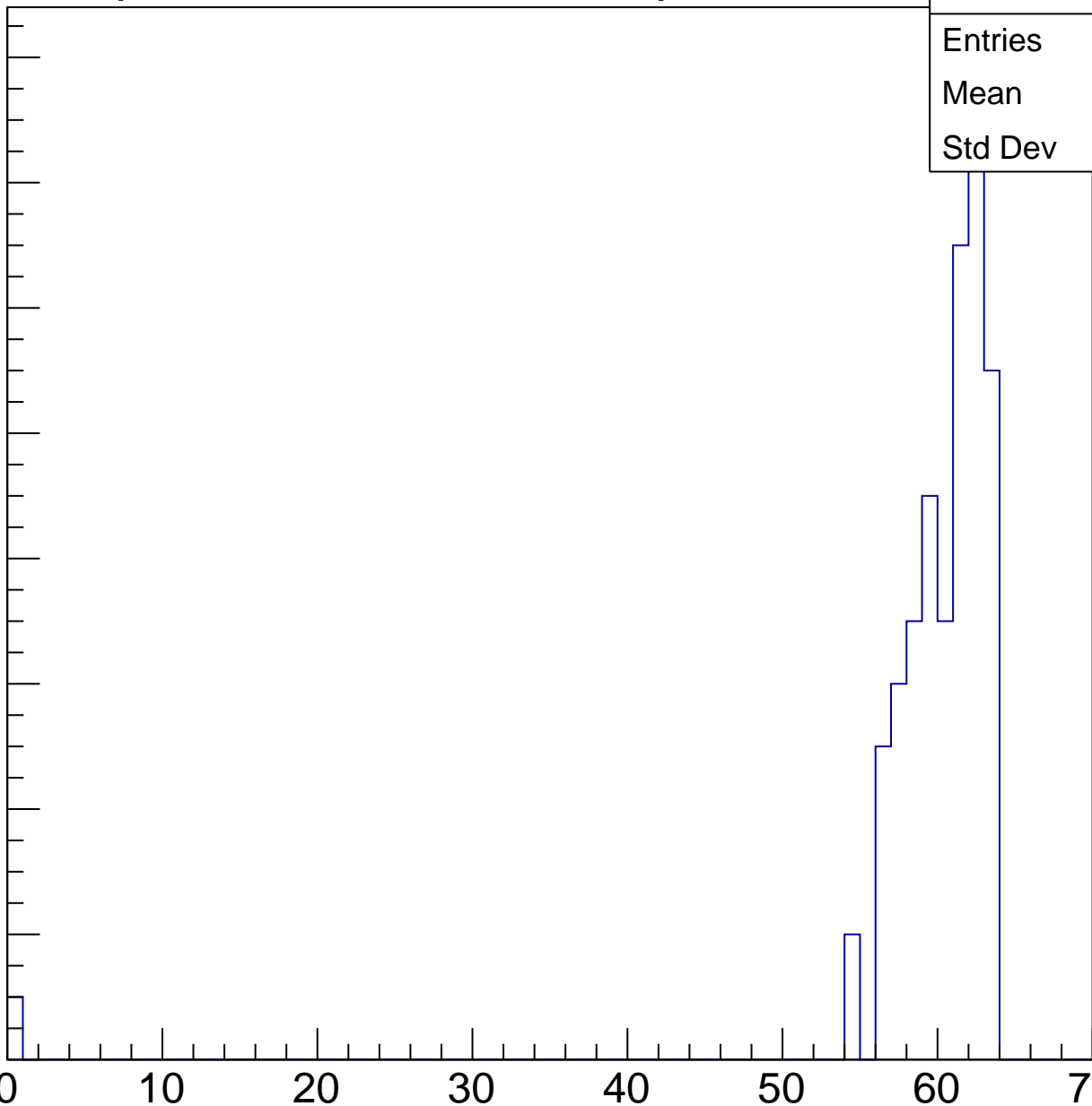
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 77 |
| Mean | 59.29 |
| Std Dev | 7.189 |

ampl

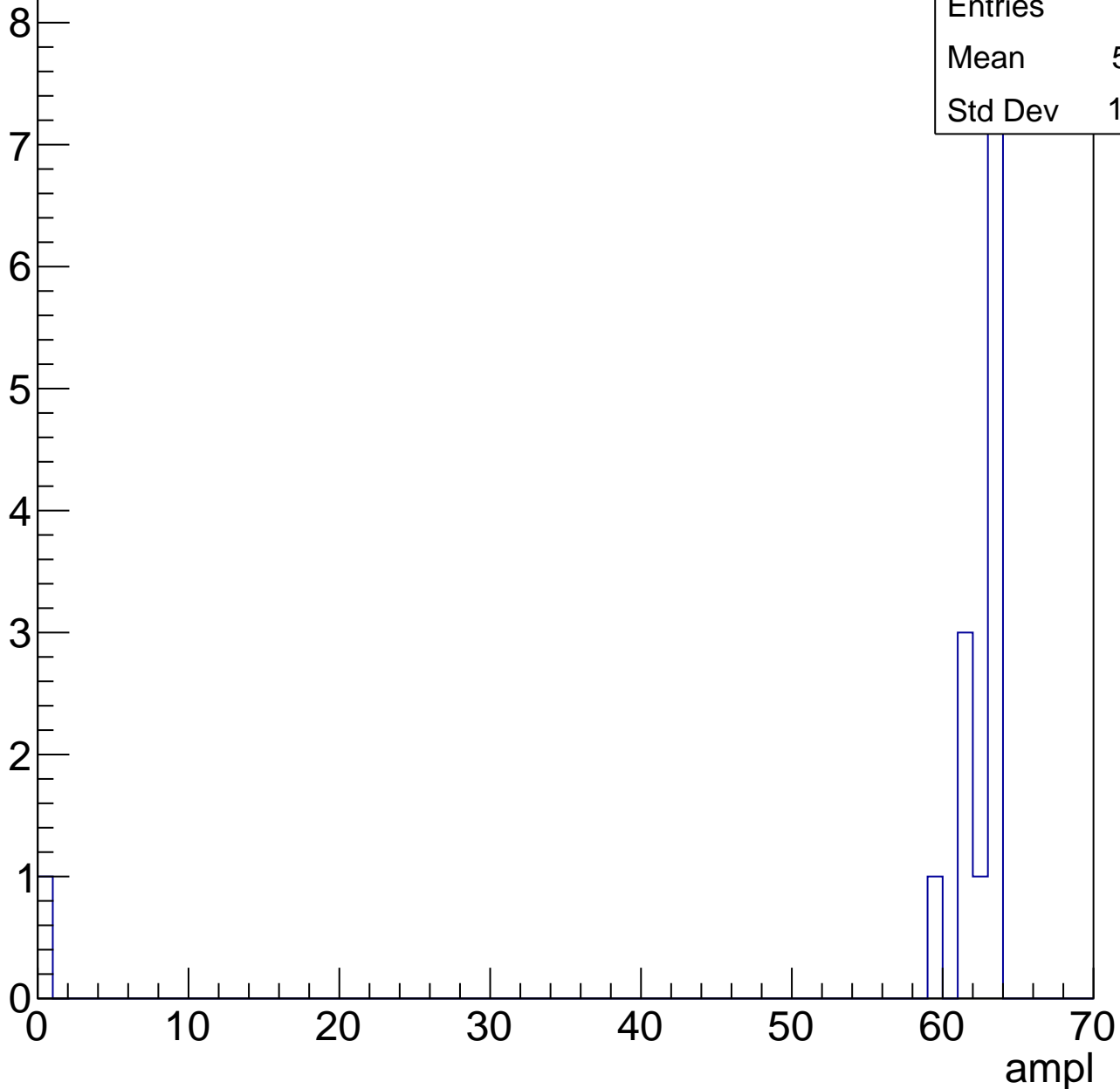


B1L001S, U26-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

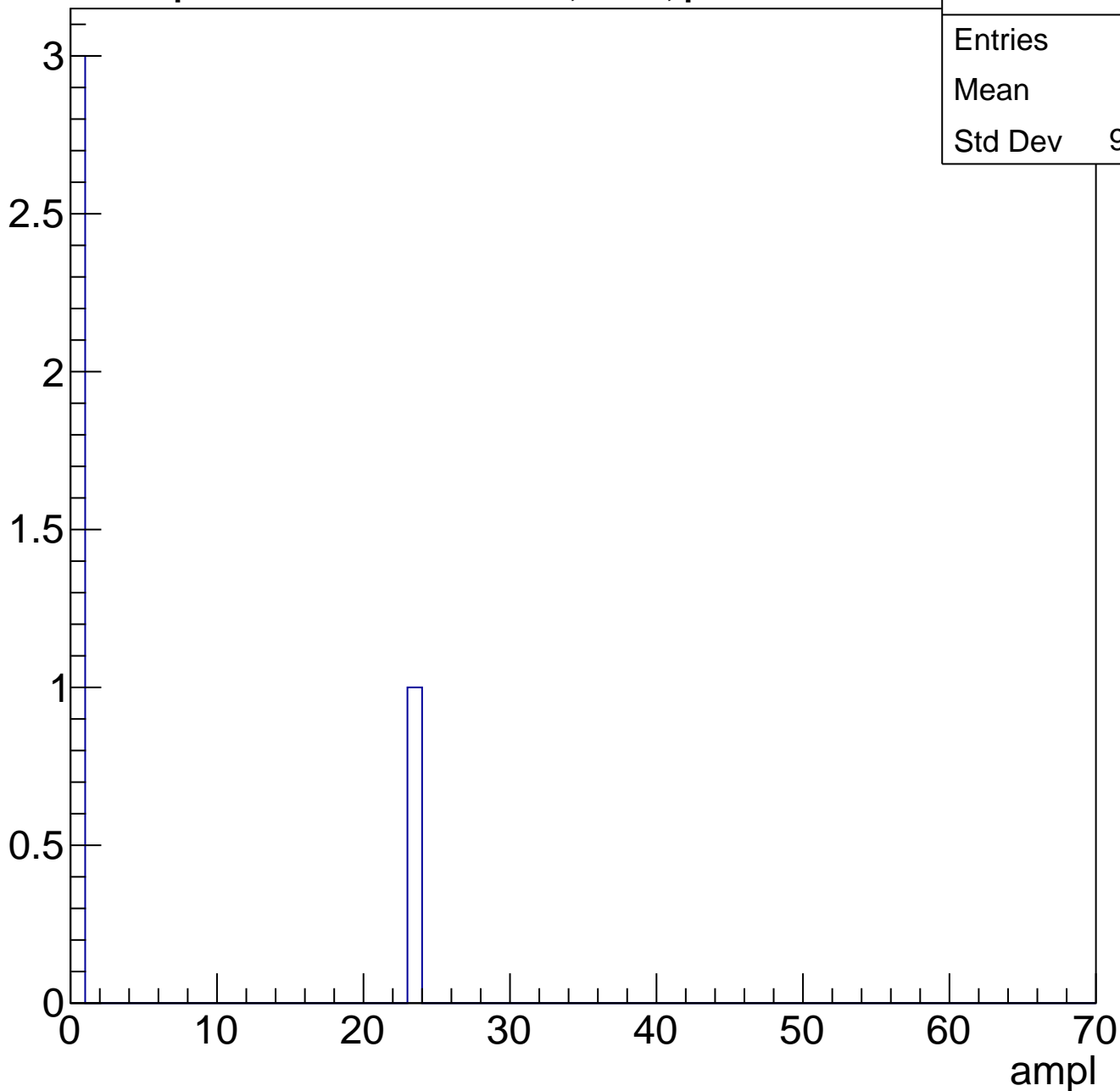
| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.71 |
| Std Dev | 16.05 |



B1L001S, U26-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch8, adc0

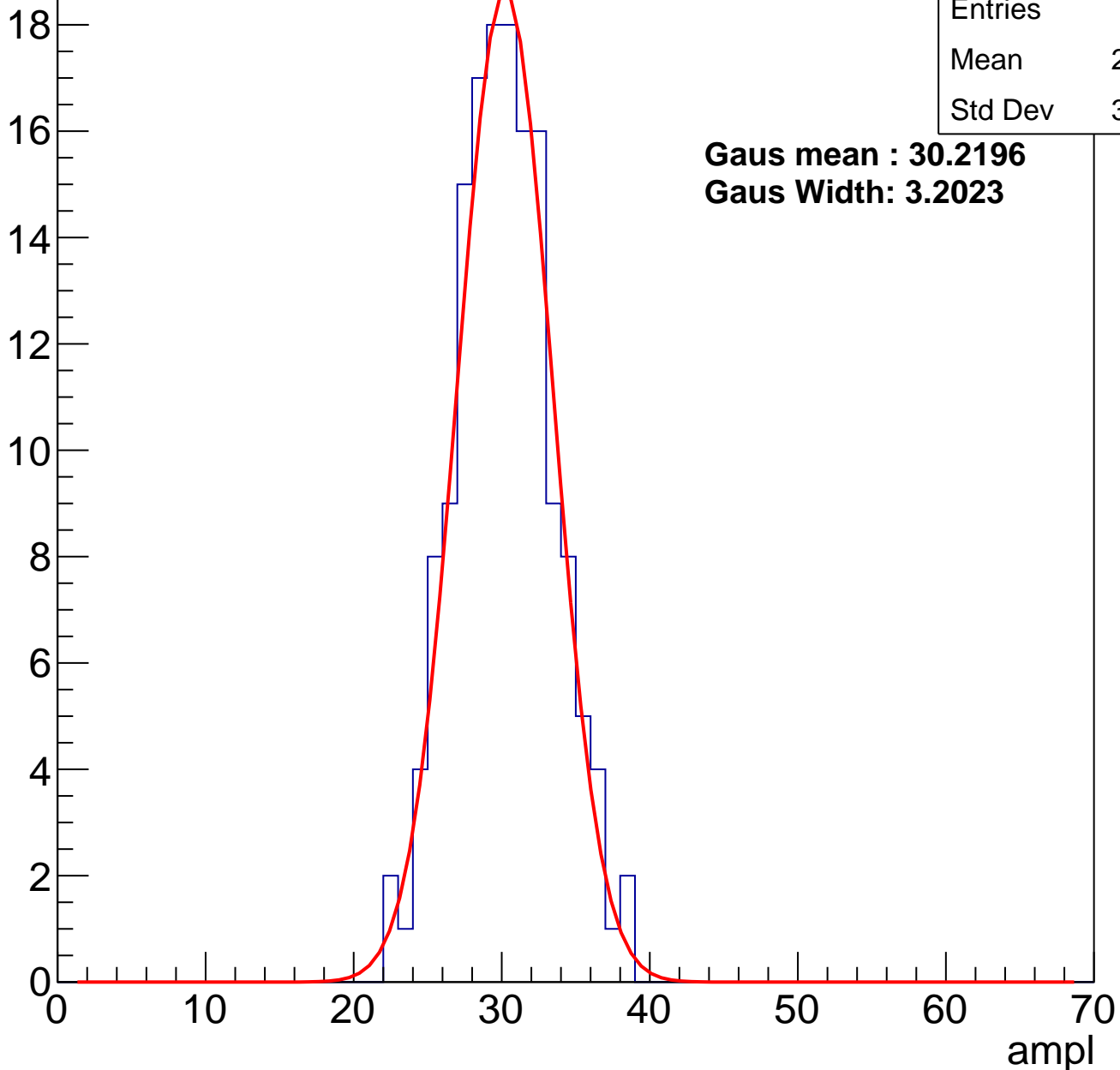
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 29.73 |
| Std Dev | 3.244 |

Gaus mean : 30.2196

Gaus Width: 3.2023

Entry



B1L001S, U26-ch8, adc1

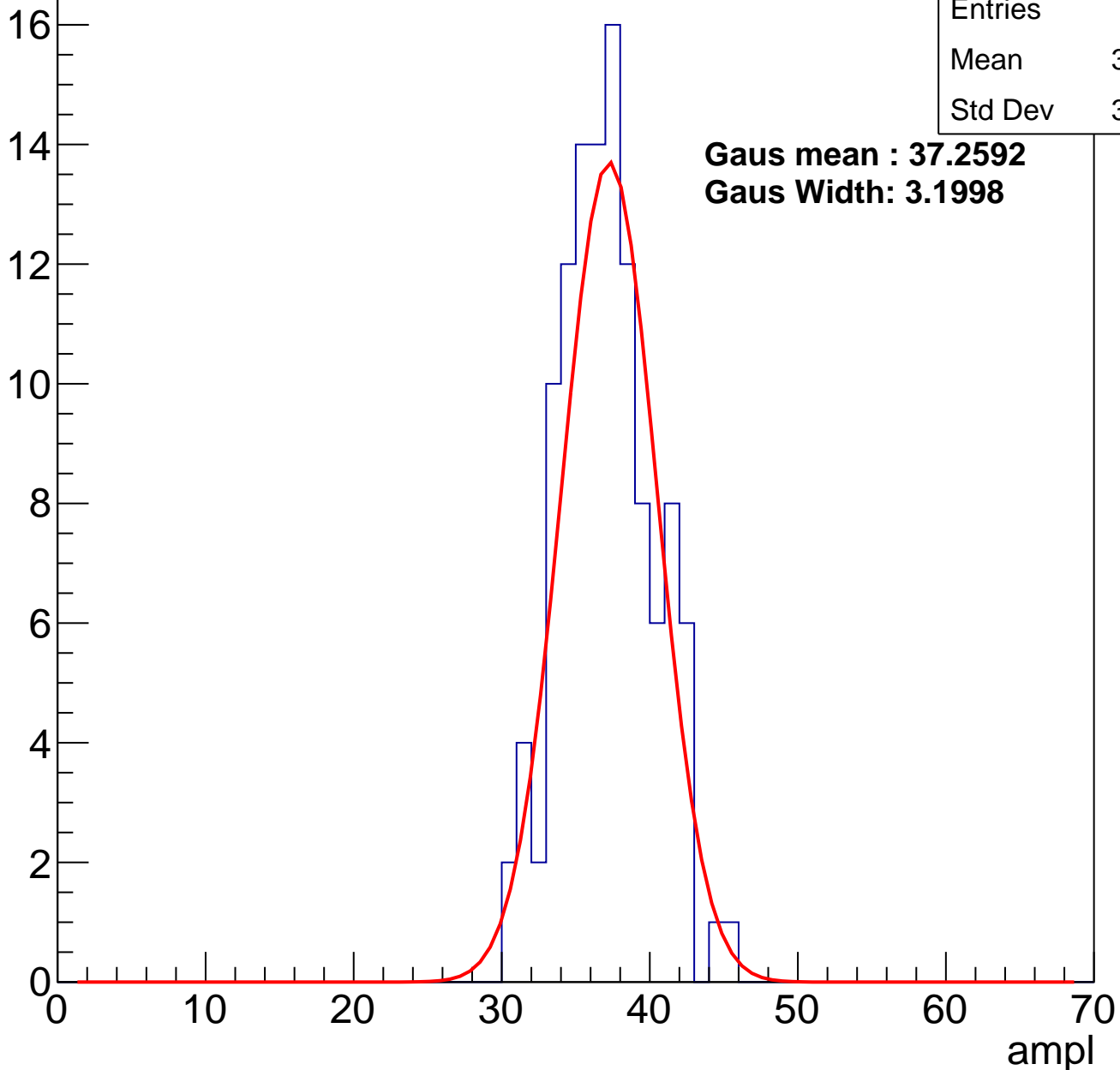
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 36.63 |
| Std Dev | 3.084 |

Gaus mean : 37.2592

Gaus Width: 3.1998

Entry



B1L001S, U26-ch8, adc2

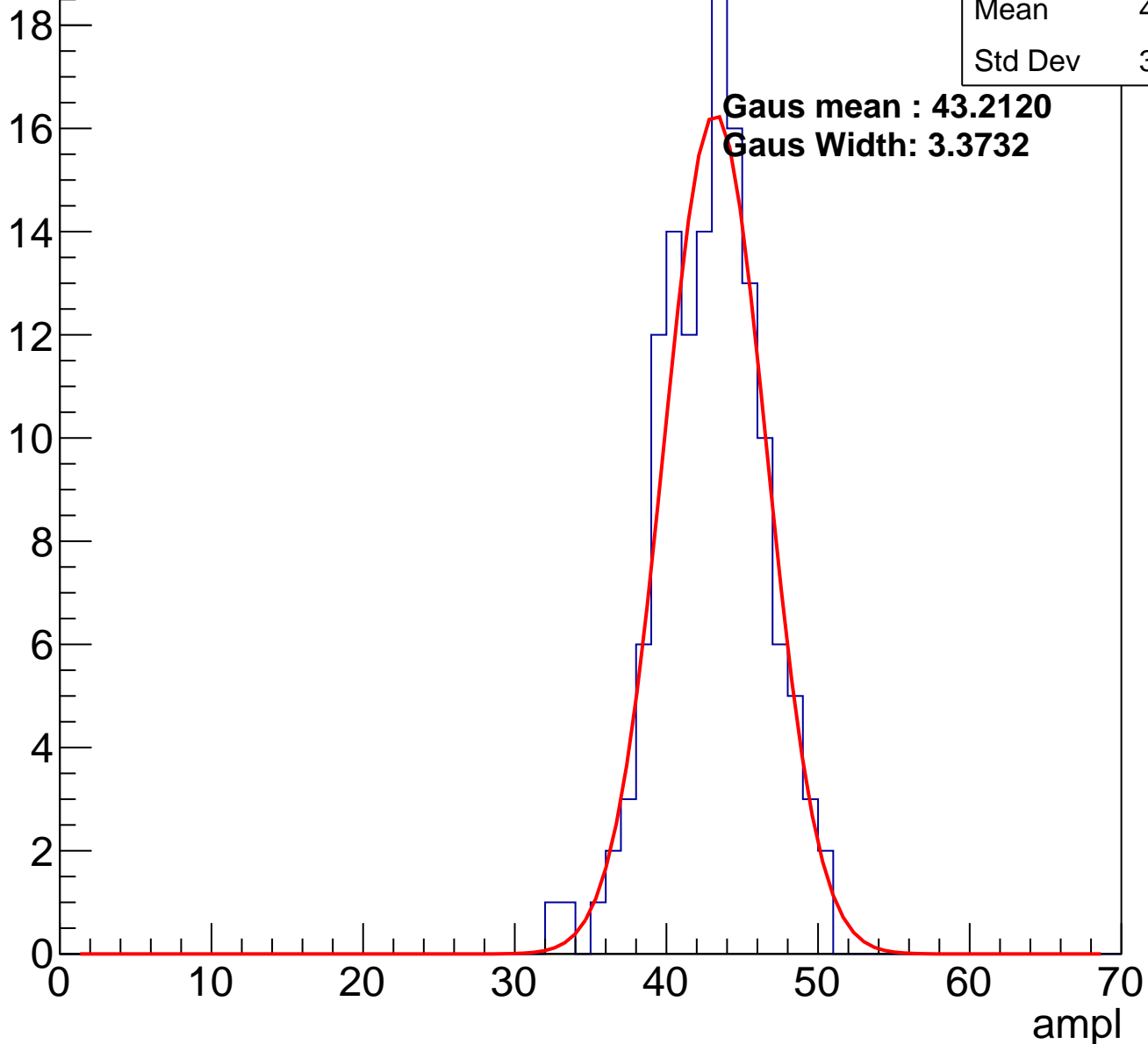
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 42.53 |
| Std Dev | 3.354 |

Gaus mean : 43.2120

Gaus Width: 3.3732

Entry

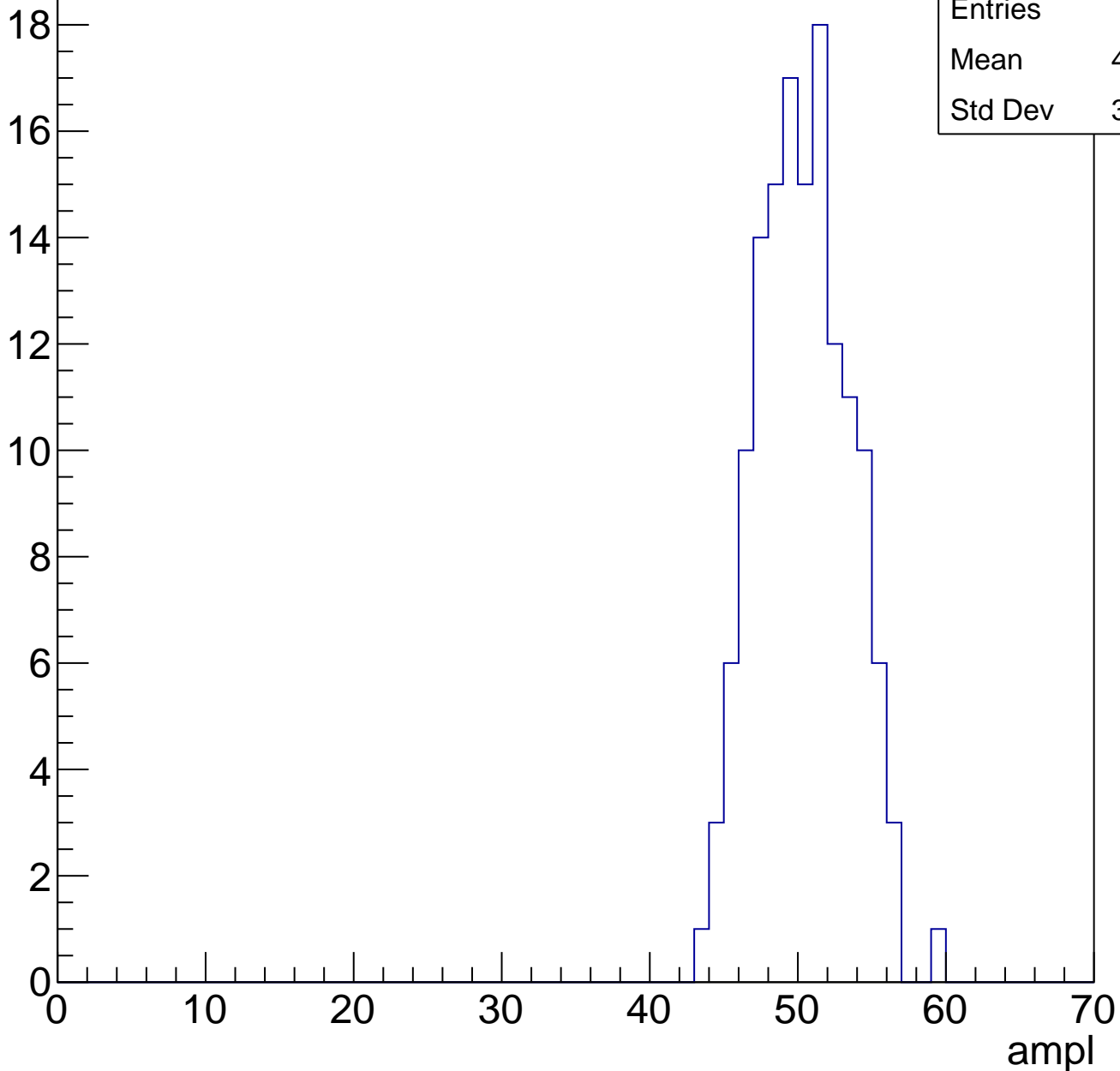


B1L001S, U26-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 49.92 |
| Std Dev | 3.064 |

Entry



B1L001S, U26-ch8, adc4

calib_packv5_042523_0143.root, FC#2, port C2

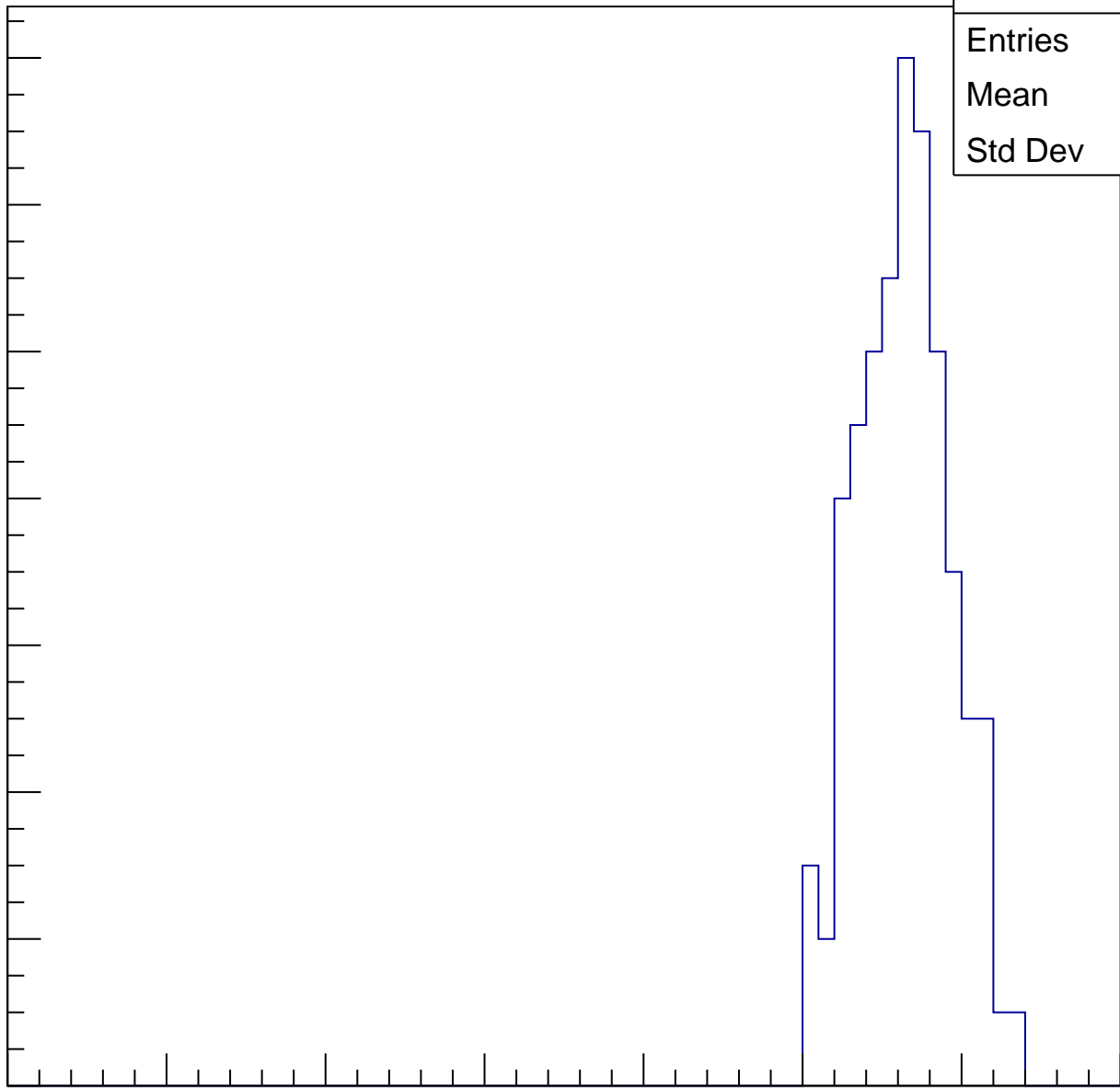
| | |
|---------|-------|
| Entries | 99 |
| Mean | 55.94 |
| Std Dev | 2.884 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch8, adc5

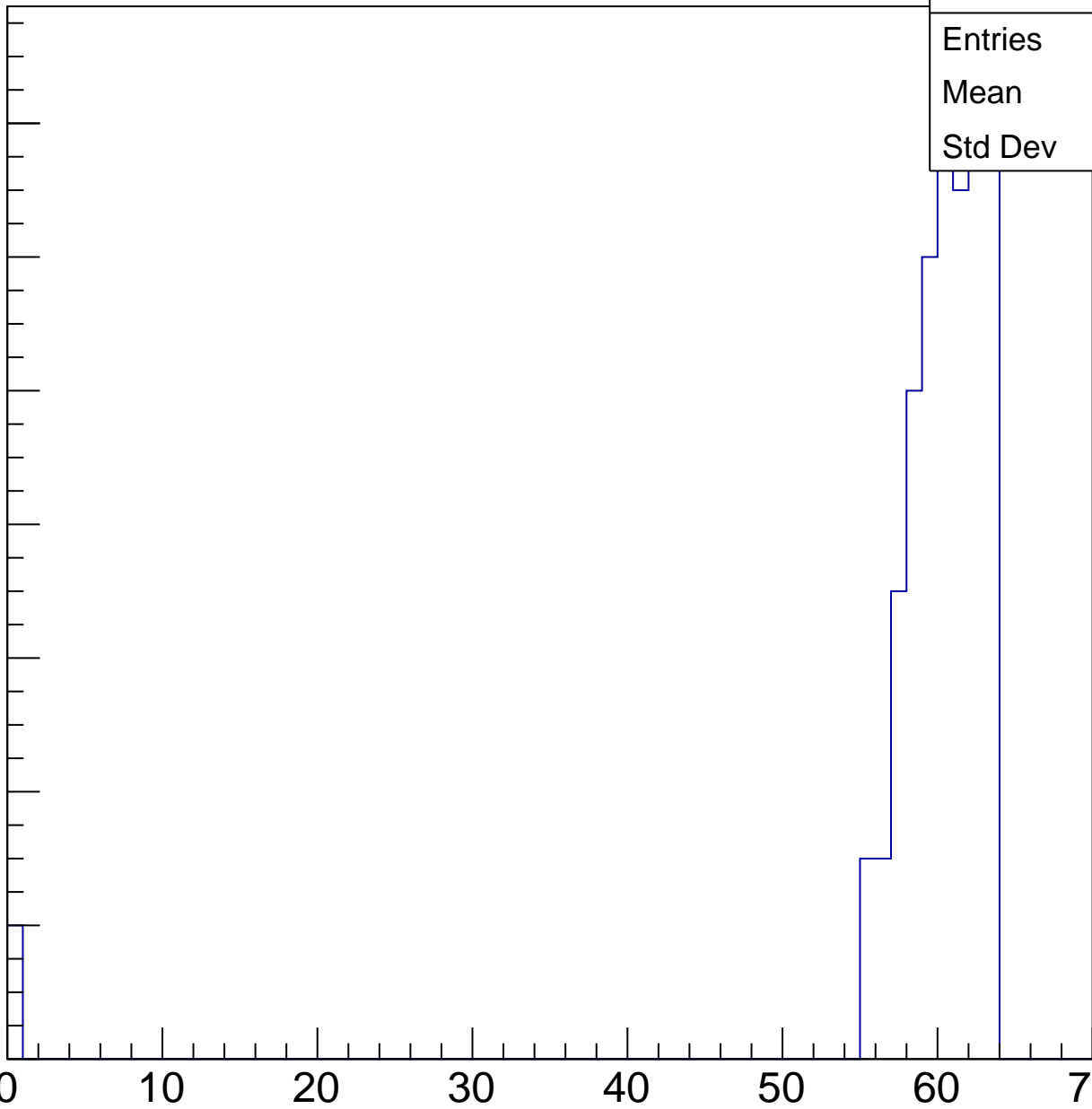
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 58.82 |
| Std Dev | 8.893 |

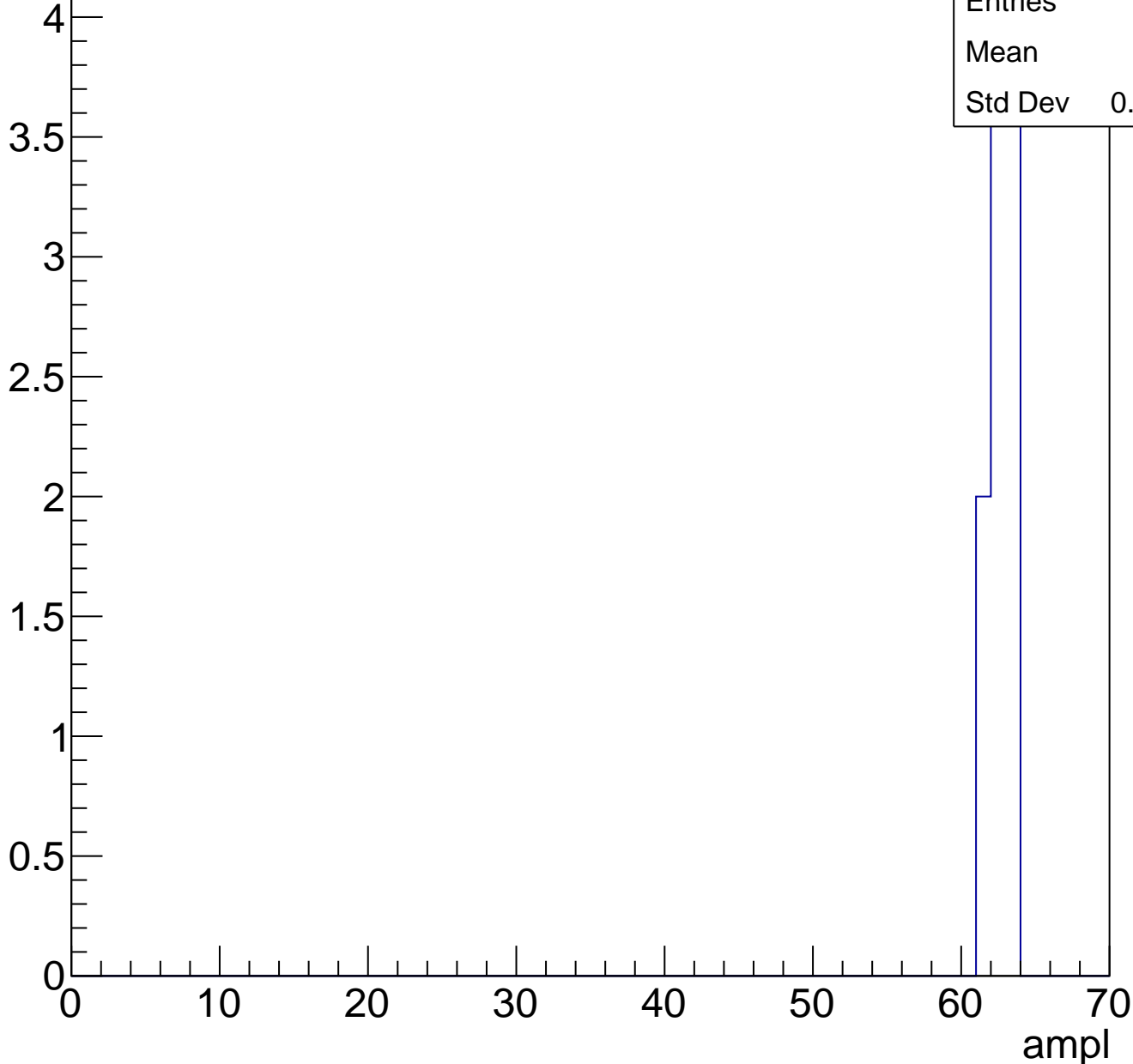
ampl



B1L001S, U26-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

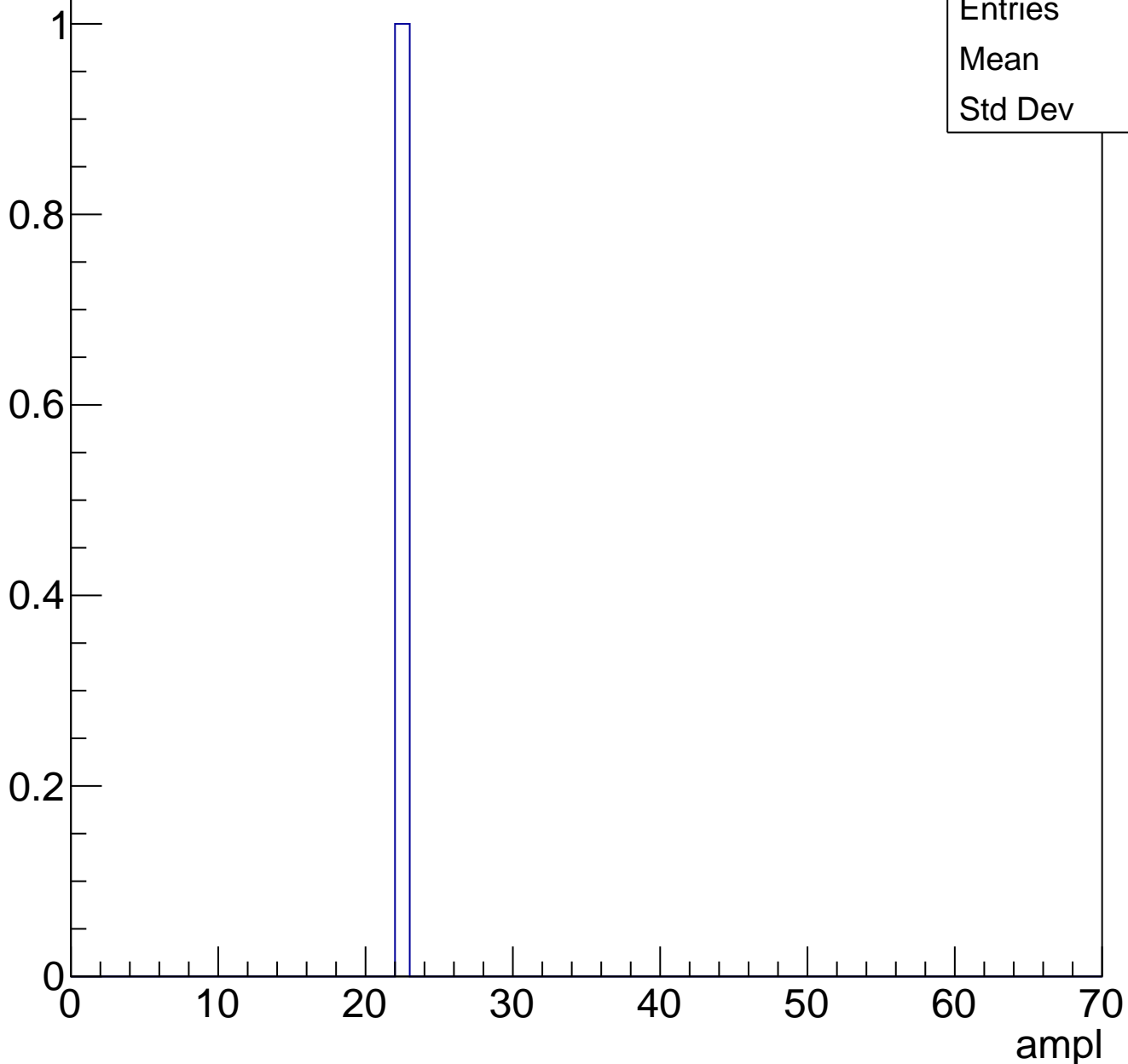
Entry



B1L001S, U26-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch9, adc0

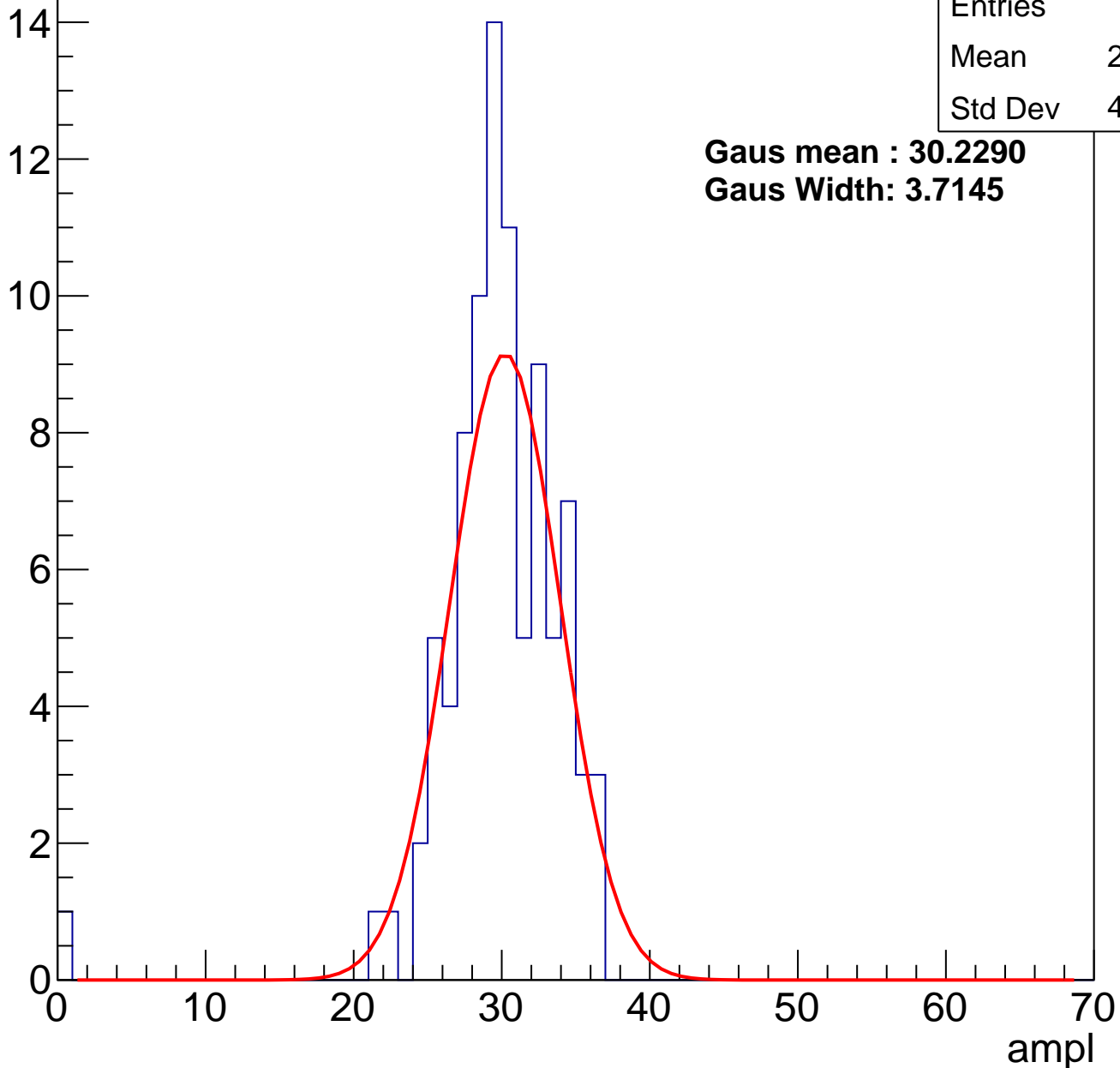
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 89 |
| Mean | 29.34 |
| Std Dev | 4.464 |

Gaus mean : 30.2290

Gaus Width: 3.7145

Entry



B1L001S, U26-ch9, adc1

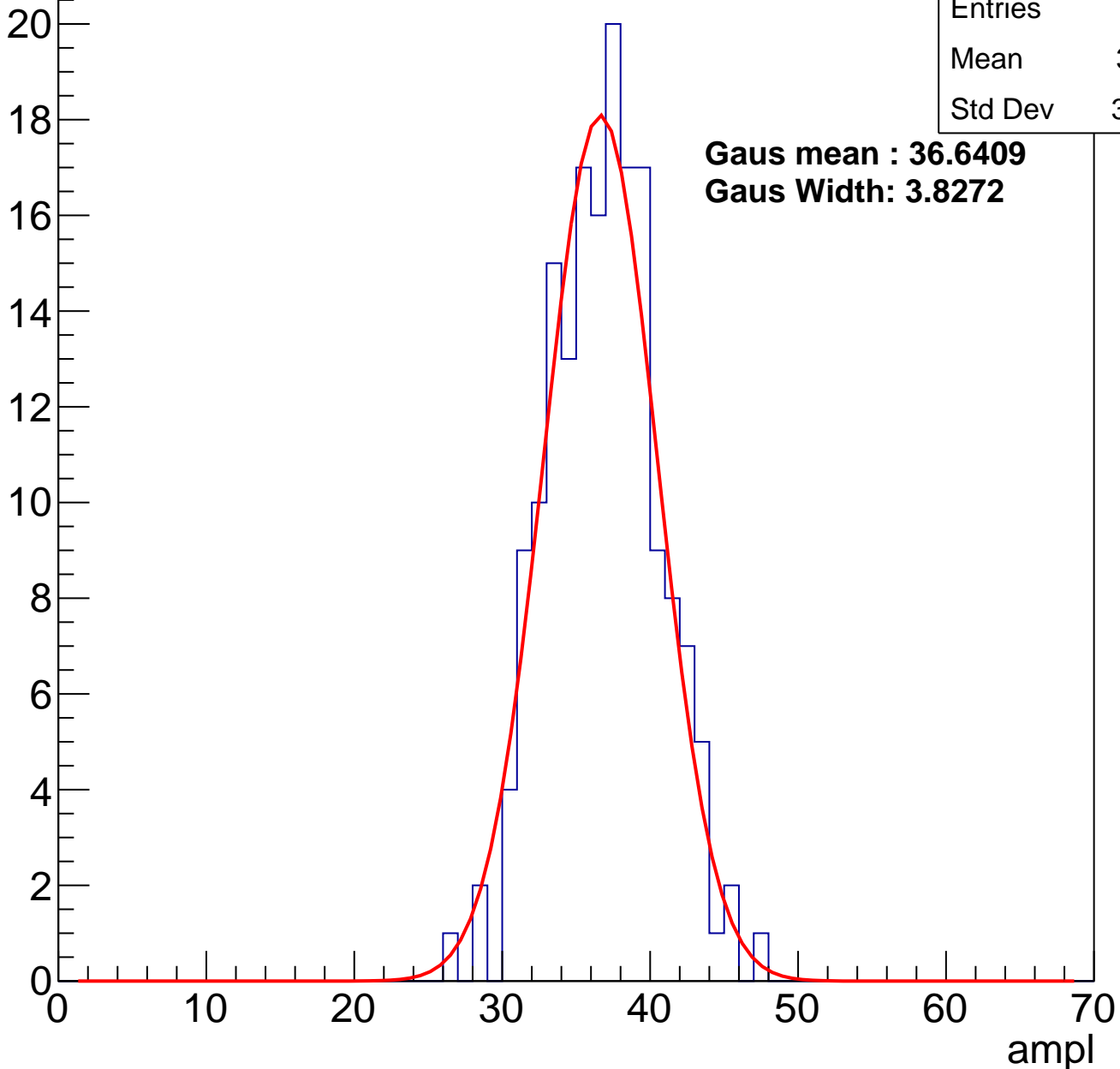
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 174 |
| Mean | 36.41 |
| Std Dev | 3.683 |

Gaus mean : 36.6409

Gaus Width: 3.8272

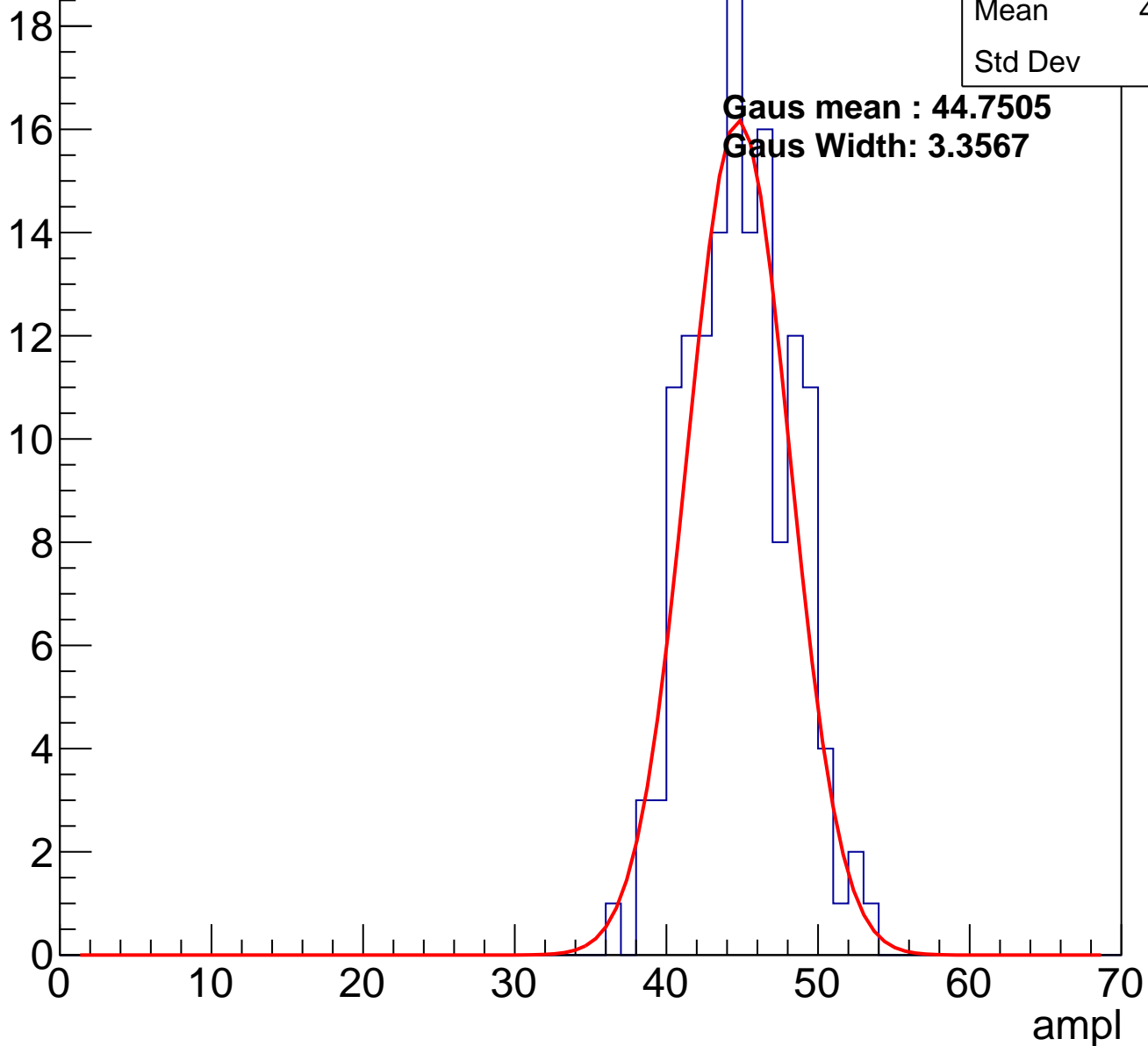
Entry



B1L001S, U26-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

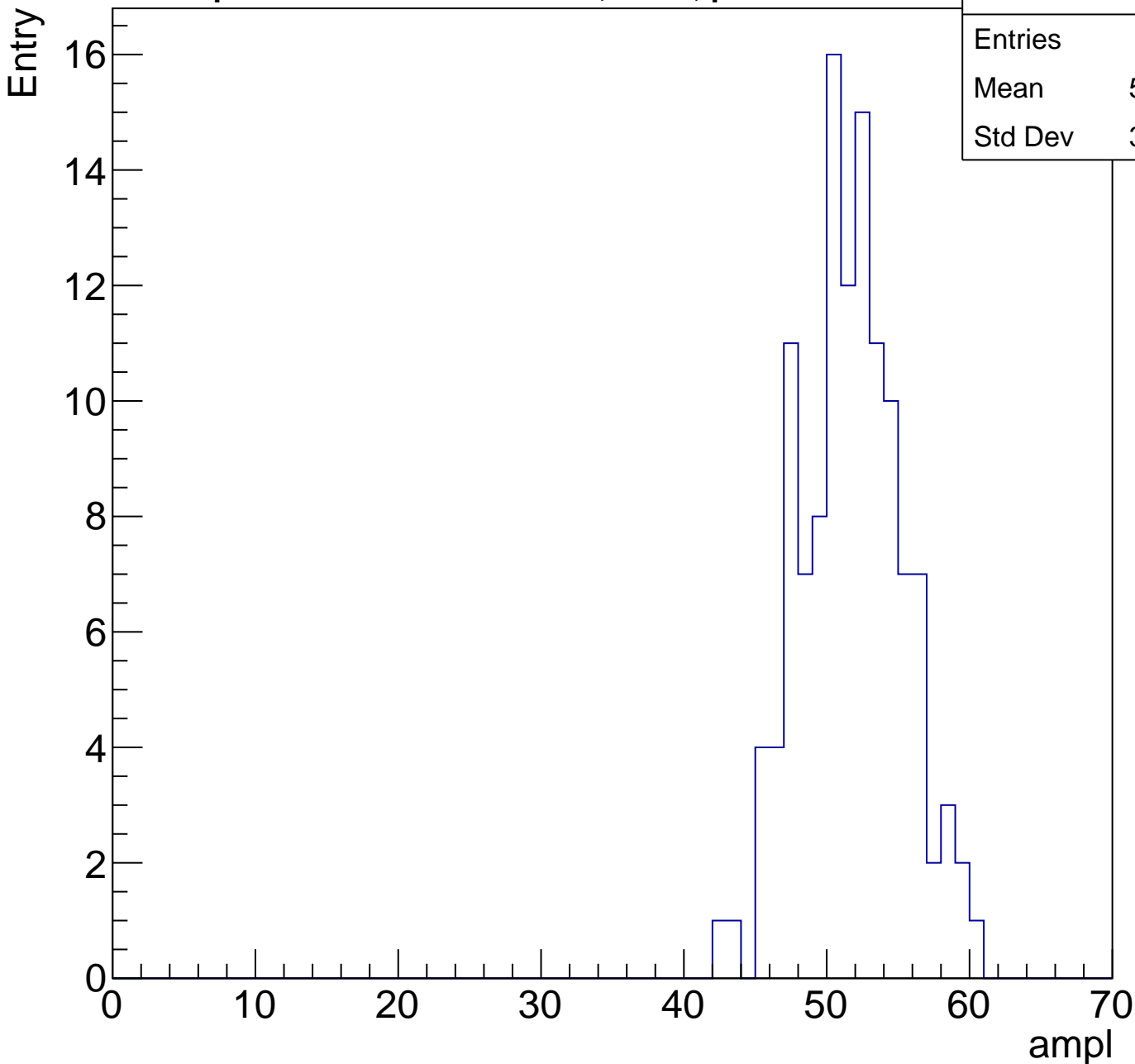
Entry



B1L001S, U26-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

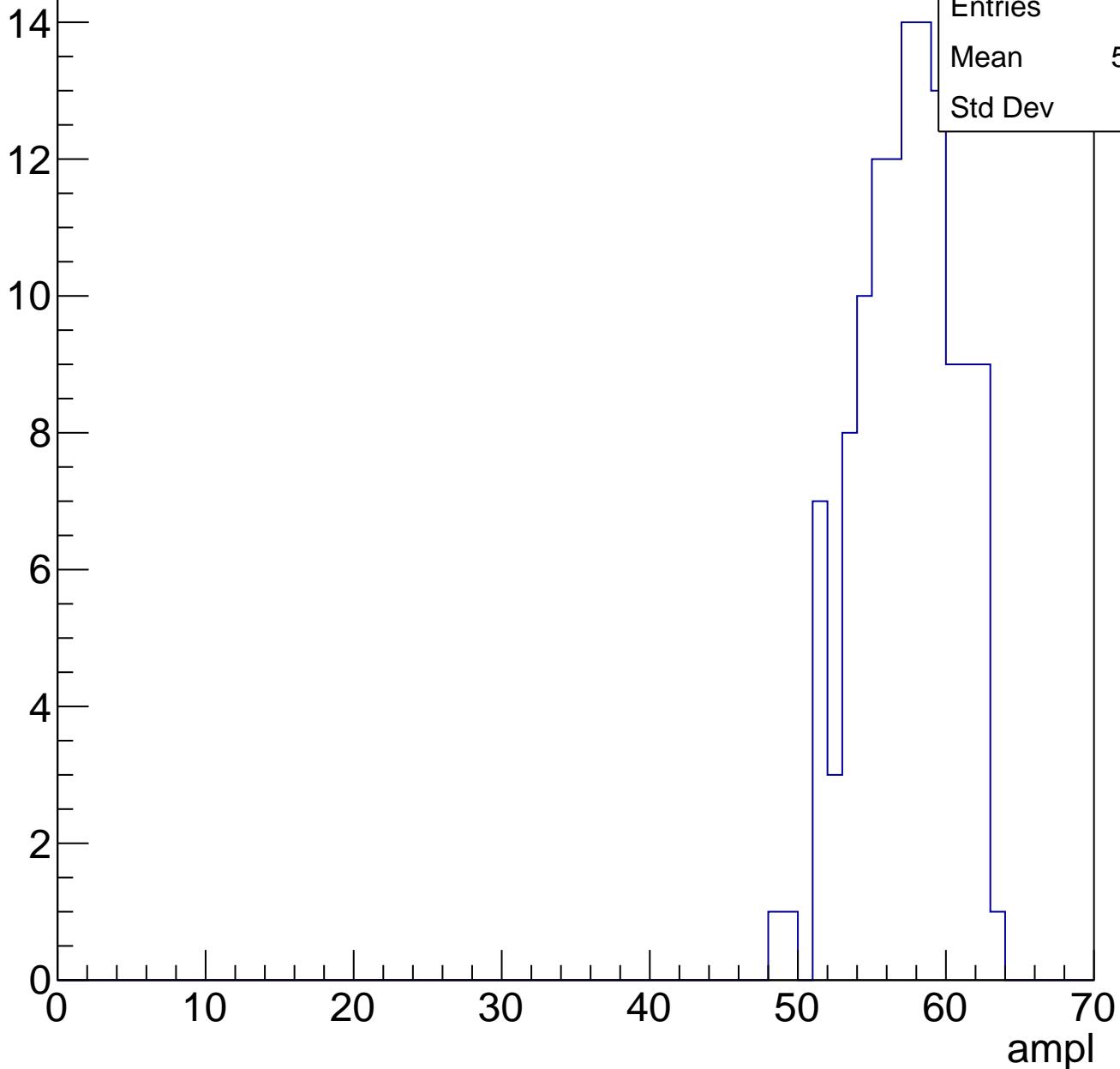
| | |
|---------|-------|
| Entries | 122 |
| Mean | 51.25 |
| Std Dev | 3.544 |



B1L001S, U26-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch9, adc5

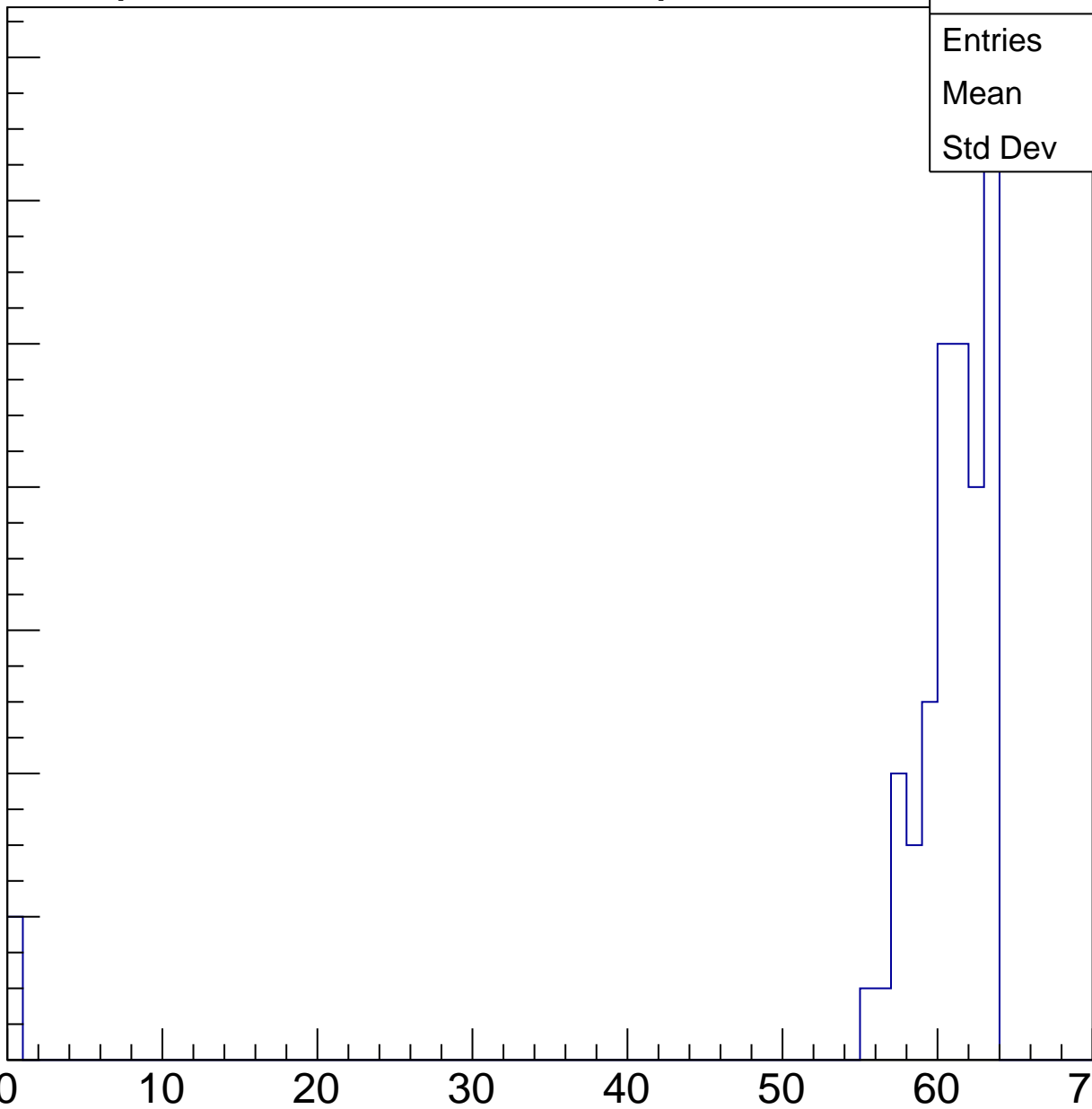
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.55 |
| Std Dev | 11.25 |

ampl

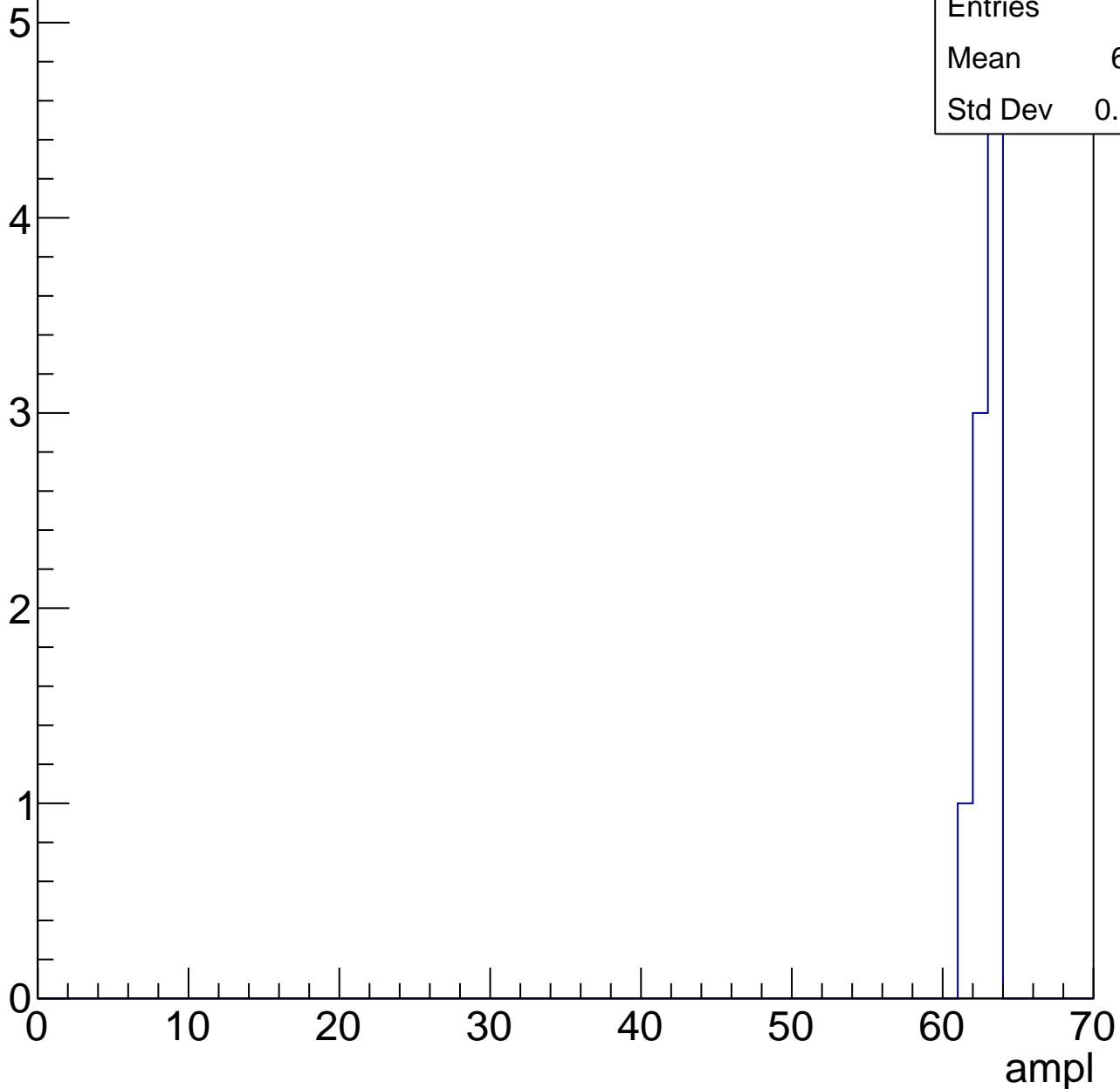


B1L001S, U26-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

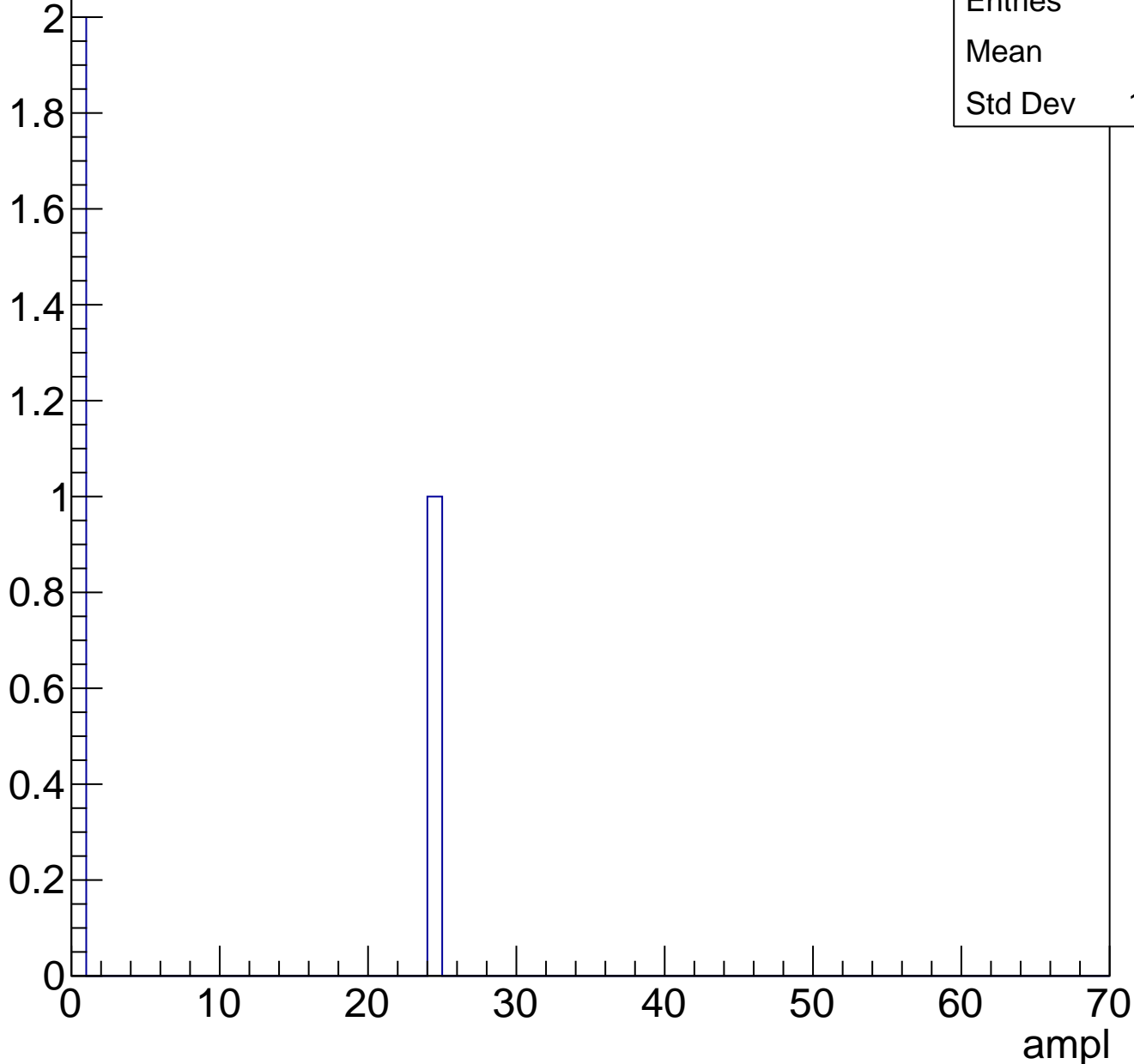
| | |
|---------|--------|
| Entries | 9 |
| Mean | 62.44 |
| Std Dev | 0.6849 |



B1L001S, U26-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch10, adc0

calib_packv5_042523_0143.root, FC#2, port C2

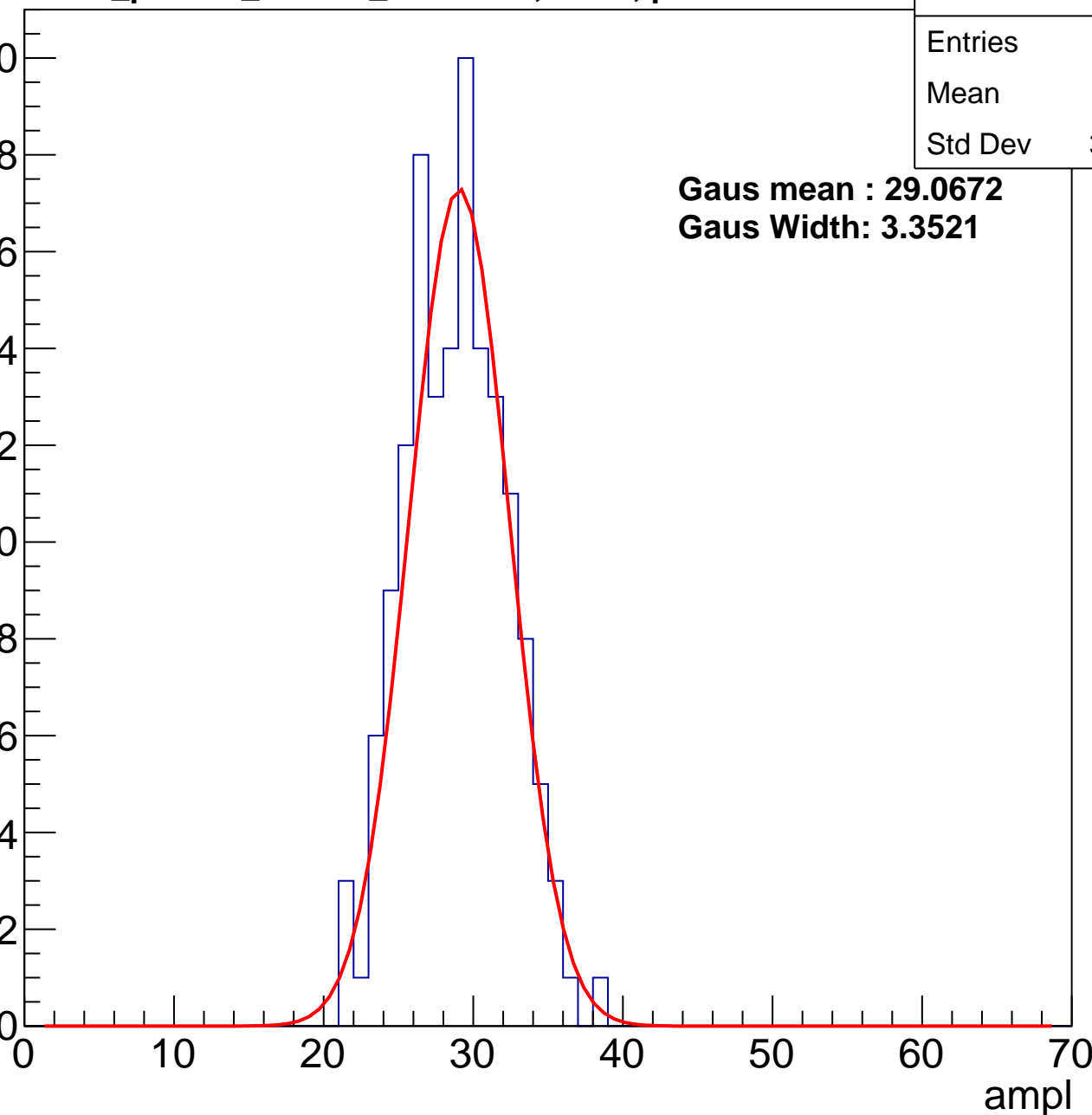
Entry

20
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 152 |
| Mean | 28.41 |
| Std Dev | 3.349 |

Gaus mean : 29.0672

Gaus Width: 3.3521



B1L001S, U26-ch10, adc1

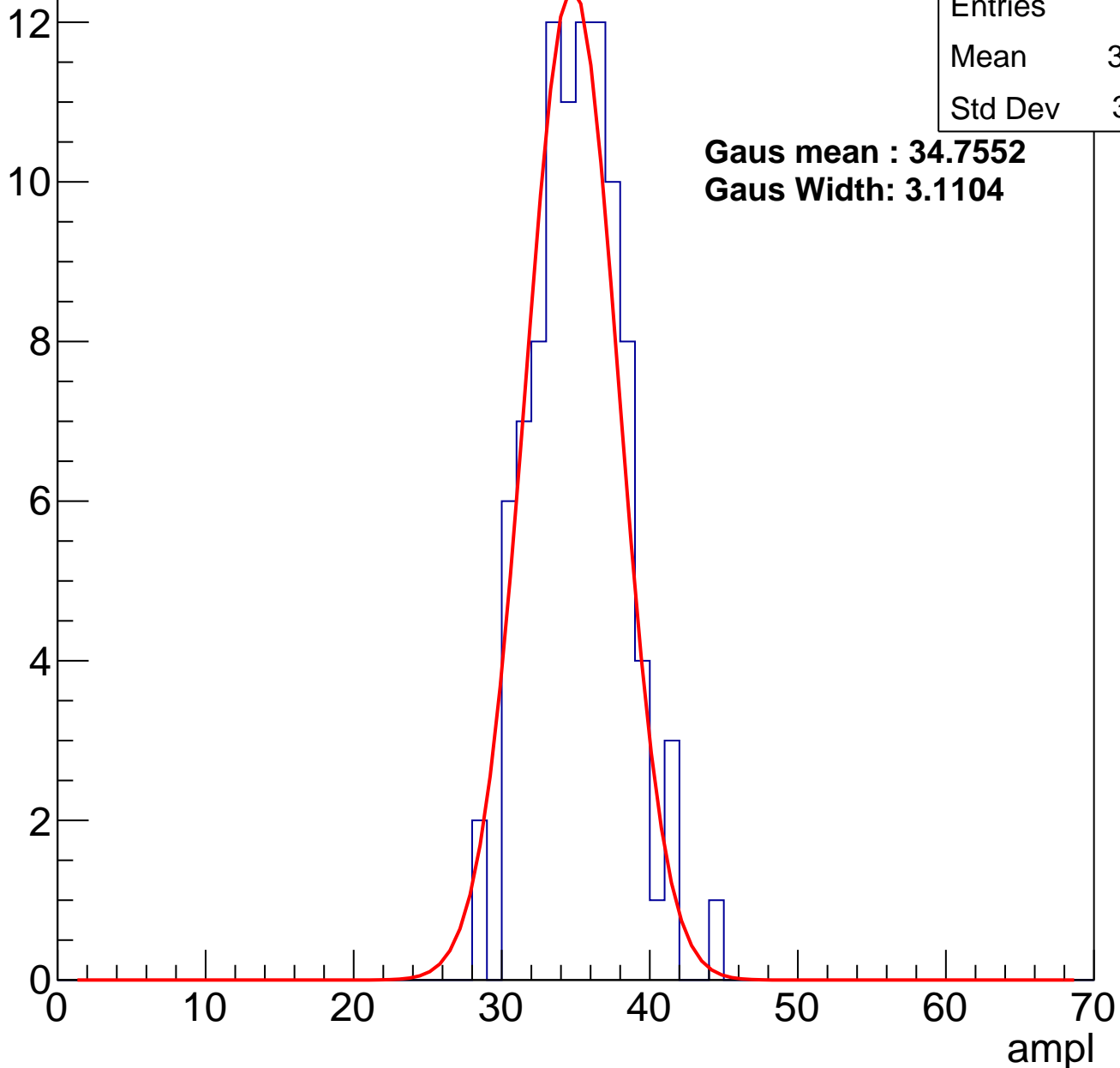
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 97 |
| Mean | 34.72 |
| Std Dev | 3.021 |

Gaus mean : 34.7552

Gaus Width: 3.1104

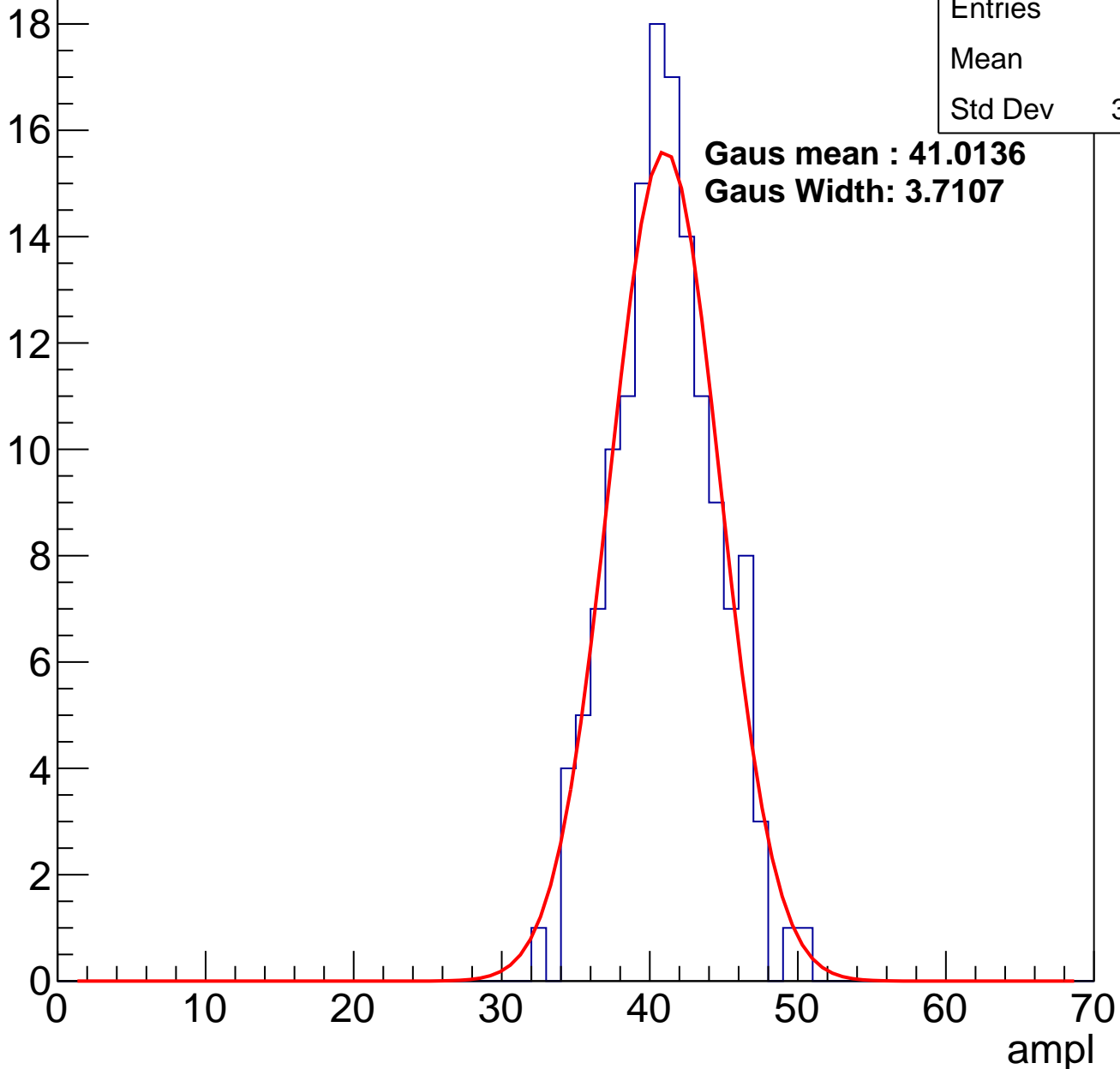
Entry



B1L001S, U26-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

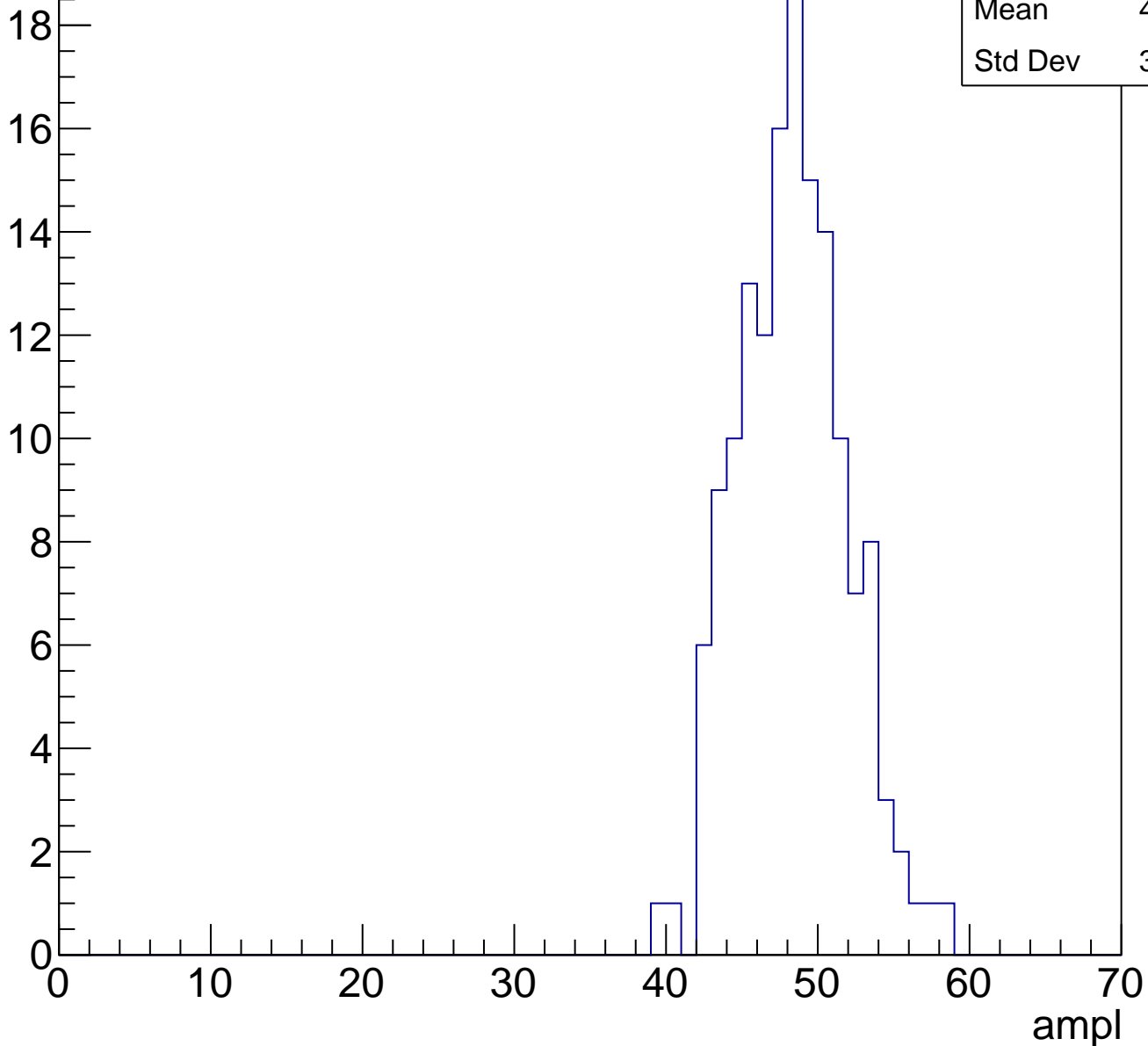


B1L001S, U26-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 149 |
| Mean | 47.89 |
| Std Dev | 3.523 |

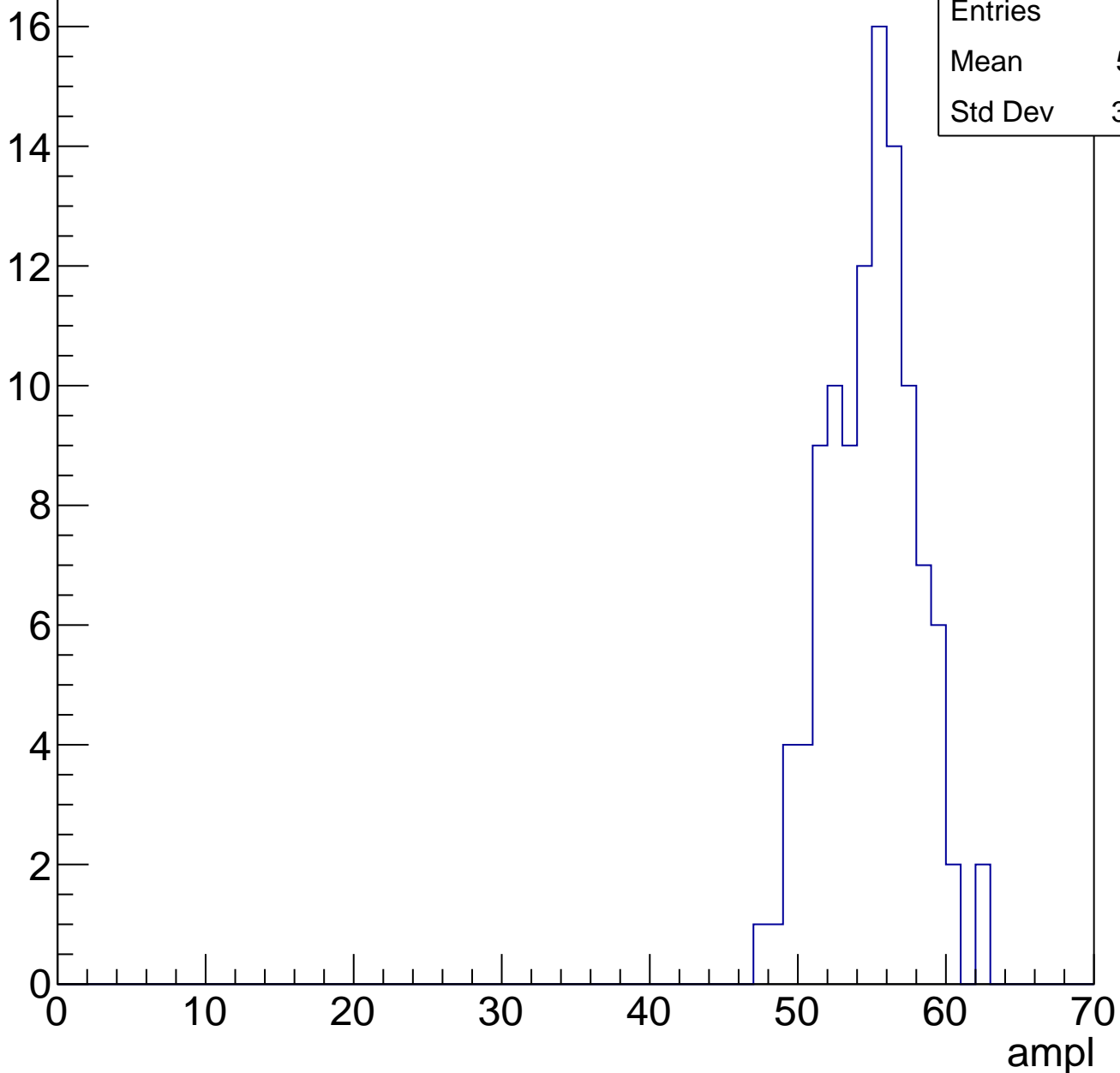
Entry



B1L001S, U26-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch10, adc5

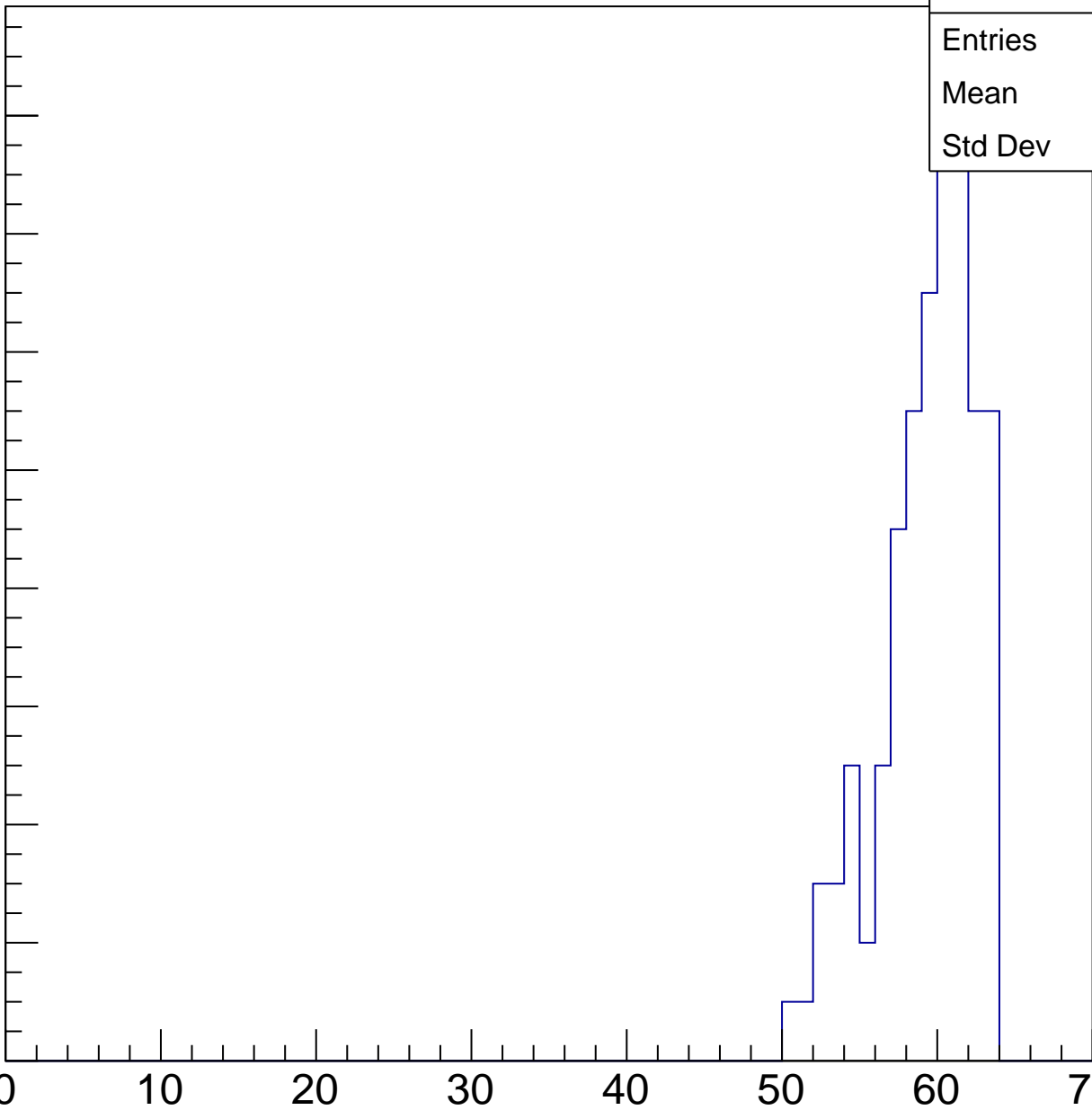
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 109 |
| Mean | 58.95 |
| Std Dev | 3.042 |

ampl

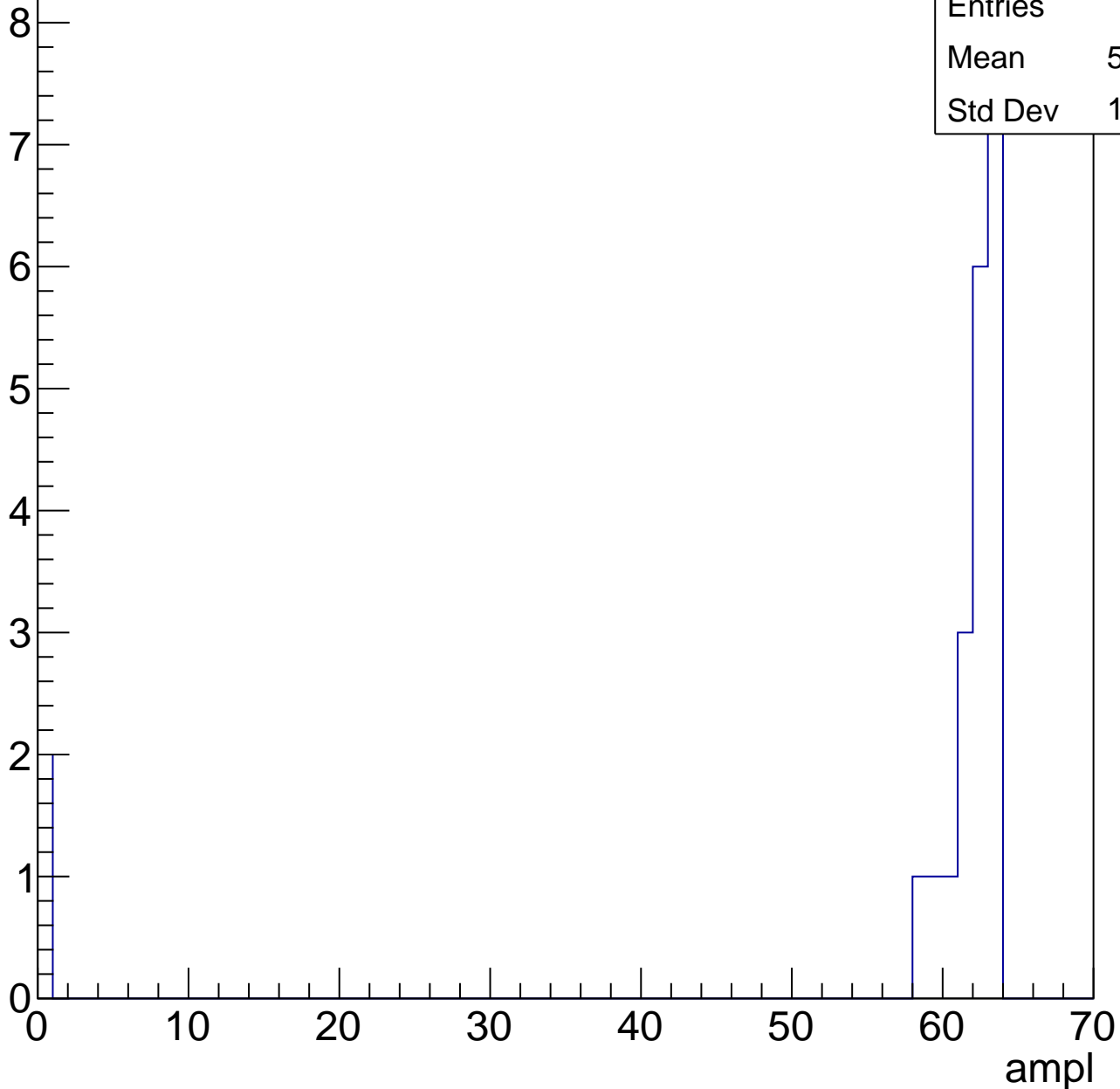


B1L001S, U26-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

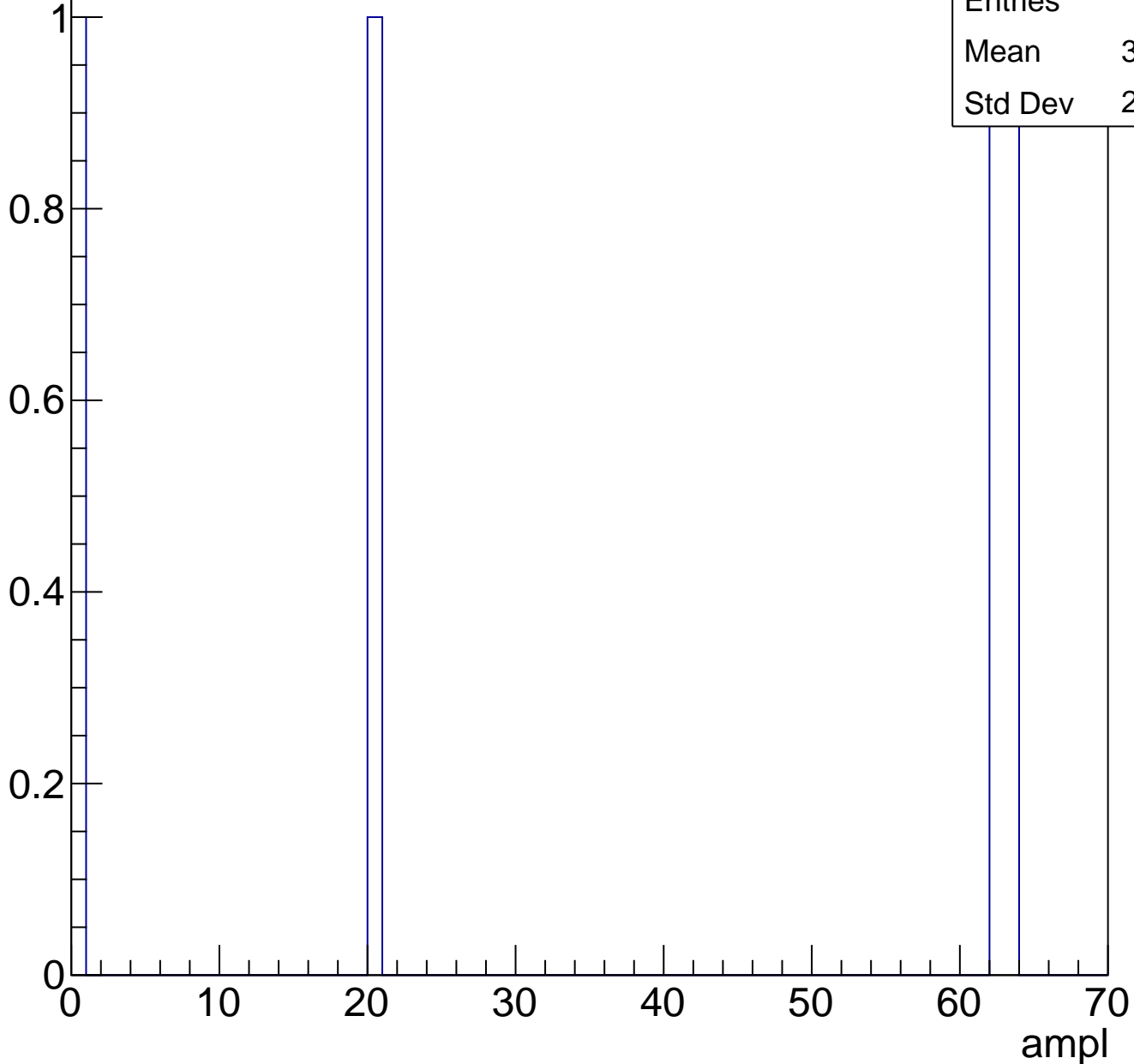
| | |
|---------|-------|
| Entries | 22 |
| Mean | 56.18 |
| Std Dev | 17.82 |



B1L001S, U26-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

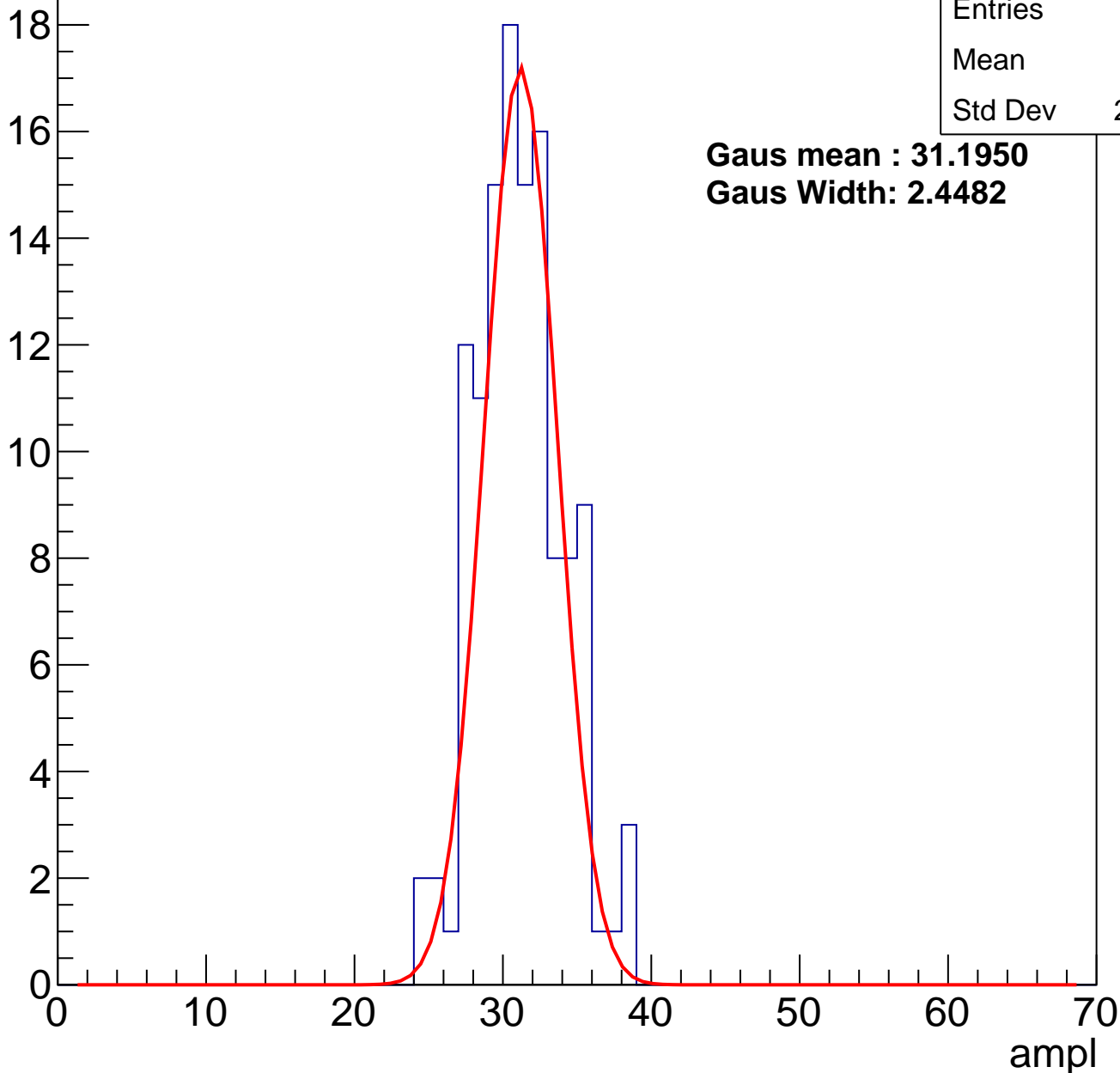


| | |
|---------|-------|
| Entries | 4 |
| Mean | 36.25 |
| Std Dev | 27.19 |

B1L001S, U26-ch11, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch11, adc1

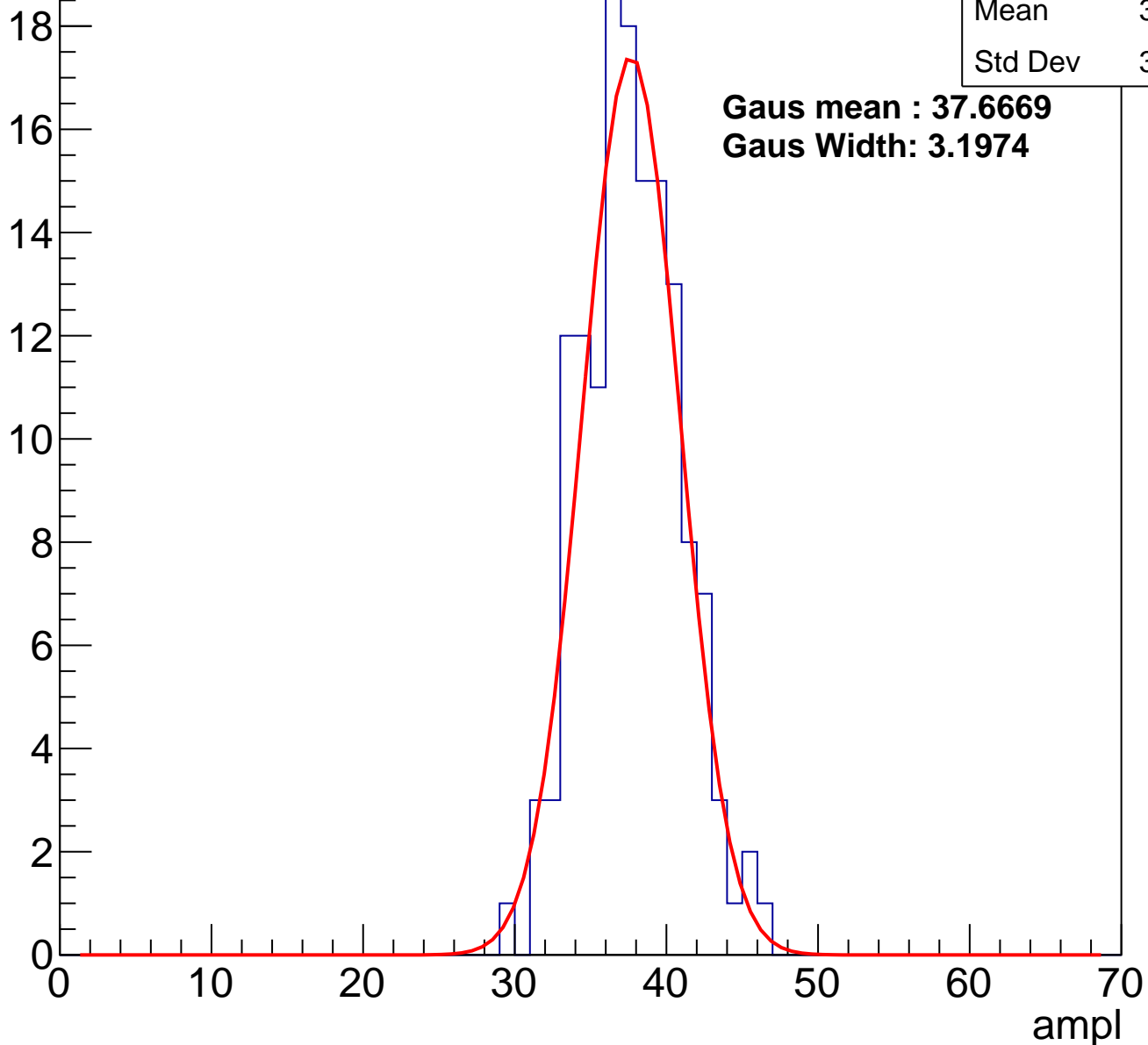
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 144 |
| Mean | 37.24 |
| Std Dev | 3.187 |

Gaus mean : 37.6669

Gaus Width: 3.1974

Entry



B1L001S, U26-ch11, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

135

Mean

44.41

Std Dev

3.251

Gaus mean : 44.8020

Gaus Width: 3.2905

0

10

20

30

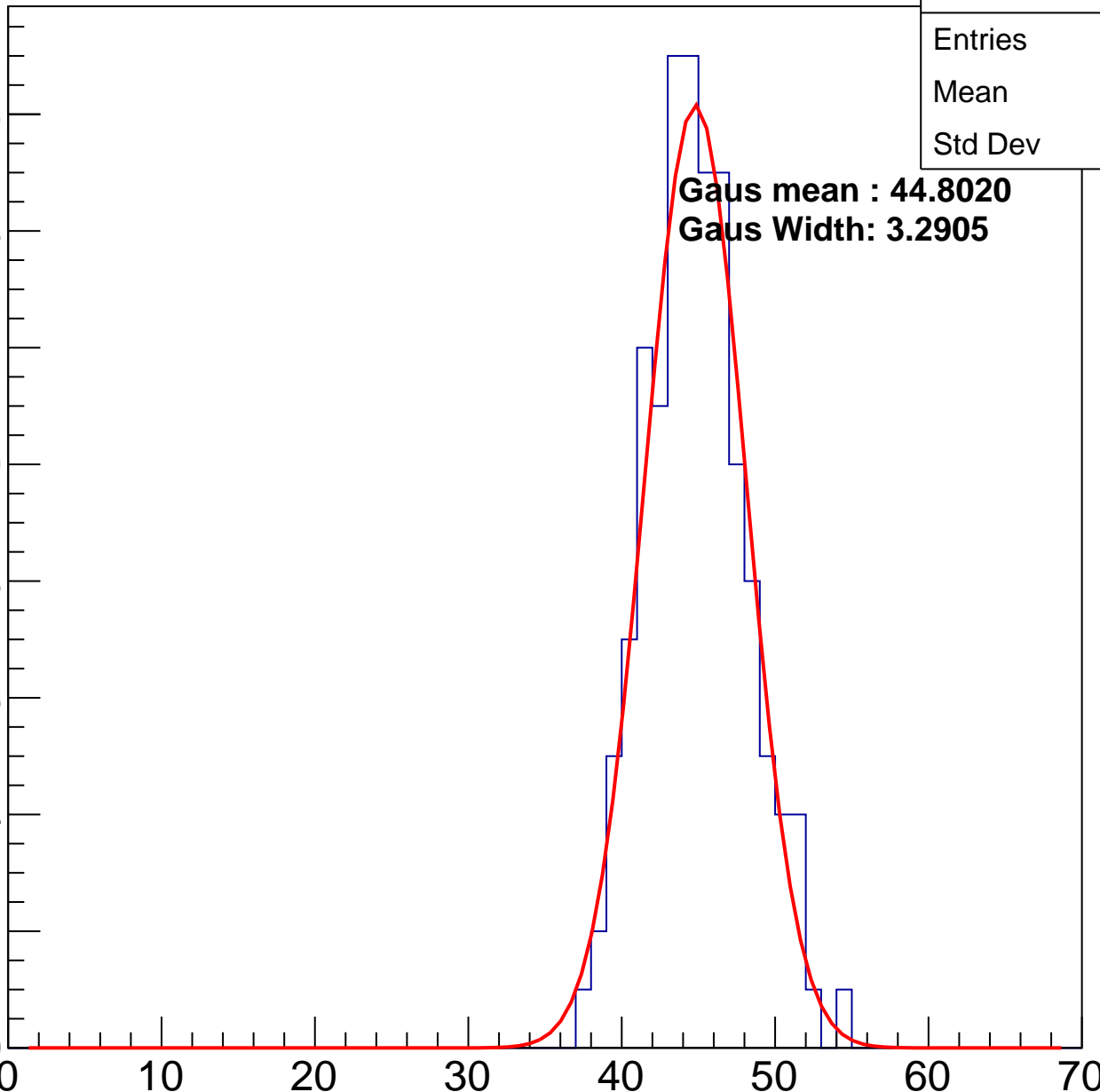
40

50

60

70

ampl



B1L001S, U26-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

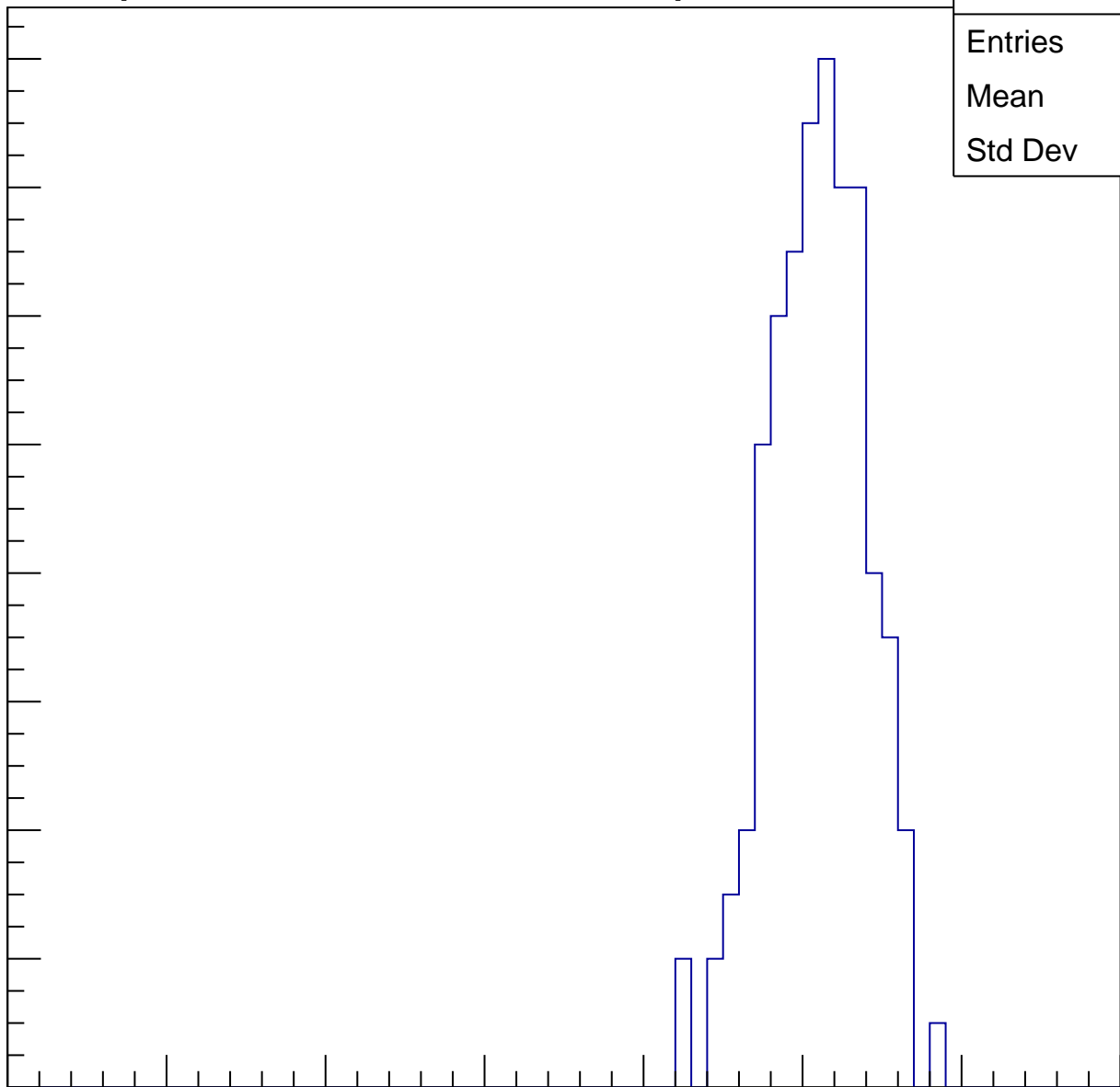
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 125 |
| Mean | 50.47 |
| Std Dev | 3.061 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

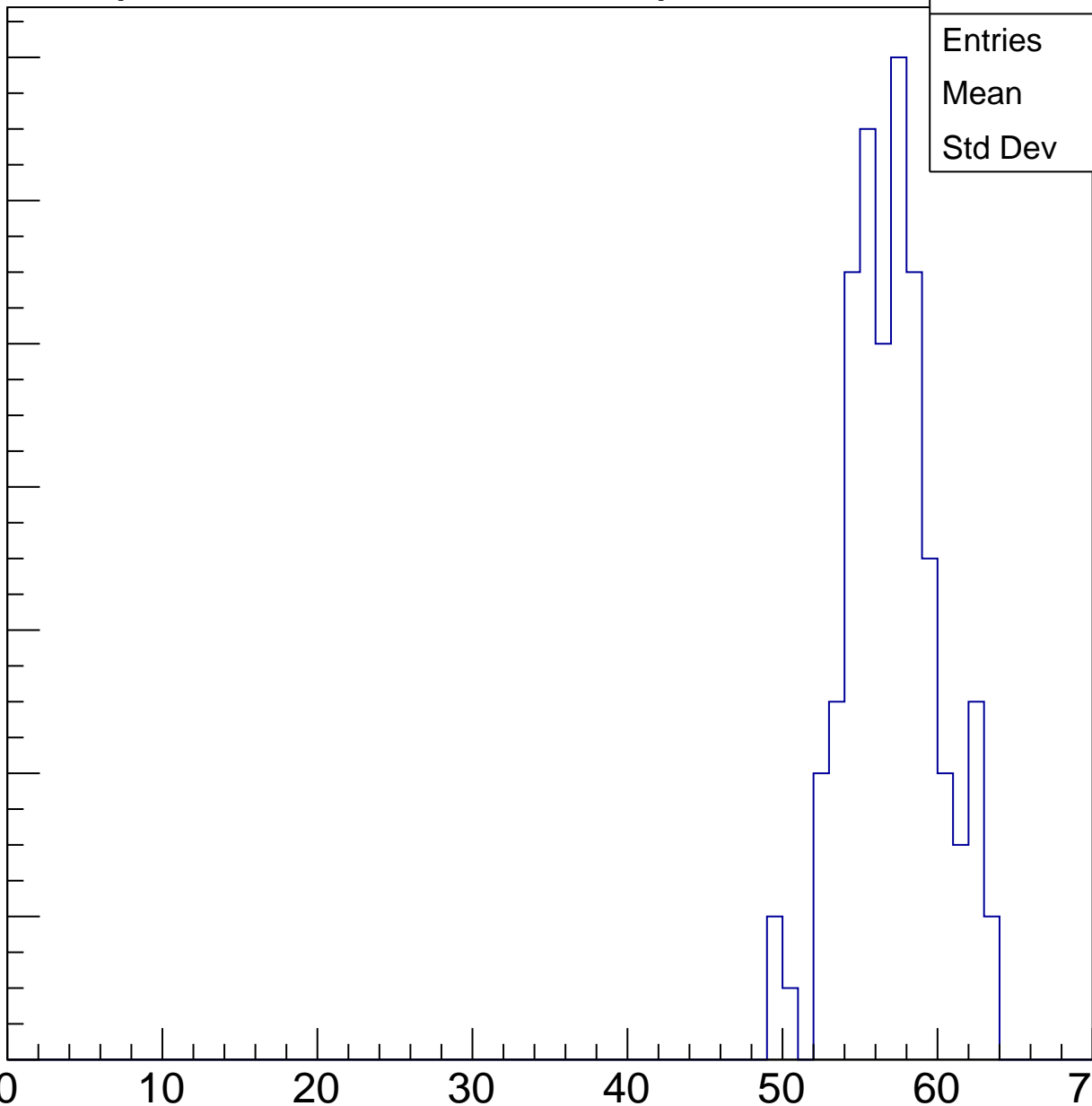
| | |
|---------|-------|
| Entries | 92 |
| Mean | 56.5 |
| Std Dev | 2.976 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch11, adc5

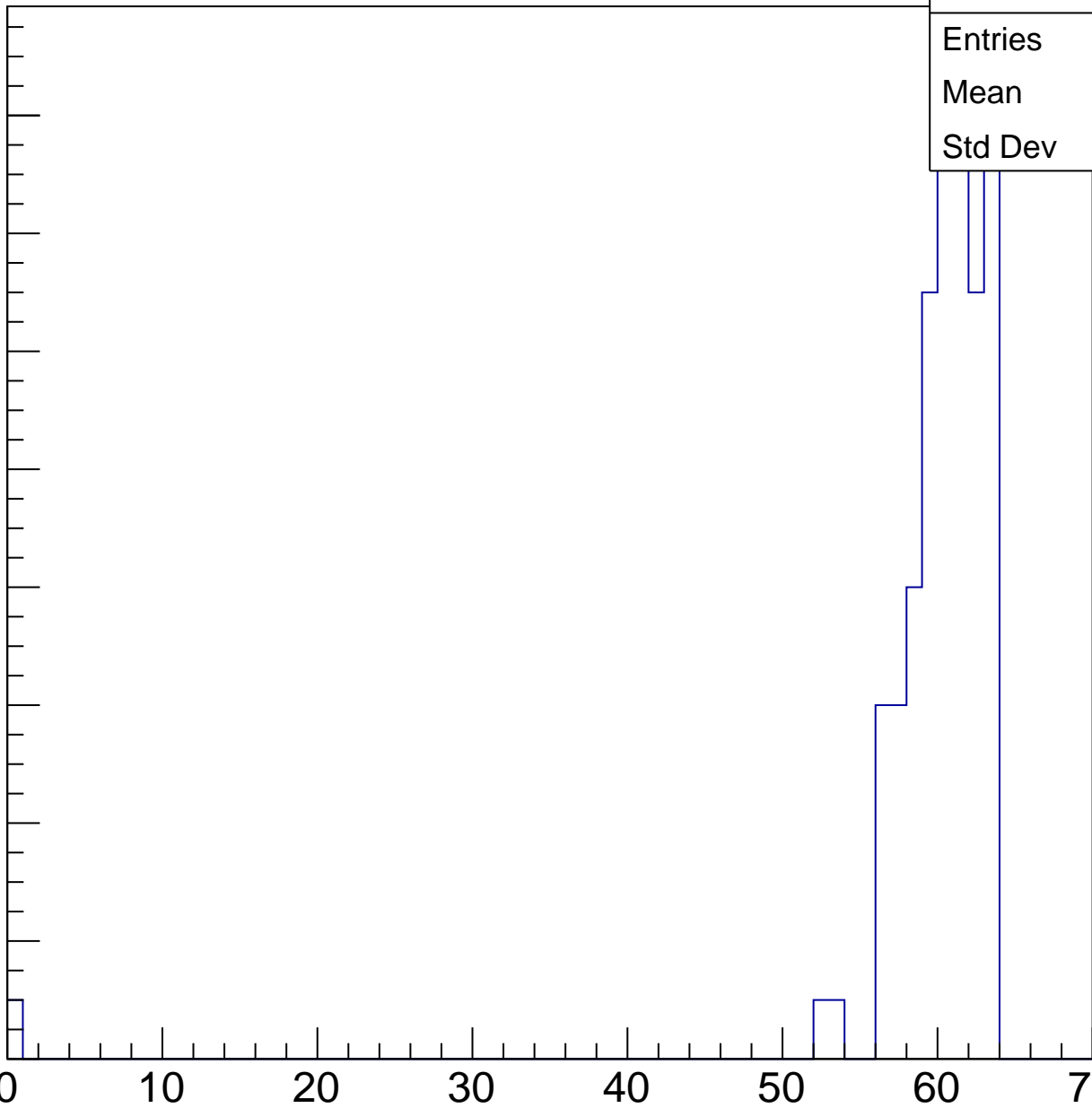
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 59.44 |
| Std Dev | 6.46 |

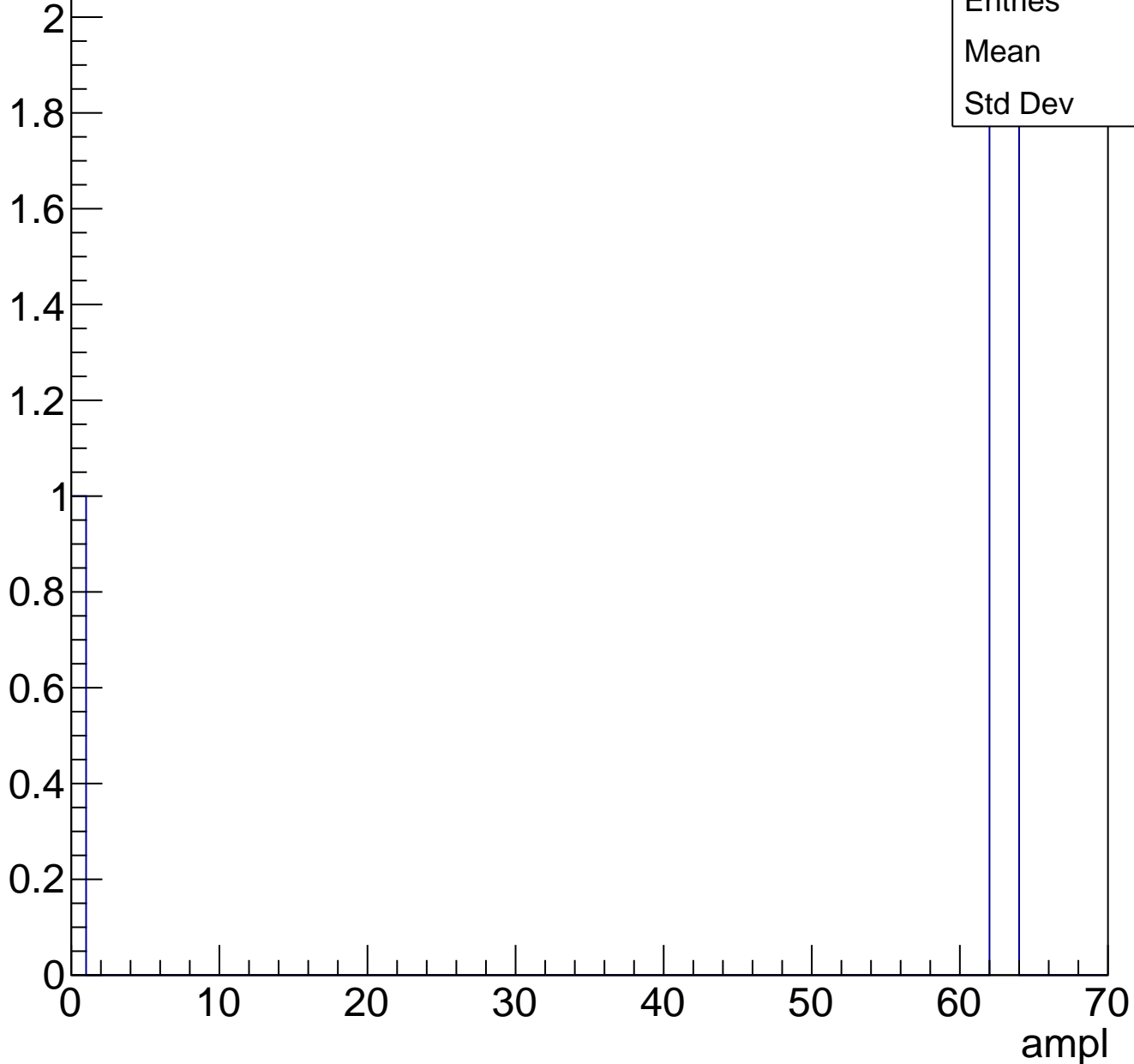
ampl



B1L001S, U26-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

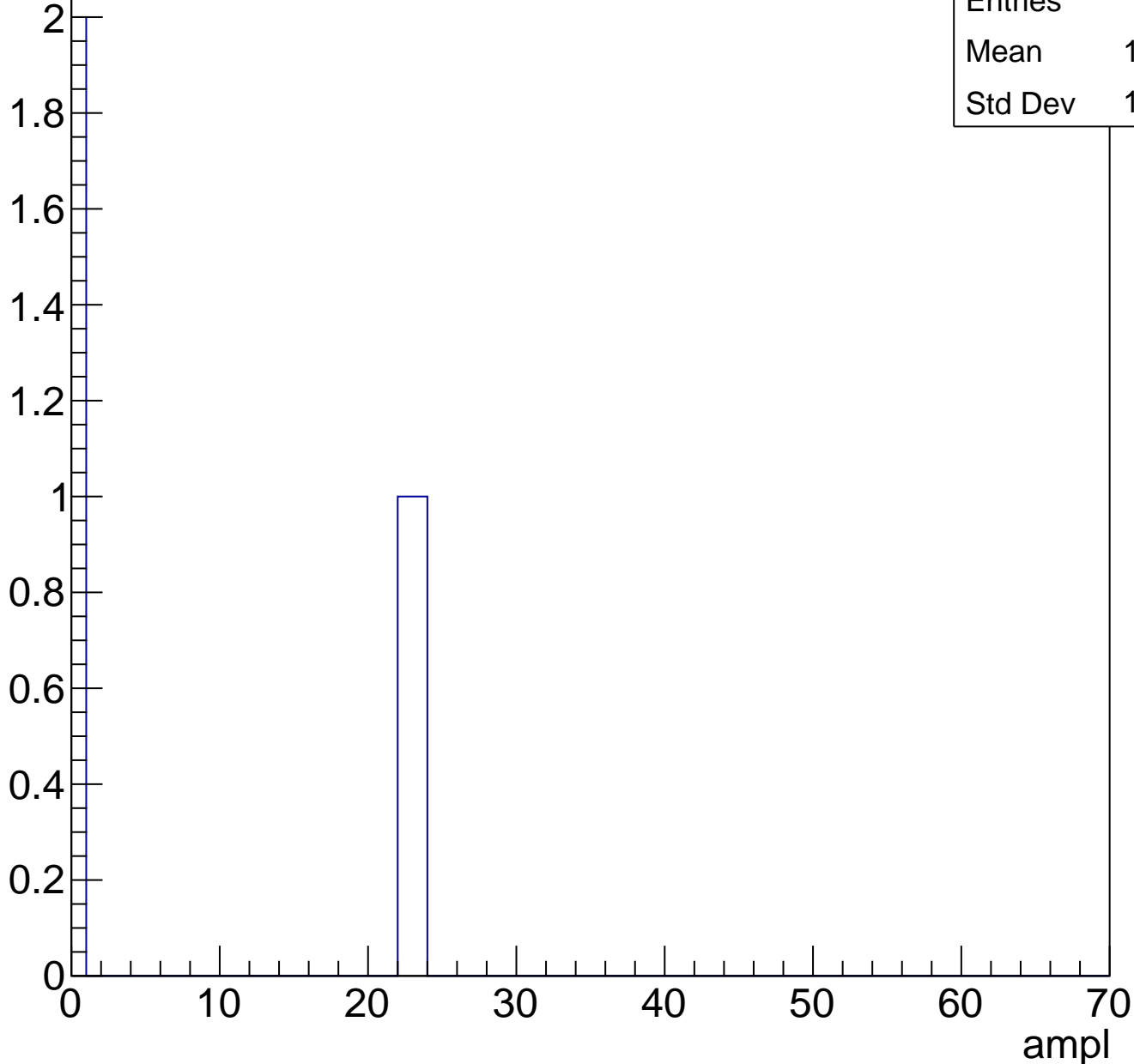
Entry



B1L001S, U26-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 11.25 |
| Std Dev | 11.26 |

B1L001S, U26-ch12, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 30.18 |
| Std Dev | 3.355 |

Gaus mean : 30.5116

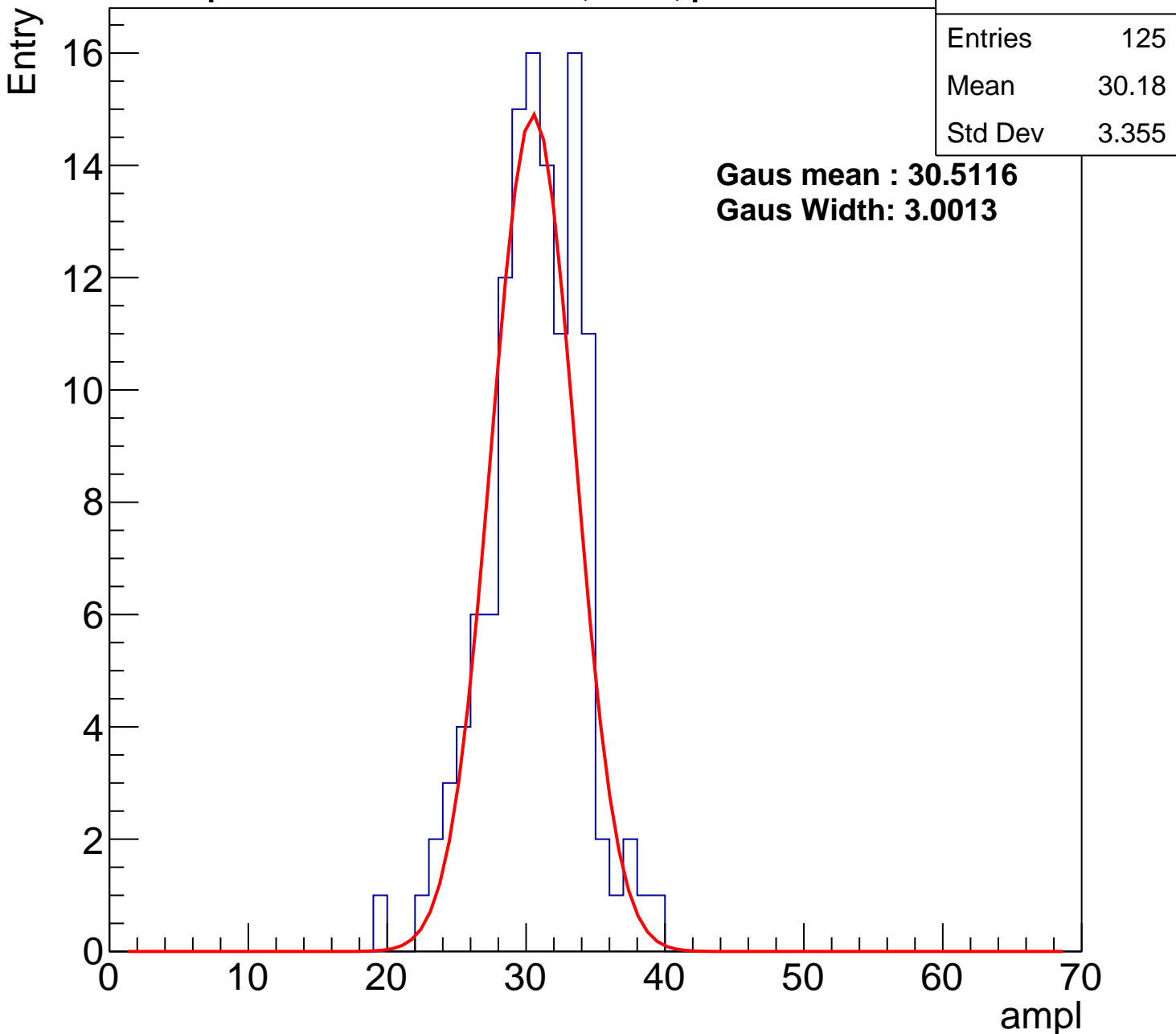
Gaus Width: 3.0013

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch12, adc1

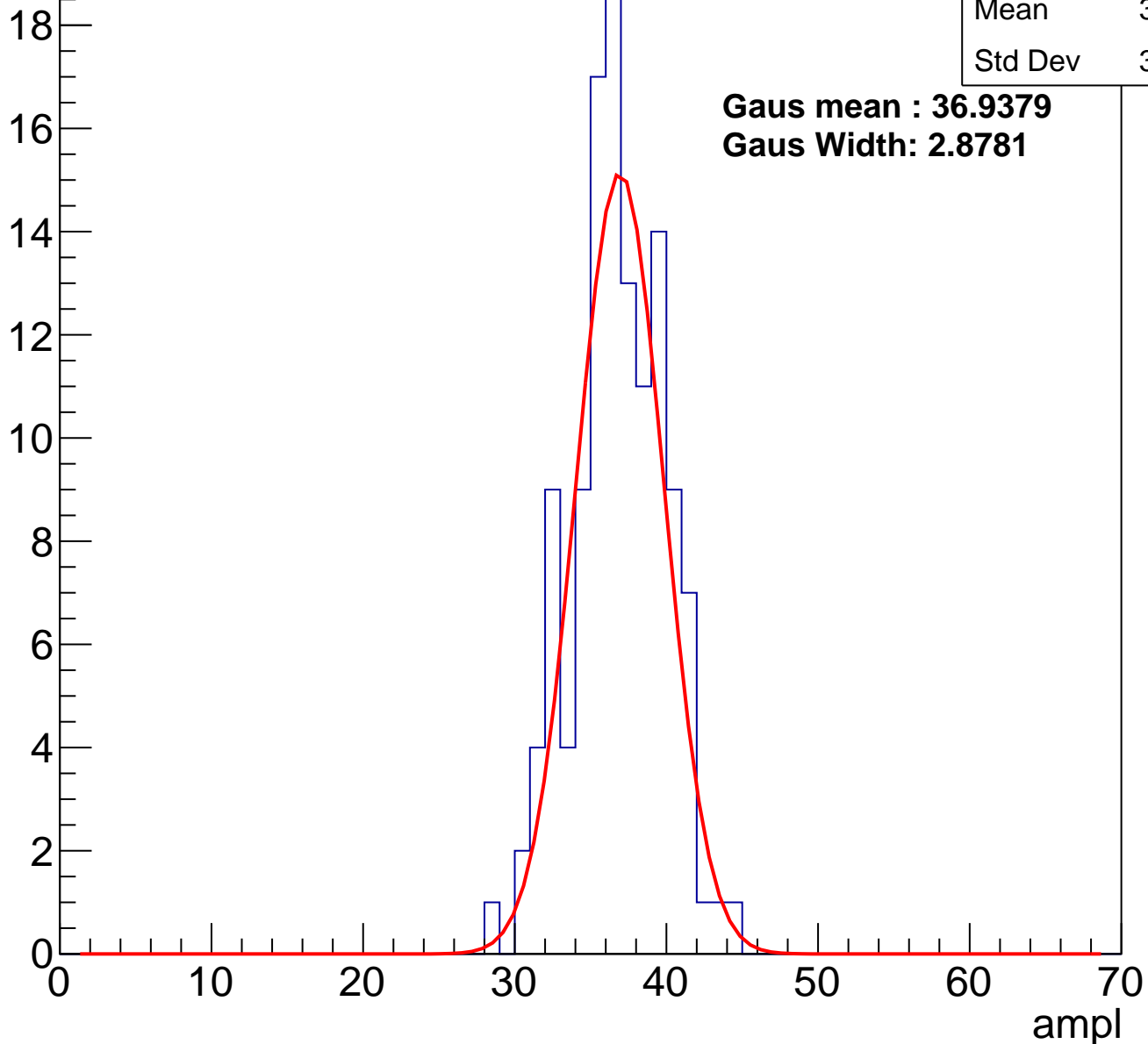
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.38 |
| Std Dev | 3.006 |

Gaus mean : 36.9379

Gaus Width: 2.8781

Entry



B1L001S, U26-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

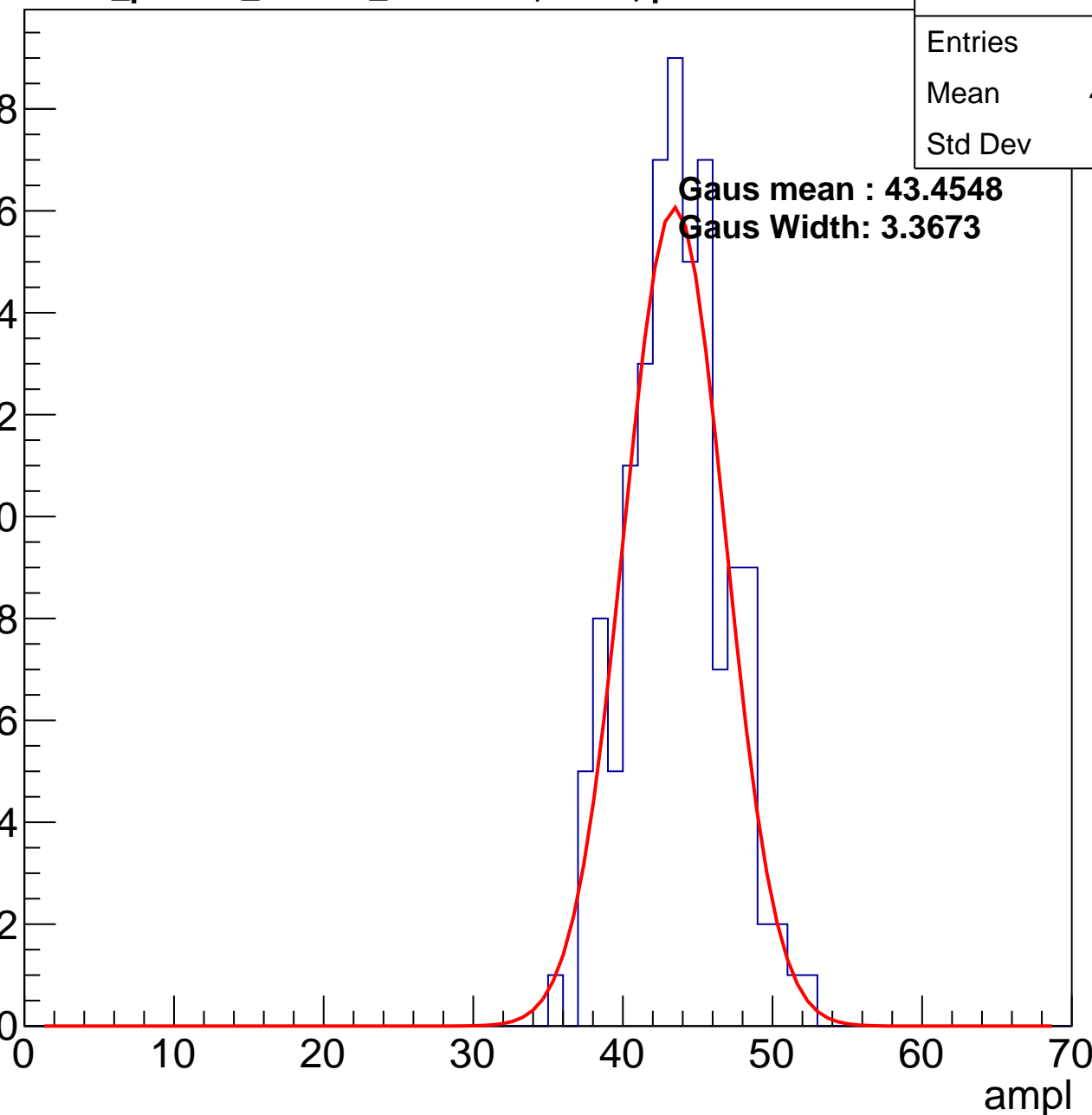
Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 142 |
| Mean | 43.14 |
| Std Dev | 3.271 |

Gaus mean : 43.4548

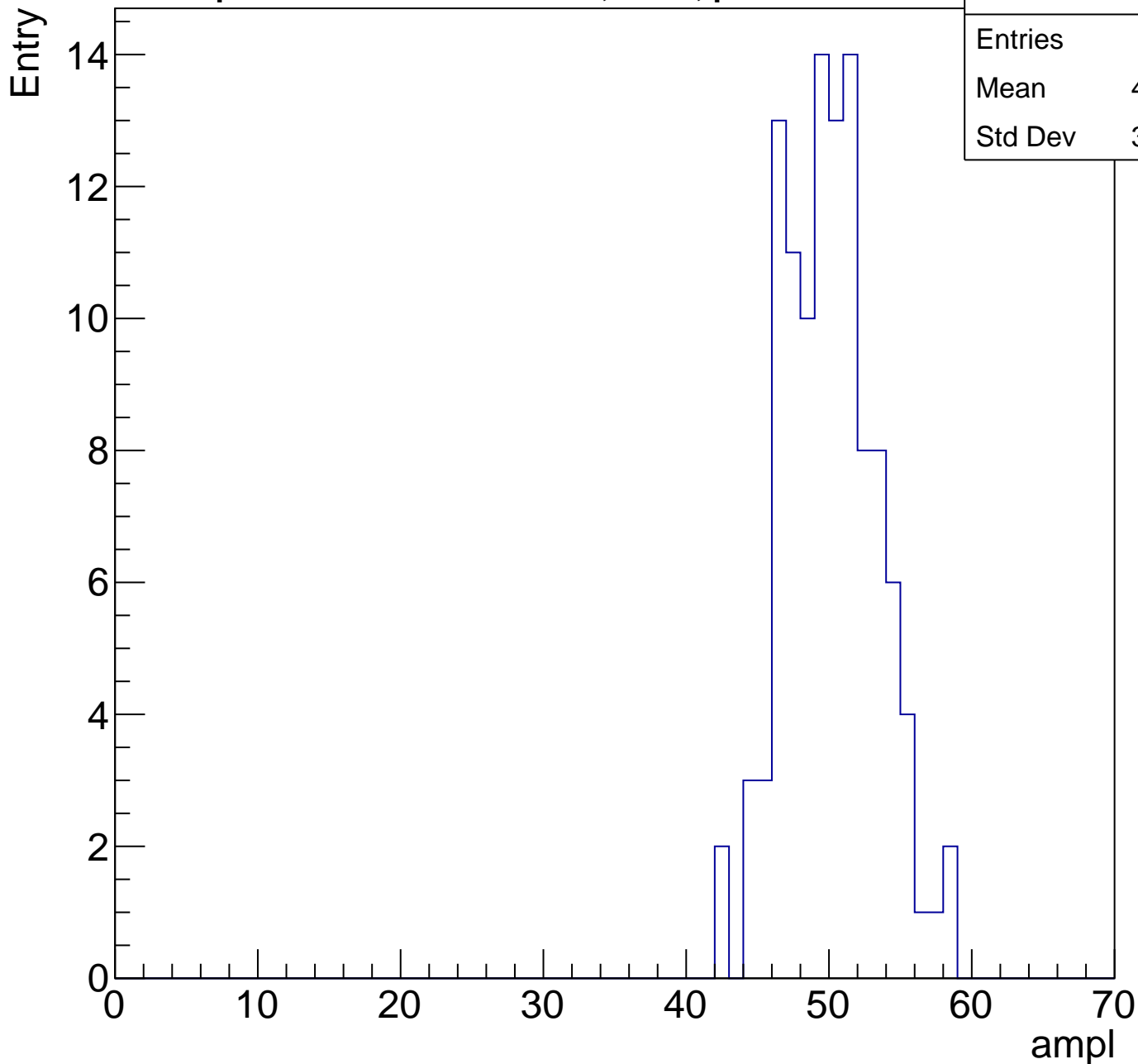
Gaus Width: 3.3673



B1L001S, U26-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

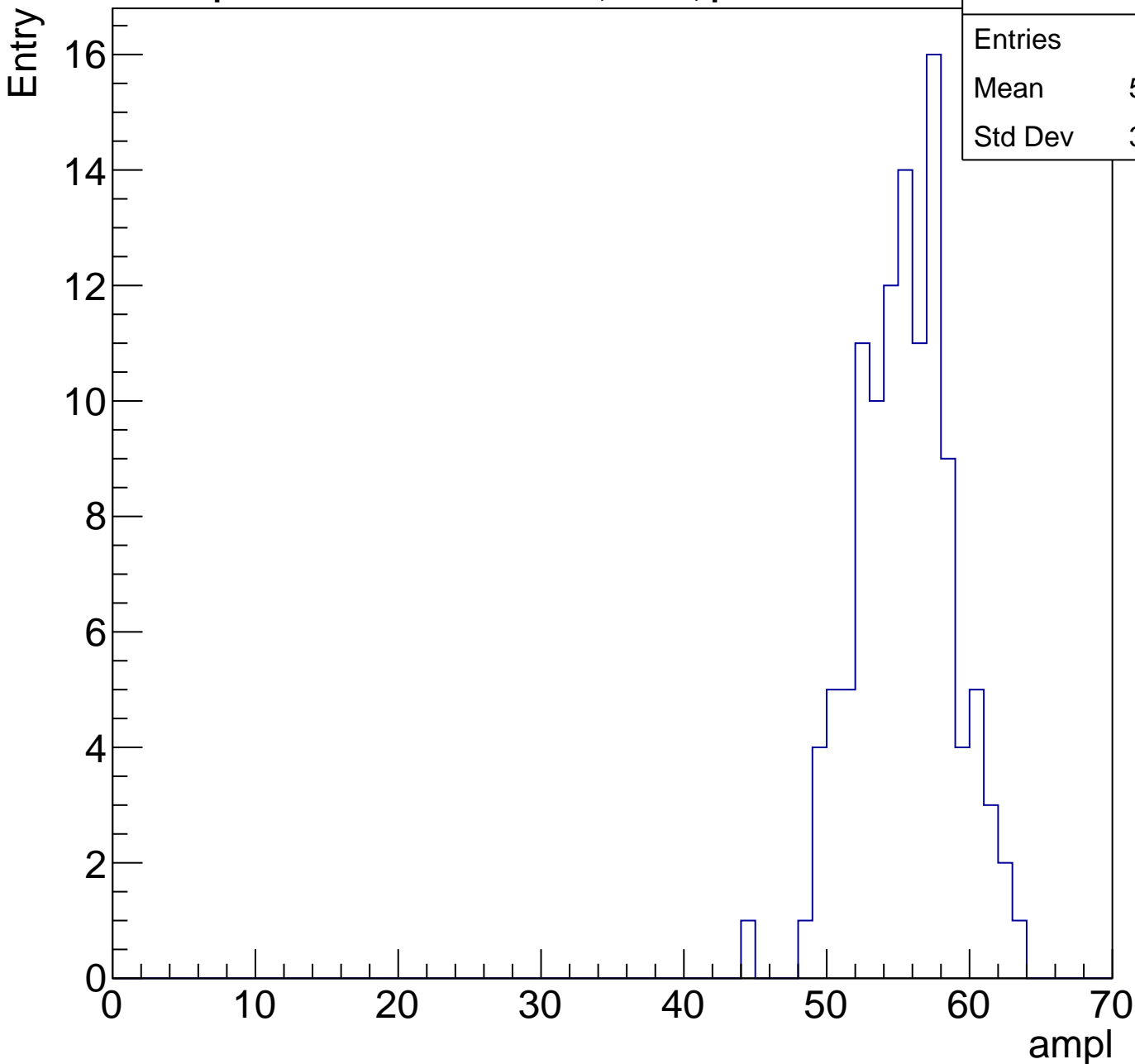
| | |
|---------|-------|
| Entries | 113 |
| Mean | 49.64 |
| Std Dev | 3.237 |



B1L001S, U26-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

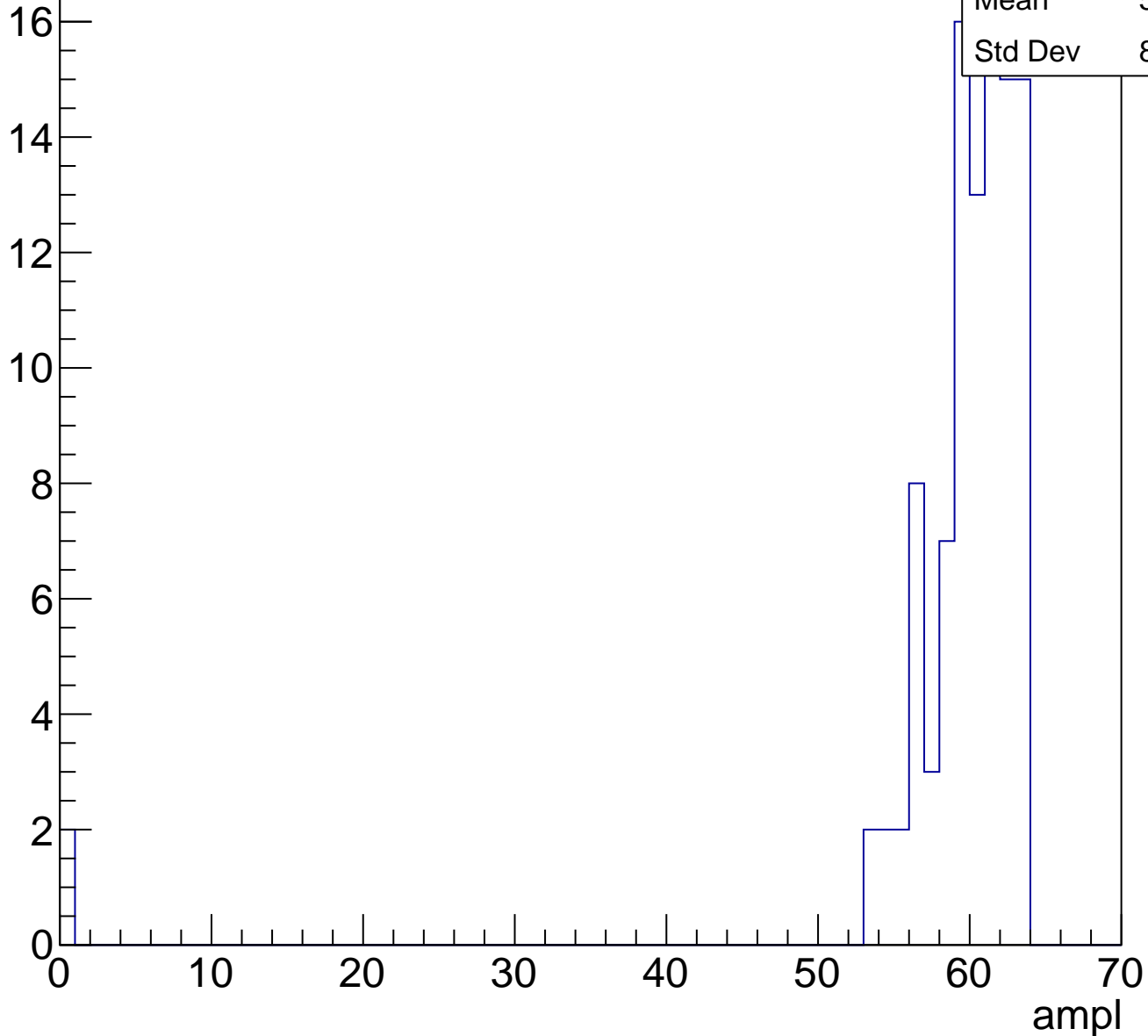
| | |
|---------|-------|
| Entries | 114 |
| Mean | 54.99 |
| Std Dev | 3.357 |



B1L001S, U26-ch12, adc5

calib_packv5_042523_0143.root, FC#2, port C2

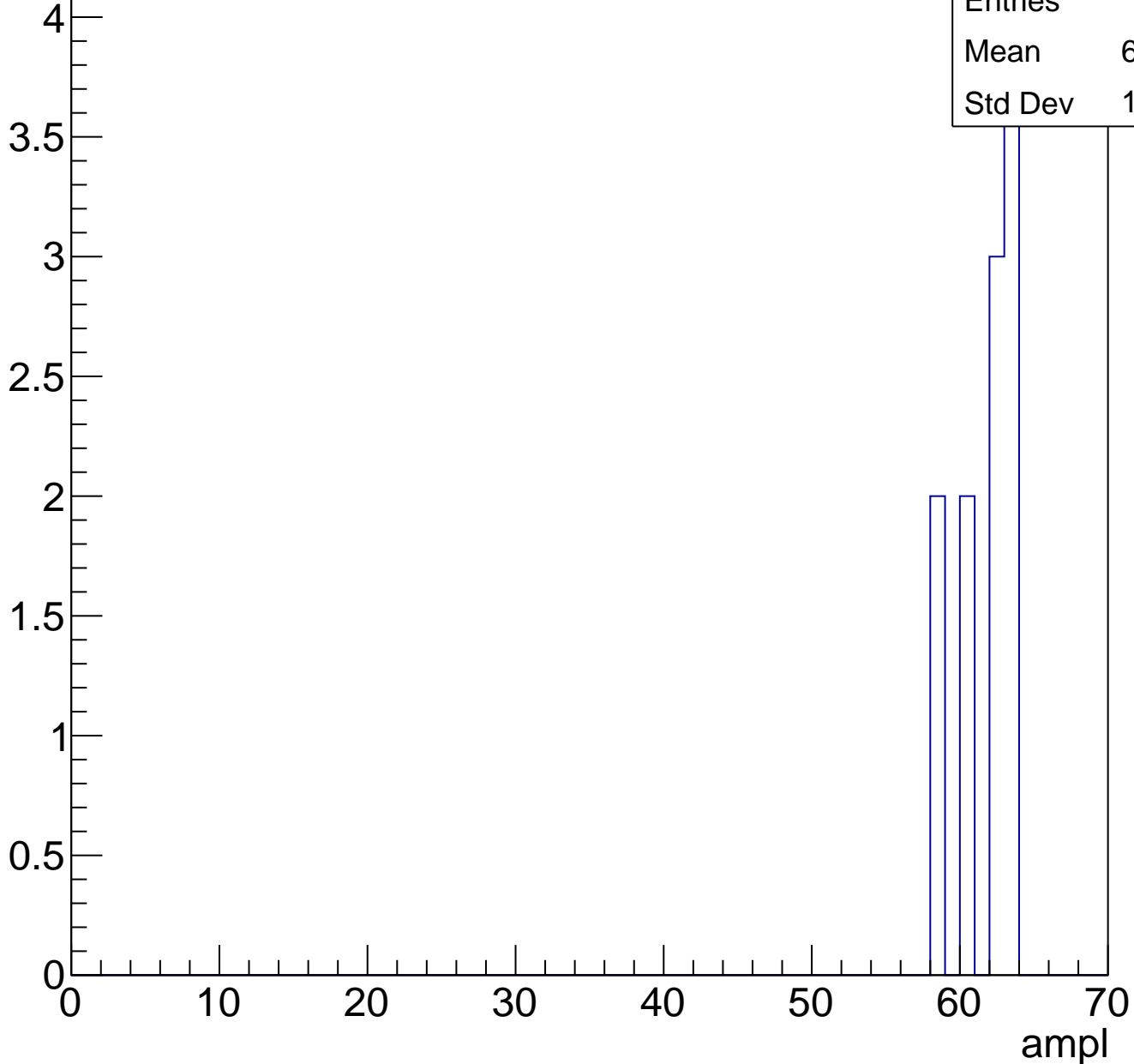
Entry



B1L001S, U26-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 11 |
| Mean | 61.27 |
| Std Dev | 1.863 |

B1L001S, U26-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch13, adc0

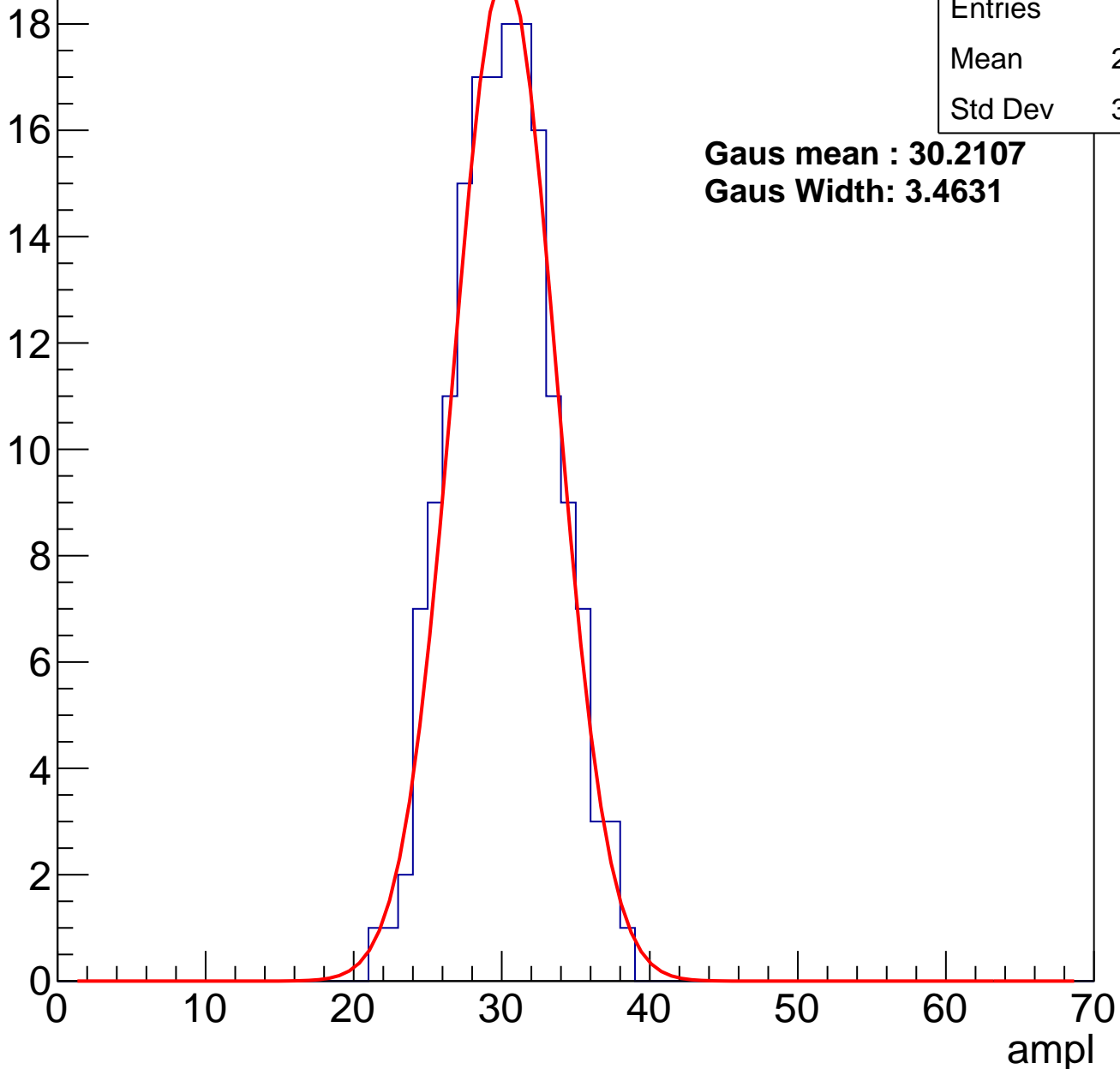
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 166 |
| Mean | 29.66 |
| Std Dev | 3.397 |

Gaus mean : 30.2107

Gaus Width: 3.4631

Entry



B1L001S, U26-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

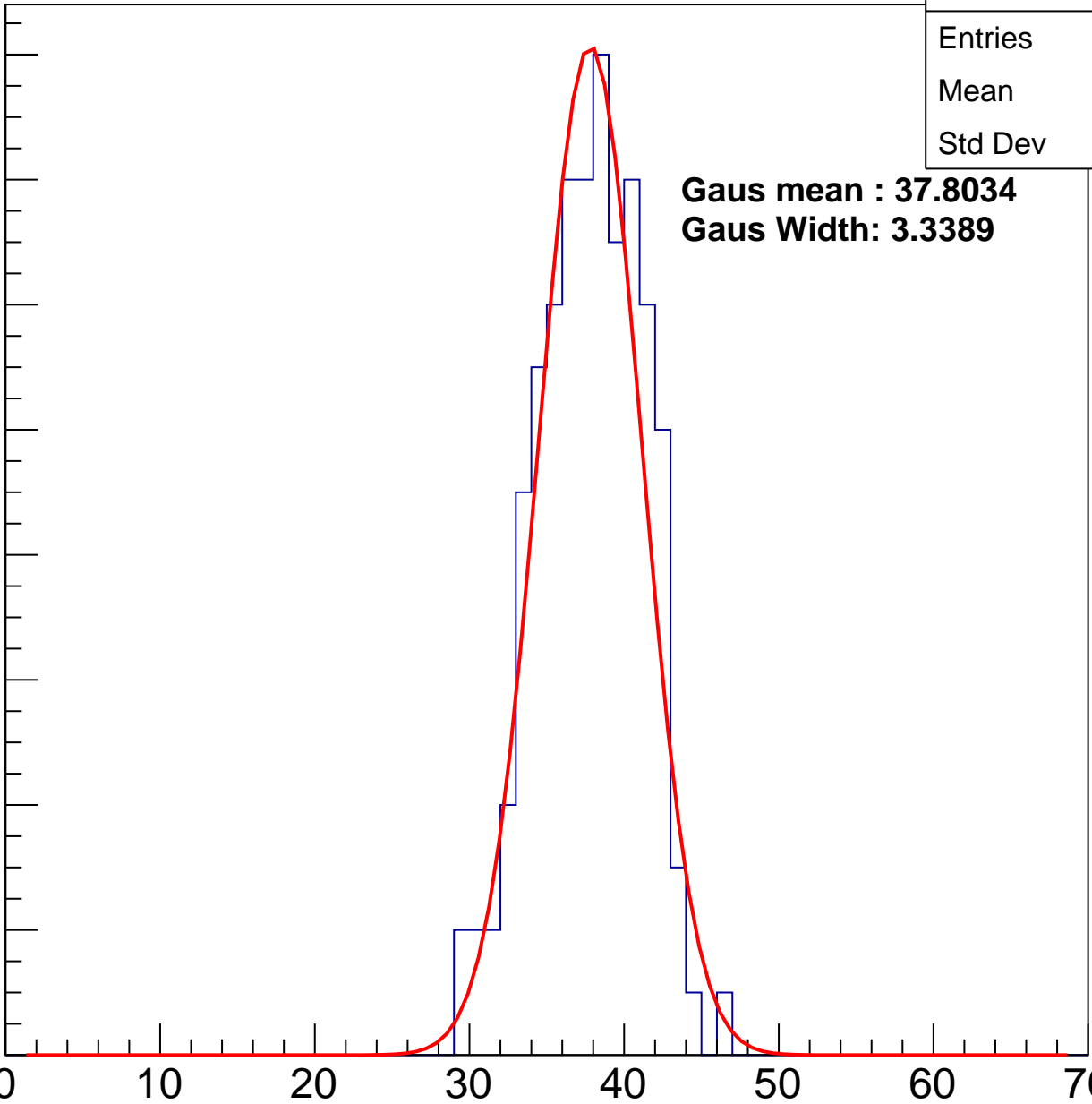
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 140 |
| Mean | 37.34 |
| Std Dev | 3.339 |

Gaus mean : 37.8034
Gaus Width: 3.3389

0 10 20 30 40 50 60 70
ampl



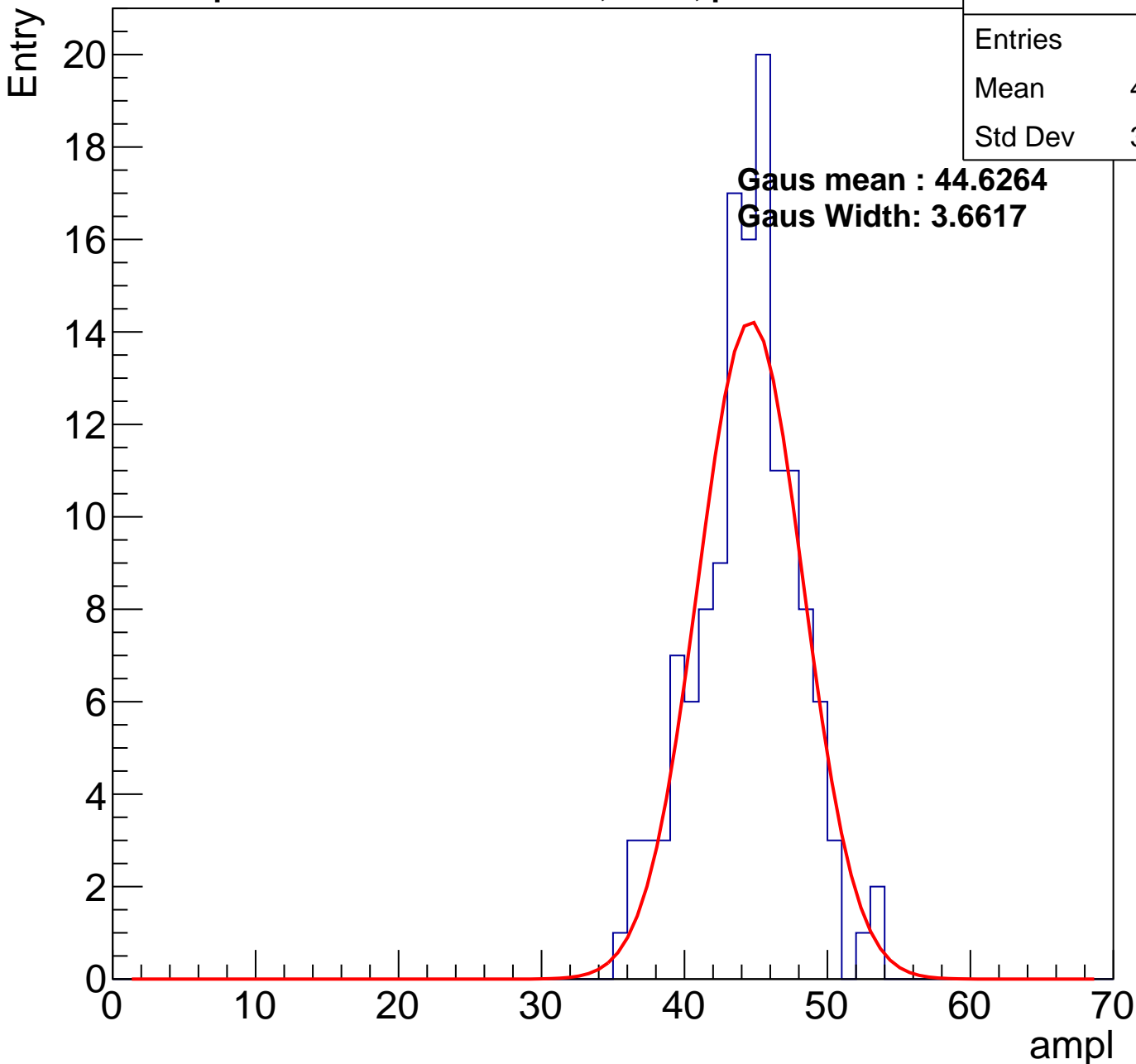
B1L001S, U26-ch13, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 43.93 |
| Std Dev | 3.547 |

Gaus mean : 44.6264

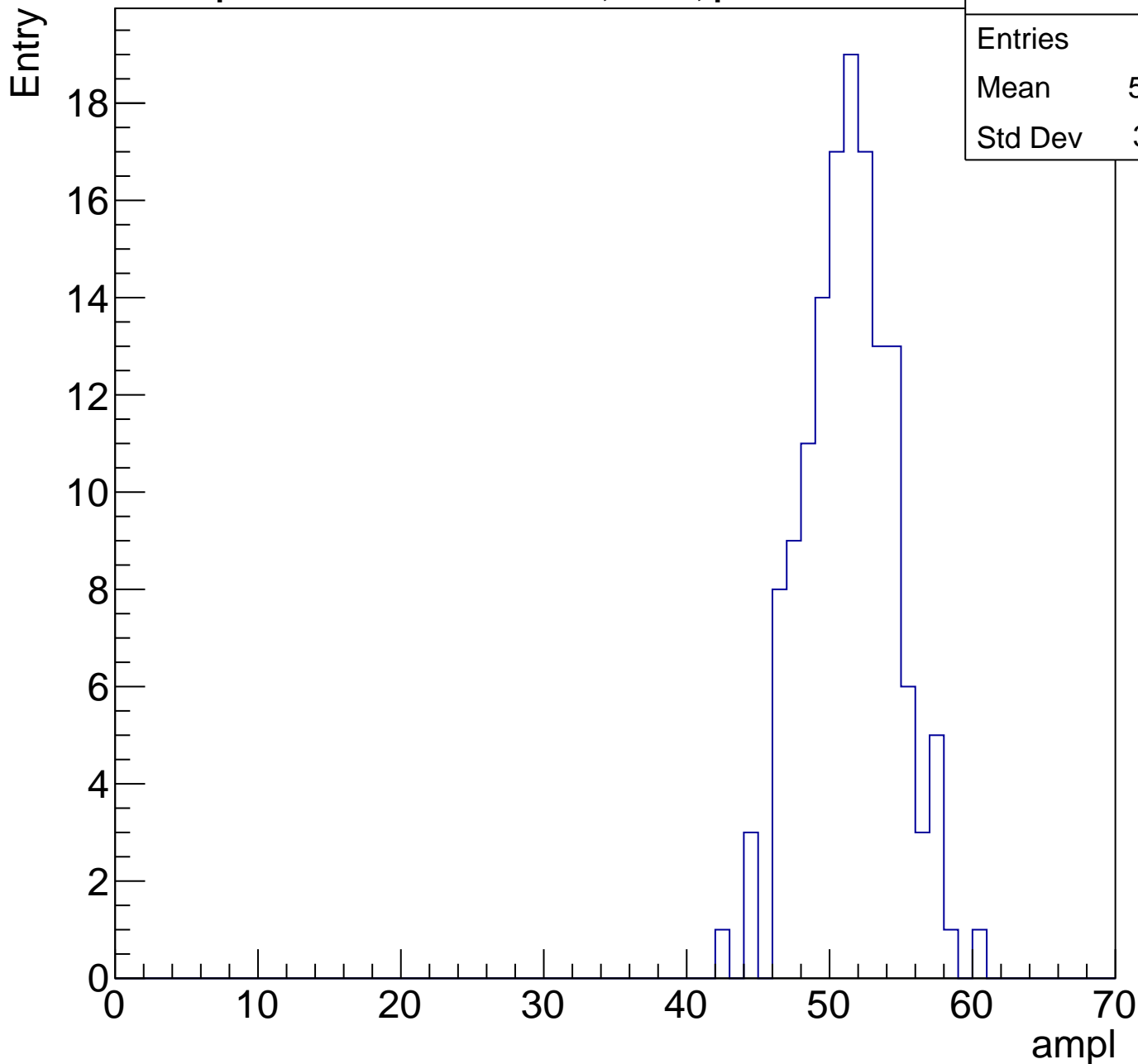
Gaus Width: 3.6617



B1L001S, U26-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 50.88 |
| Std Dev | 3.161 |



B1L001S, U26-ch13, adc4

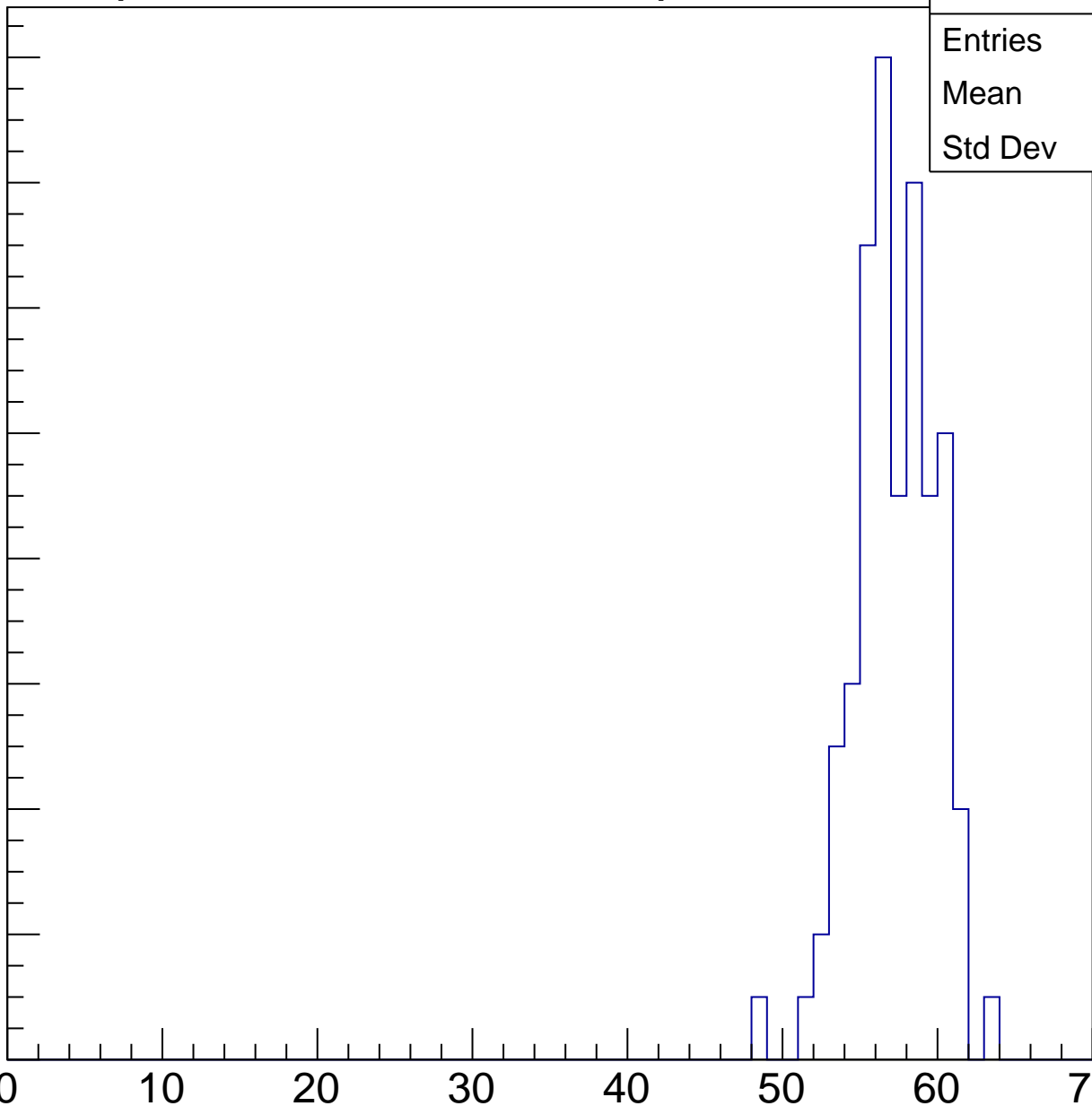
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 56.77 |
| Std Dev | 2.577 |

ampl



B1L001S, U26-ch13, adc5

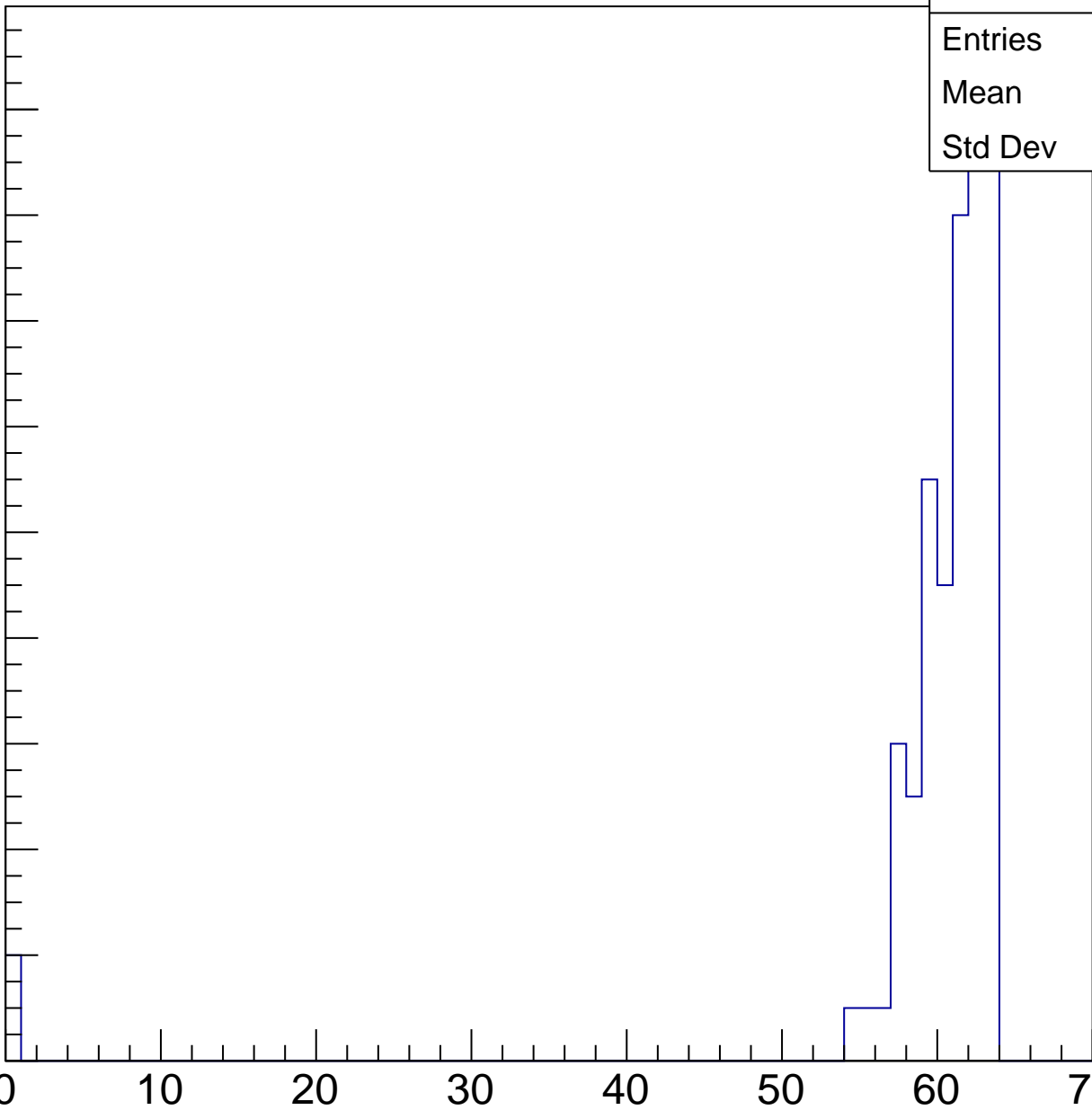
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 59.3 |
| Std Dev | 9.178 |

ampl



B1L001S, U26-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

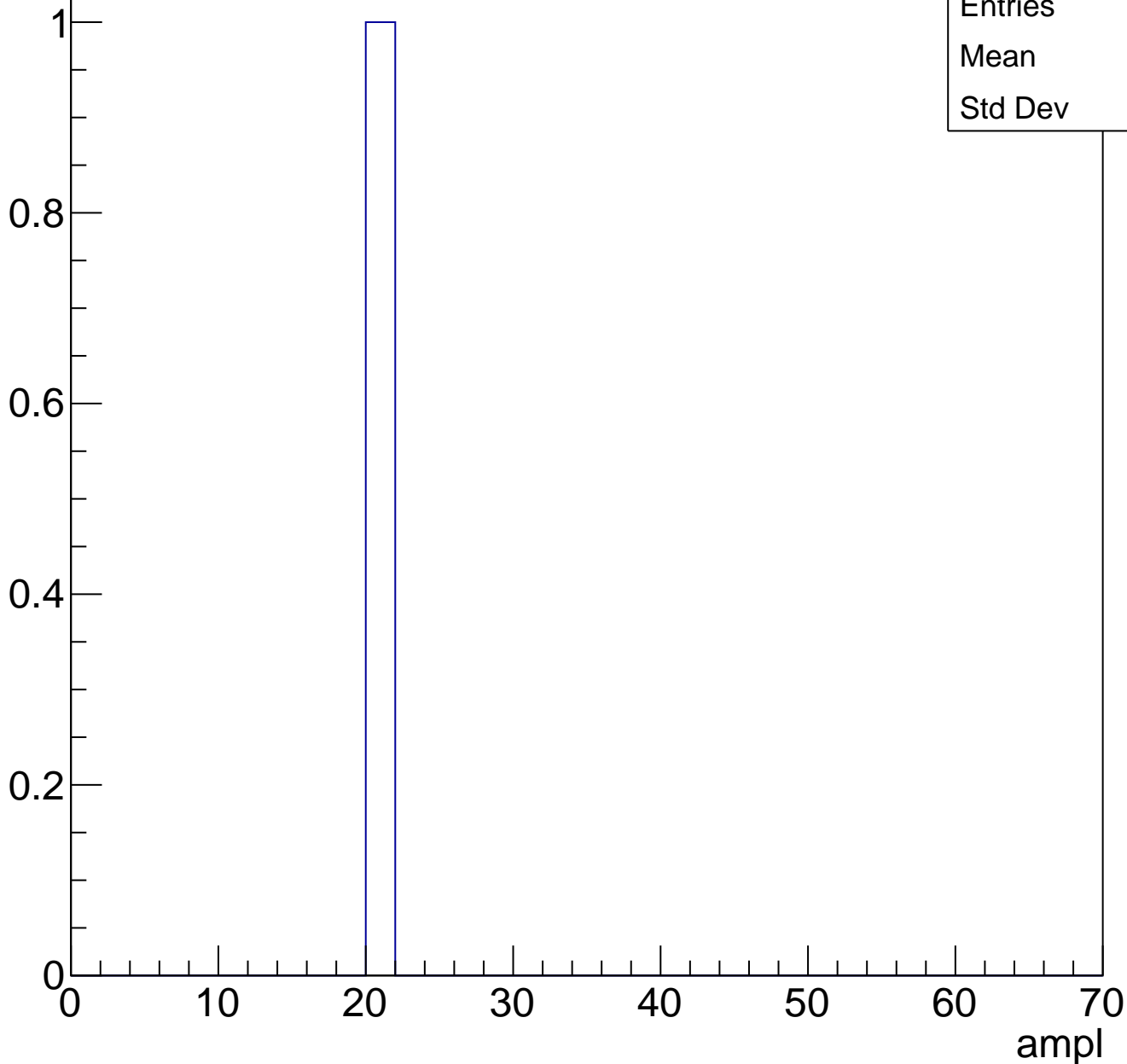
Entry



B1L001S, U26-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch14, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 29.82 |
| Std Dev | 3.391 |

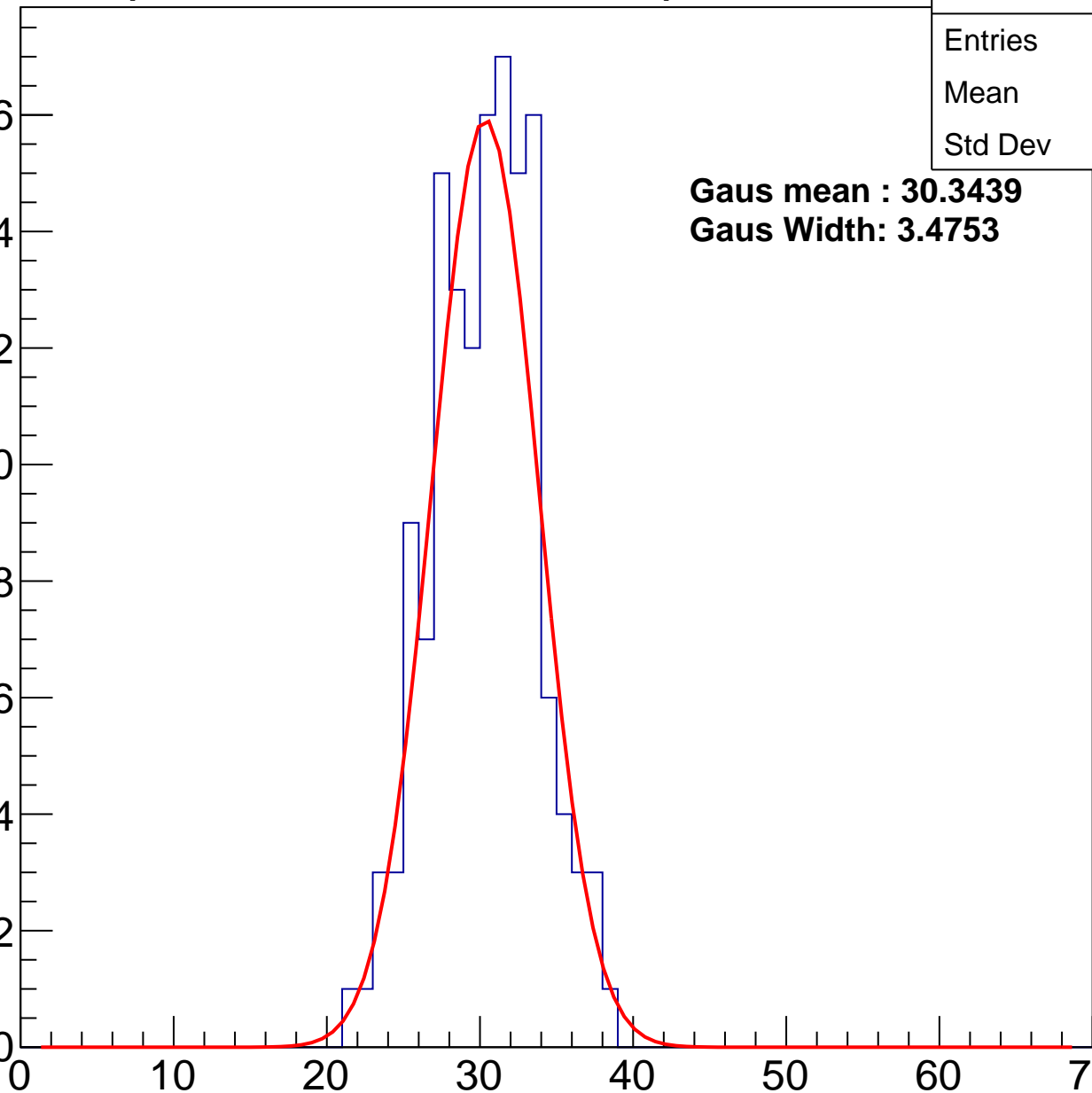
Gaus mean : 30.3439

Gaus Width: 3.4753

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch14, adc1

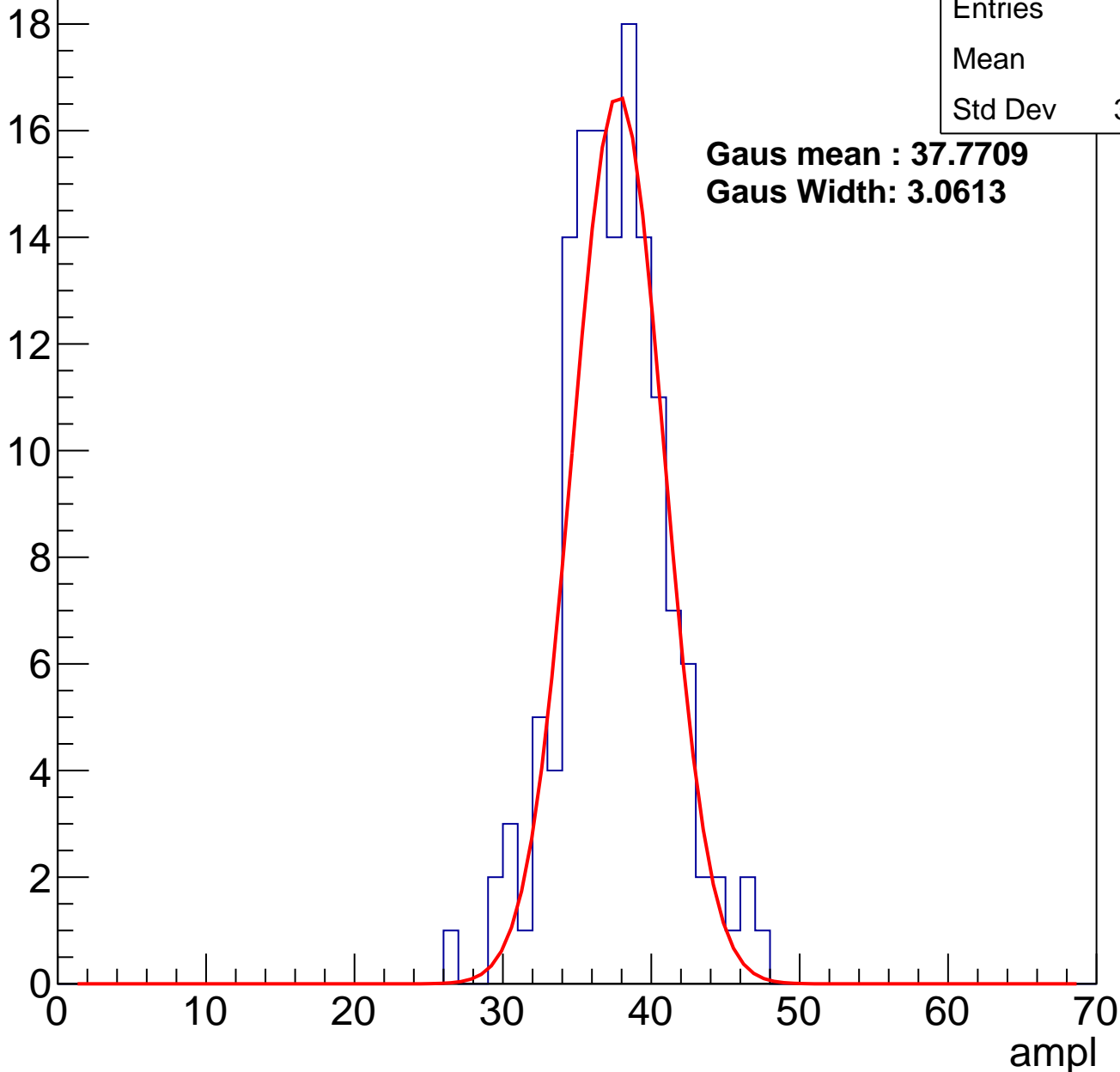
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 37.1 |
| Std Dev | 3.542 |

Gaus mean : 37.7709

Gaus Width: 3.0613

Entry



B1L001S, U26-ch14, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

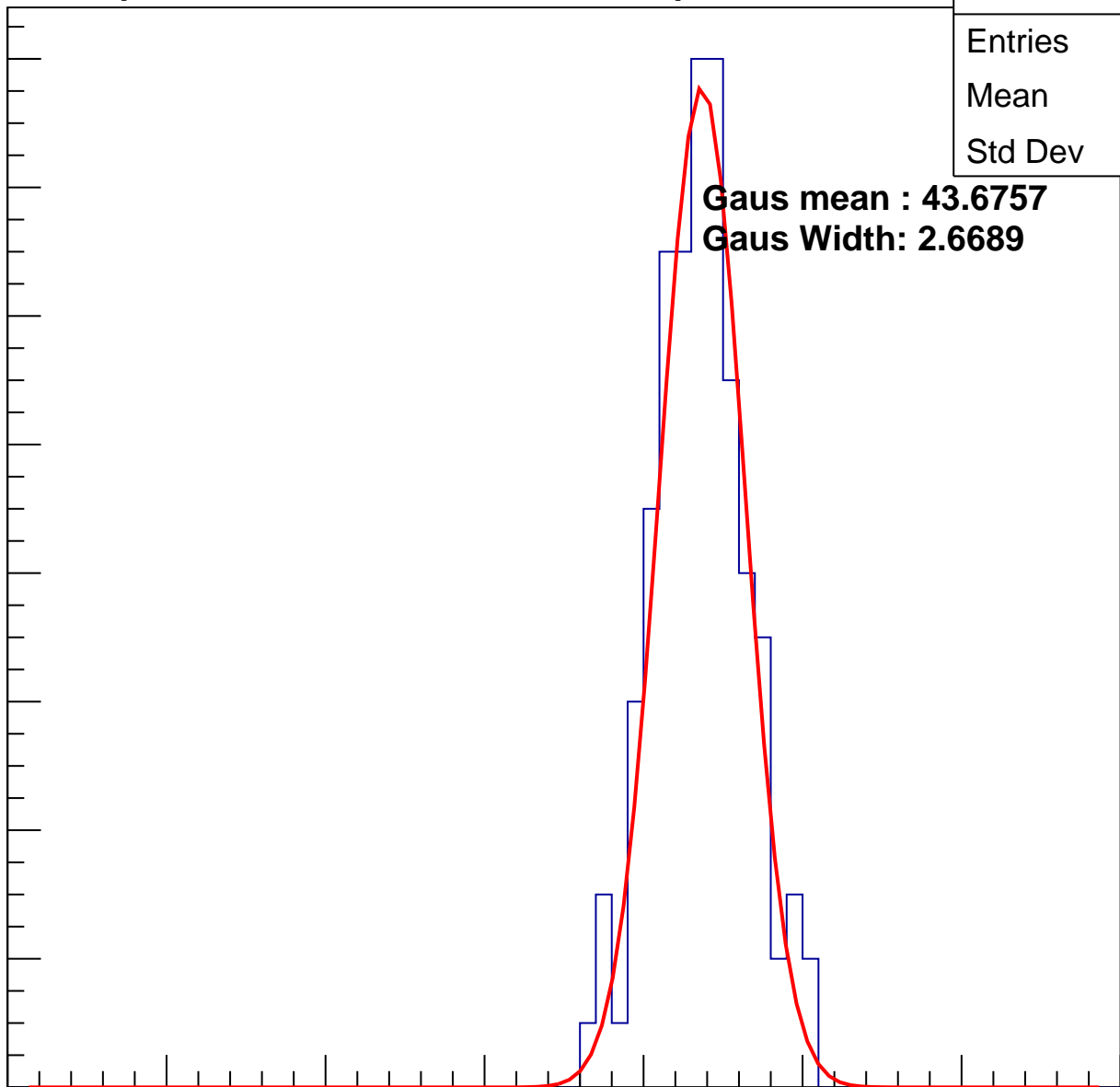
| | |
|---------|-------|
| Entries | 111 |
| Mean | 43.11 |
| Std Dev | 2.883 |

Gaus mean : 43.6757

Gaus Width: 2.6689

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch14, adc3

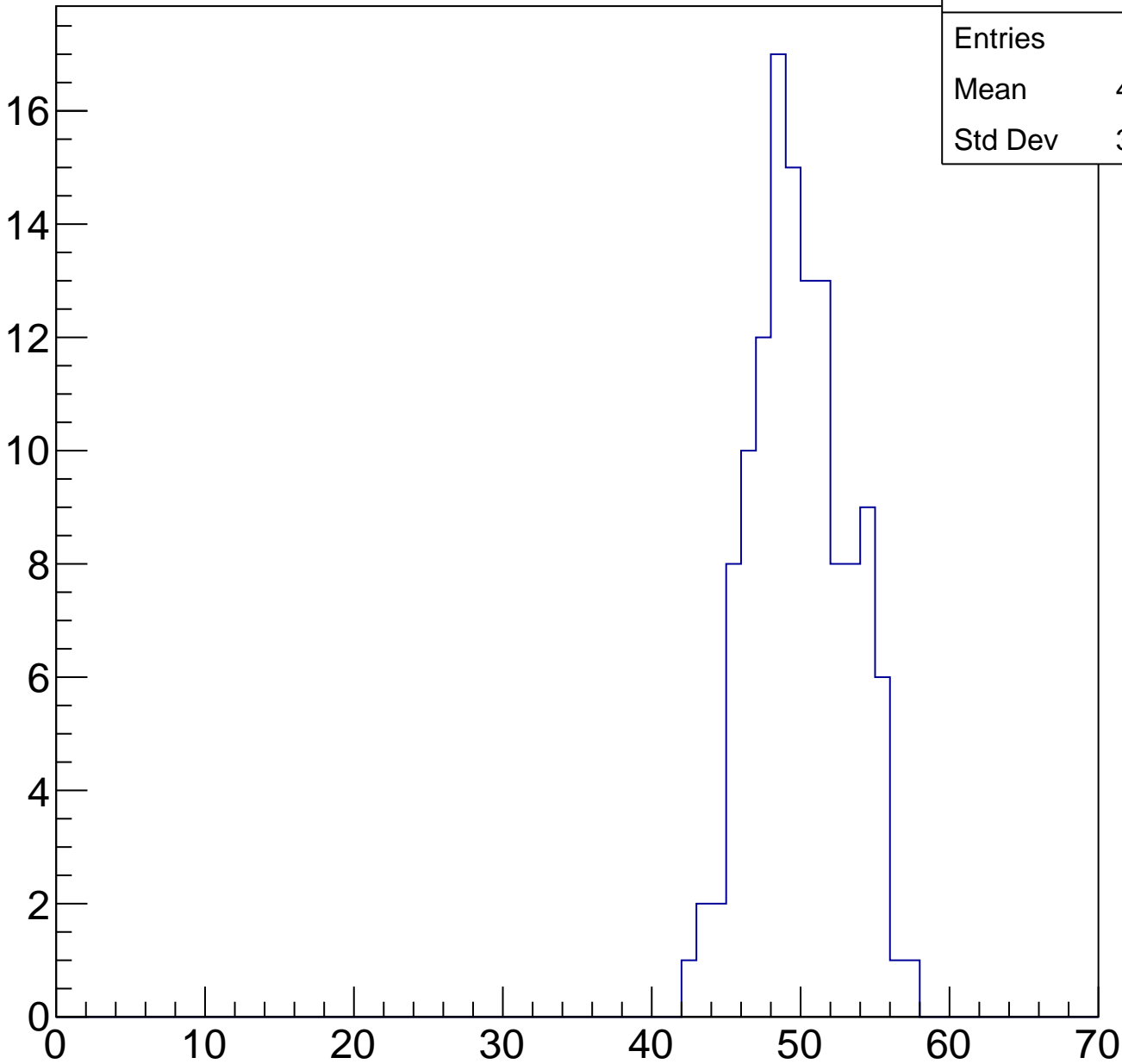
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 126 |
| Mean | 49.47 |
| Std Dev | 3.144 |

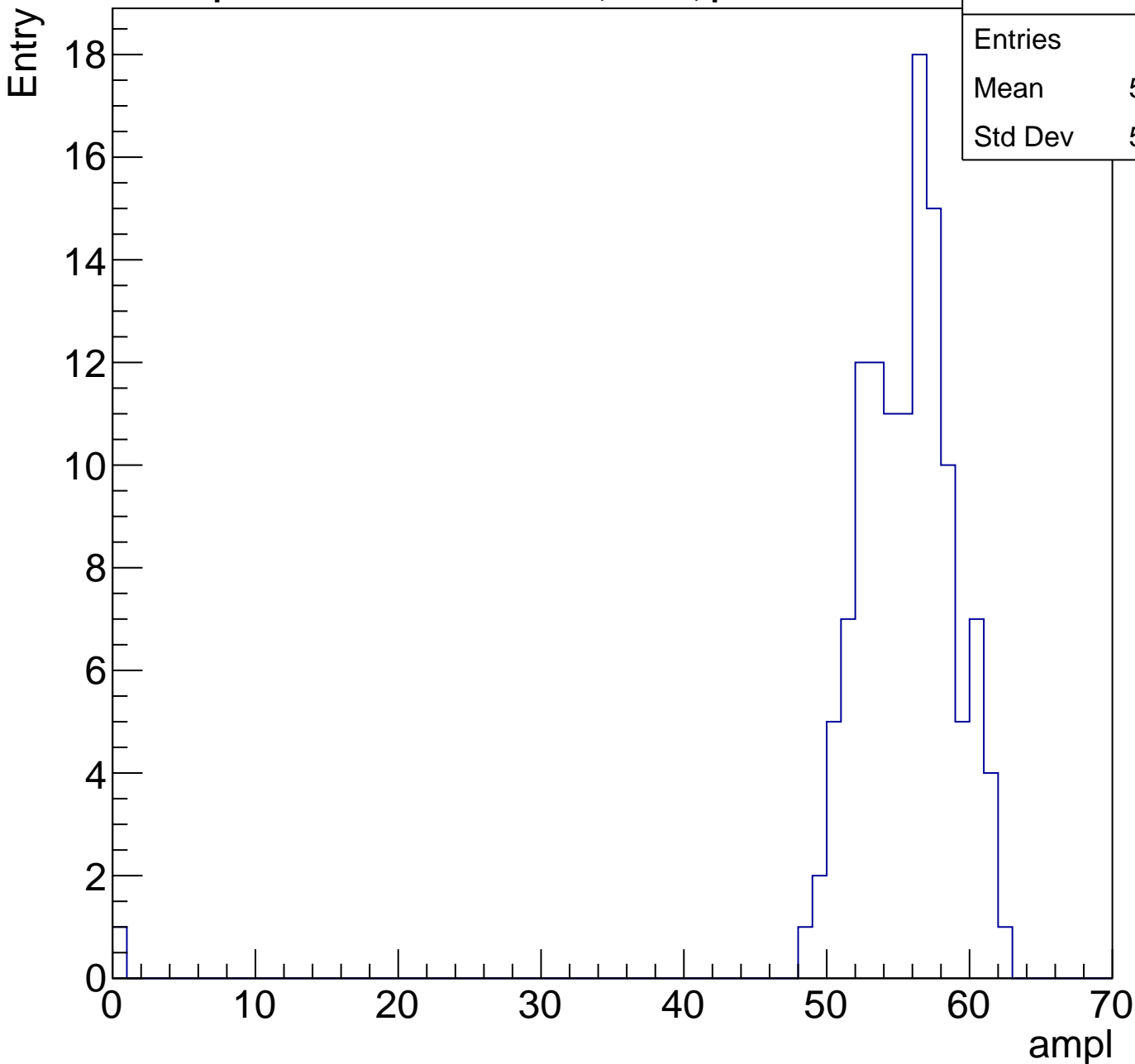
ampl



B1L001S, U26-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 54.72 |
| Std Dev | 5.837 |



B1L001S, U26-ch14, adc5

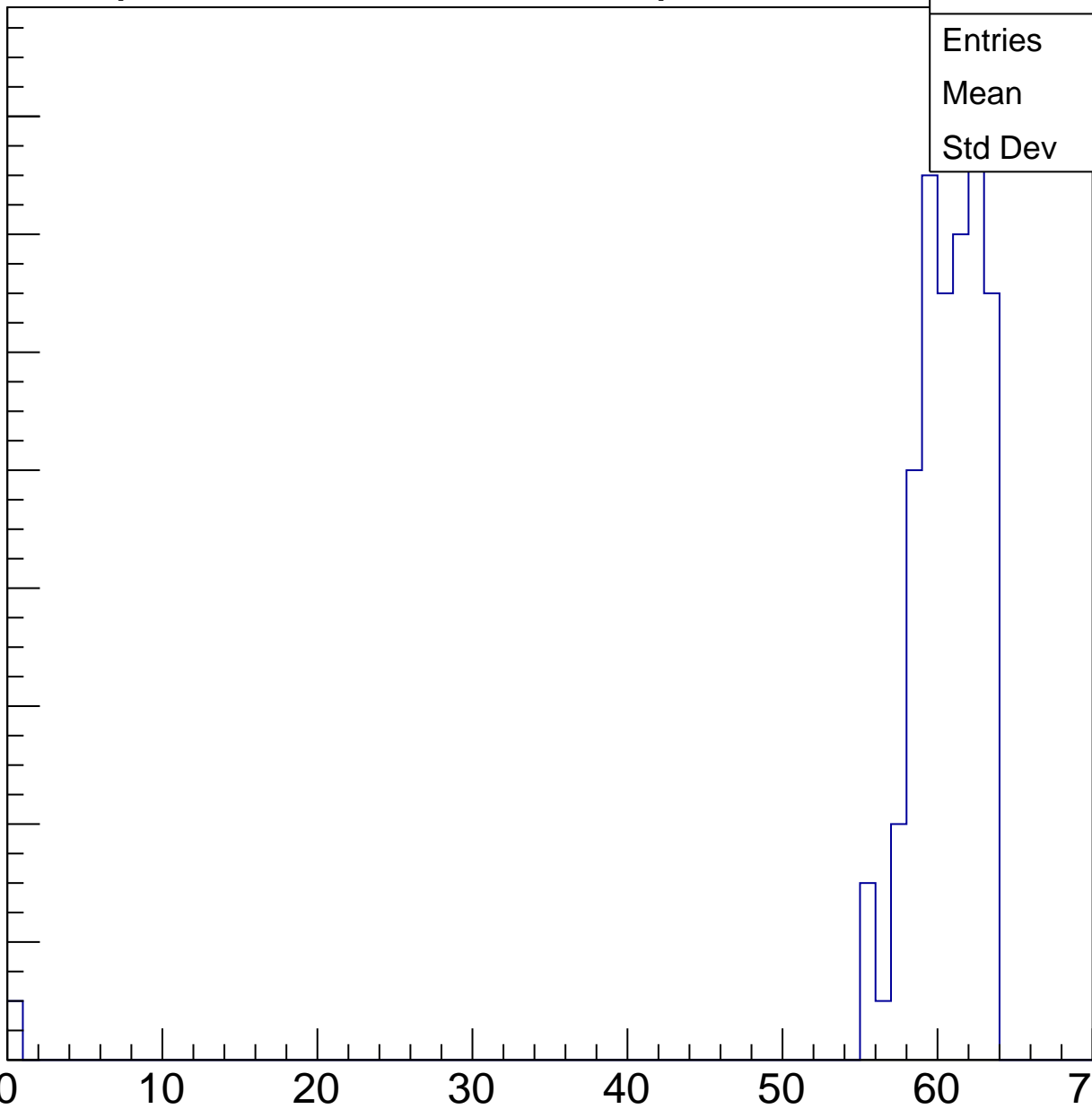
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 59.57 |
| Std Dev | 6.6 |

ampl

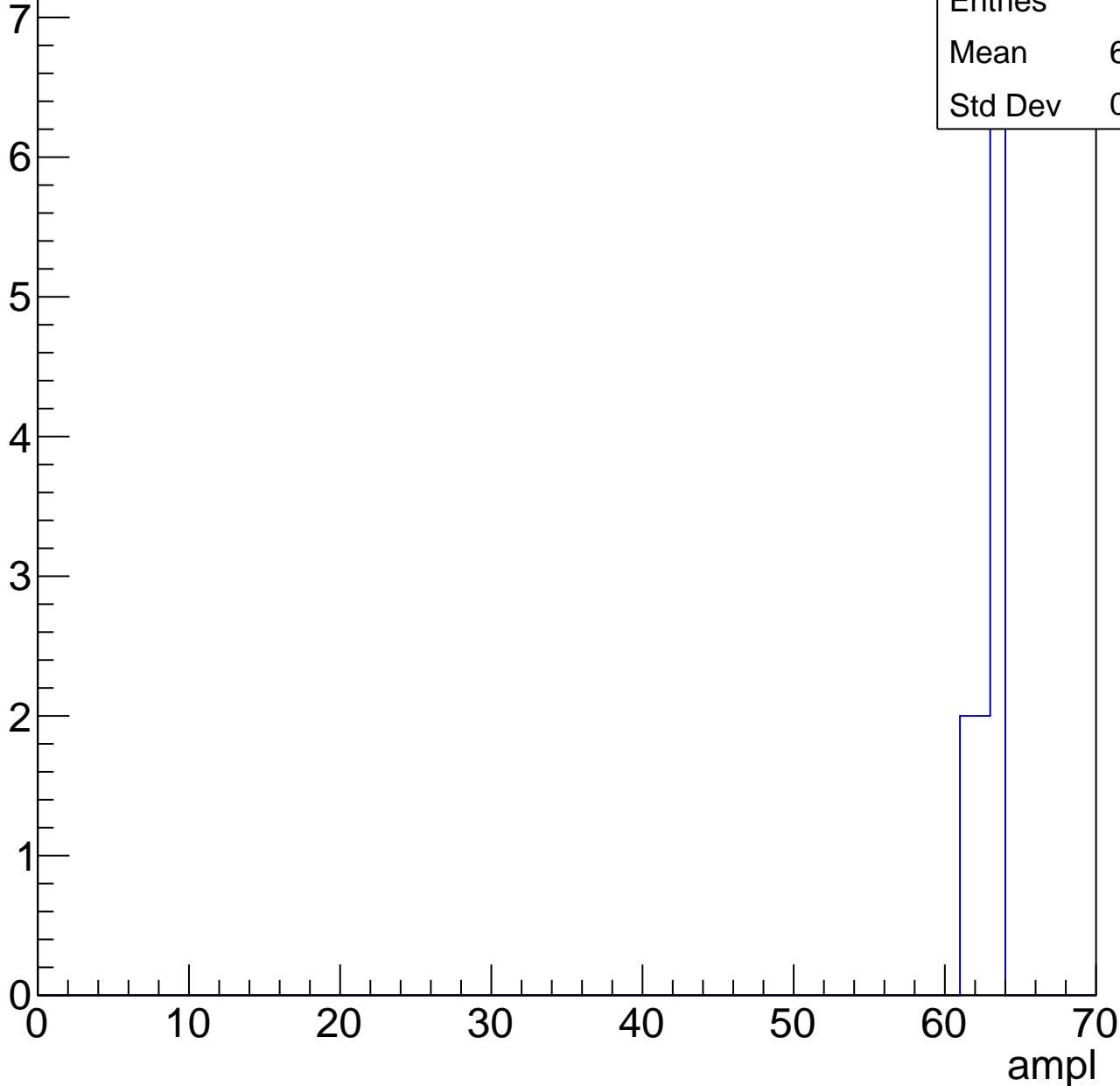


B1L001S, U26-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 62.45 |
| Std Dev | 0.782 |



B1L001S, U26-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch15, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

144

Mean

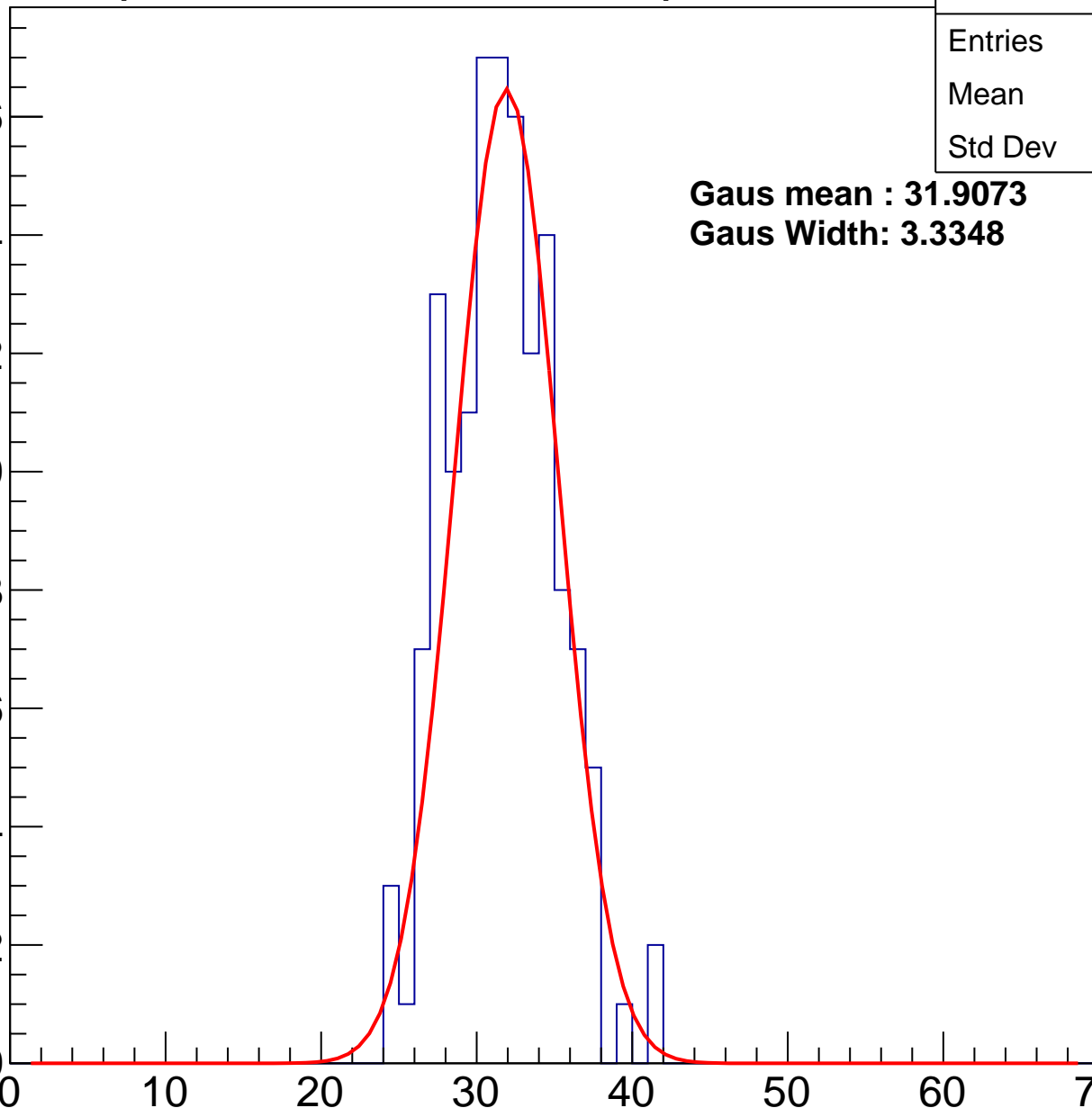
31.17

Std Dev

3.385

Gaus mean : 31.9073

Gaus Width: 3.3348



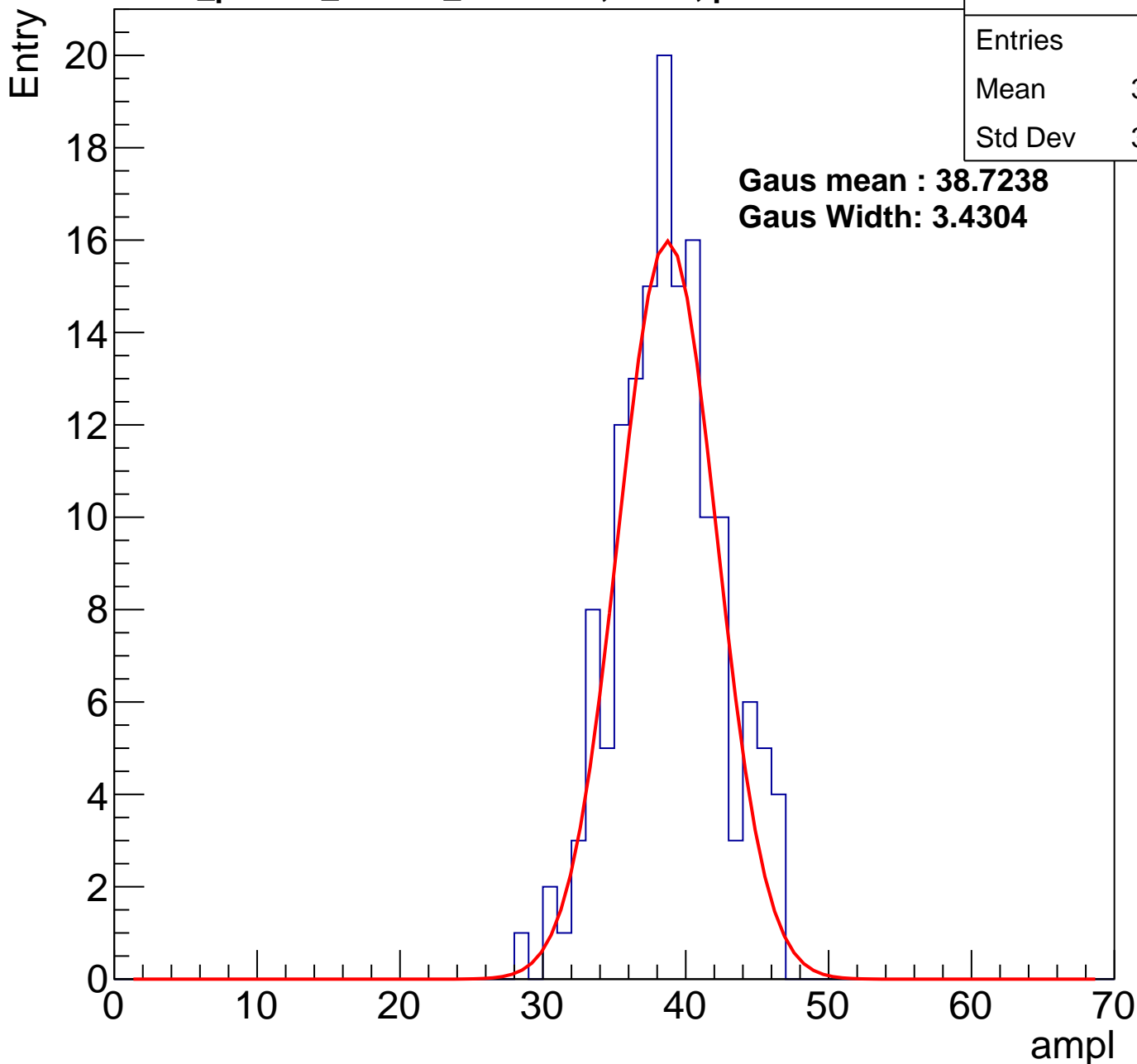
B1L001S, U26-ch15, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 149 |
| Mean | 38.32 |
| Std Dev | 3.614 |

Gaus mean : 38.7238

Gaus Width: 3.4304



B1L001S, U26-ch15, adc2

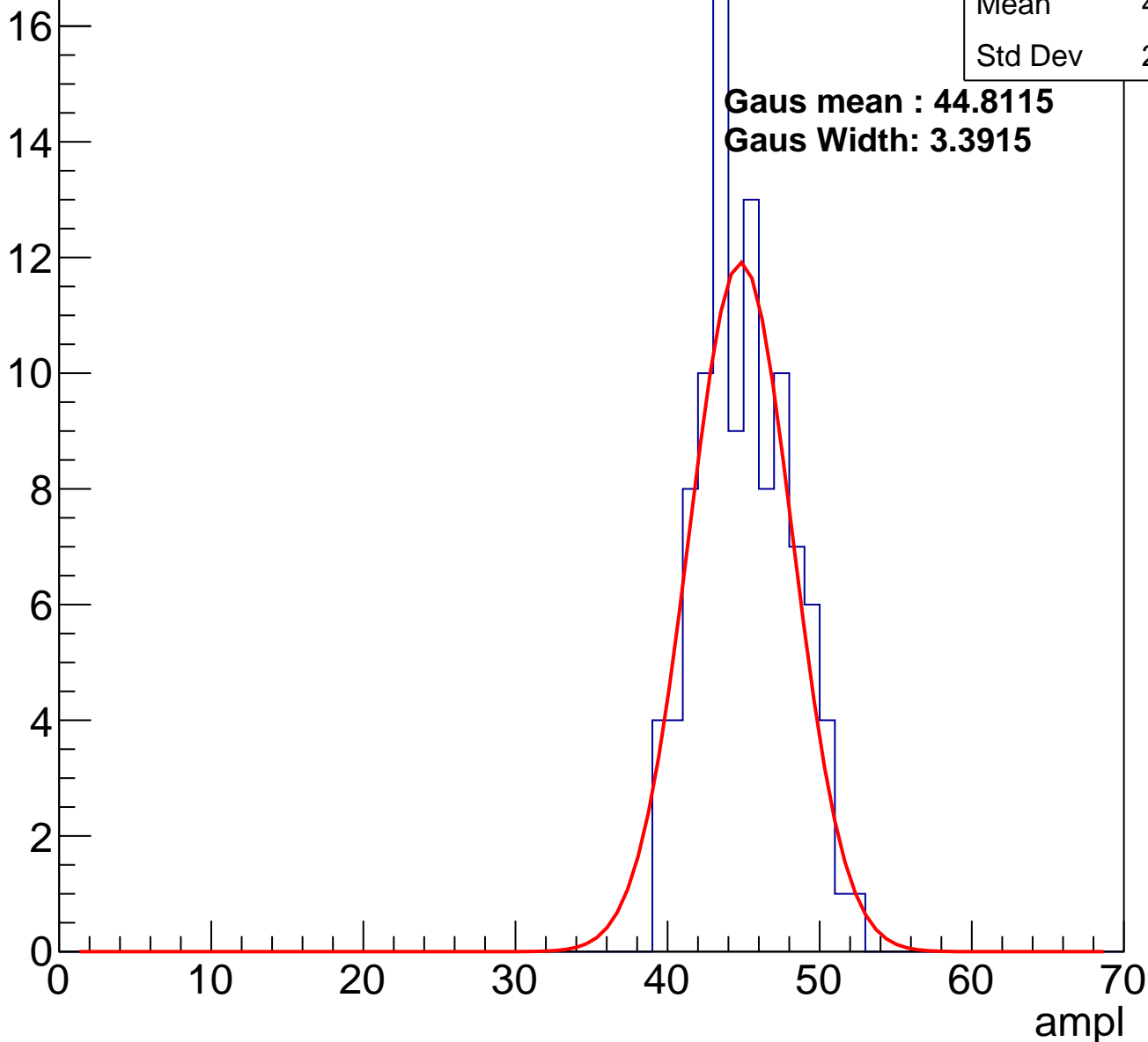
calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 102 |
| Mean | 44.58 |
| Std Dev | 2.992 |

Gaus mean : 44.8115

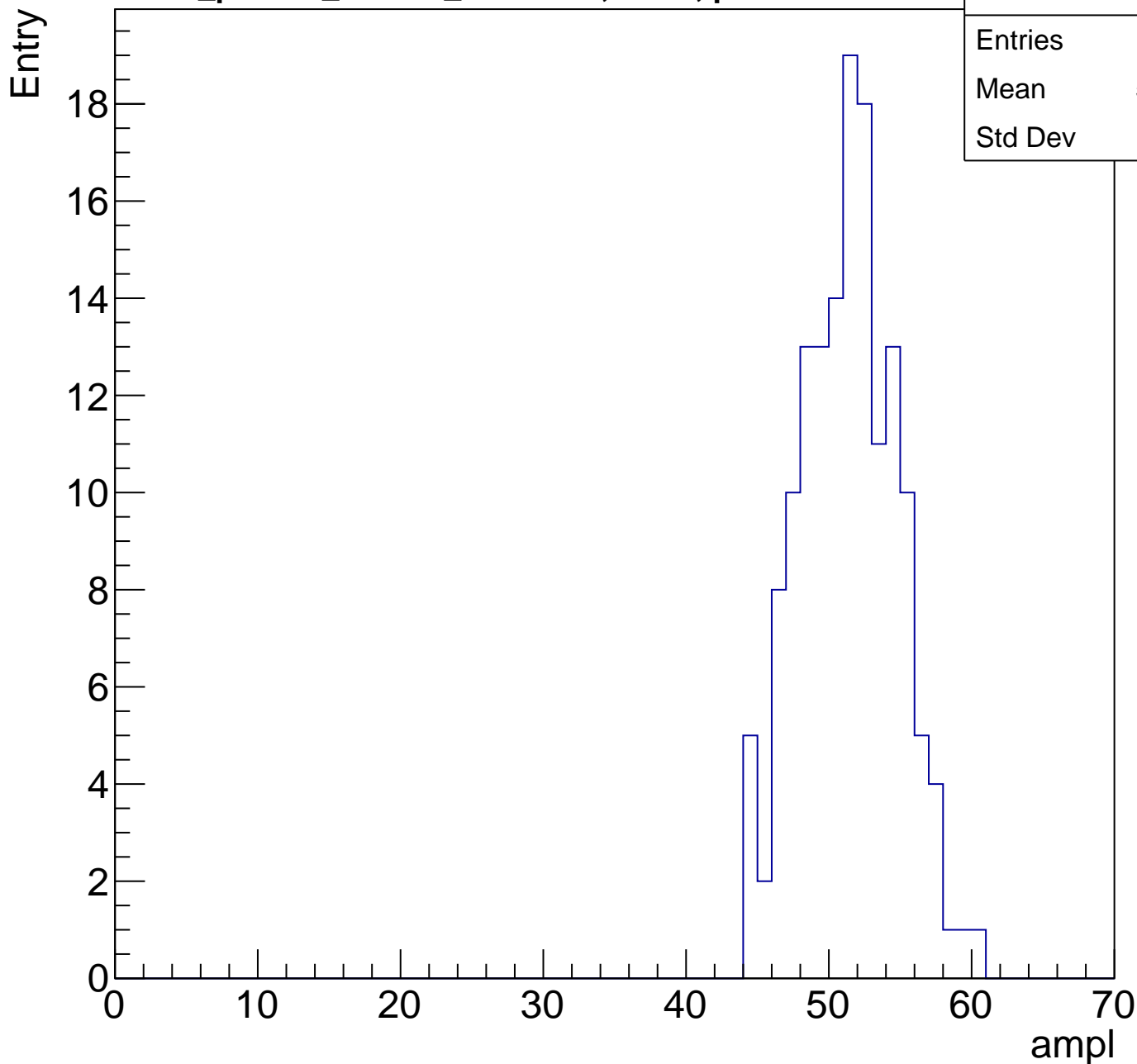
Gaus Width: 3.3915



B1L001S, U26-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 50.91 |
| Std Dev | 3.35 |



B1L001S, U26-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

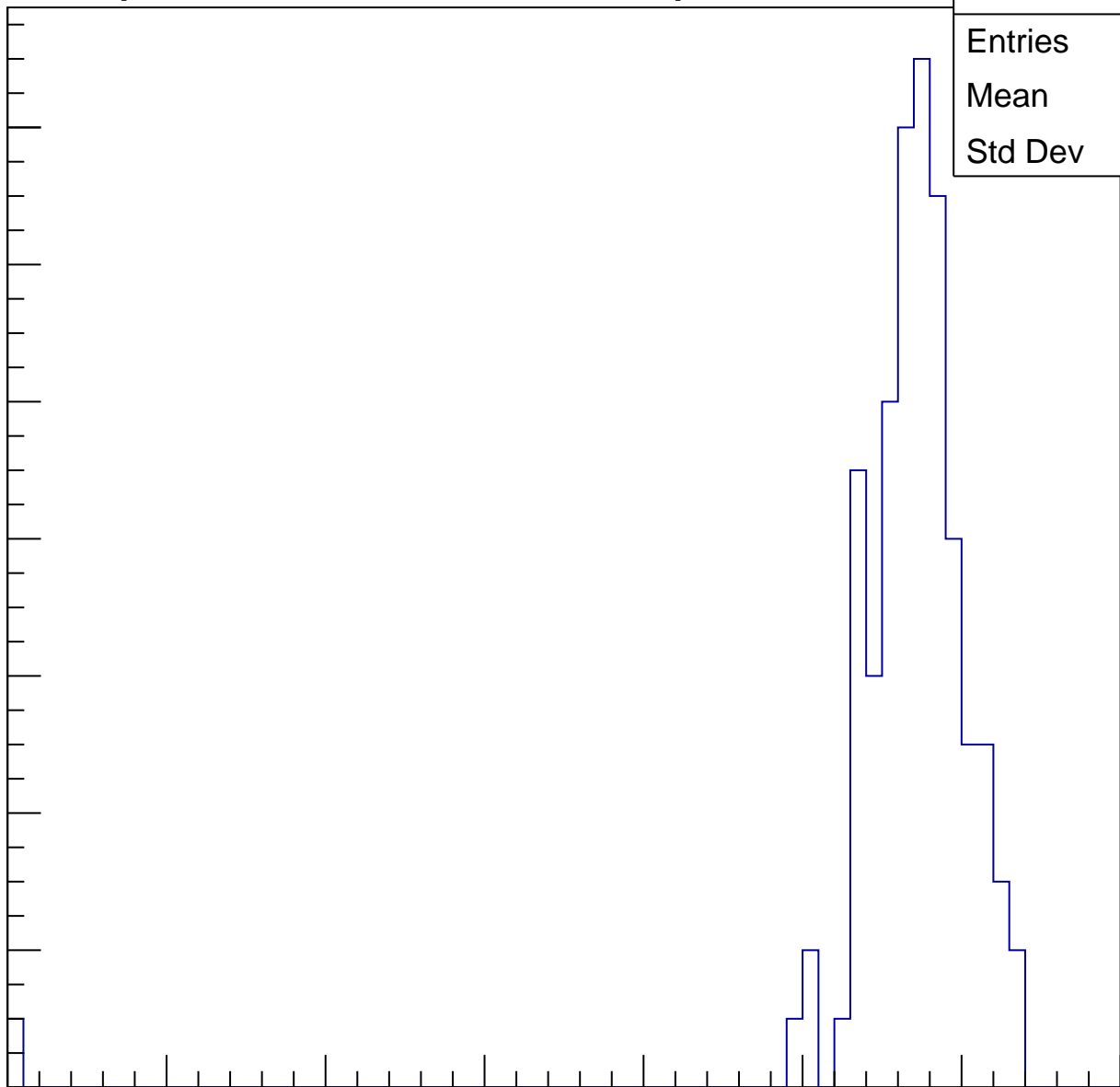
| | |
|---------|-------|
| Entries | 95 |
| Mean | 56.15 |
| Std Dev | 6.437 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch15, adc5

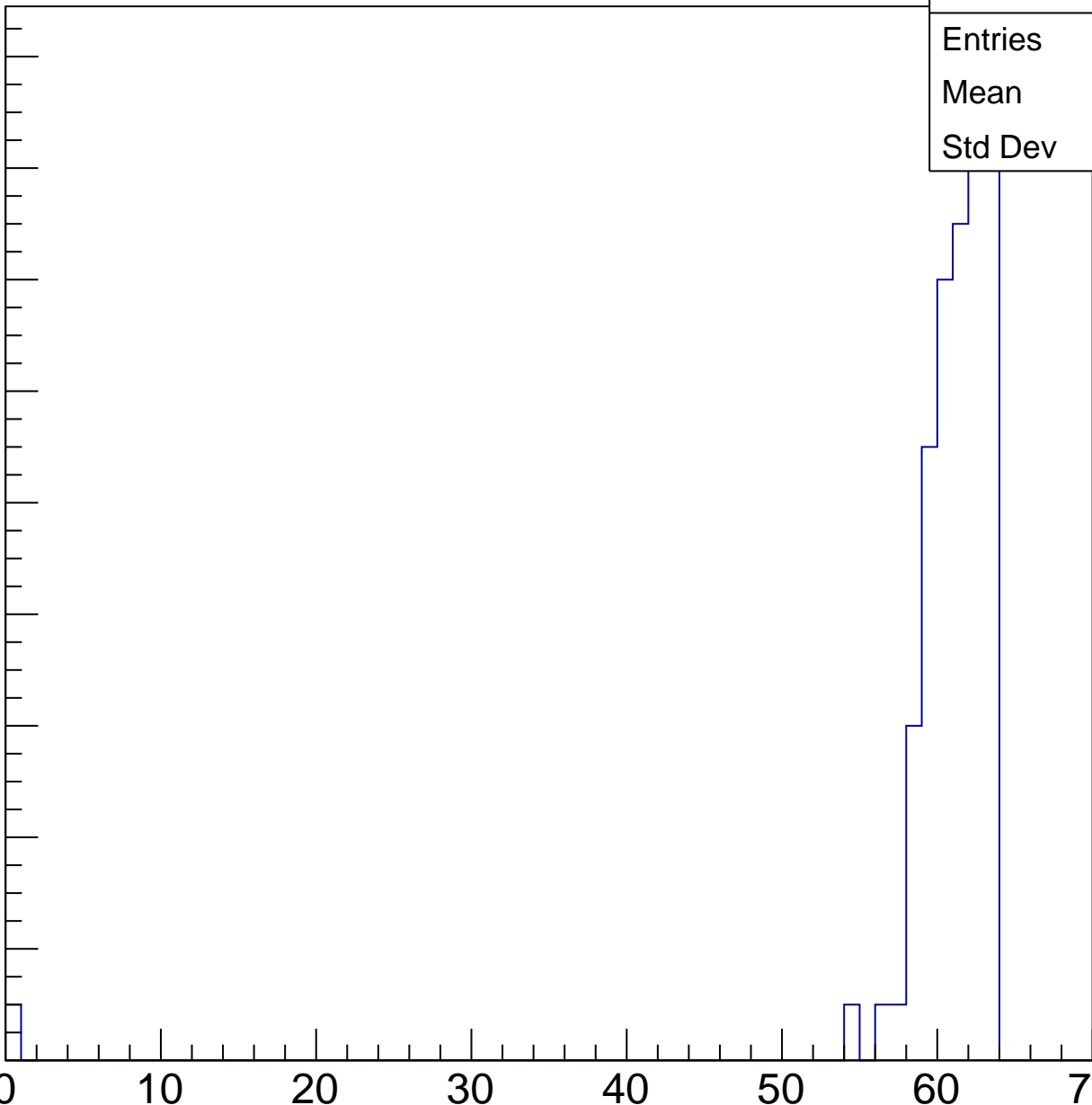
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 60.08 |
| Std Dev | 6.809 |

ampl



B1L001S, U26-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch16, adc0

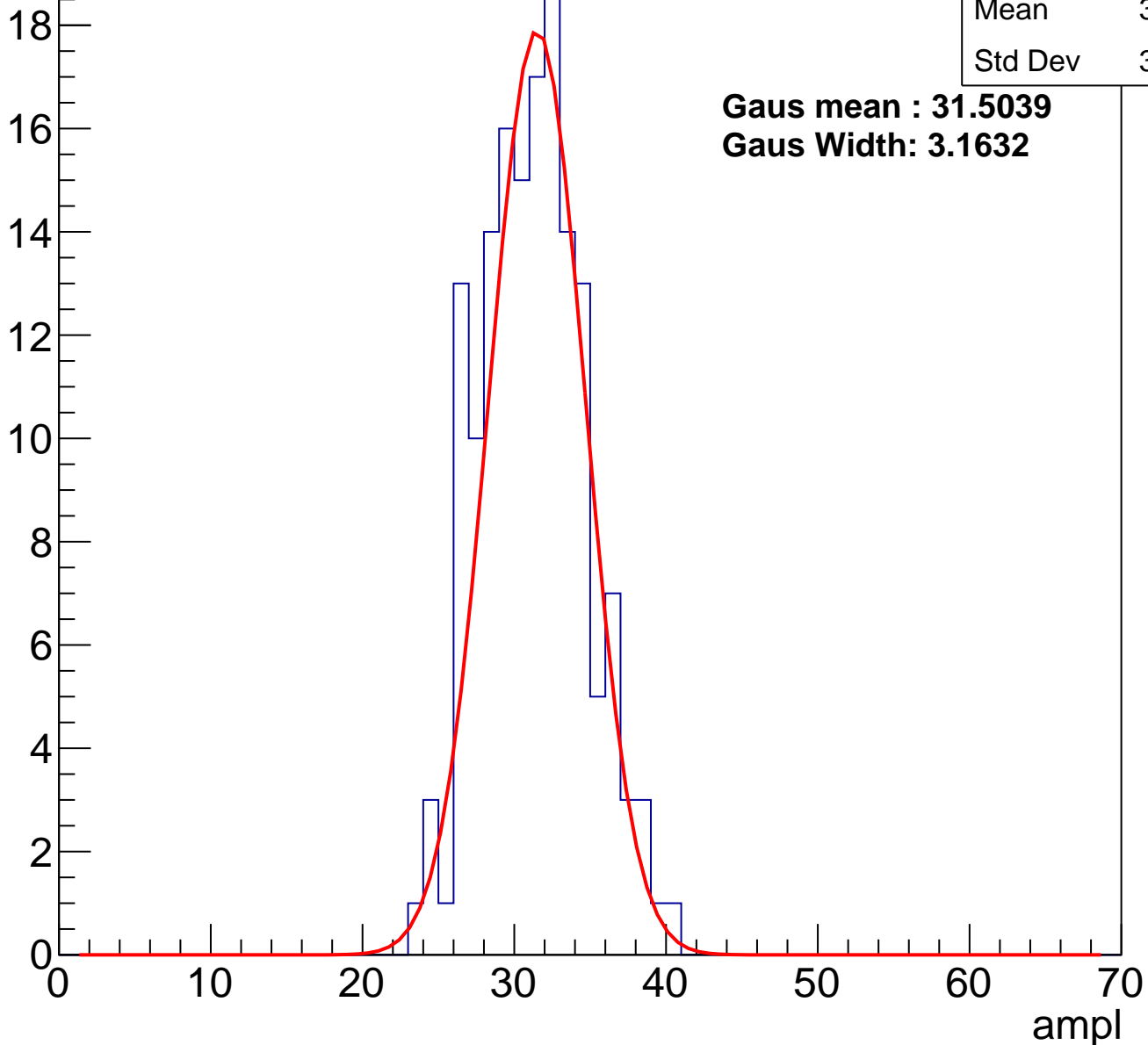
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 156 |
| Mean | 30.79 |
| Std Dev | 3.375 |

Gaus mean : 31.5039

Gaus Width: 3.1632

Entry



B1L001S, U26-ch16, adc1

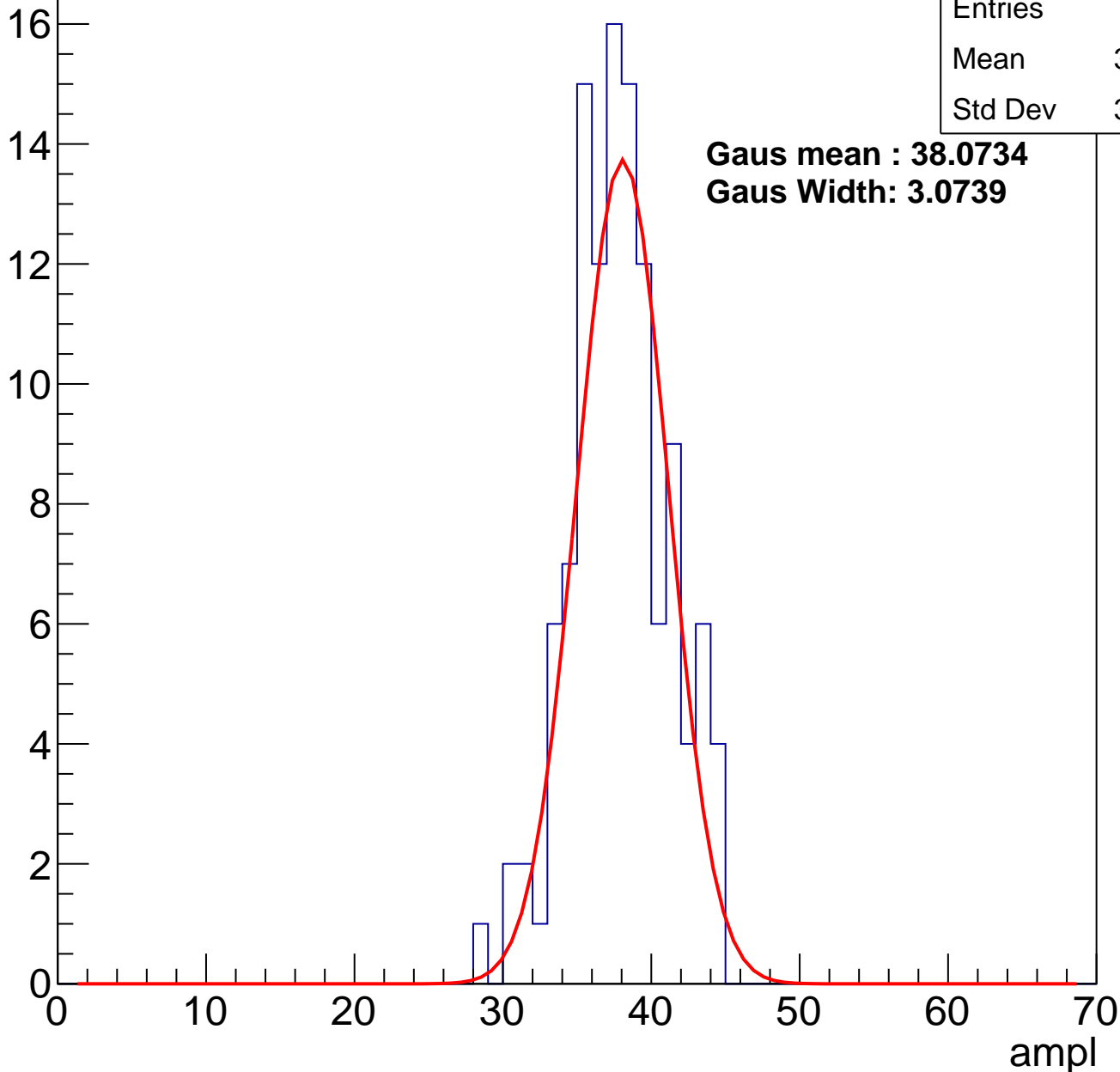
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 37.42 |
| Std Dev | 3.266 |

Gaus mean : 38.0734

Gaus Width: 3.0739

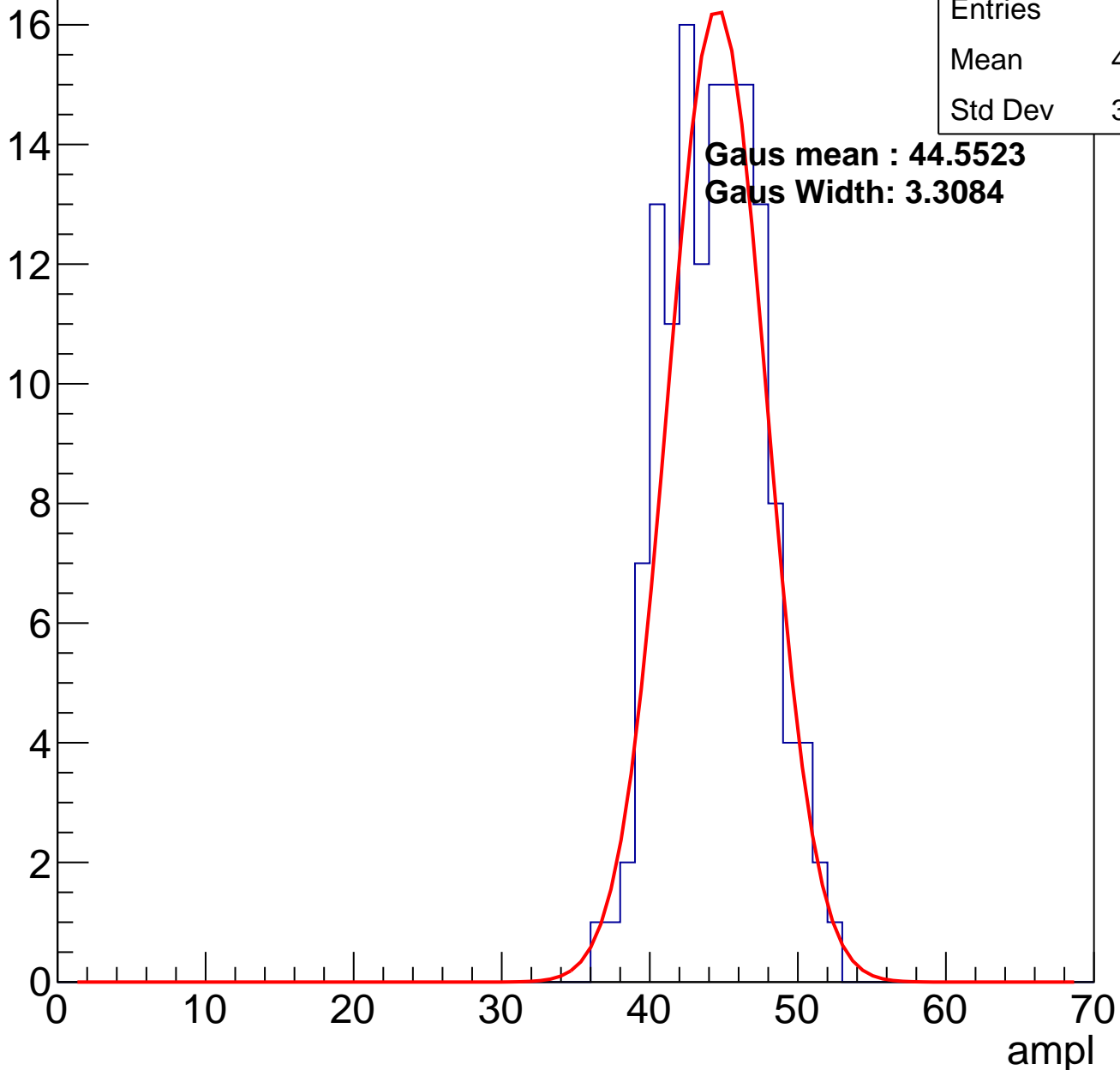
Entry



B1L001S, U26-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

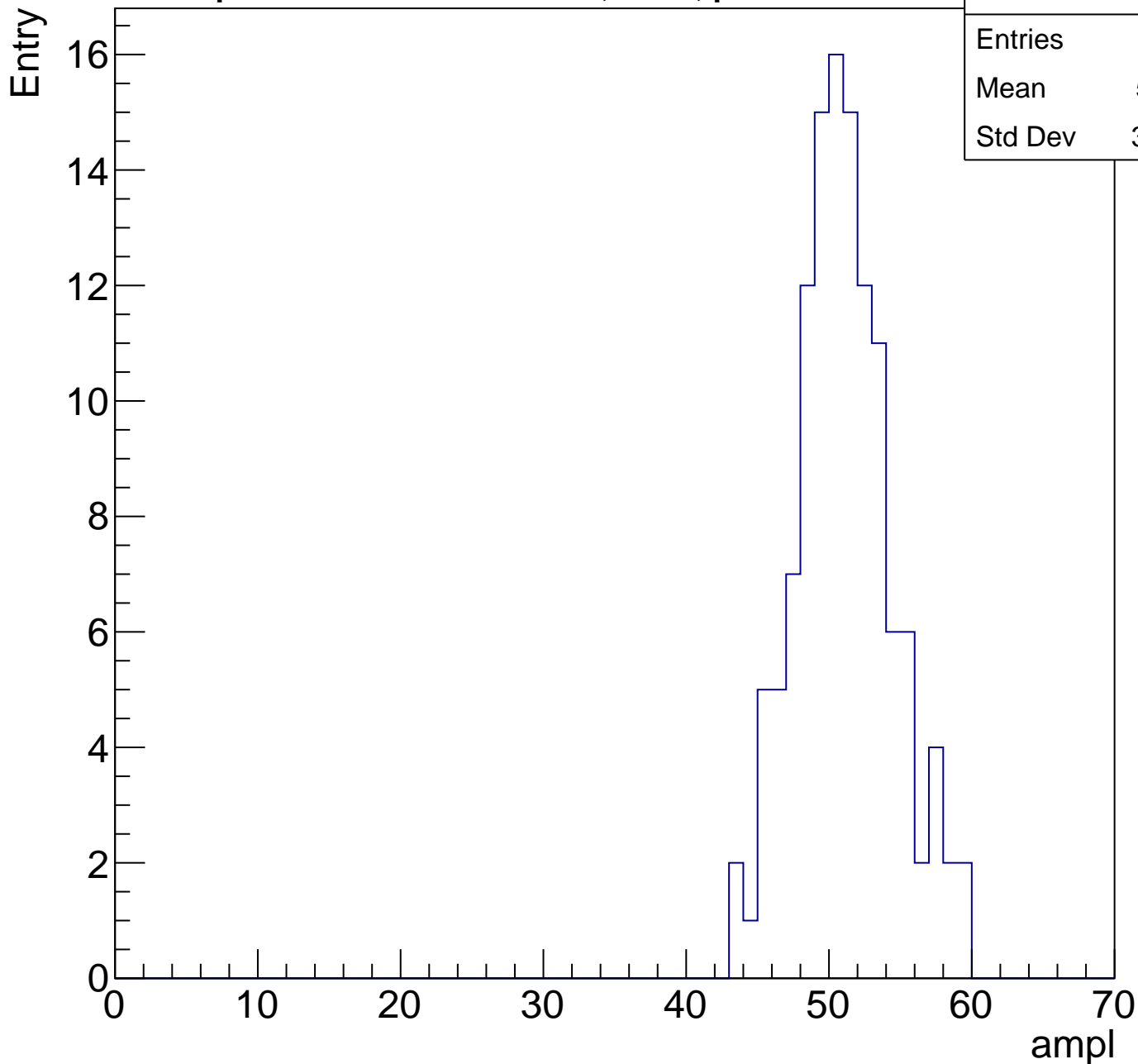
Entry



B1L001S, U26-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 50.61 |
| Std Dev | 3.356 |



B1L001S, U26-ch16, adc4

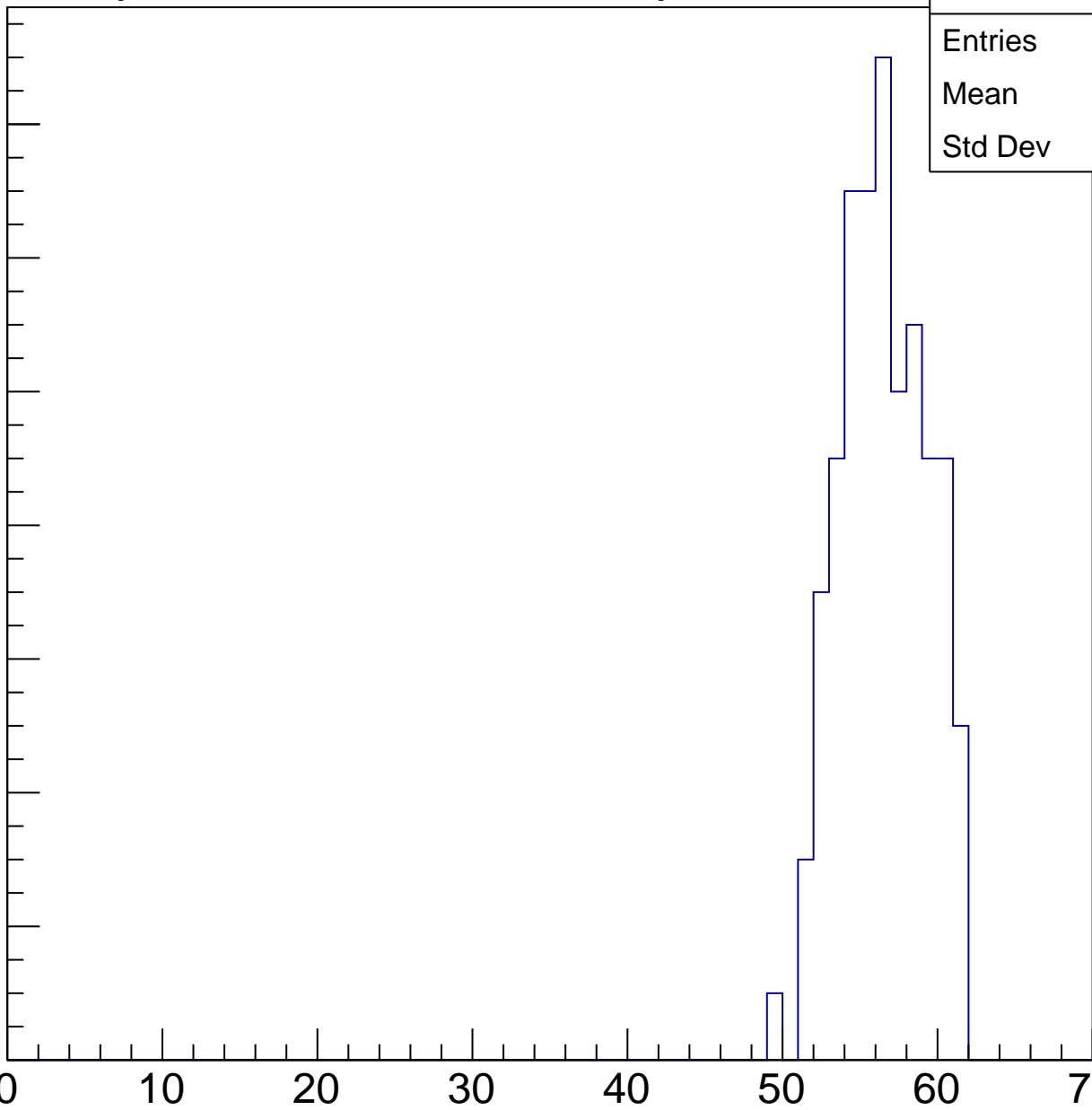
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 105 |
| Mean | 56.04 |
| Std Dev | 2.736 |

ampl



B1L001S, U26-ch16, adc5

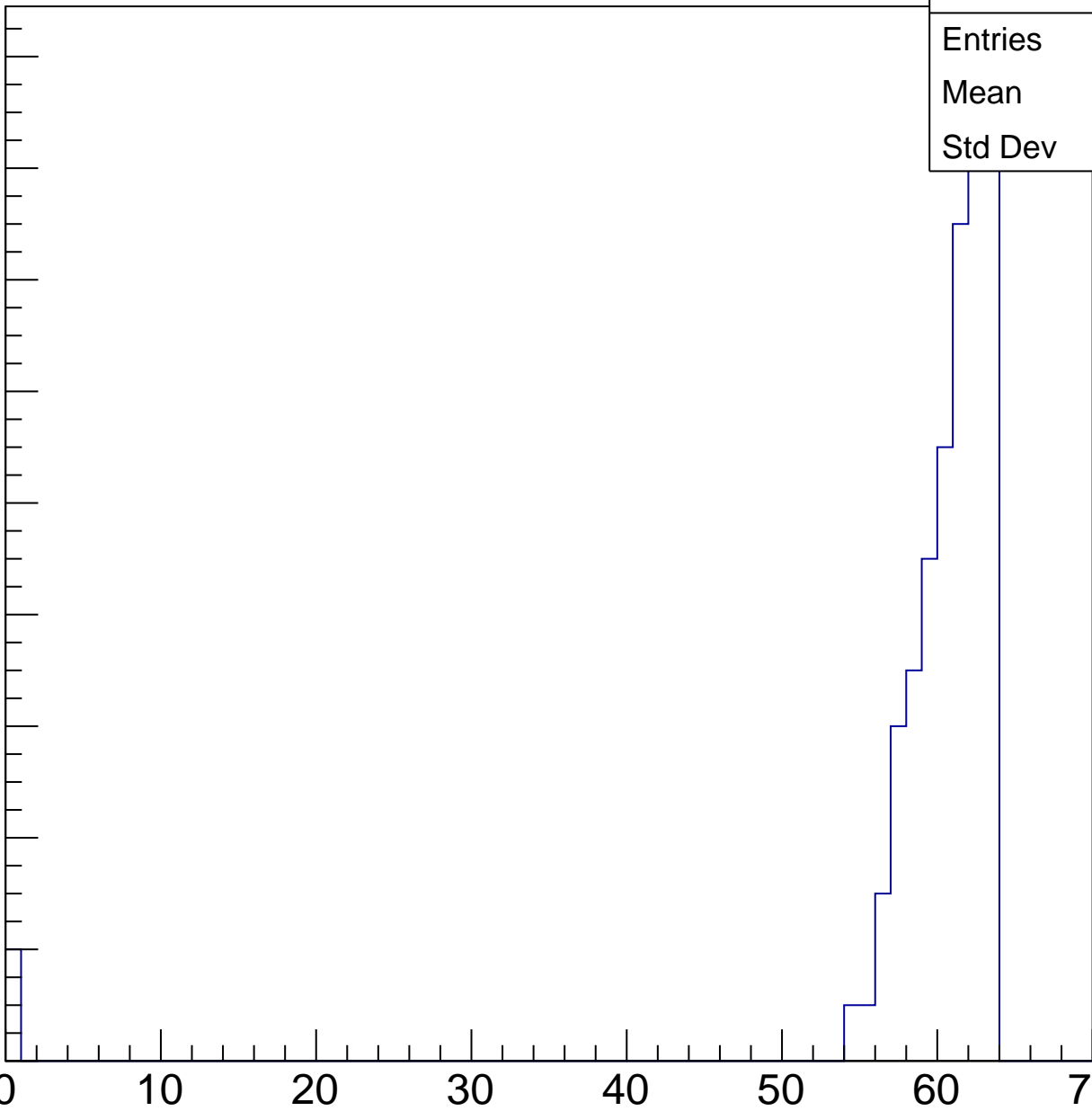
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 59.09 |
| Std Dev | 9.168 |

ampl



B1L001S, U26-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

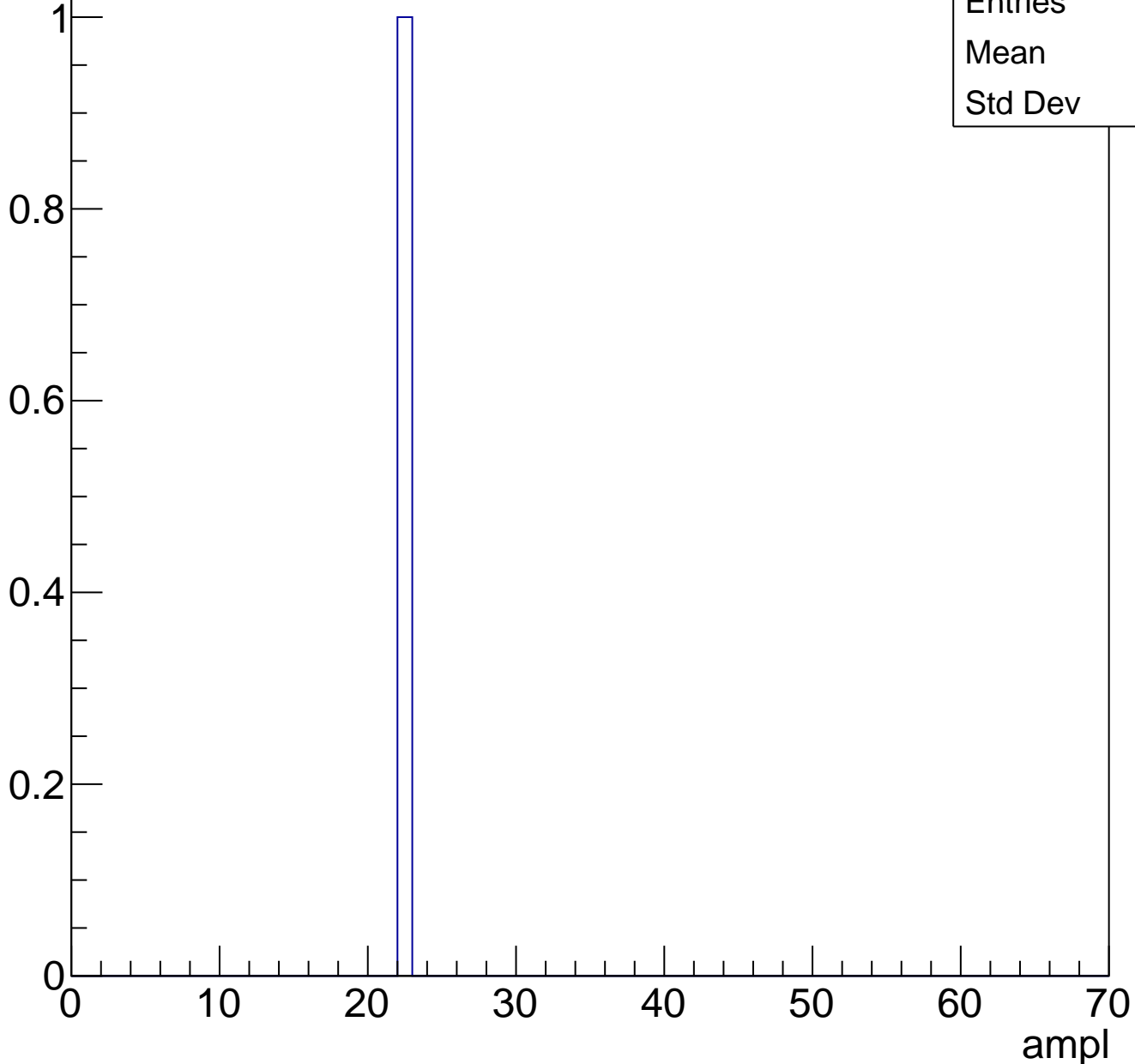
Entry



B1L001S, U26-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 22 |
| Std Dev | 0 |

B1L001S, U26-ch17, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 30.45 |
| Std Dev | 3.371 |

Gaus mean : 30.9608

Gaus Width: 3.4403

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

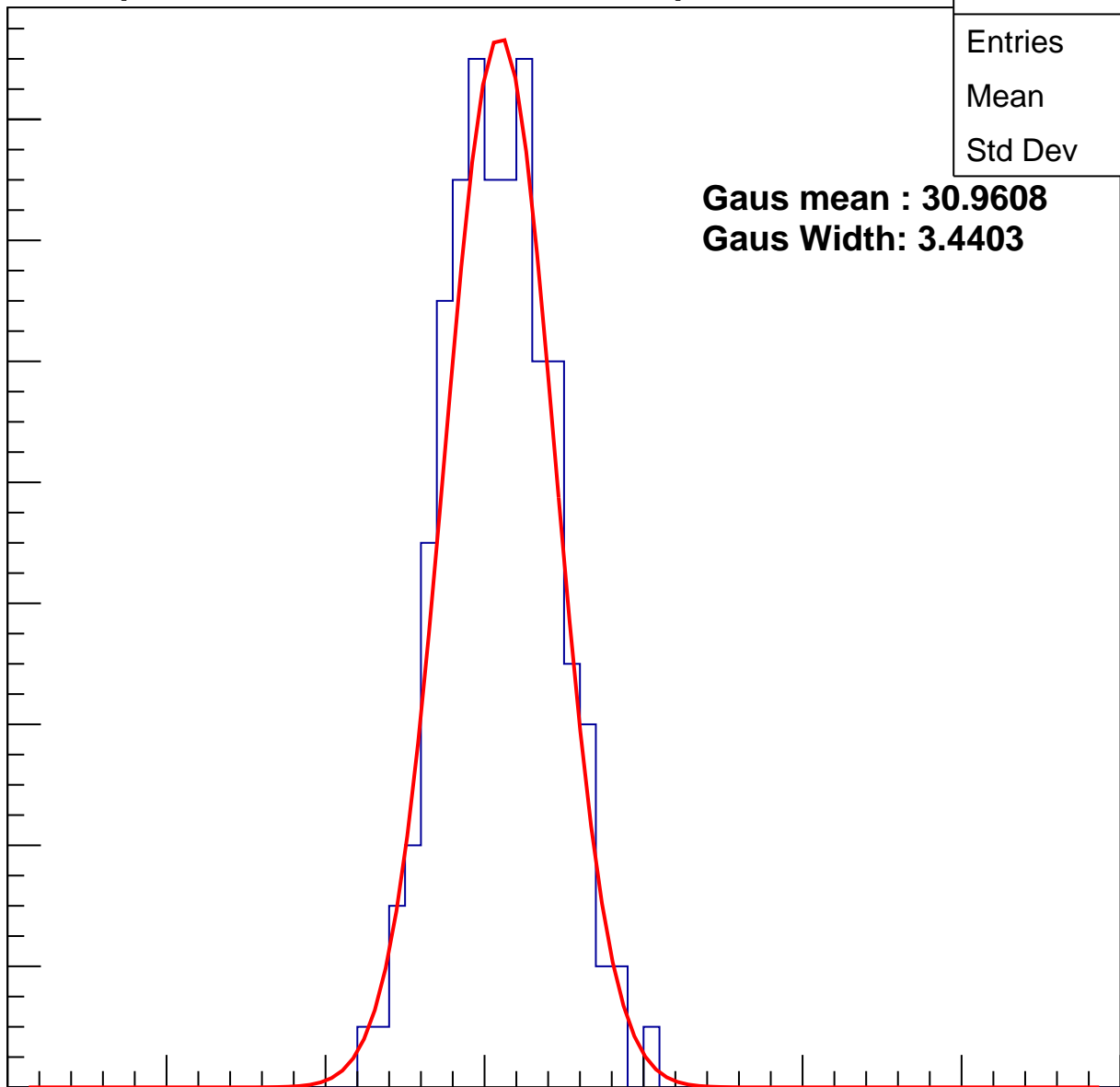
40

50

60

70

ampl

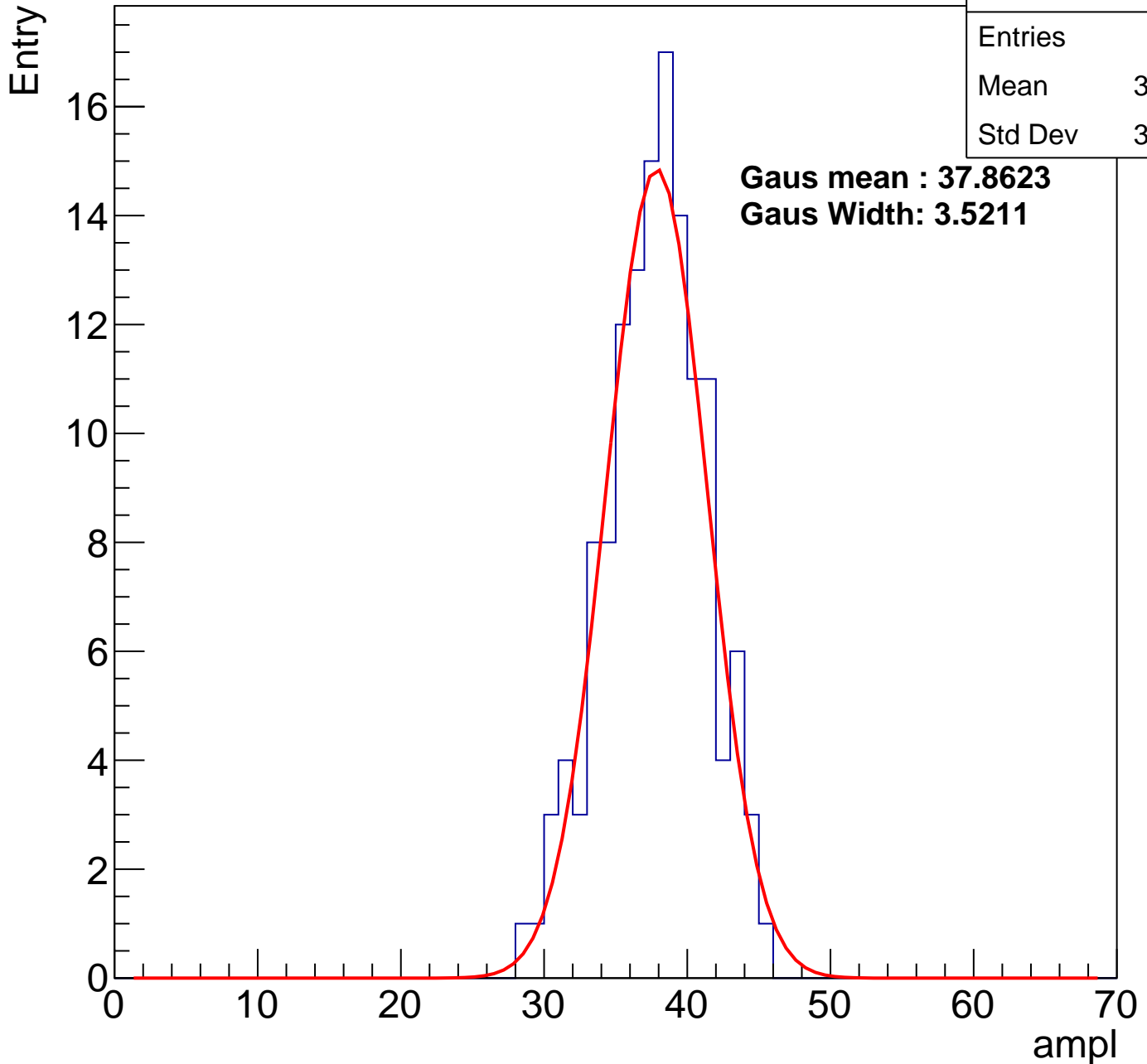


B1L001S, U26-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 37.27 |
| Std Dev | 3.482 |

Gaus mean : 37.8623
Gaus Width: 3.5211



B1L001S, U26-ch17, adc2

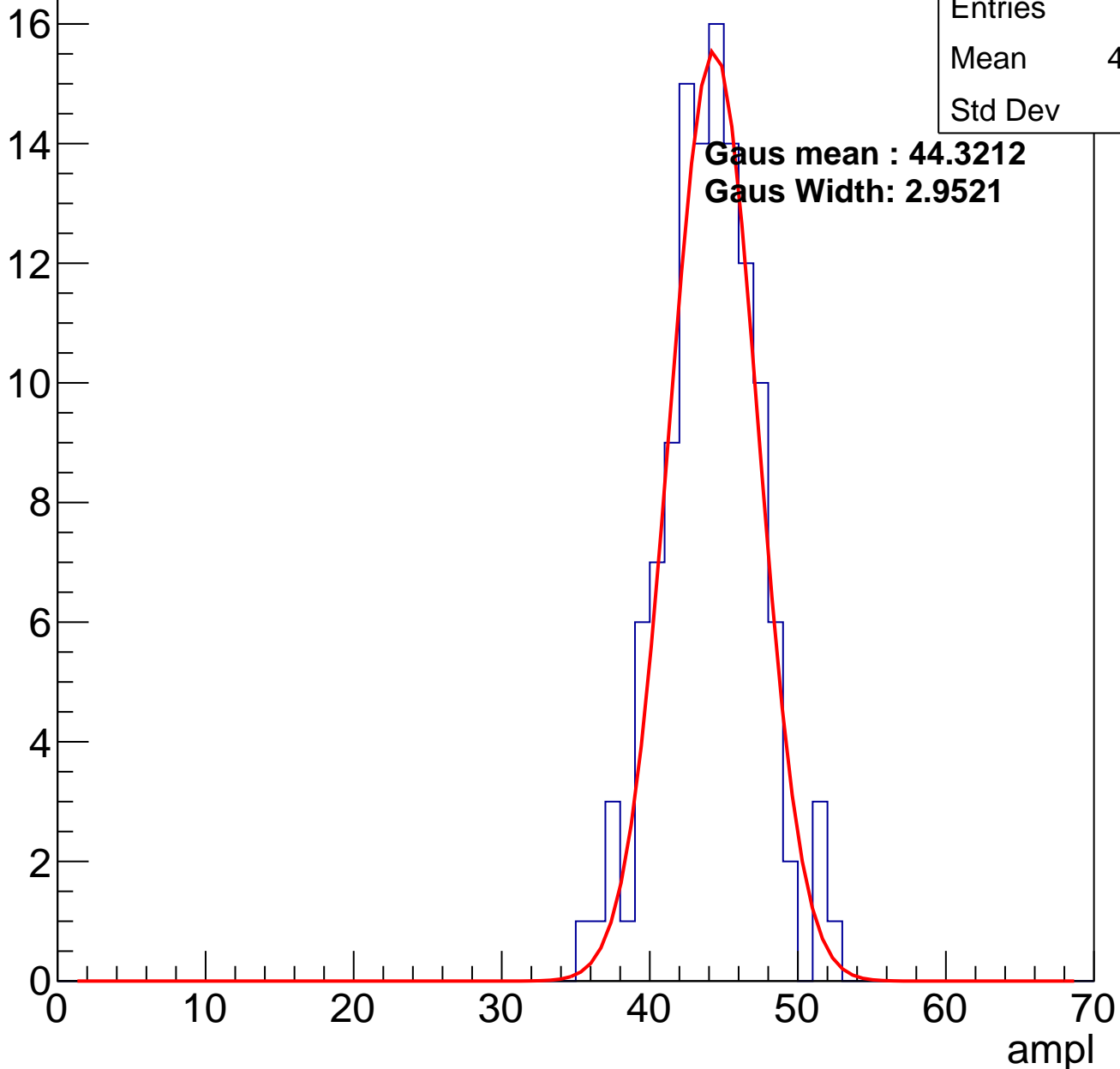
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 43.65 |
| Std Dev | 3.2 |

Gaus mean : 44.3212

Gaus Width: 2.9521

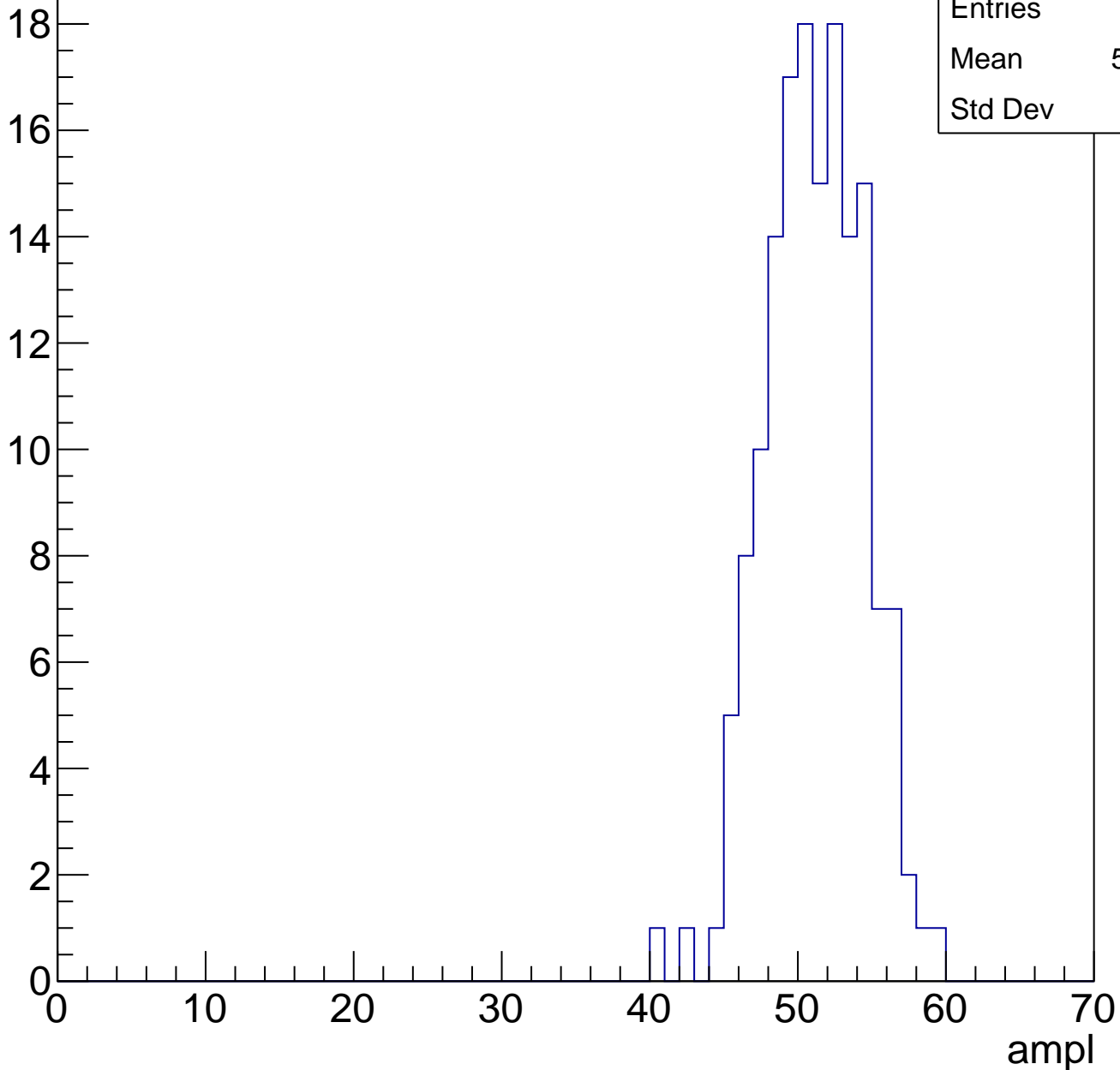
Entry



B1L001S, U26-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 155 |
| Mean | 50.68 |
| Std Dev | 3.28 |

B1L001S, U26-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

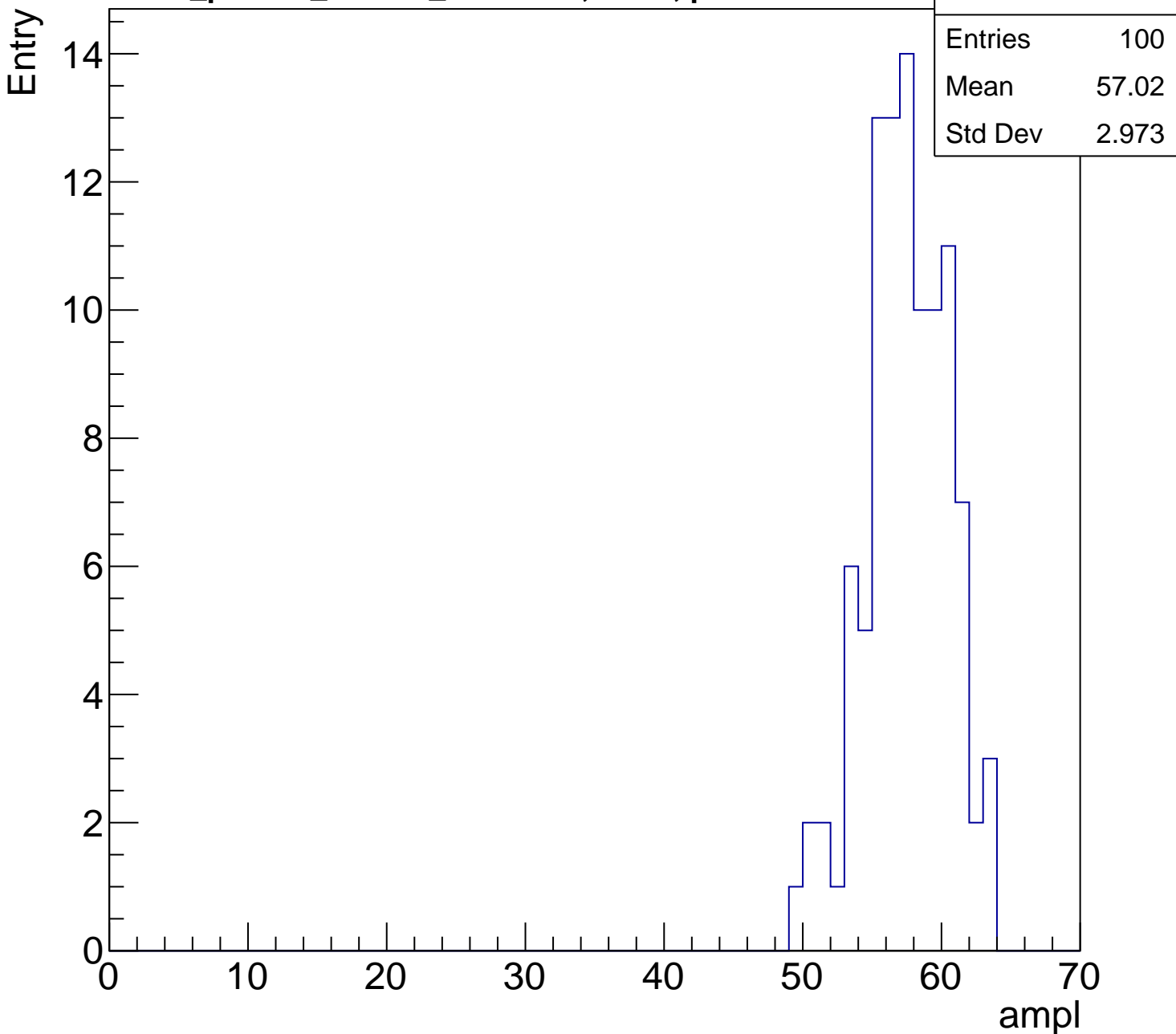
| | |
|---------|-------|
| Entries | 100 |
| Mean | 57.02 |
| Std Dev | 2.973 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch17, adc5

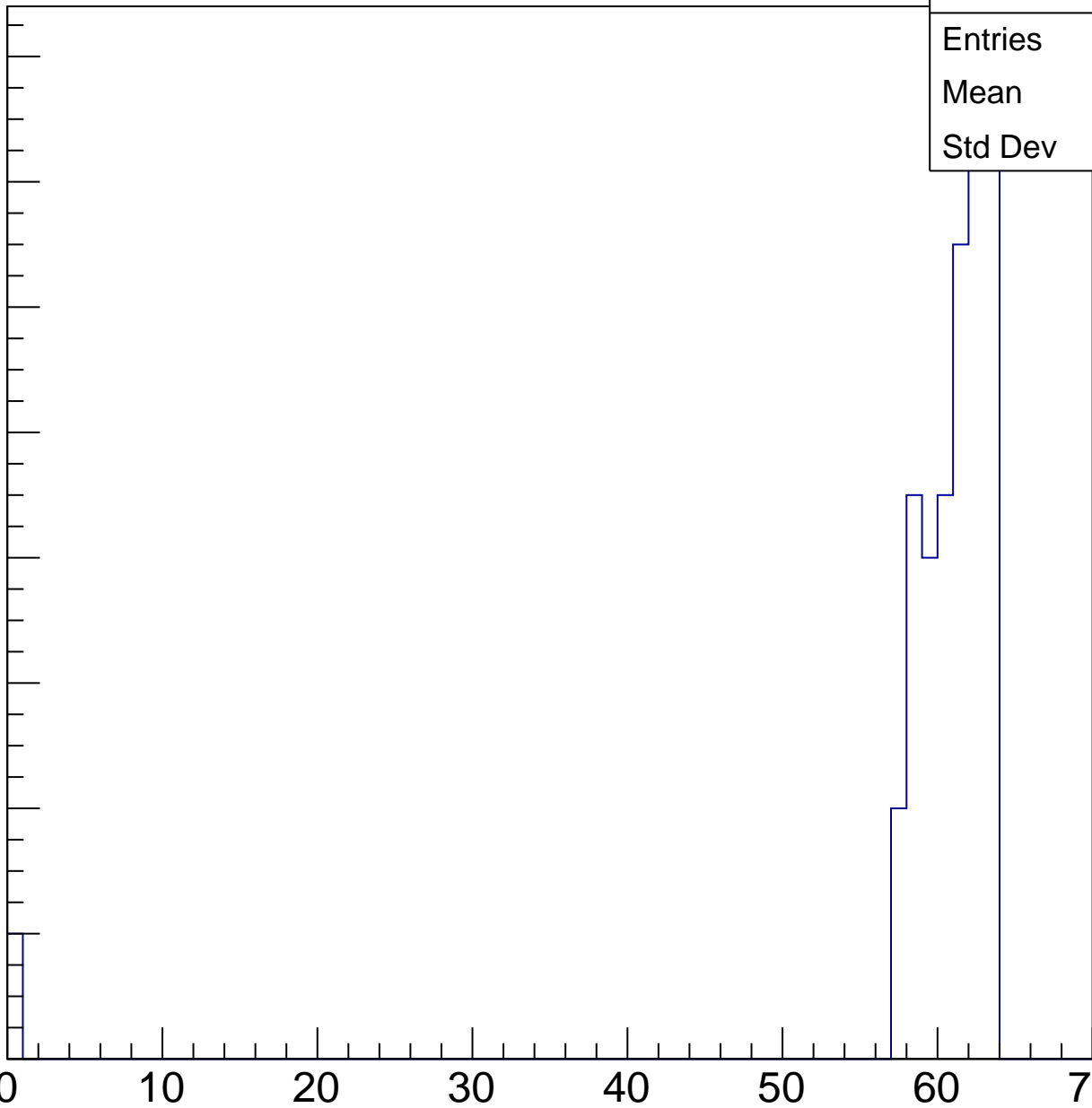
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 77 |
| Mean | 59.16 |
| Std Dev | 9.834 |

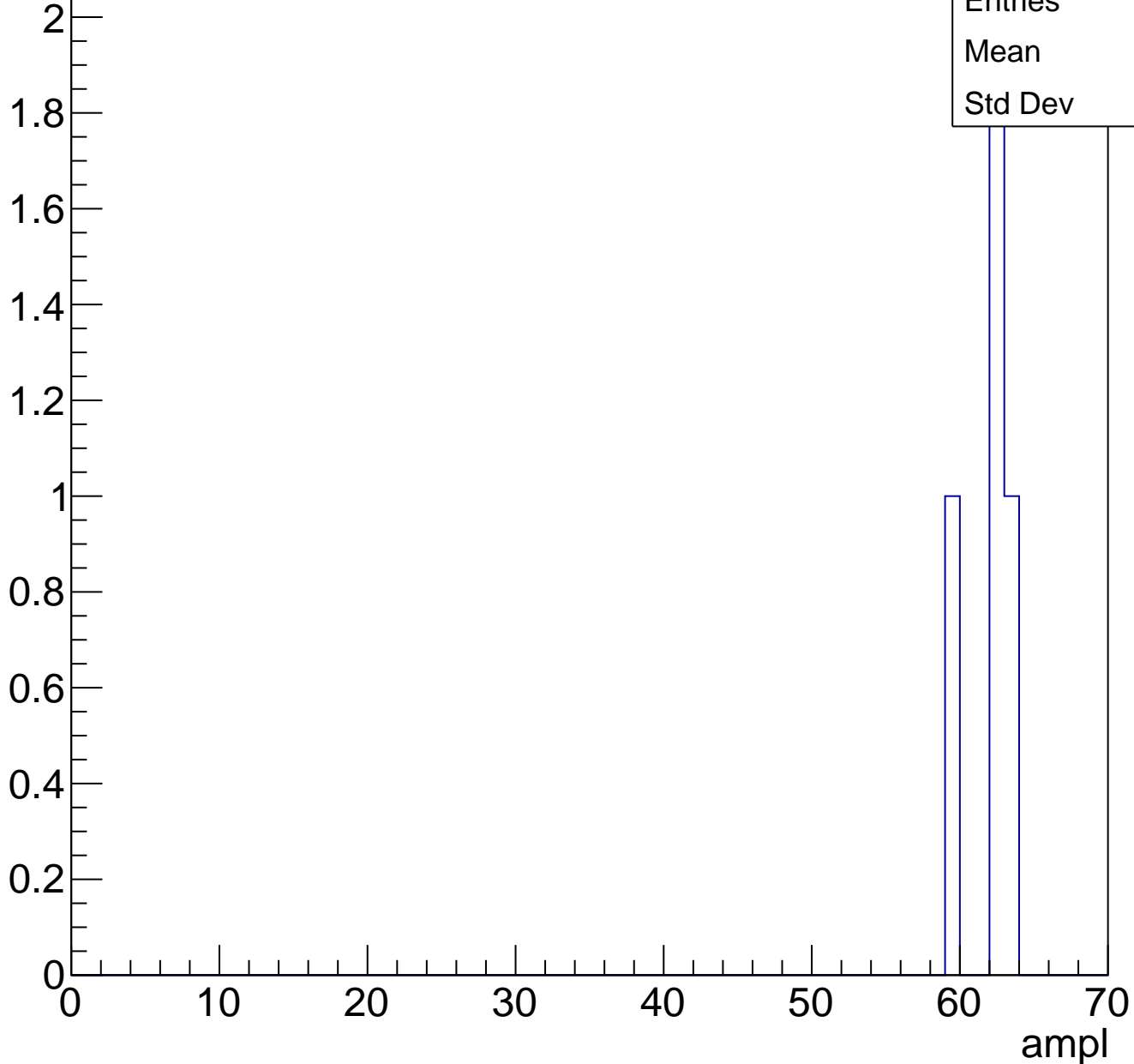
ampl



B1L001S, U26-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.667 |
| Std Dev | 10.84 |

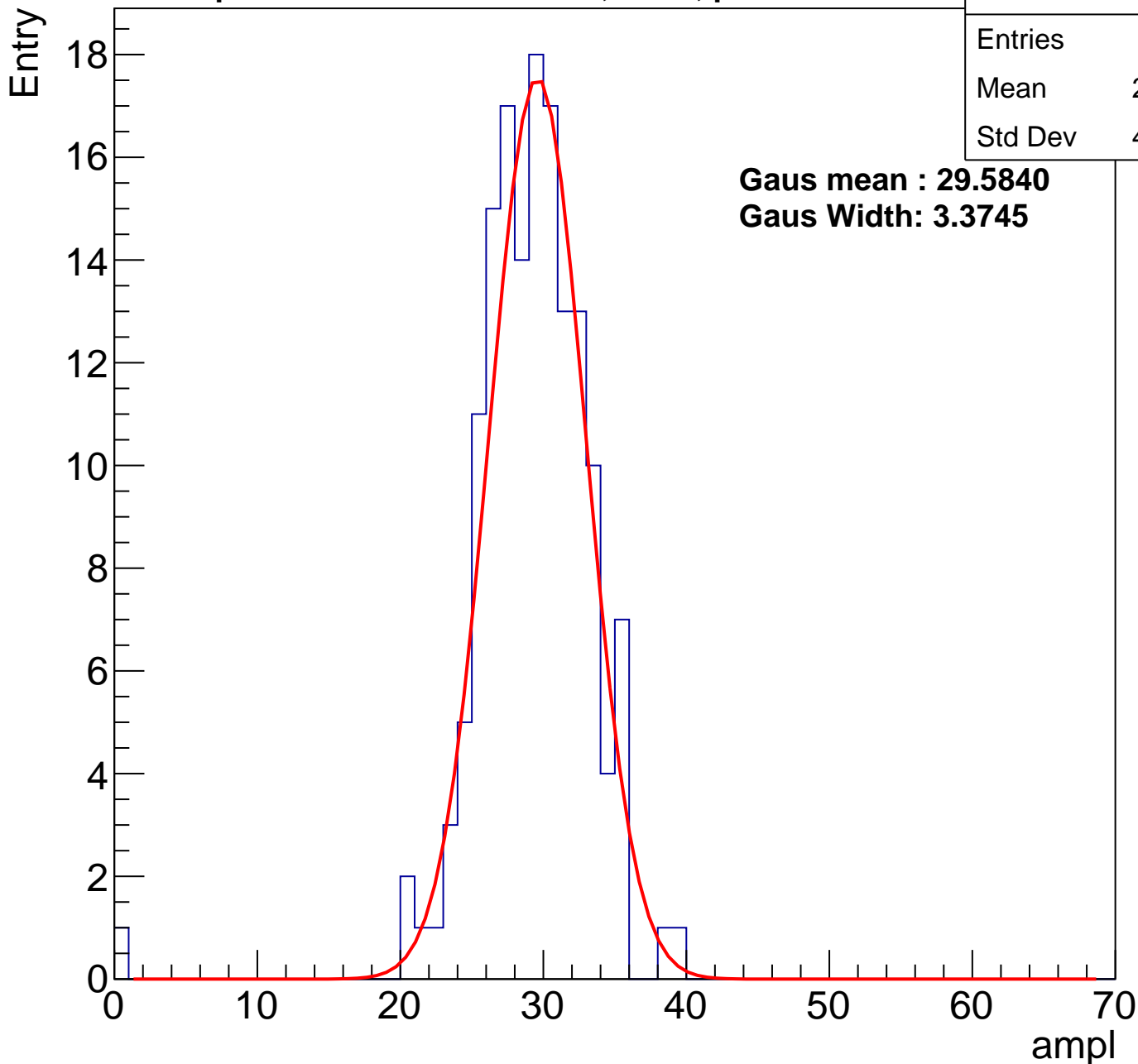
B1L001S, U26-ch18, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 154 |
| Mean | 28.75 |
| Std Dev | 4.118 |

Gaus mean : 29.5840

Gaus Width: 3.3745



B1L001S, U26-ch18, adc1

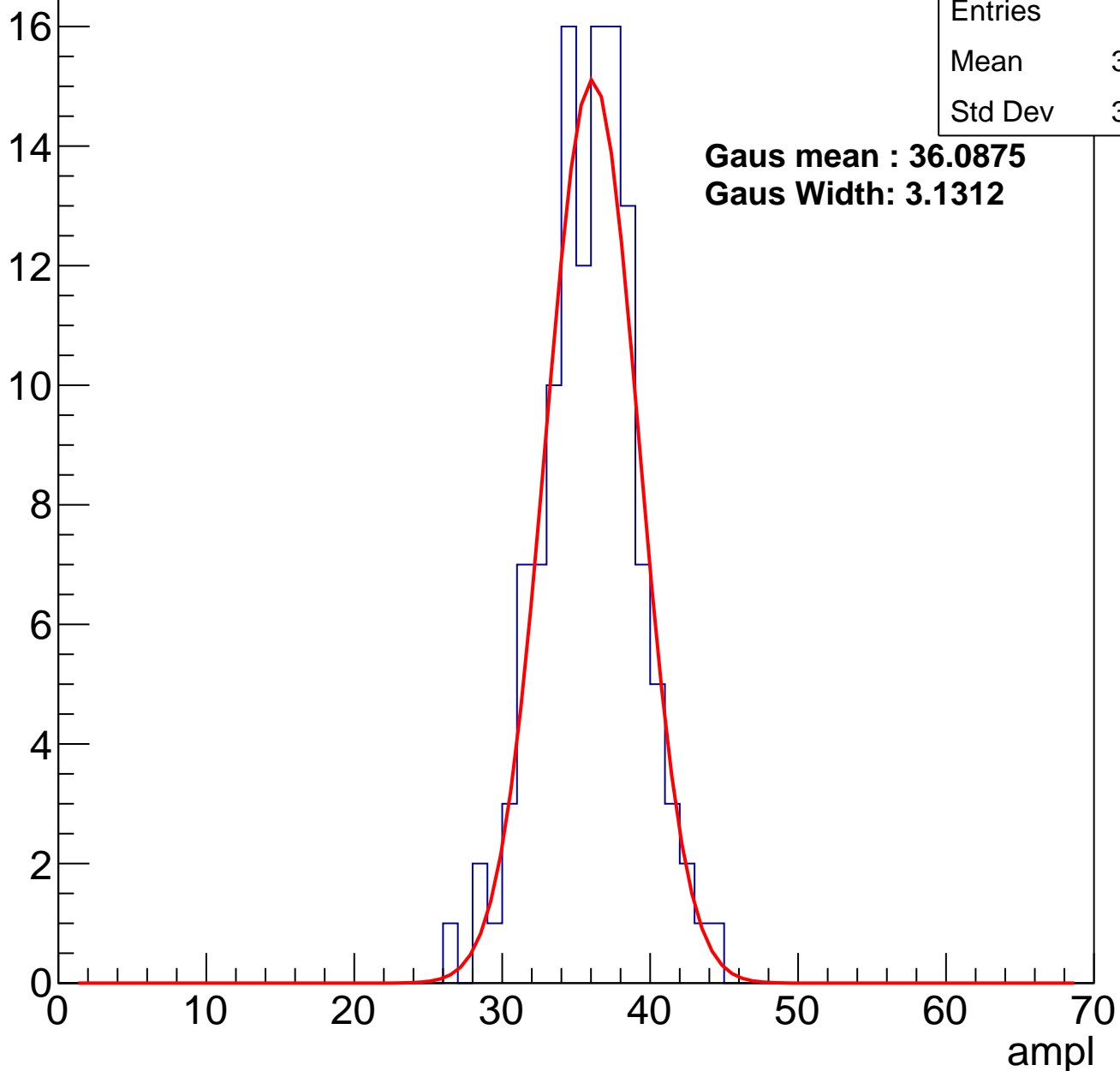
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 35.49 |
| Std Dev | 3.222 |

Gaus mean : 36.0875

Gaus Width: 3.1312

Entry



B1L001S, U26-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 42.29 |
| Std Dev | 3.242 |

Gaus mean : 42.6105

Gaus Width: 3.1255

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

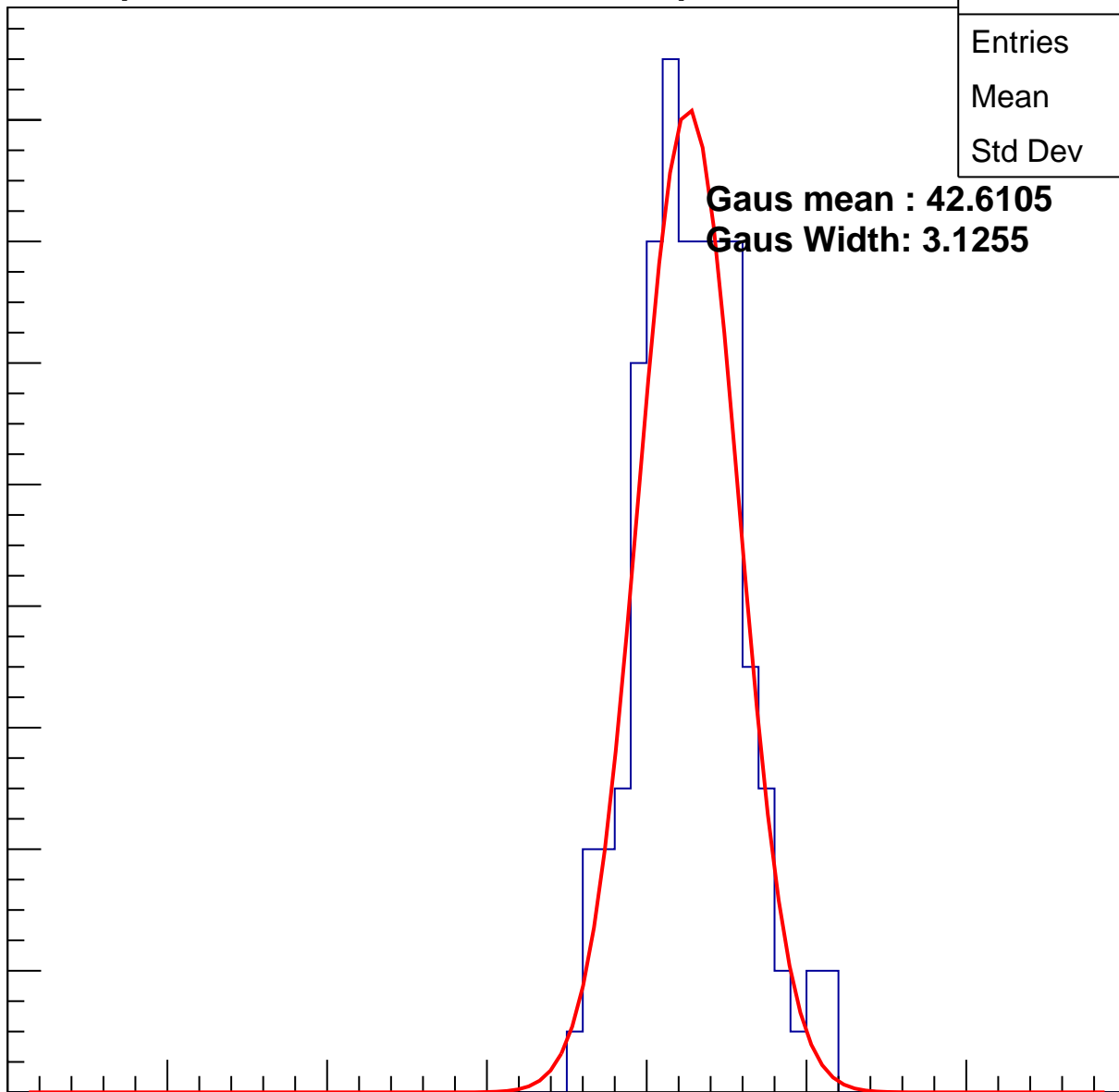
40

50

60

70

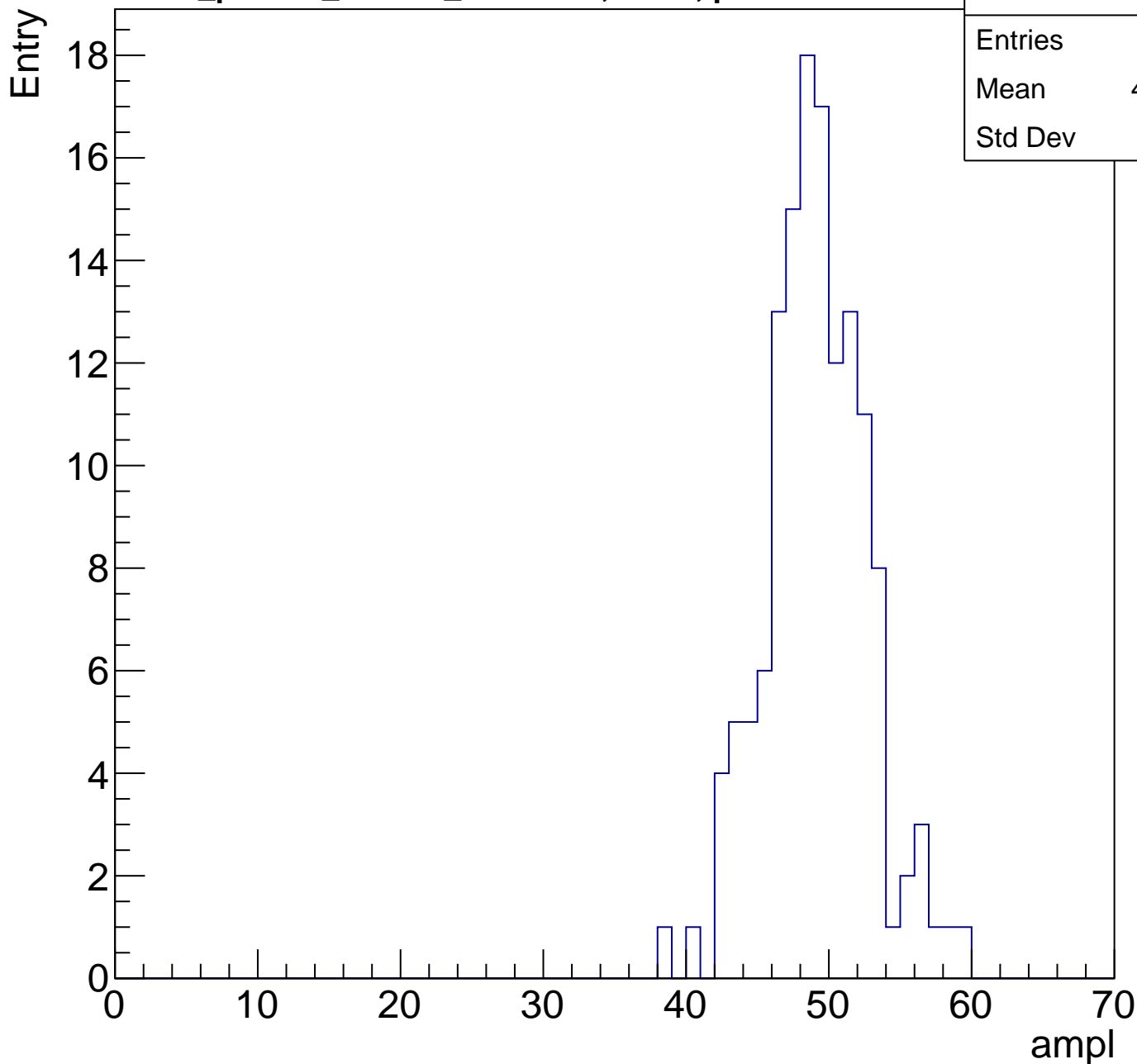
ampl



B1L001S, U26-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

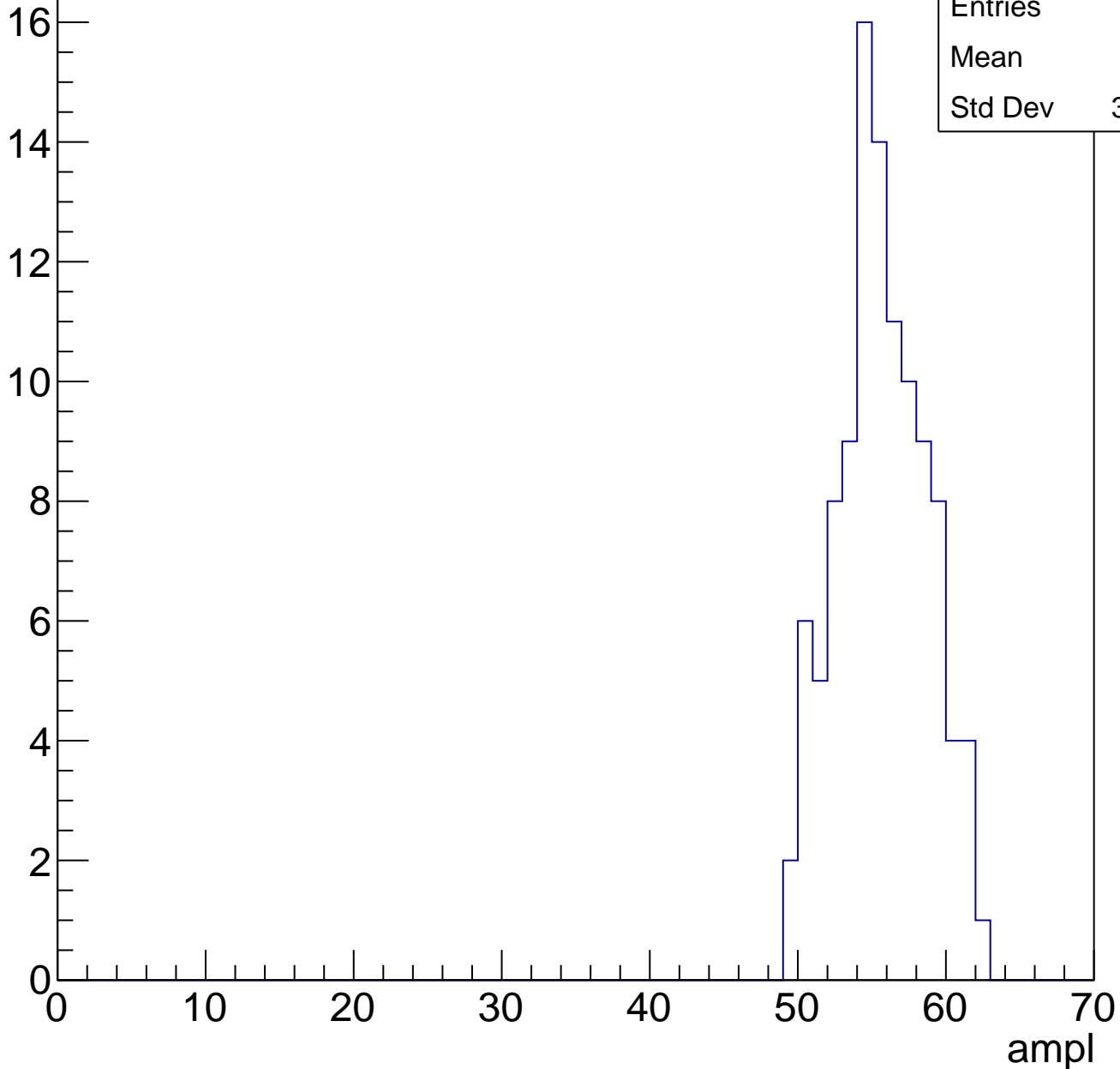
| | |
|---------|-------|
| Entries | 138 |
| Mean | 48.67 |
| Std Dev | 3.57 |



B1L001S, U26-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 107 |
| Mean | 55.2 |
| Std Dev | 3.018 |

B1L001S, U26-ch18, adc5

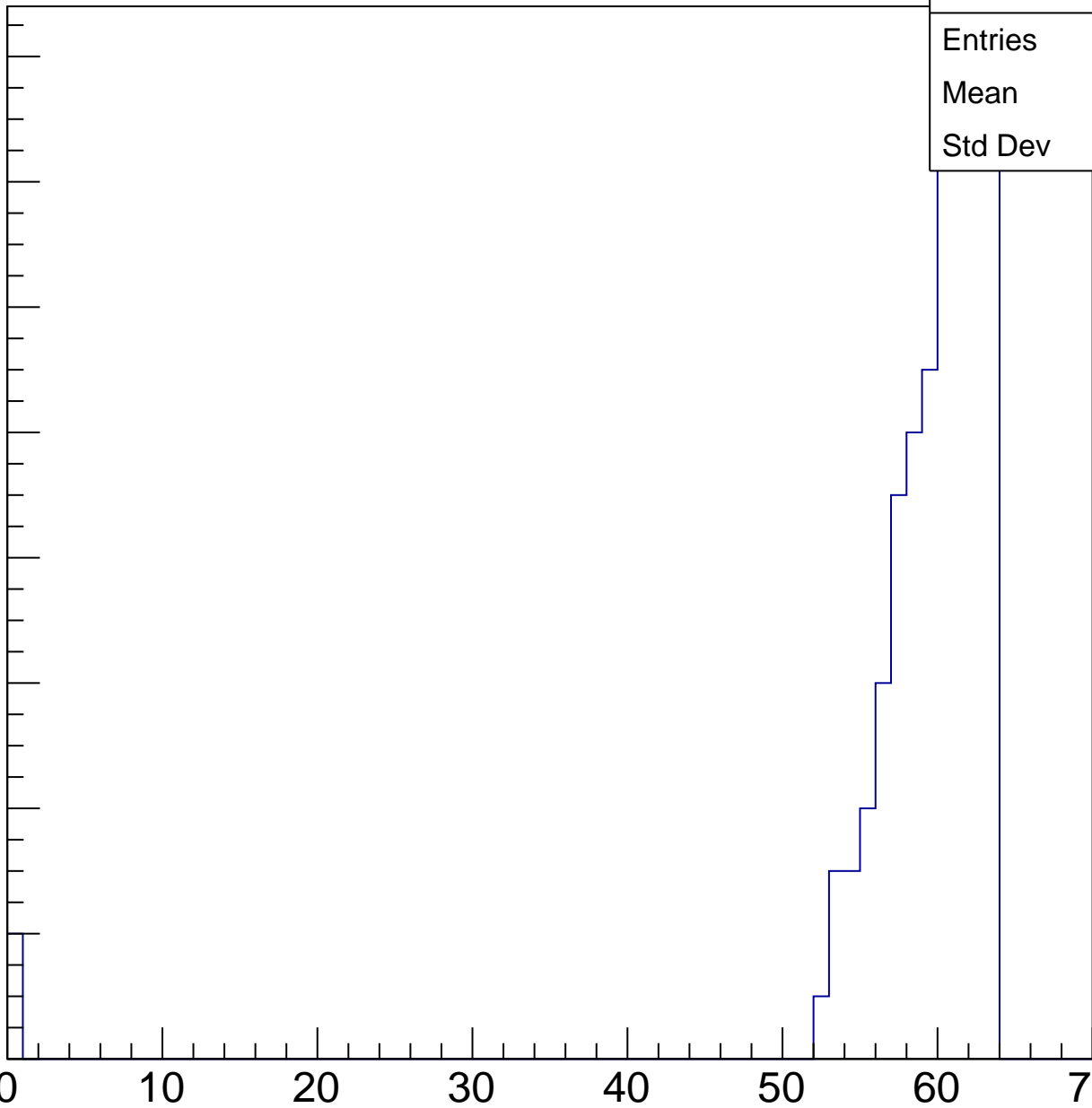
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 110 |
| Mean | 58.37 |
| Std Dev | 8.402 |

ampl

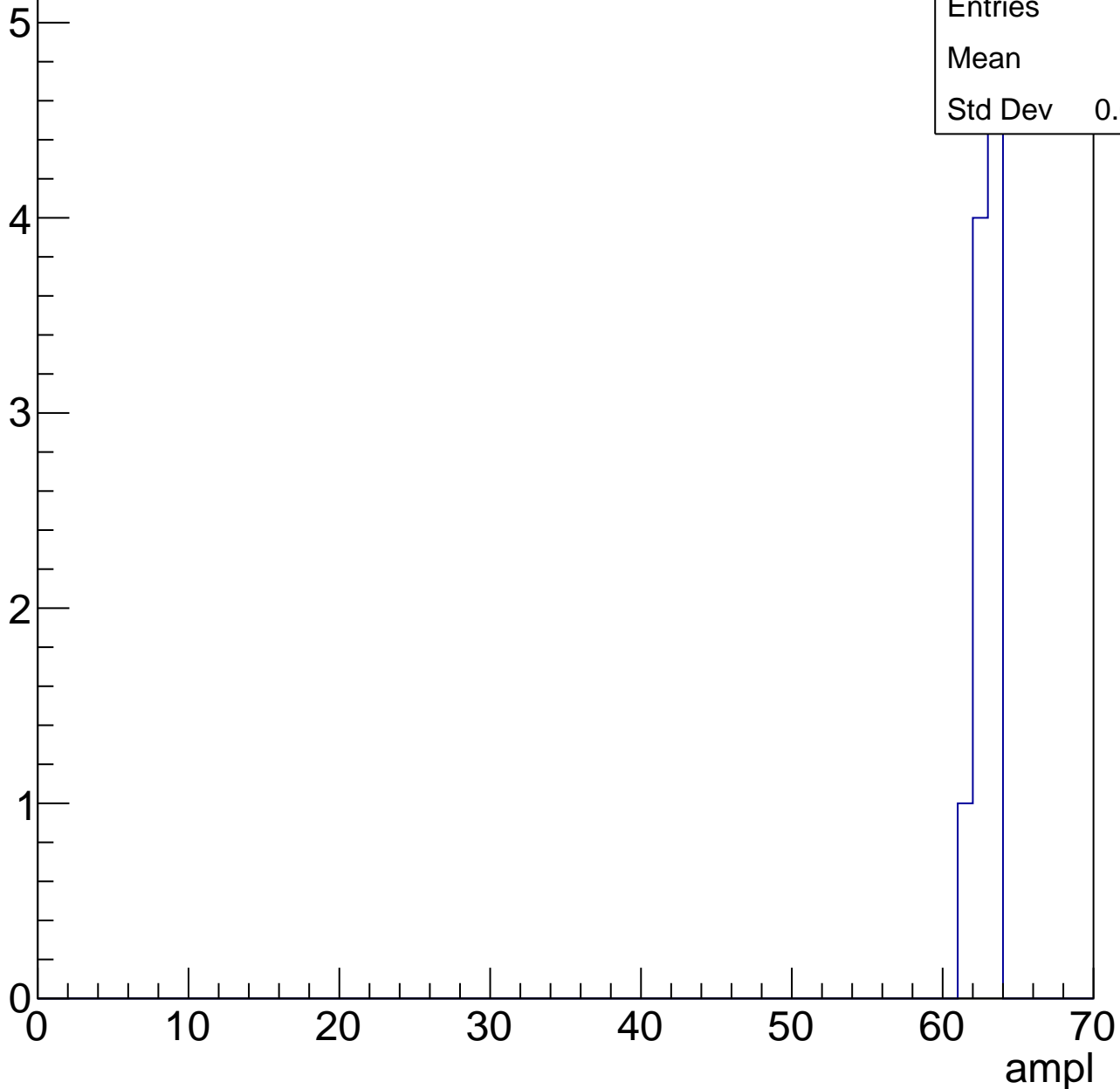


B1L001S, U26-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|--------|
| Entries | 10 |
| Mean | 62.4 |
| Std Dev | 0.6633 |



B1L001S, U26-ch18, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

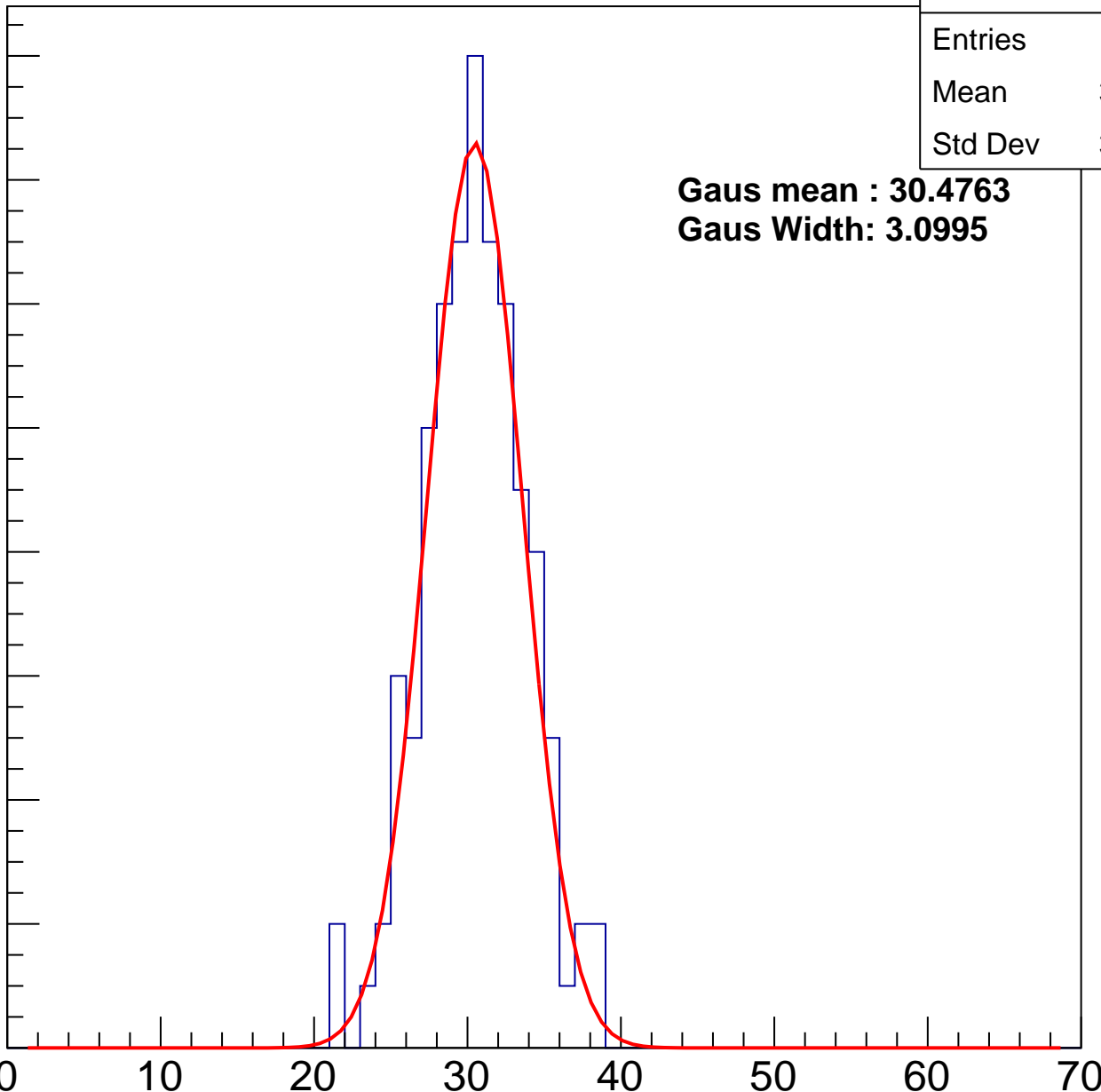
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 119 |
| Mean | 30.03 |
| Std Dev | 3.332 |

Gaus mean : 30.4763

Gaus Width: 3.0995

ampl



B1L001S, U26-ch19, adc1

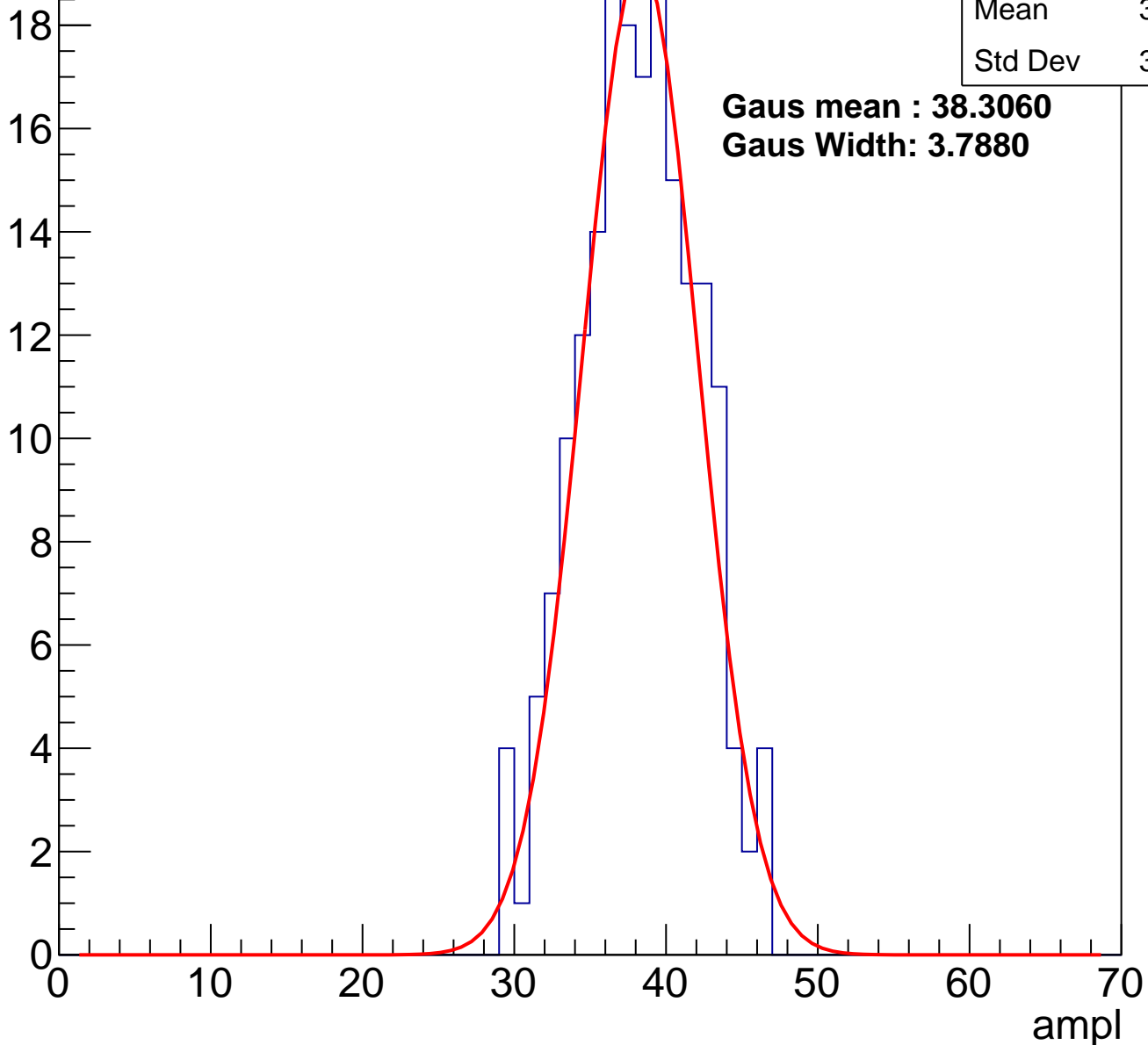
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 188 |
| Mean | 37.72 |
| Std Dev | 3.796 |

Gaus mean : 38.3060

Gaus Width: 3.7880

Entry



B1L001S, U26-ch19, adc2

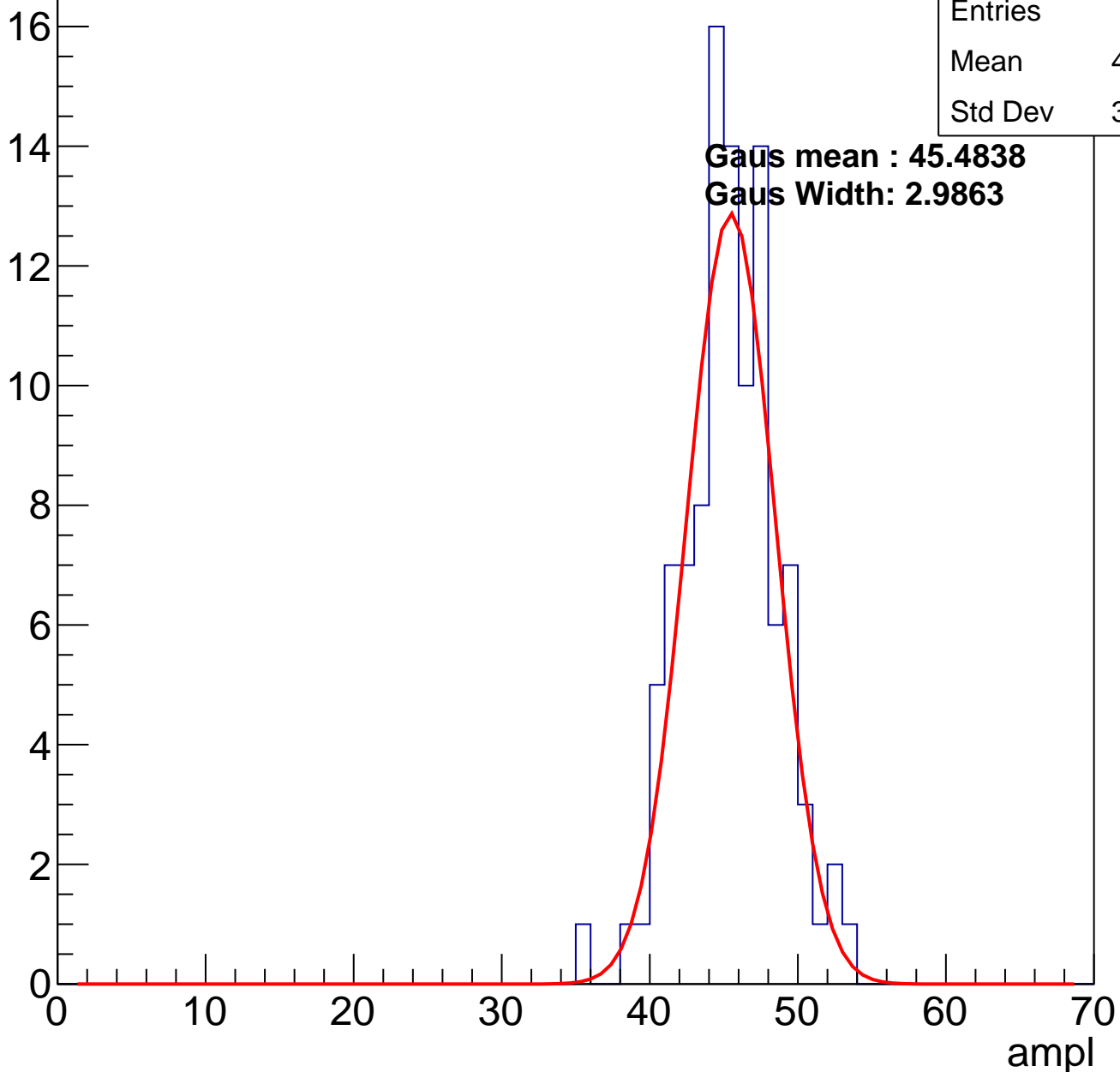
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 104 |
| Mean | 44.98 |
| Std Dev | 3.159 |

Gaus mean : 45.4838

Gaus Width: 2.9863

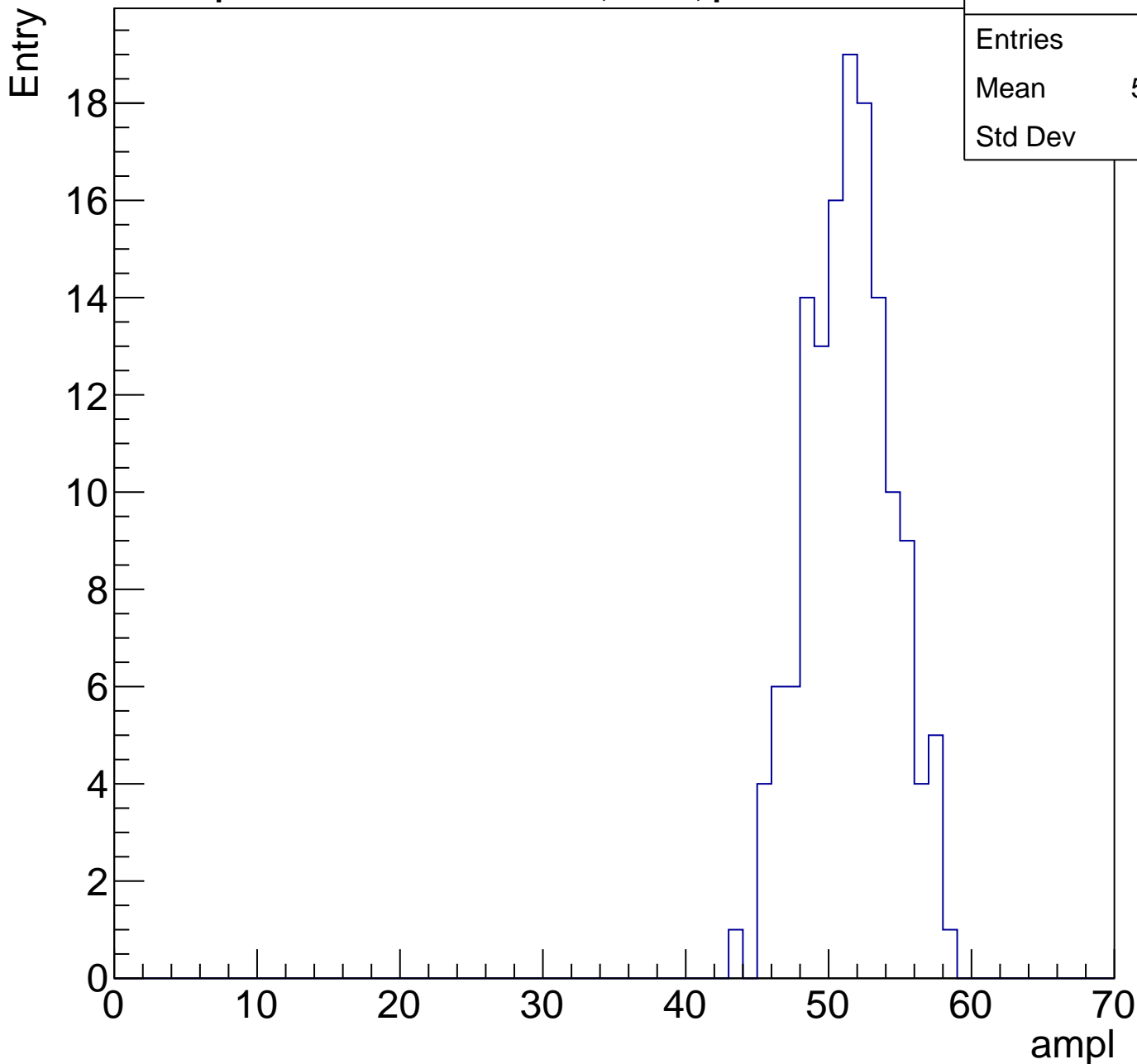
Entry



B1L001S, U26-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 50.99 |
| Std Dev | 3.03 |



B1L001S, U26-ch19, adc4

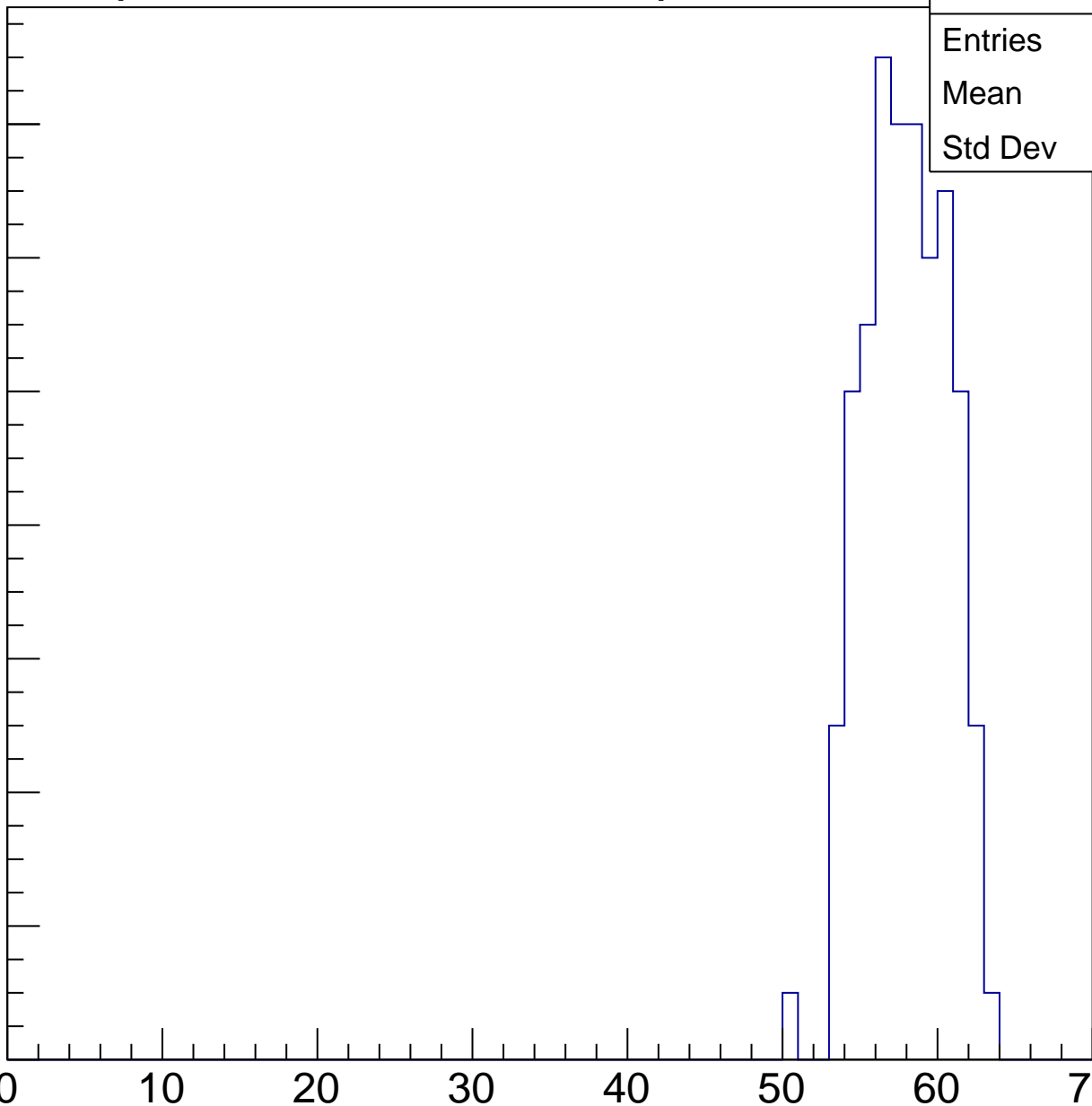
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 111 |
| Mean | 57.49 |
| Std Dev | 2.602 |

ampl



B1L001S, U26-ch19, adc5

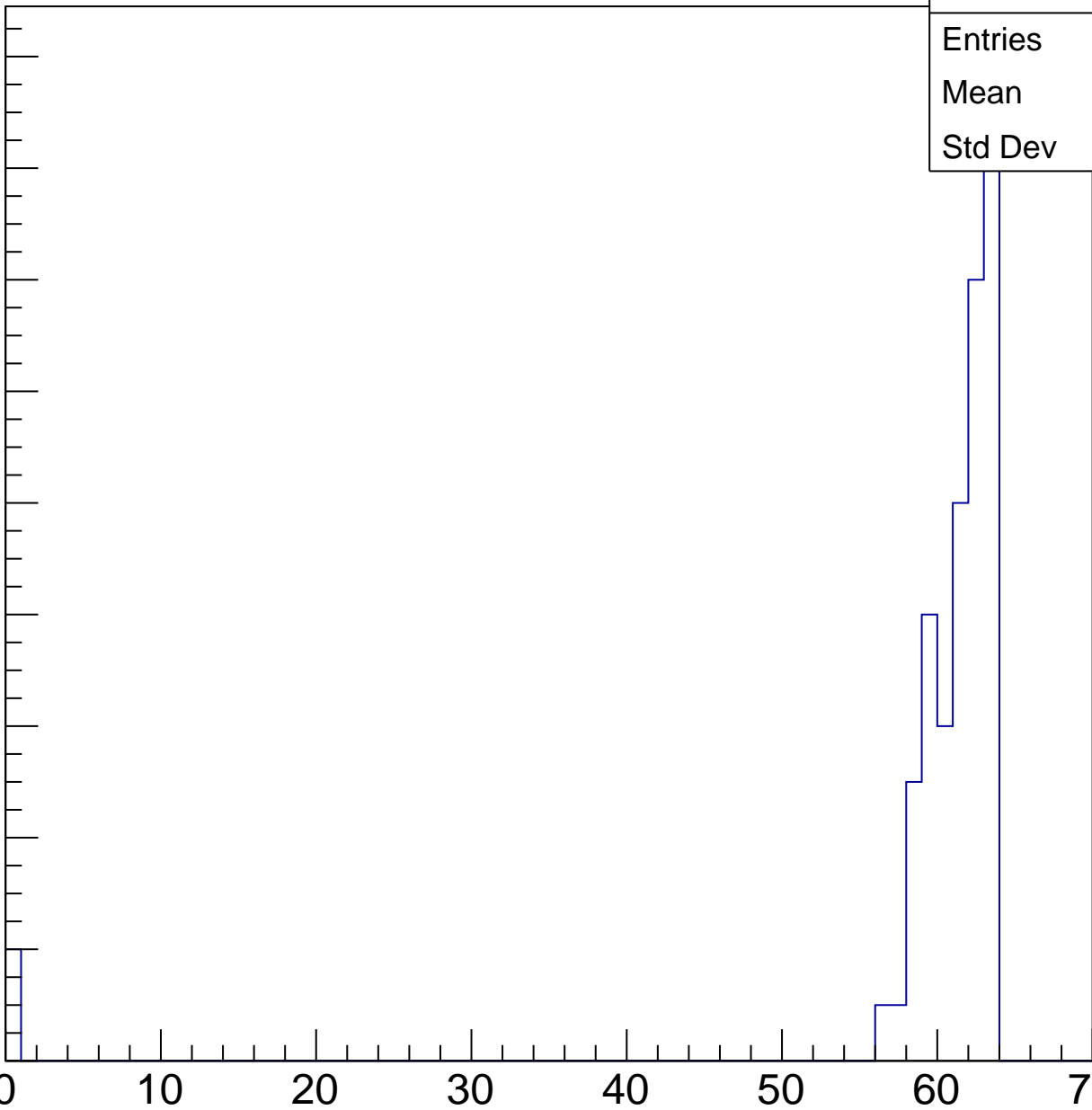
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 65 |
| Mean | 59.18 |
| Std Dev | 10.7 |

ampl



B1L001S, U26-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl

B1L001S, U26-ch20, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 28.85 |
| Std Dev | 3.471 |

Gaus mean : 29.3665

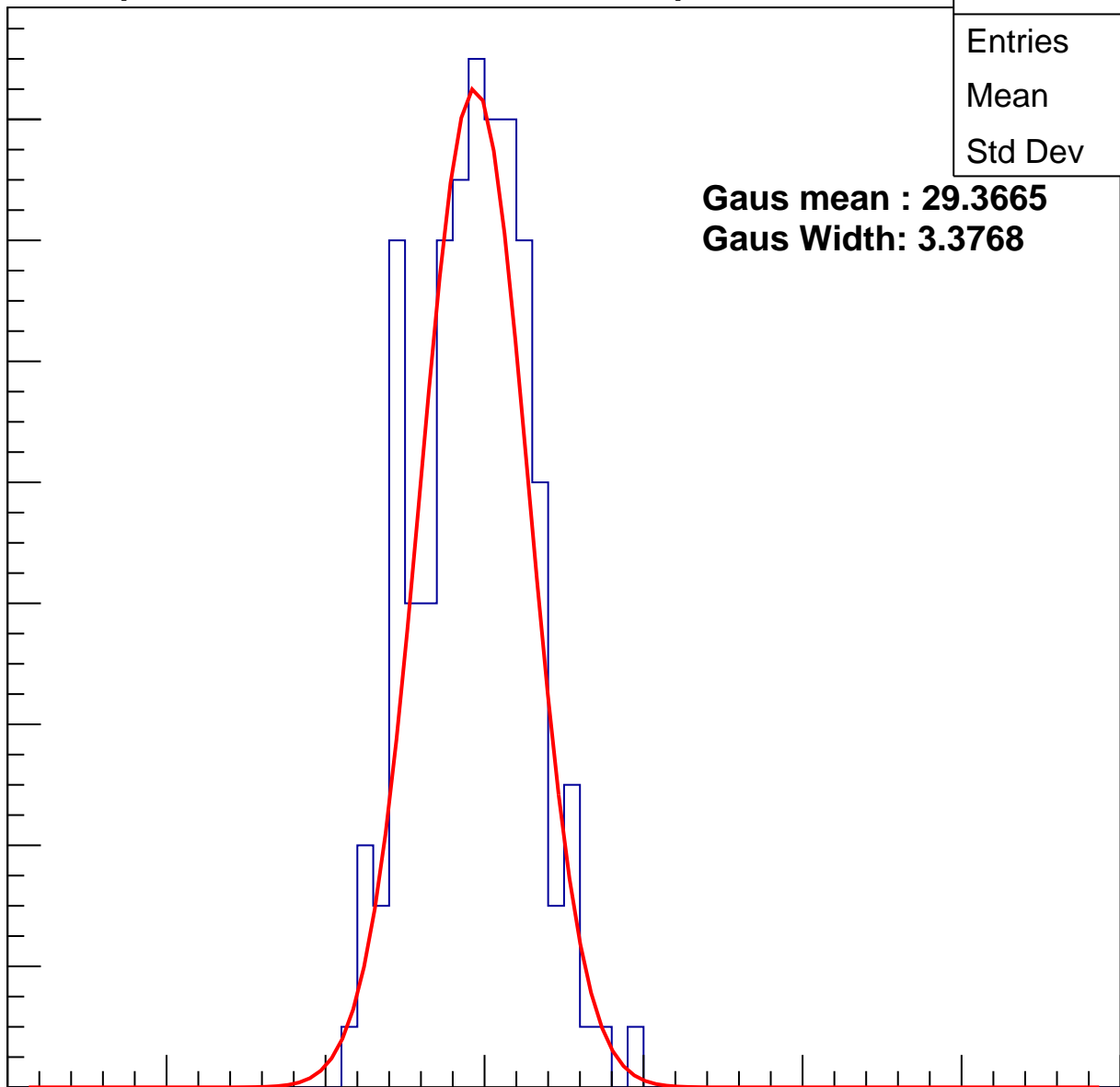
Gaus Width: 3.3768

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

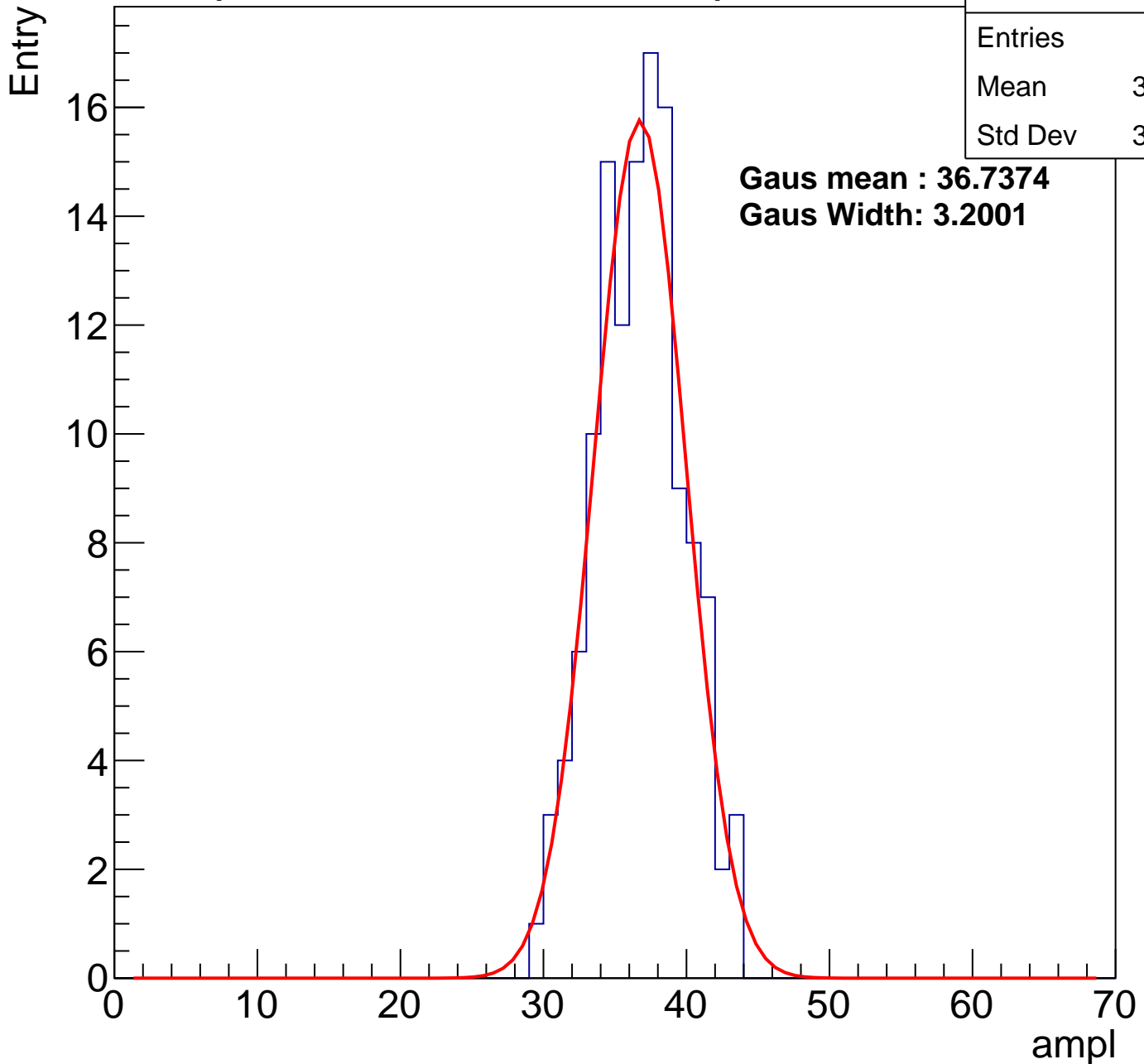


B1L001S, U26-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 36.27 |
| Std Dev | 3.048 |

Gaus mean : 36.7374
Gaus Width: 3.2001



B1L001S, U26-ch20, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

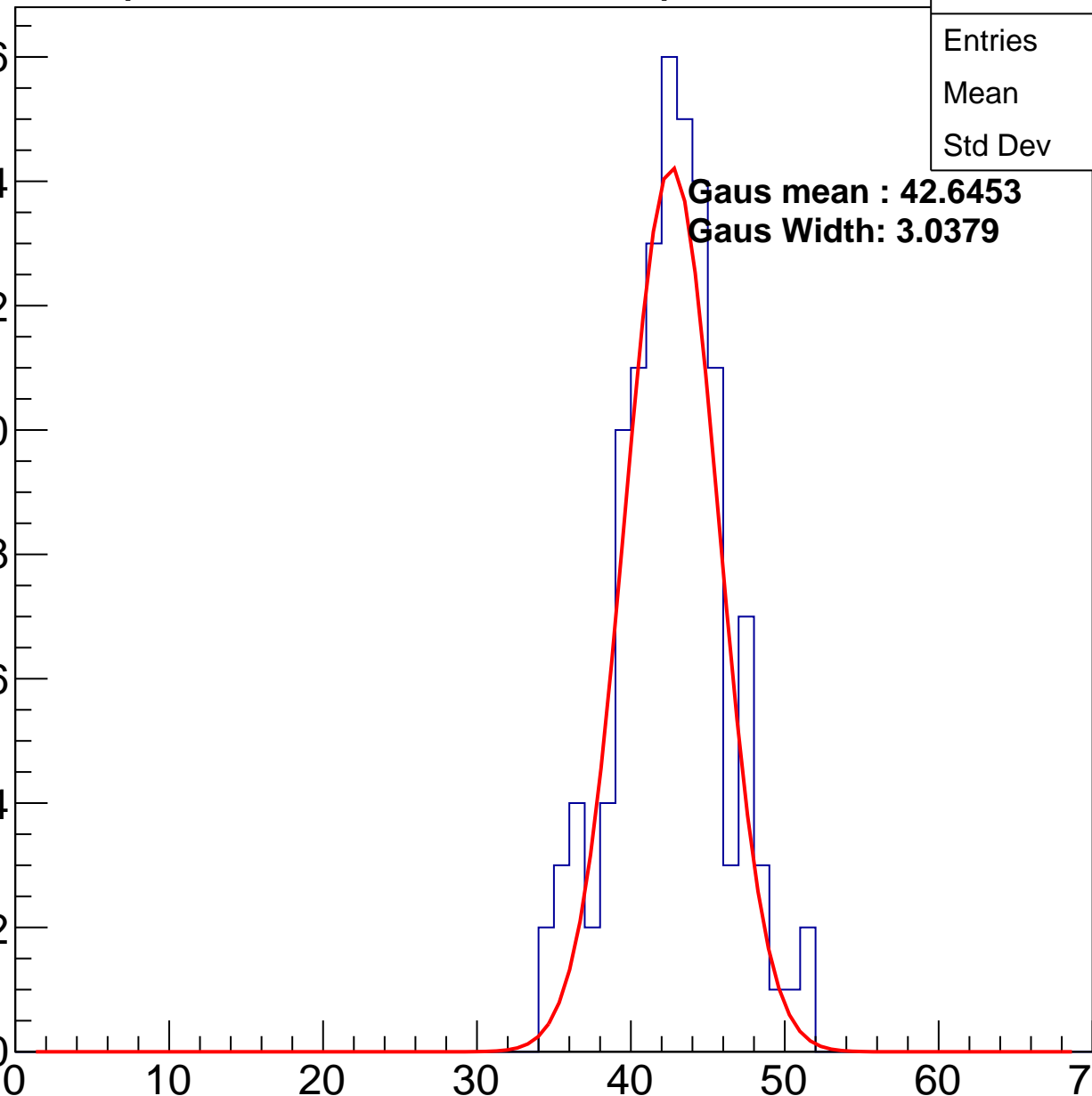
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 122 |
| Mean | 42.18 |
| Std Dev | 3.45 |

Gaus mean : 42.6453

Gaus Width: 3.0379

ampl



B1L001S, U26-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

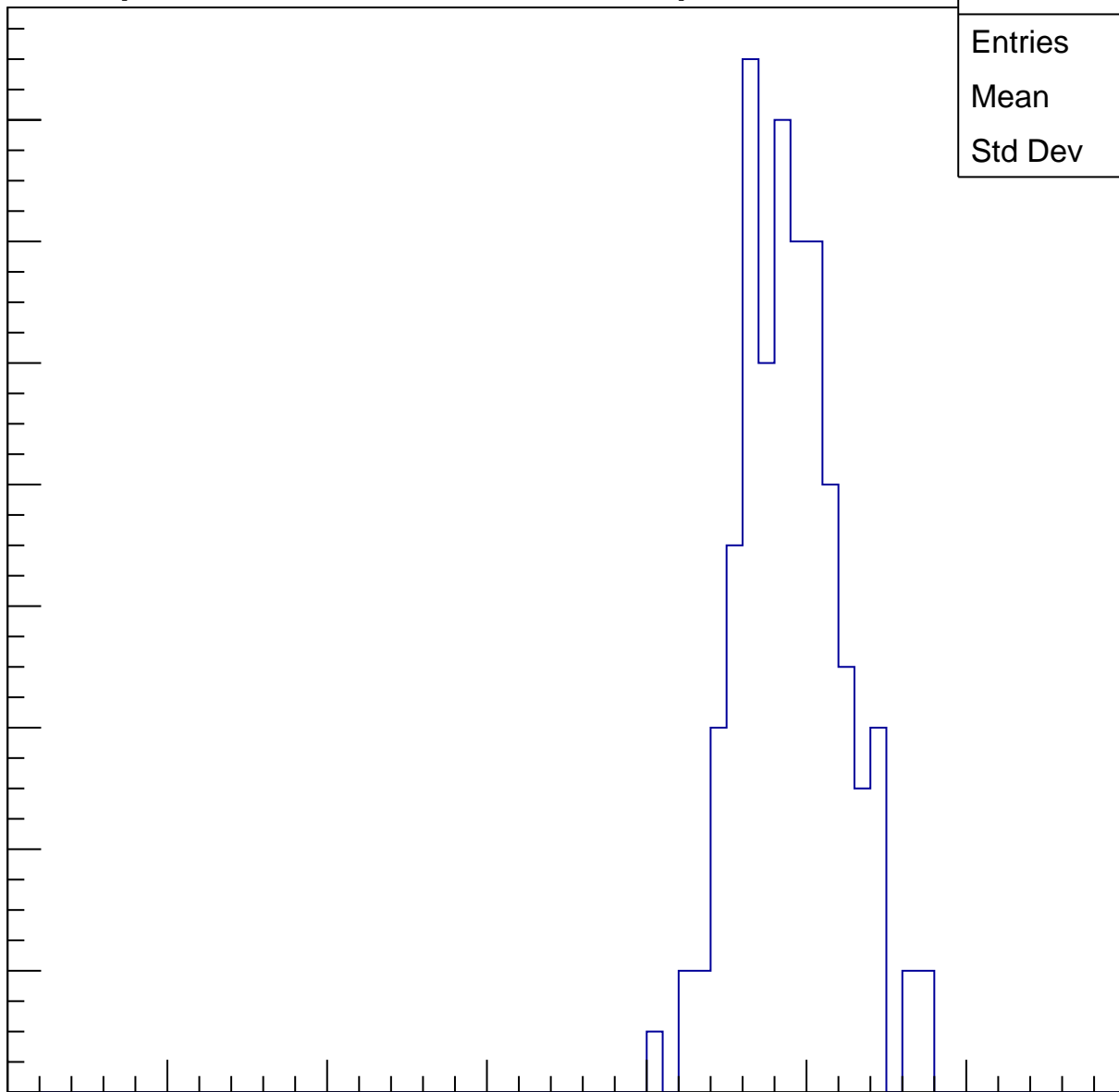
| | |
|---------|-------|
| Entries | 125 |
| Mean | 48.54 |
| Std Dev | 3.236 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

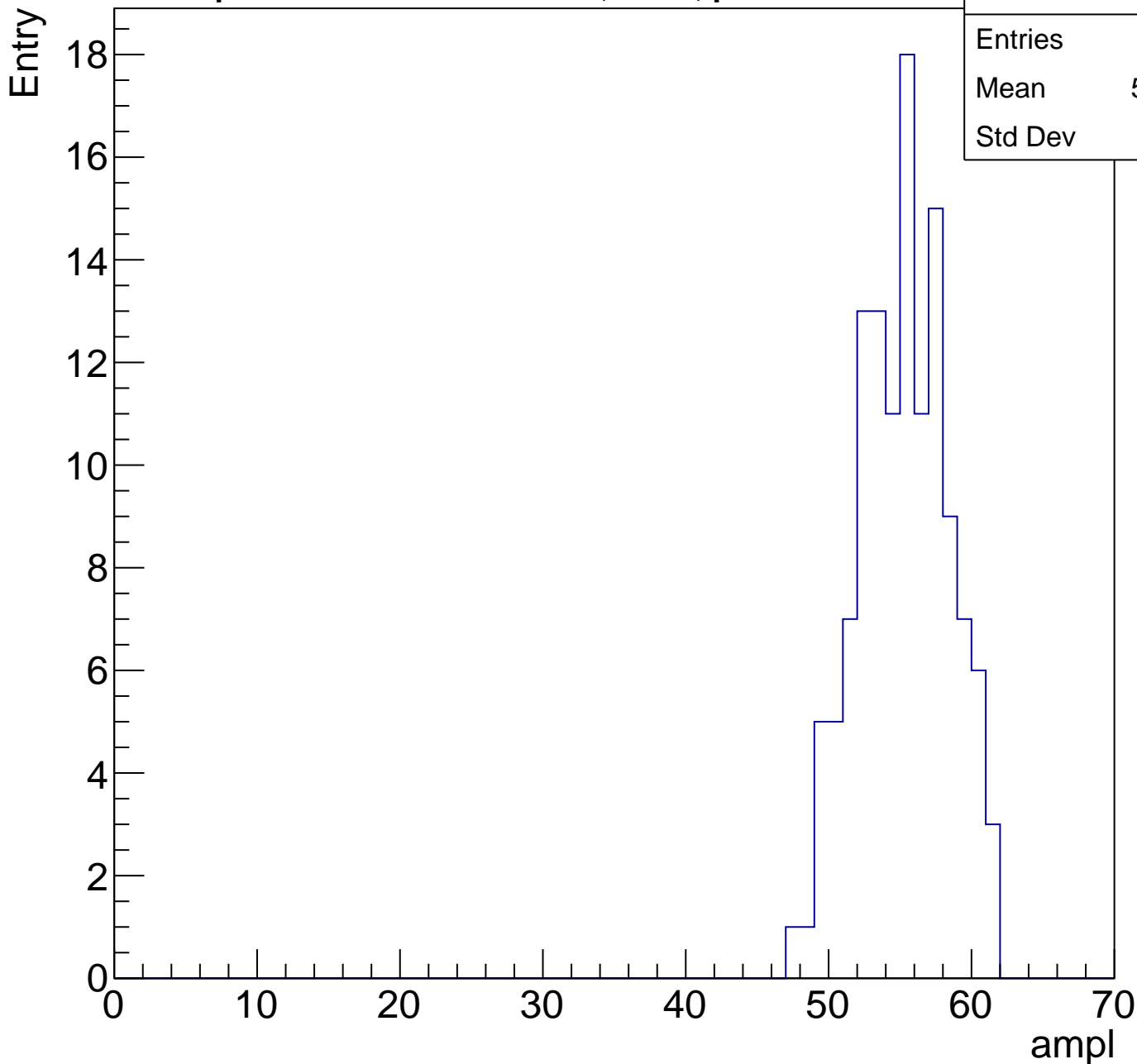
ampl



B1L001S, U26-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 54.76 |
| Std Dev | 3.13 |



B1L001S, U26-ch20, adc5

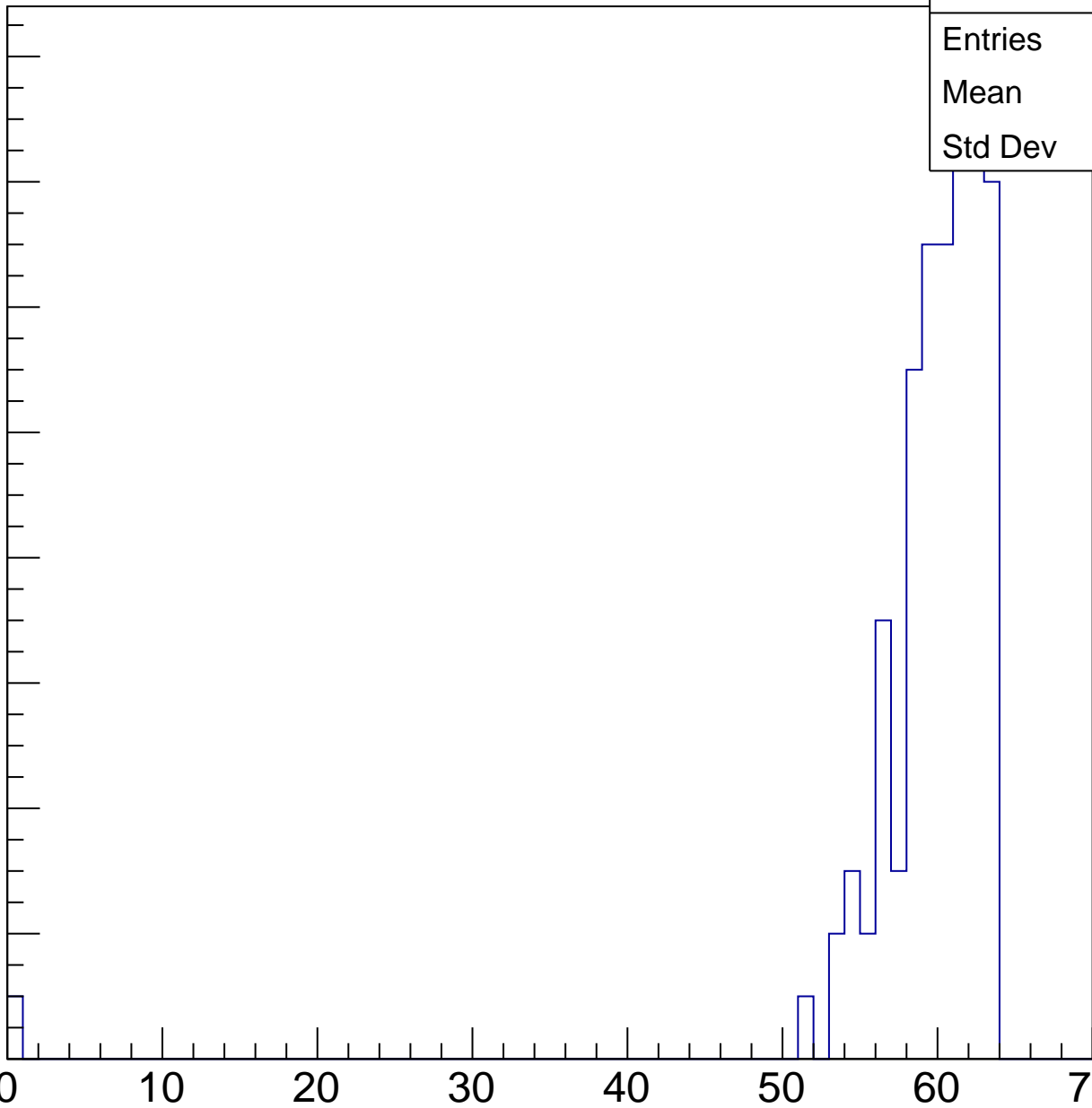
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 101 |
| Mean | 59.07 |
| Std Dev | 6.487 |

ampl

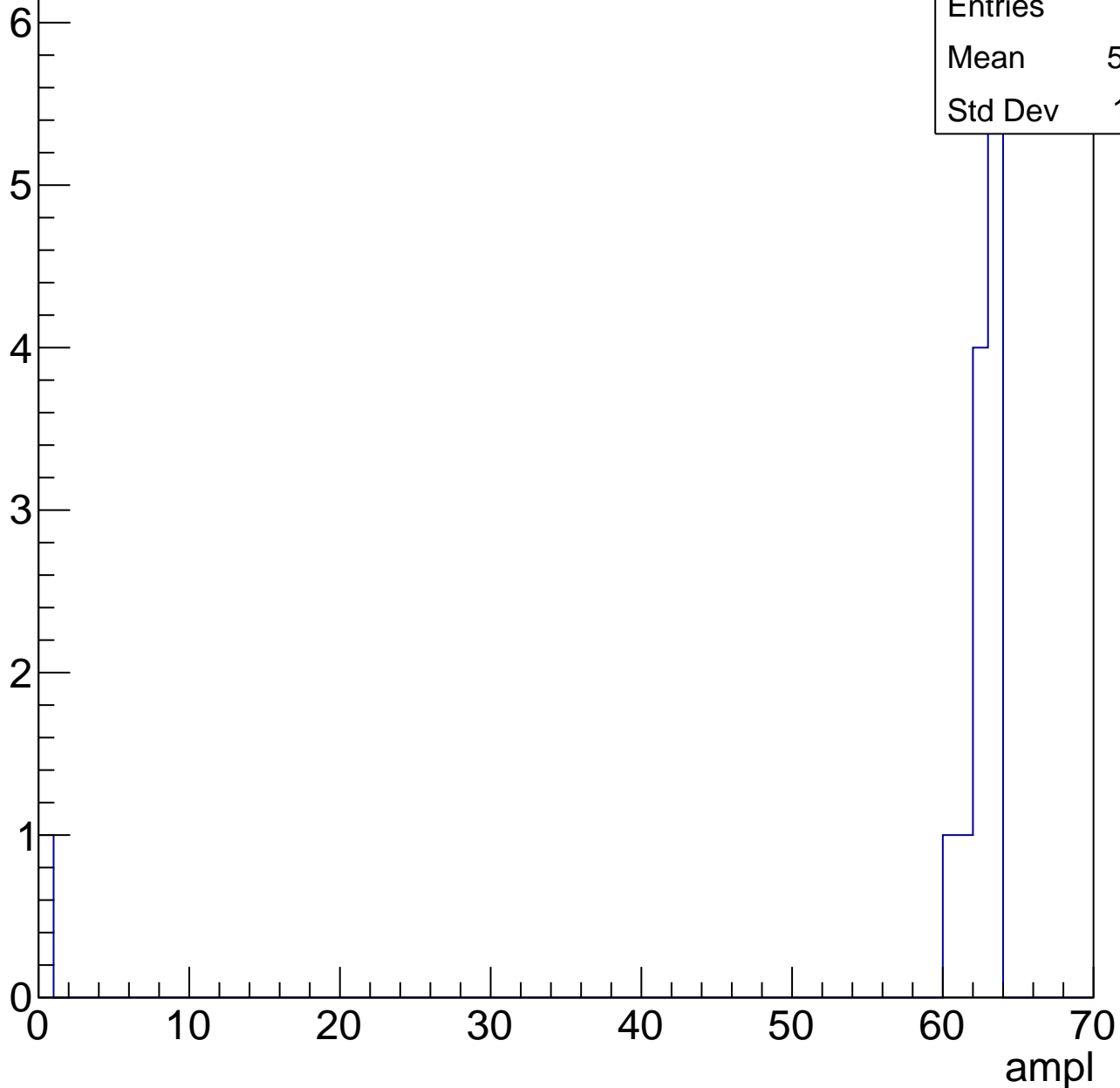


B1L001S, U26-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

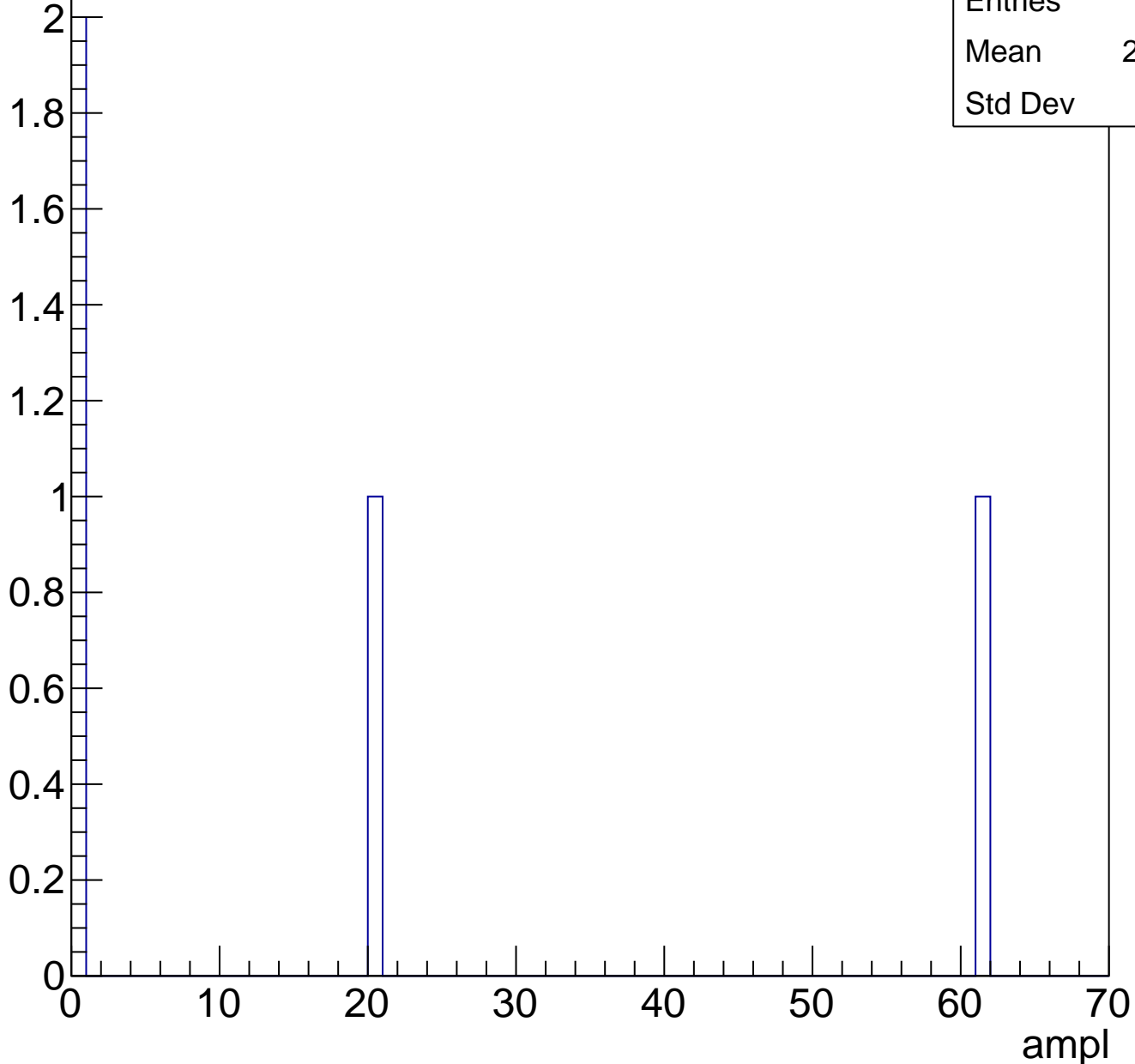
| | |
|---------|-------|
| Entries | 13 |
| Mean | 57.46 |
| Std Dev | 16.61 |



B1L001S, U26-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 20.25 |
| Std Dev | 24.9 |

B1L001S, U26-ch21, adc0

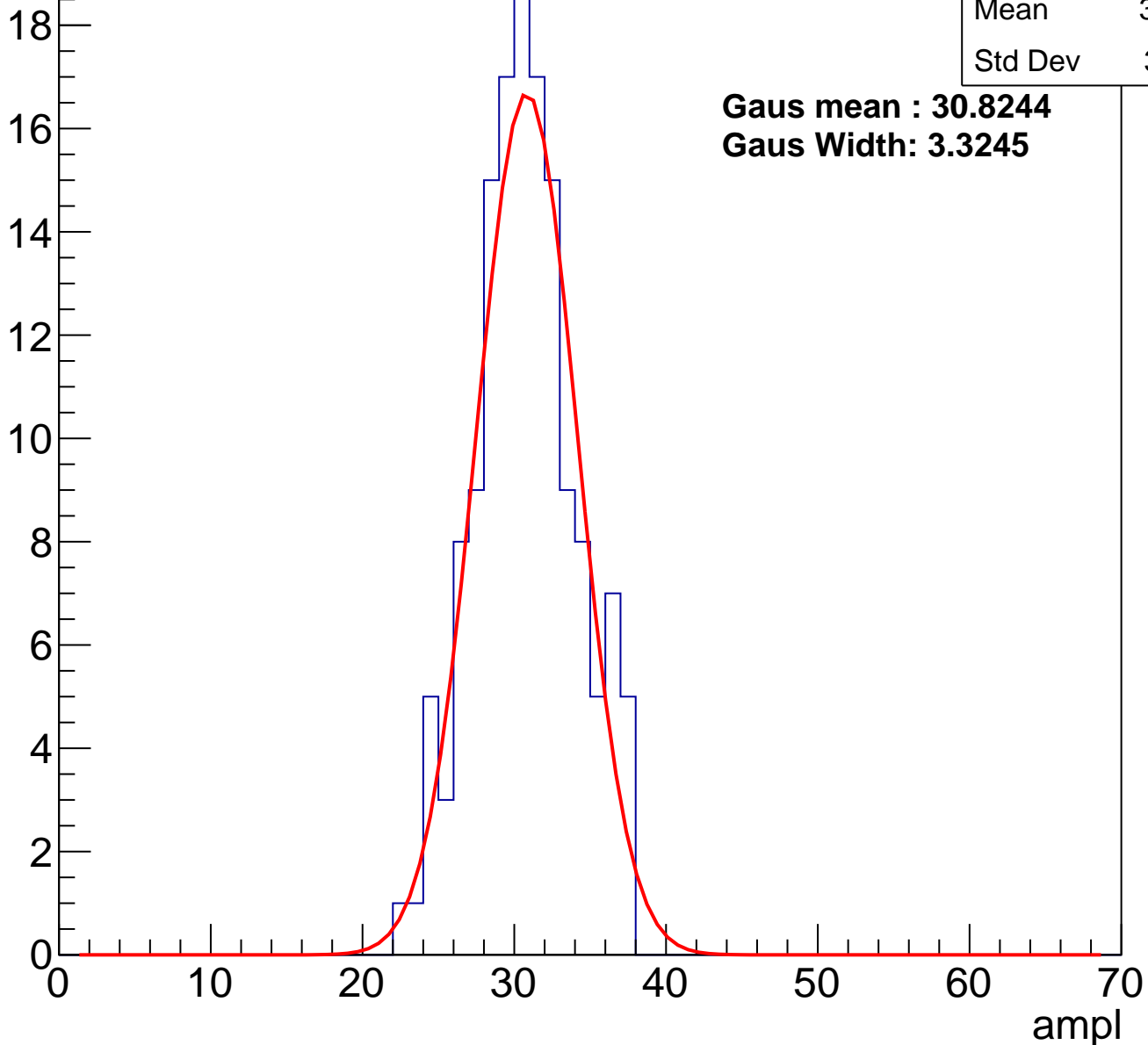
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 144 |
| Mean | 30.29 |
| Std Dev | 3.281 |

Gaus mean : 30.8244

Gaus Width: 3.3245

Entry



B1L001S, U26-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 37.15 |
| Std Dev | 3.341 |

Gaus mean : 37.6459

Gaus Width: 3.3858

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

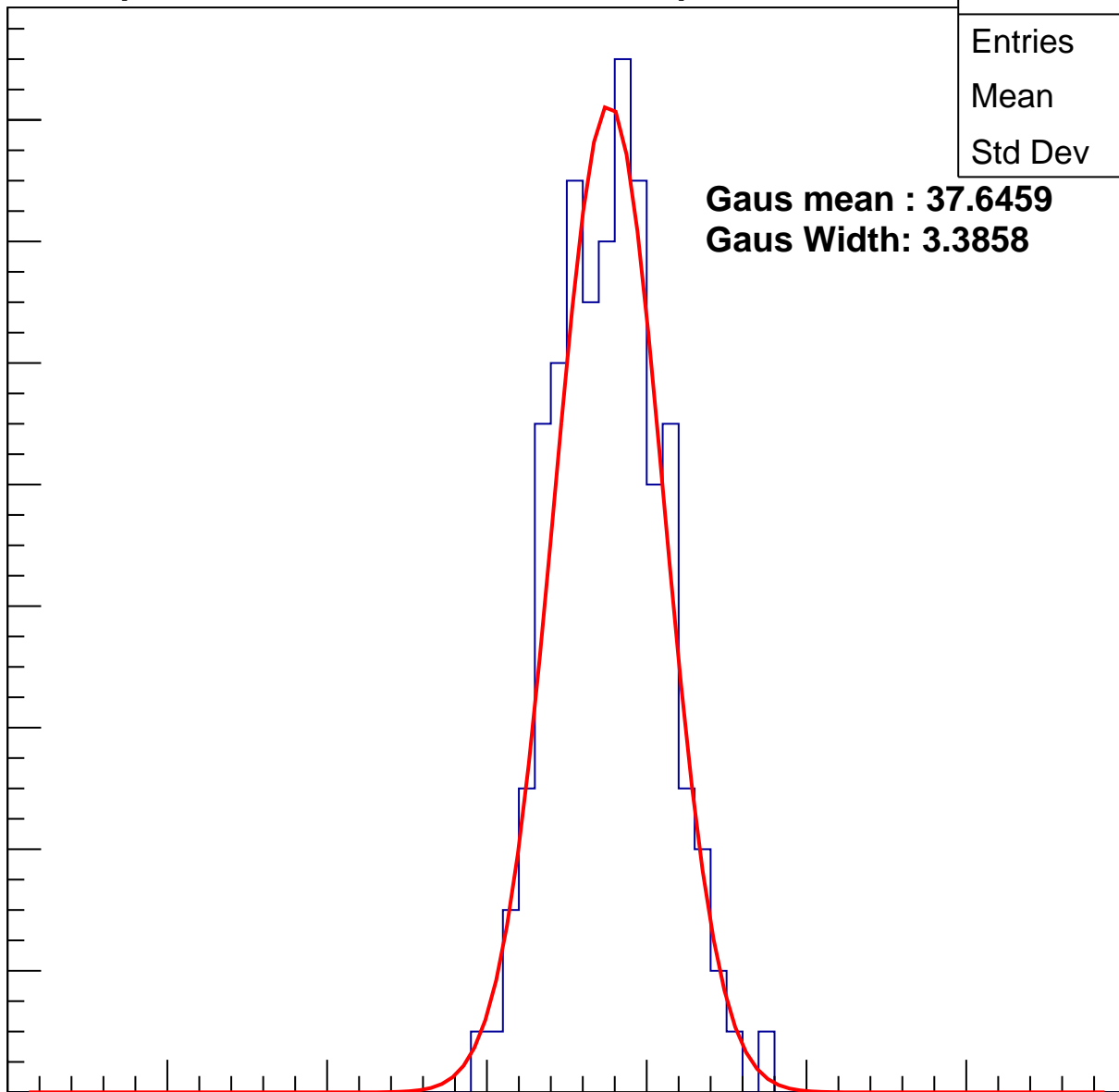
40

50

60

70

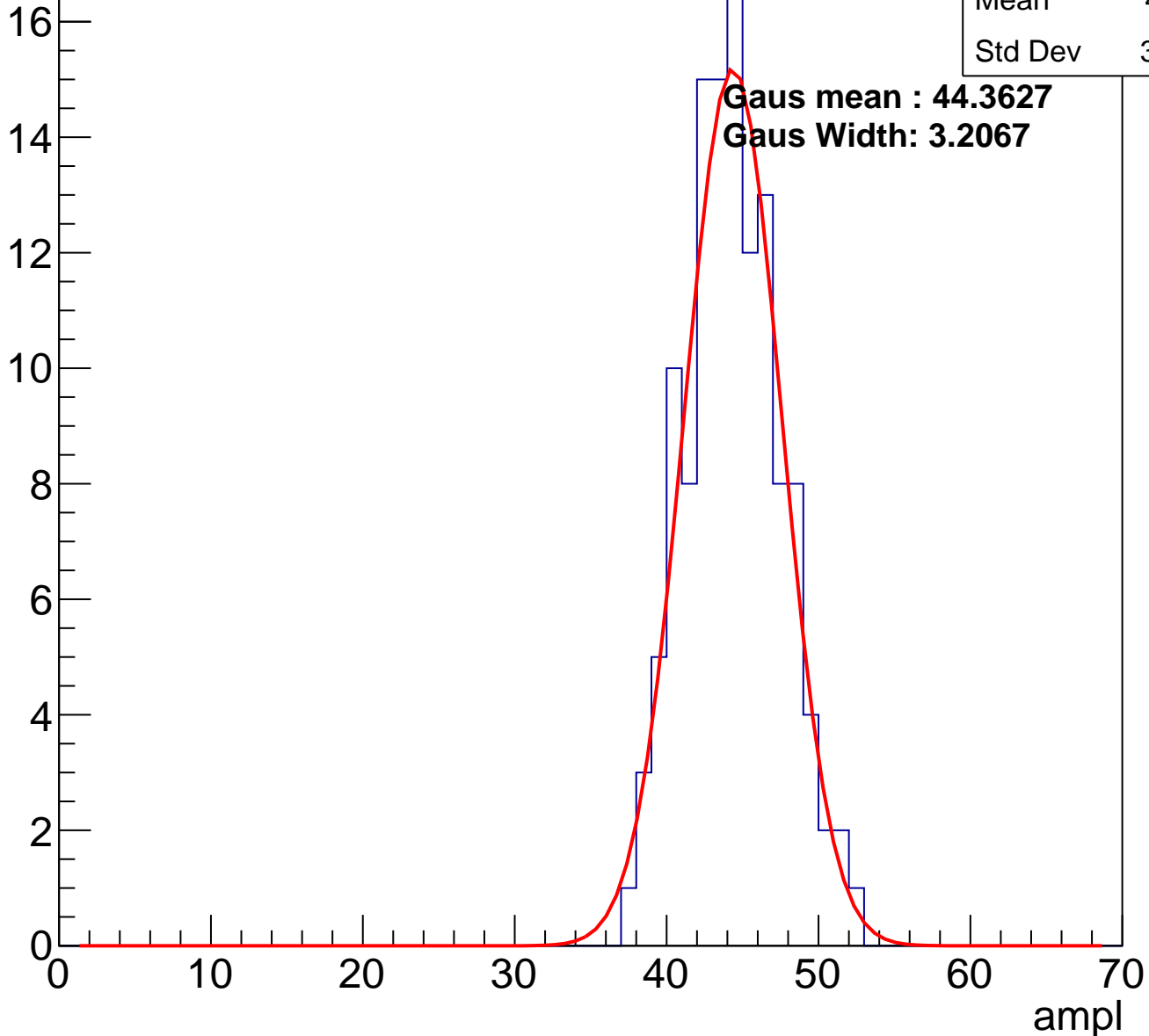
ampl



B1L001S, U26-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

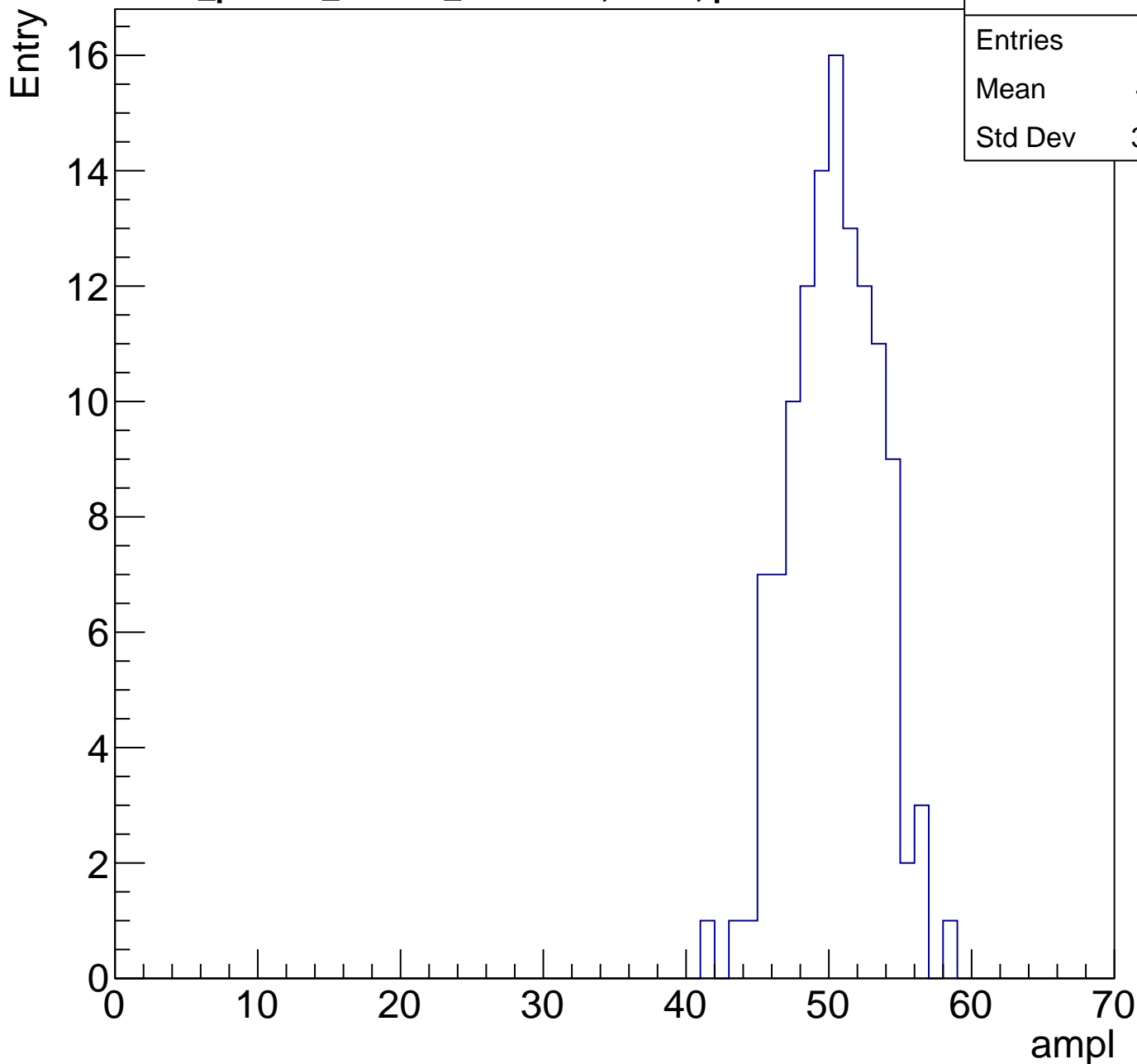
Entry



B1L001S, U26-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 49.91 |
| Std Dev | 3.069 |

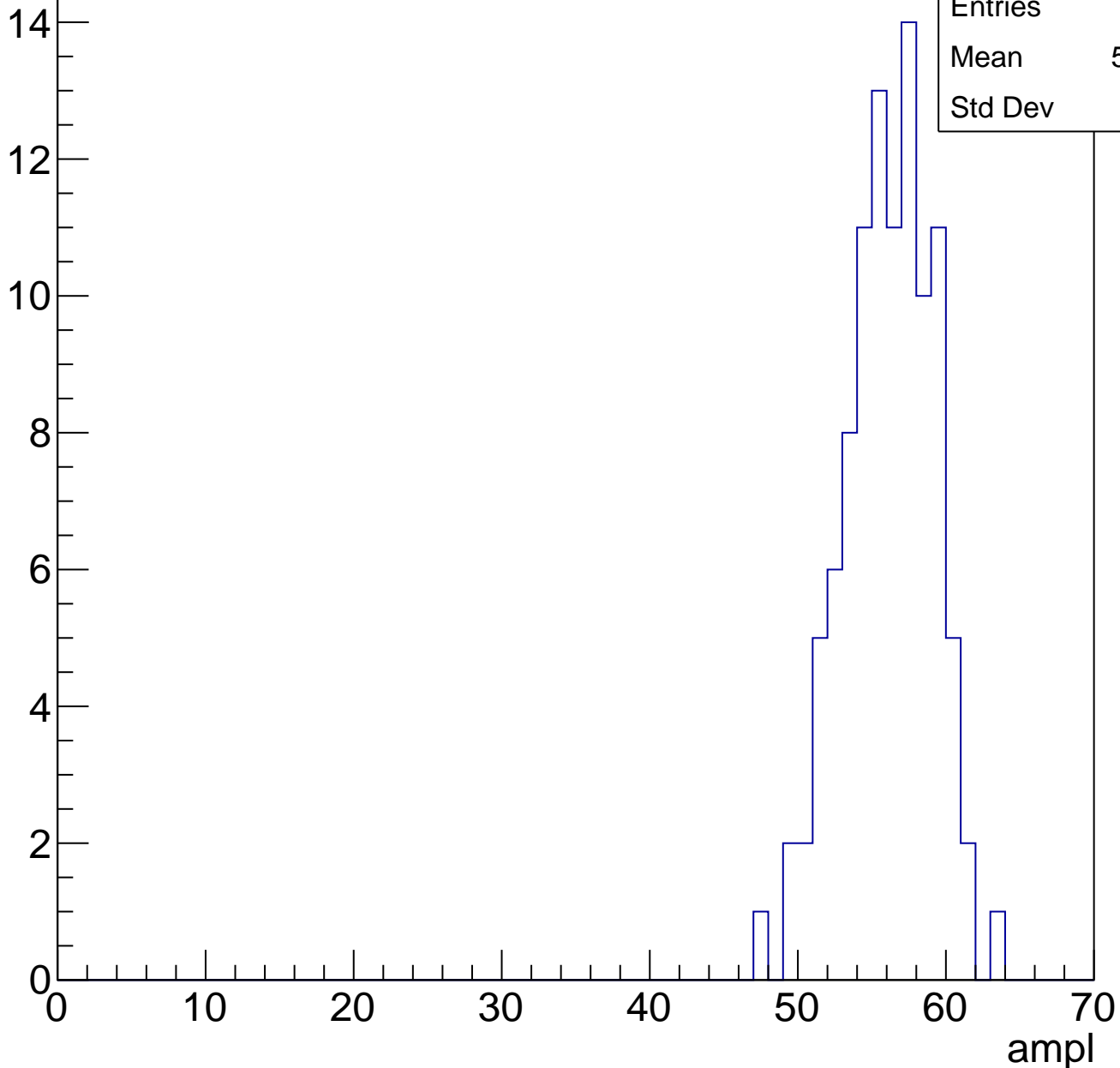


B1L001S, U26-ch21, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 102 |
| Mean | 55.62 |
| Std Dev | 3 |

Entry



B1L001S, U26-ch21, adc5

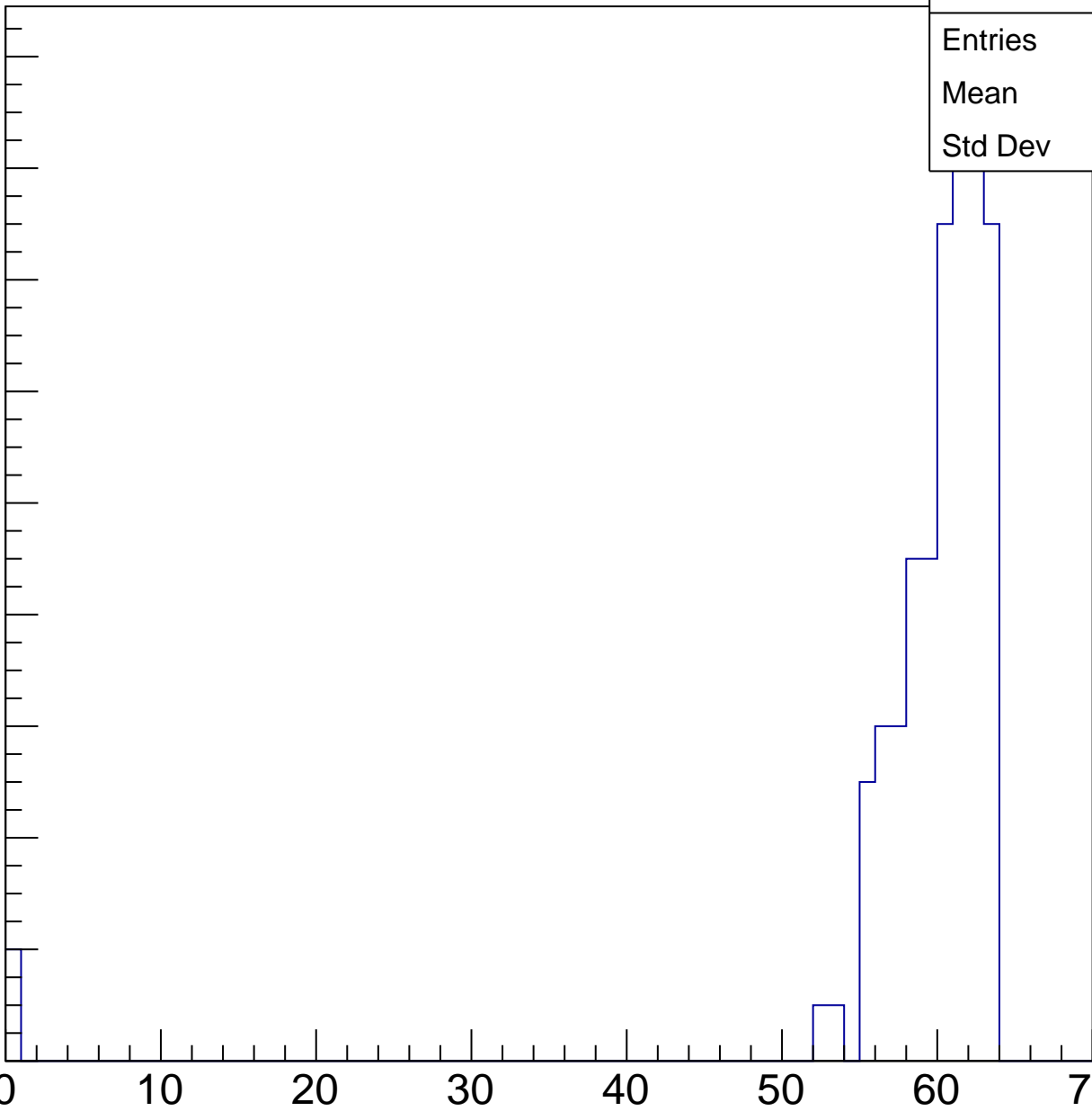
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 104 |
| Mean | 58.74 |
| Std Dev | 8.6 |

ampl



B1L001S, U26-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch21, adc7

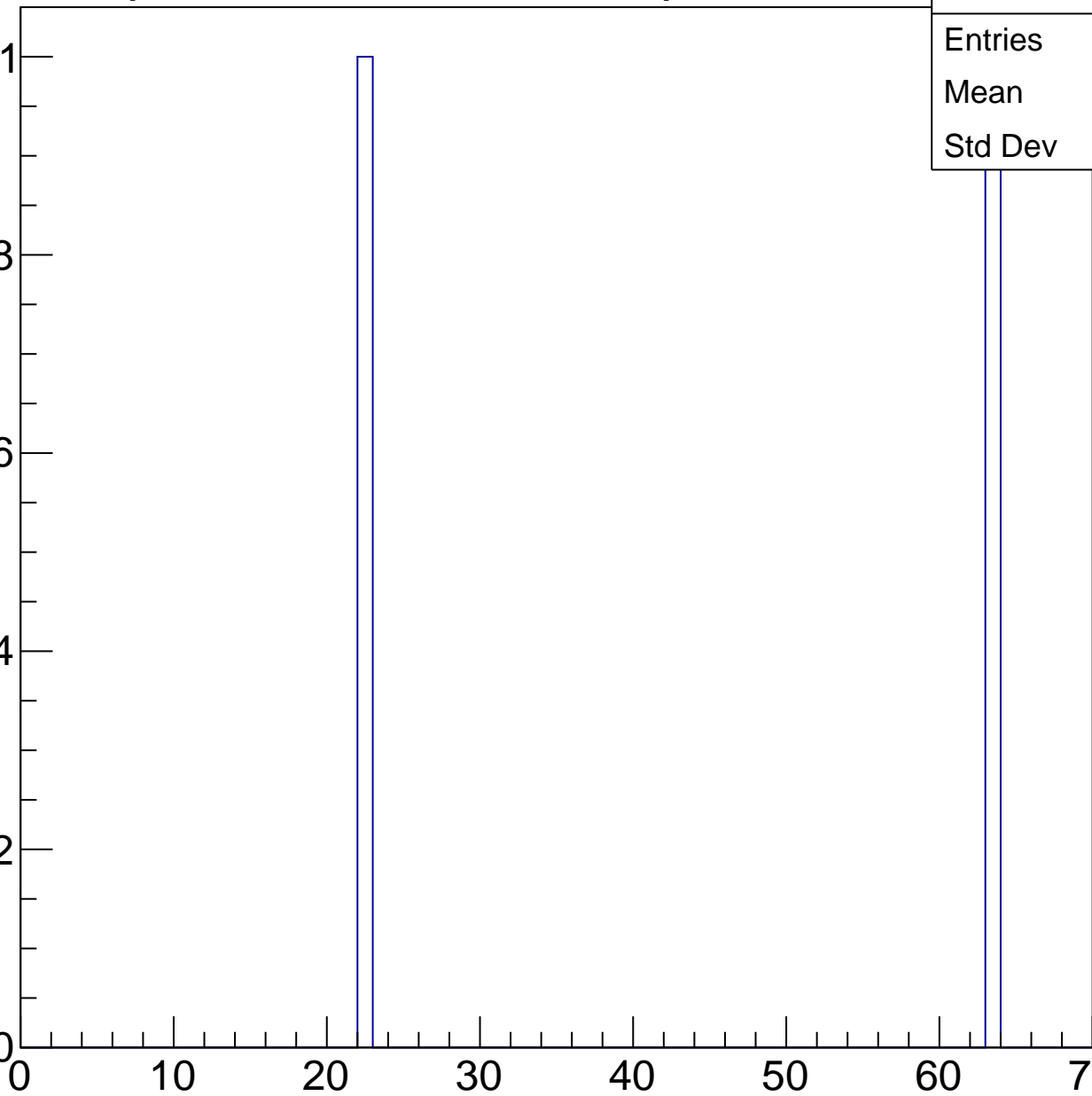
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 2 |
| Mean | 42.5 |
| Std Dev | 20.5 |

ampl



B1L001S, U26-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 30.21 |
| Std Dev | 4.096 |

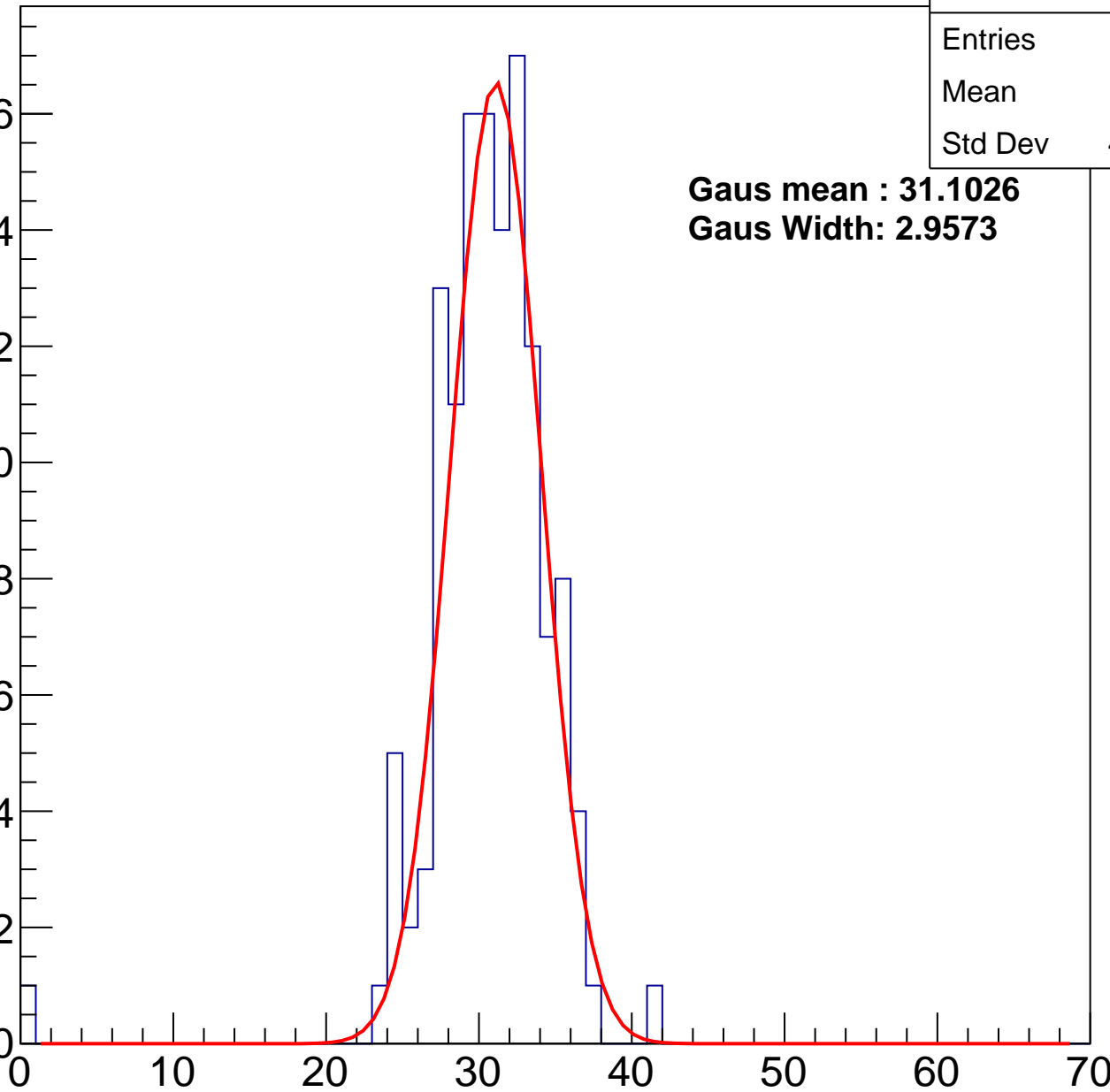
Gaus mean : 31.1026

Gaus Width: 2.9573

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch22, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 37.06 |
| Std Dev | 3.211 |

Gaus mean : 37.2906

Gaus Width: 2.7848

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

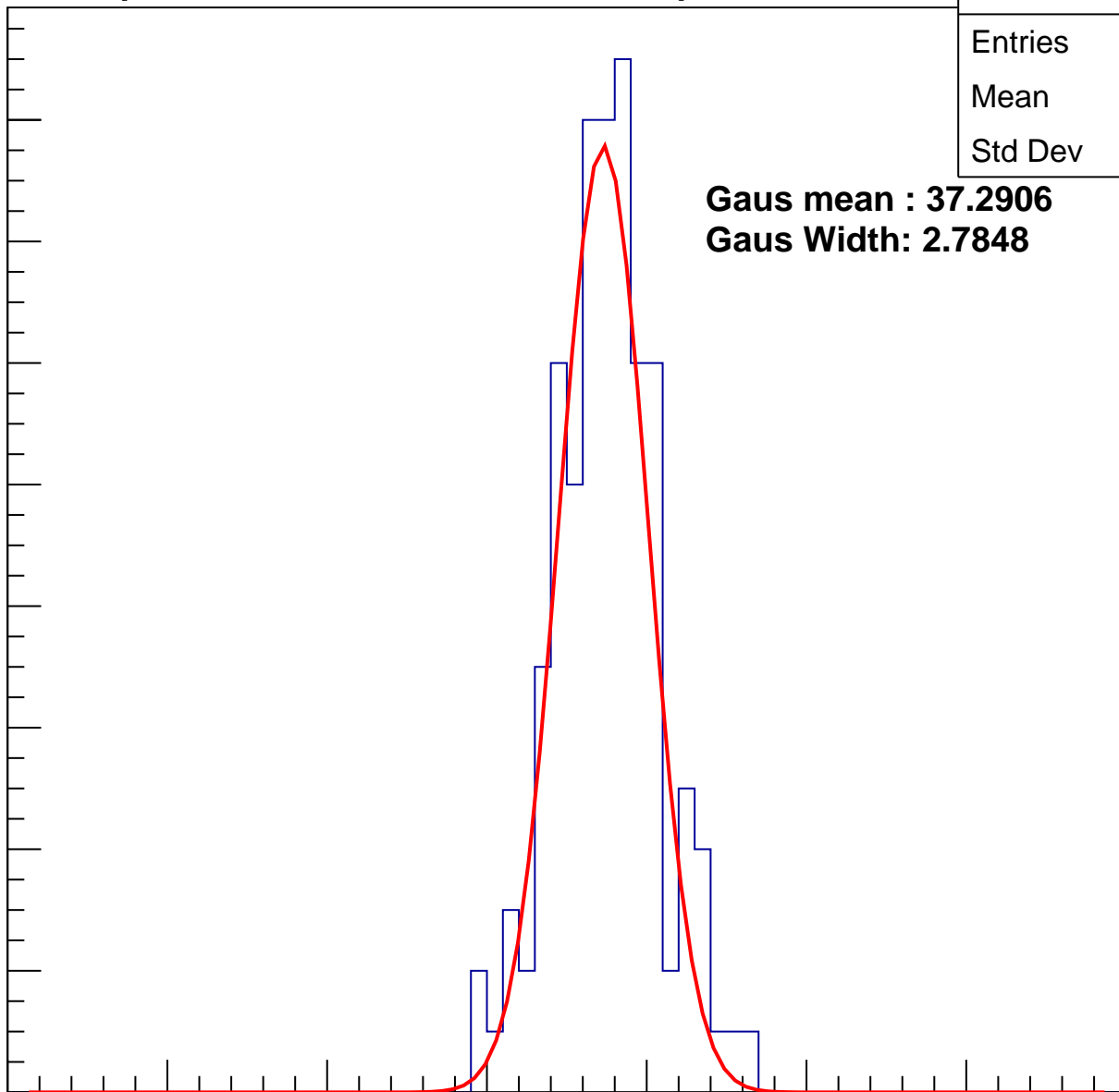
40

50

60

70

ampl



B1L001S, U26-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 43.13 |
| Std Dev | 3.404 |

Gaus mean : 43.6255

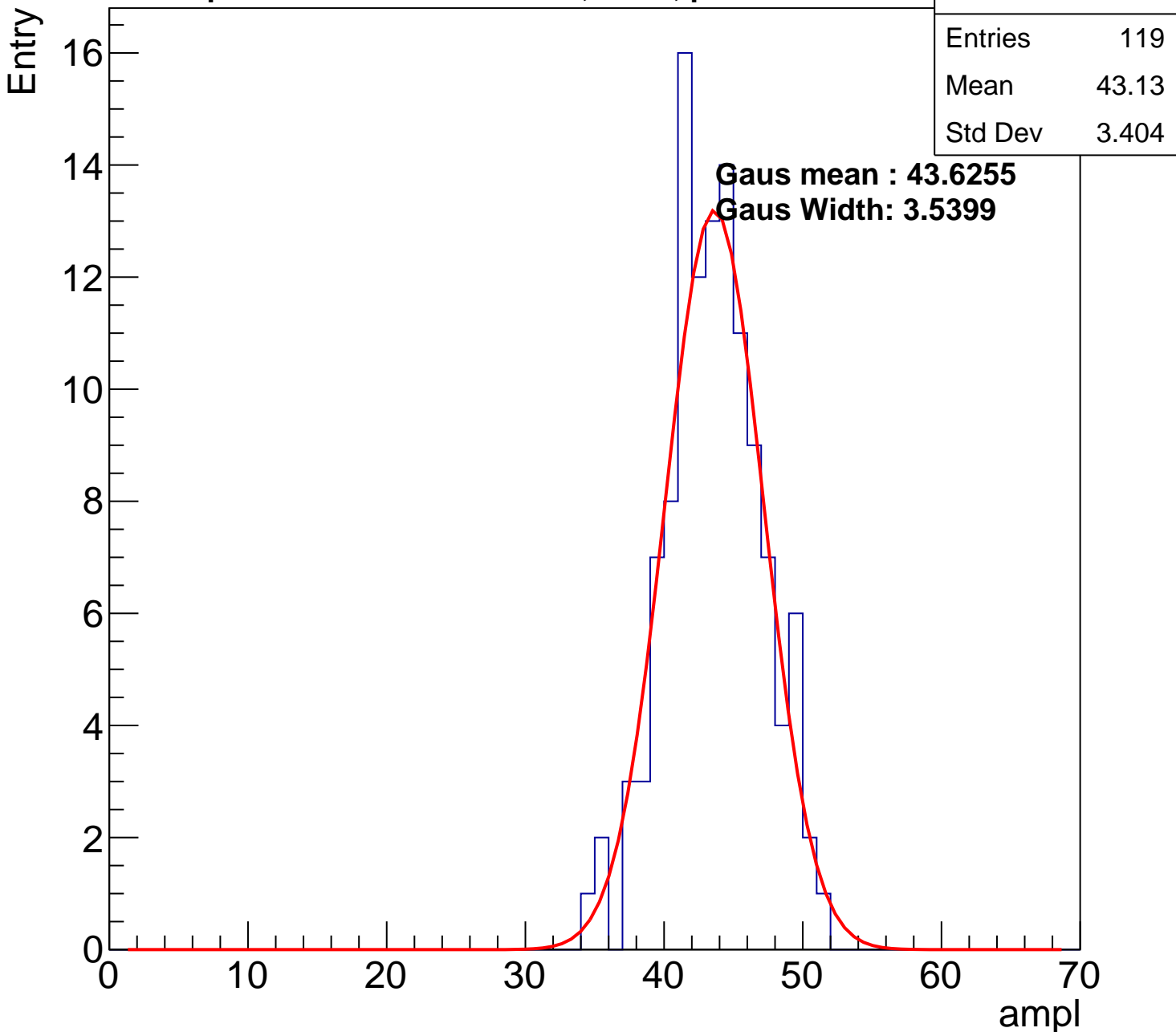
Gaus Width: 3.5399

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

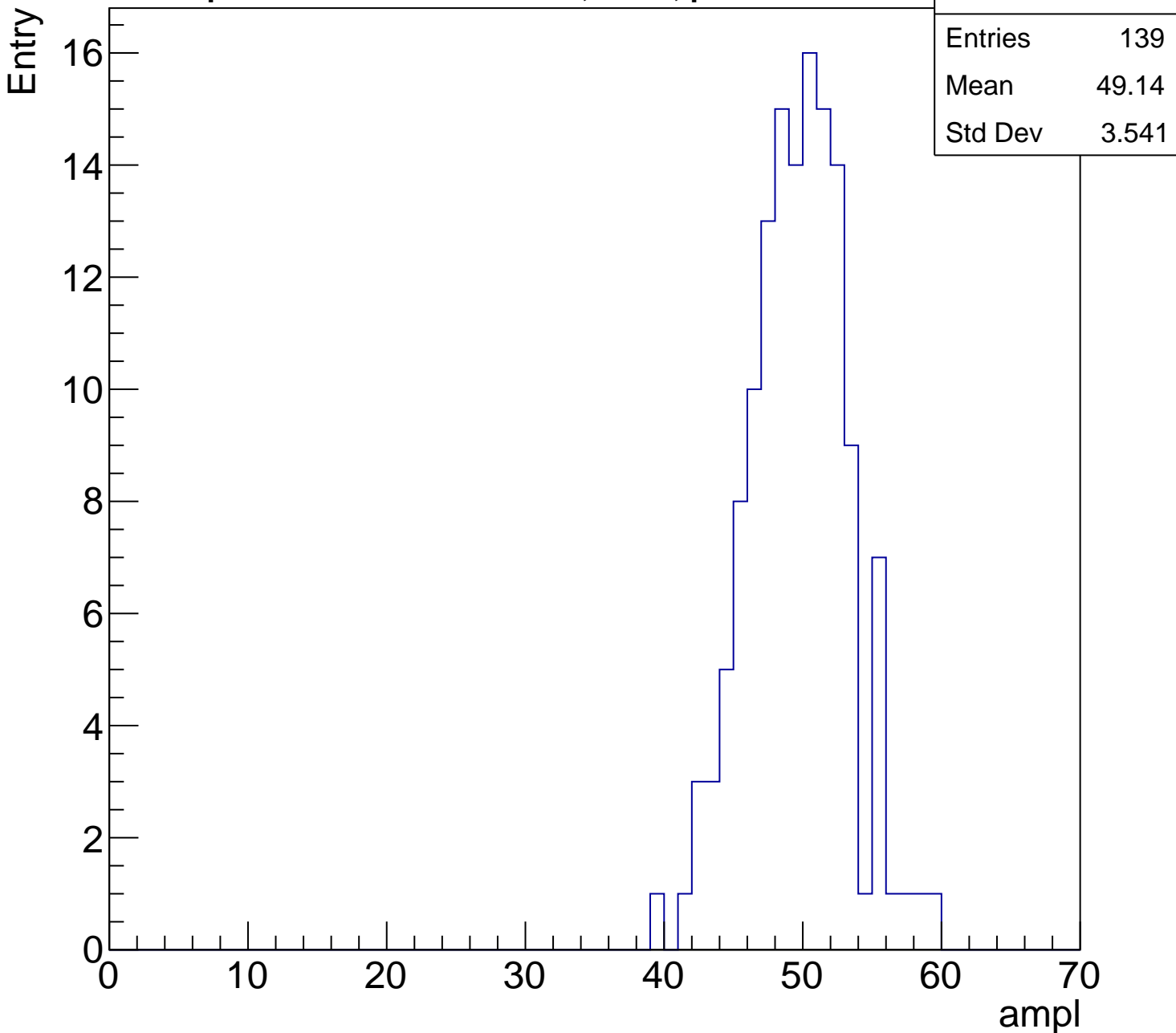
| | |
|---------|-------|
| Entries | 139 |
| Mean | 49.14 |
| Std Dev | 3.541 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

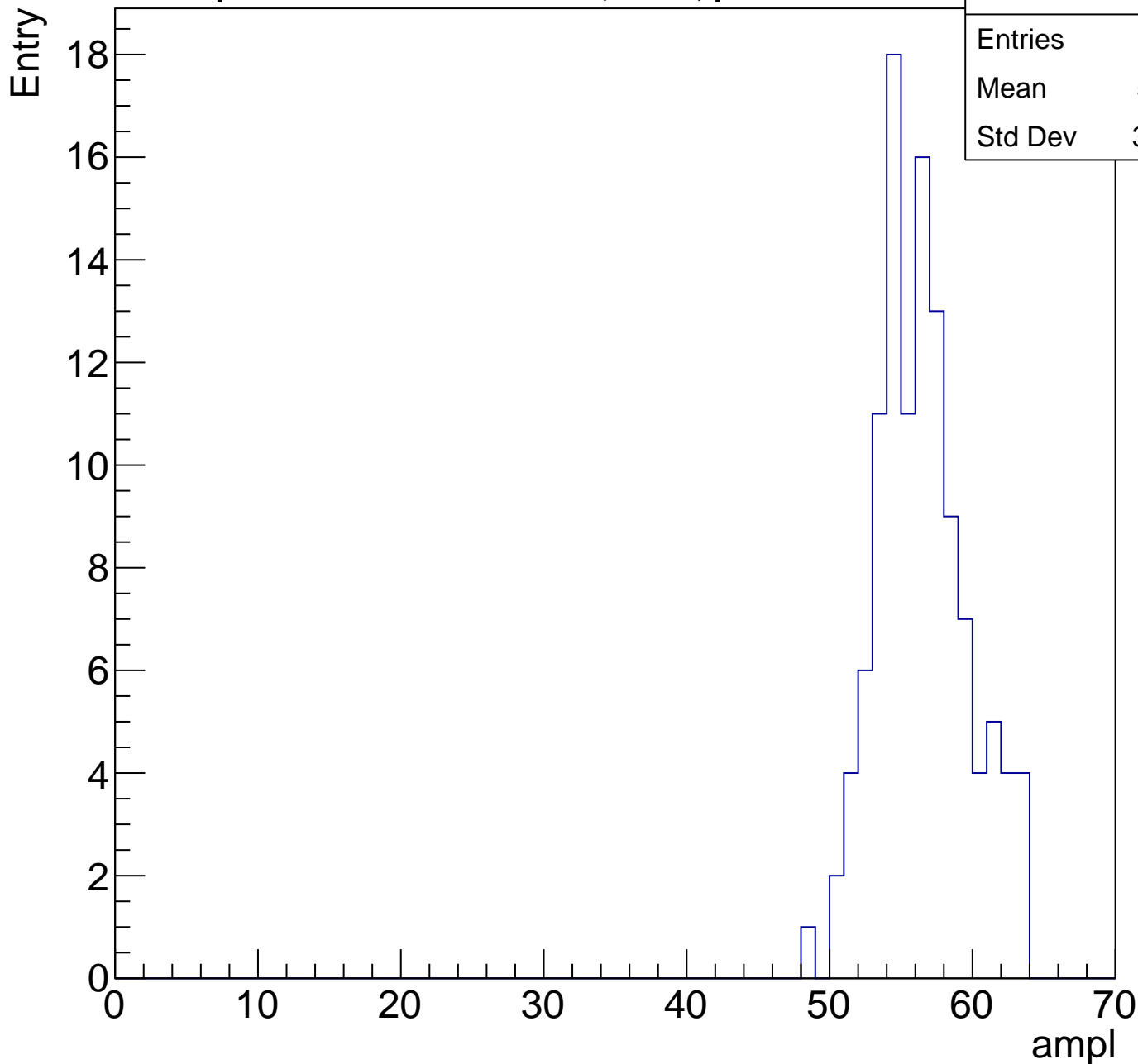
ampl



B1L001S, U26-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 56.01 |
| Std Dev | 3.169 |



B1L001S, U26-ch22, adc5

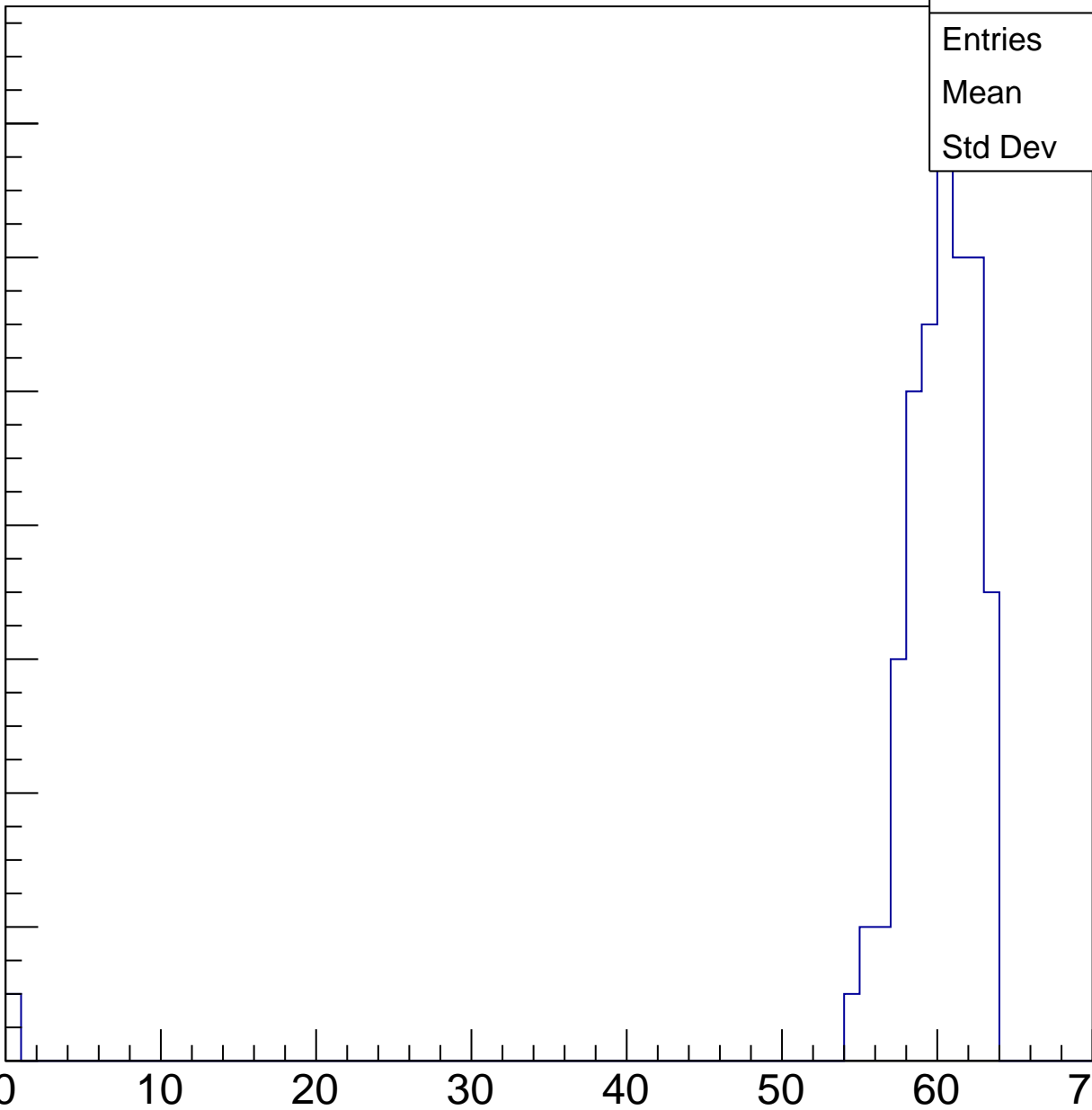
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 59.04 |
| Std Dev | 7.002 |

ampl

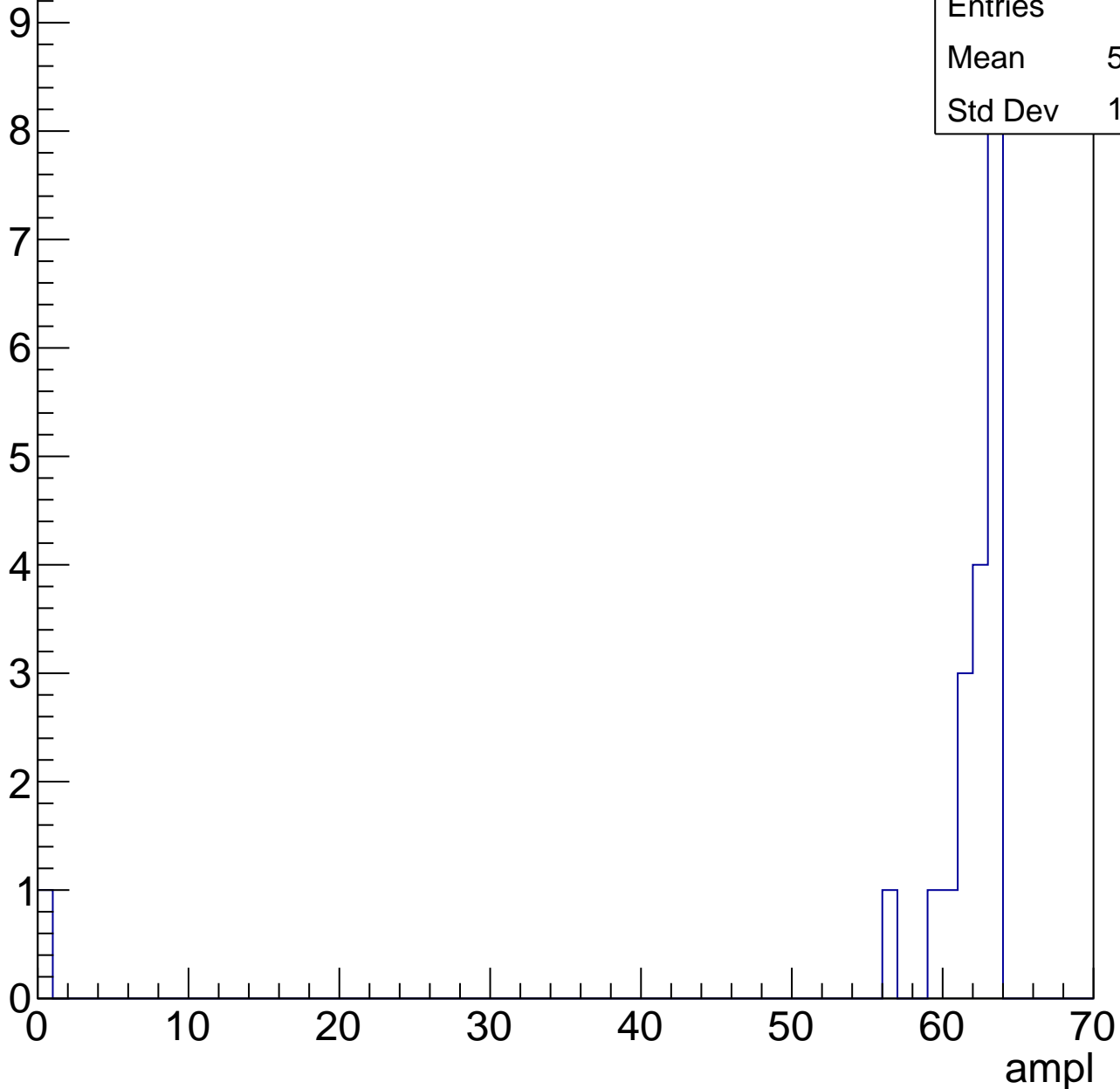


B1L001S, U26-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 58.65 |
| Std Dev | 13.57 |



B1L001S, U26-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

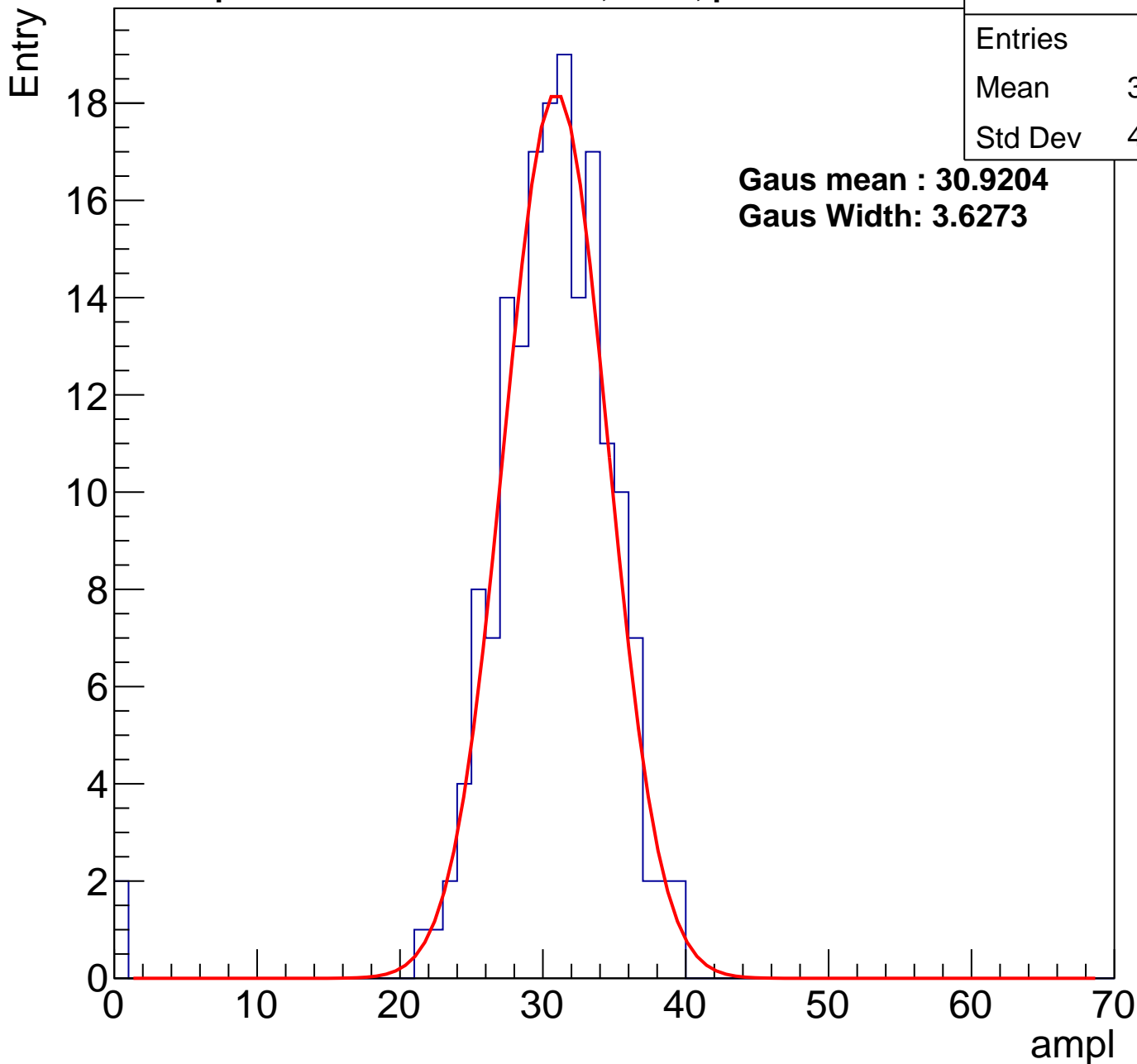
B1L001S, U26-ch23, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 171 |
| Mean | 30.08 |
| Std Dev | 4.829 |

Gaus mean : 30.9204

Gaus Width: 3.6273



B1L001S, U26-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

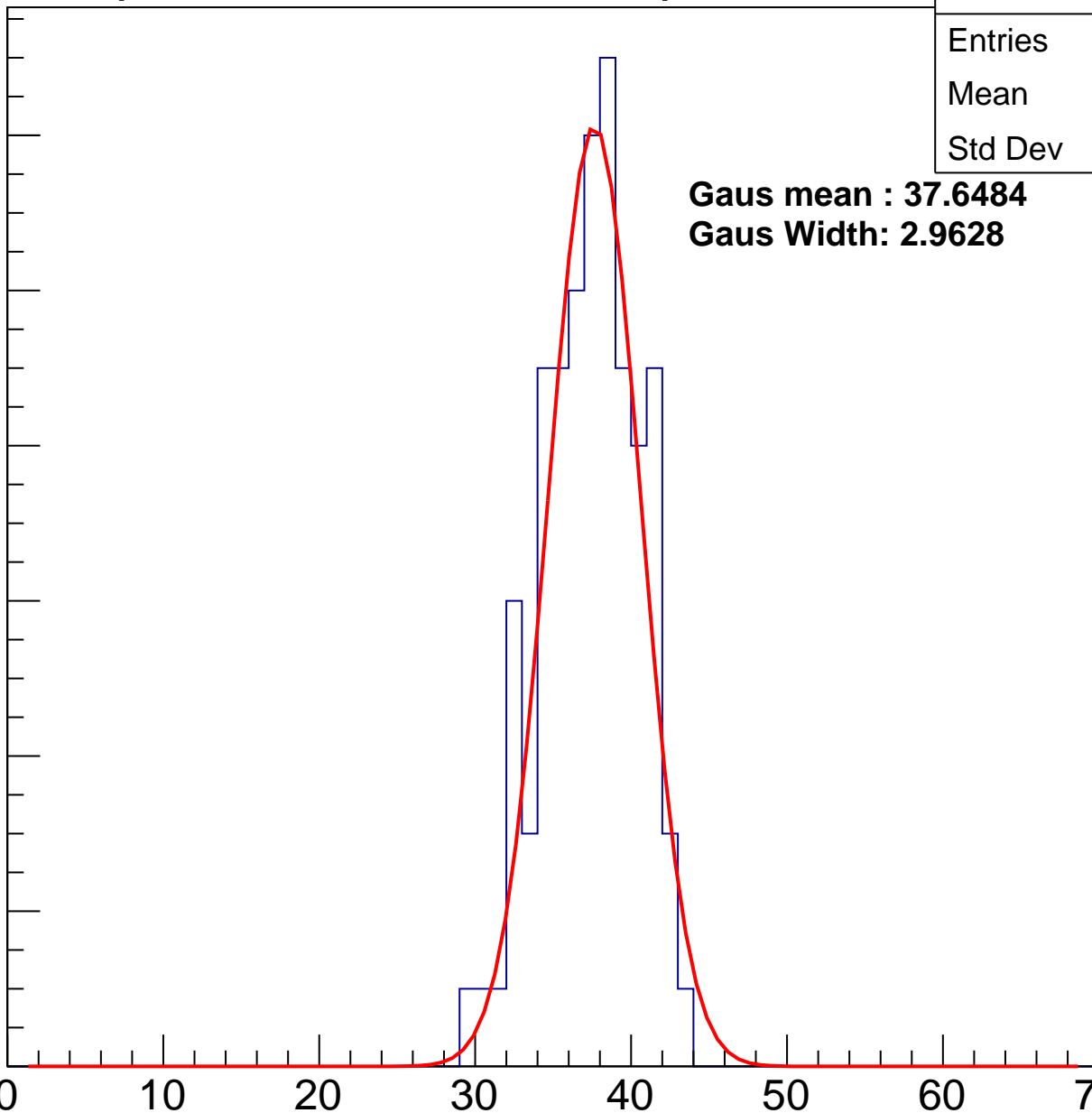
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 36.94 |
| Std Dev | 2.991 |

Gaus mean : 37.6484

Gaus Width: 2.9628

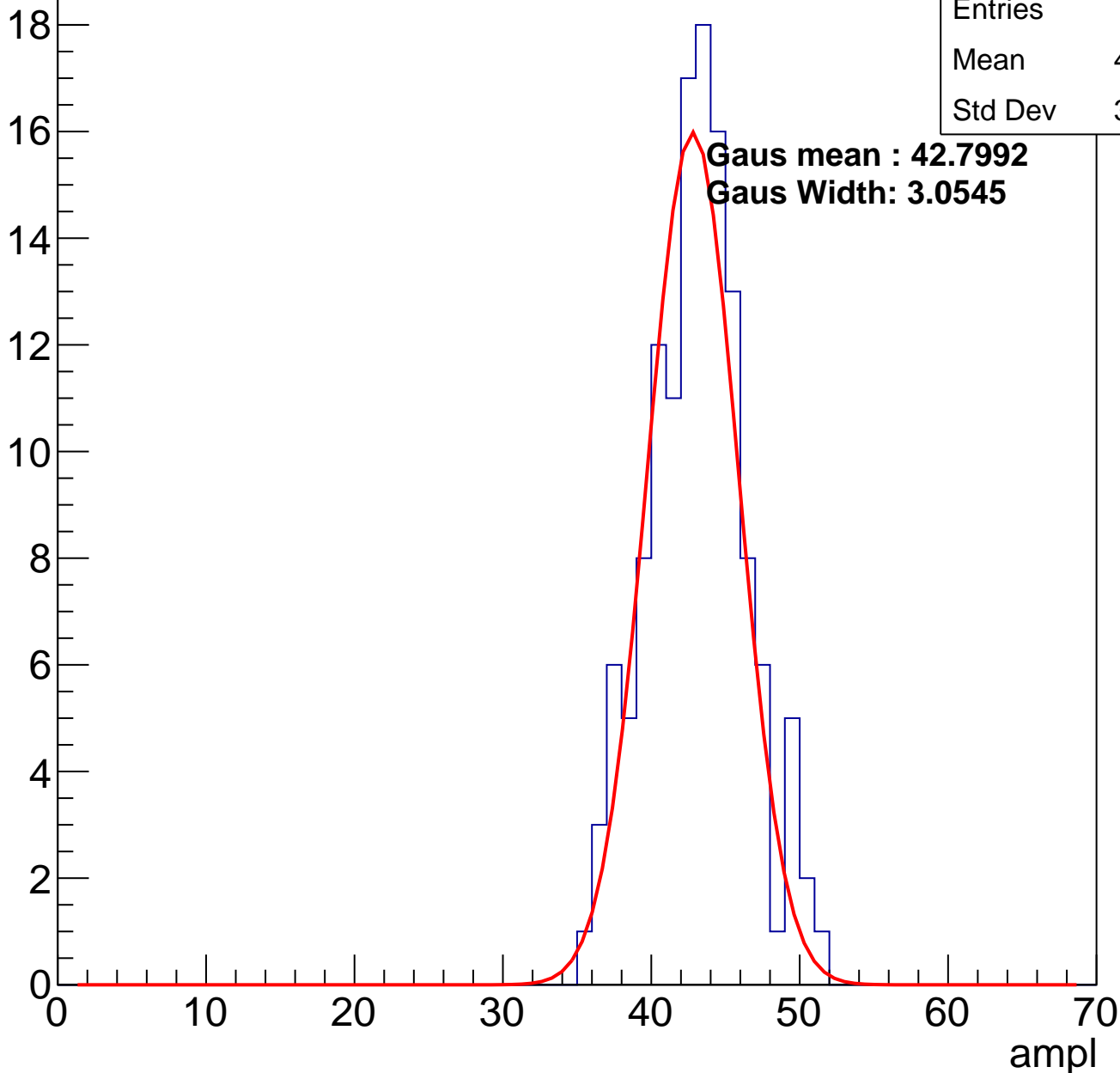
ampl



B1L001S, U26-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

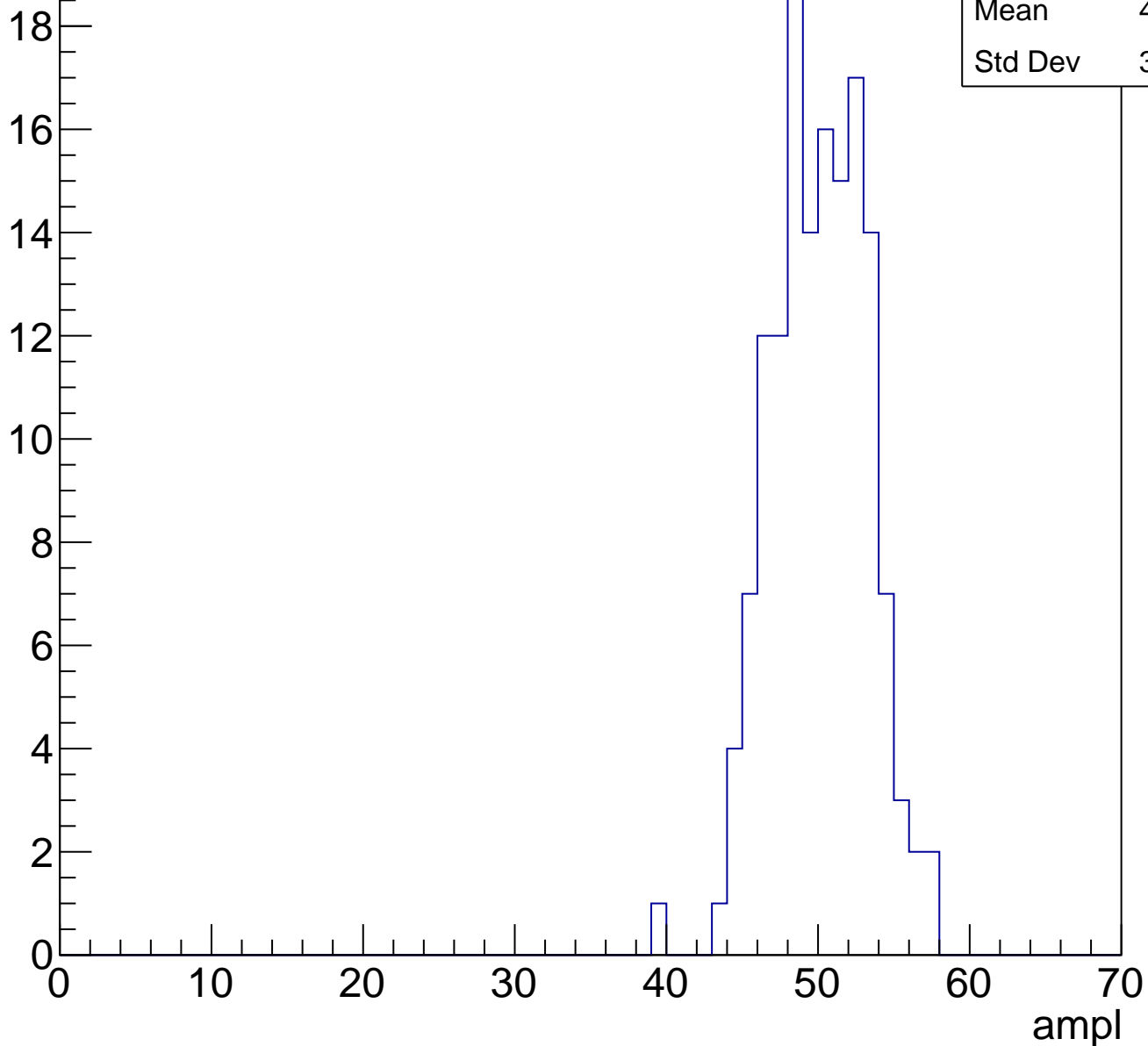


B1L001S, U26-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 49.64 |
| Std Dev | 3.125 |

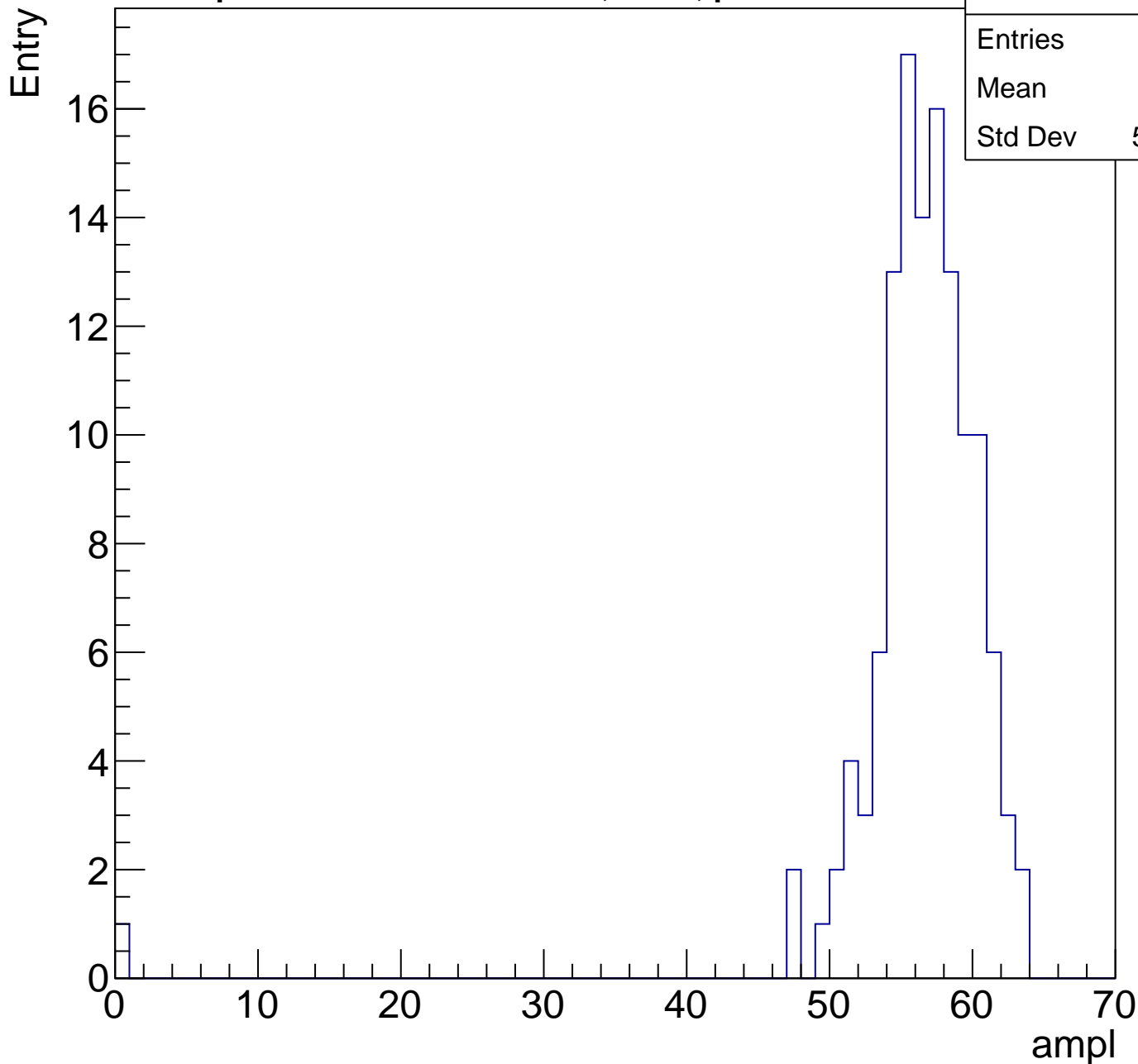
Entry



B1L001S, U26-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 55.9 |
| Std Dev | 5.956 |



B1L001S, U26-ch23, adc5

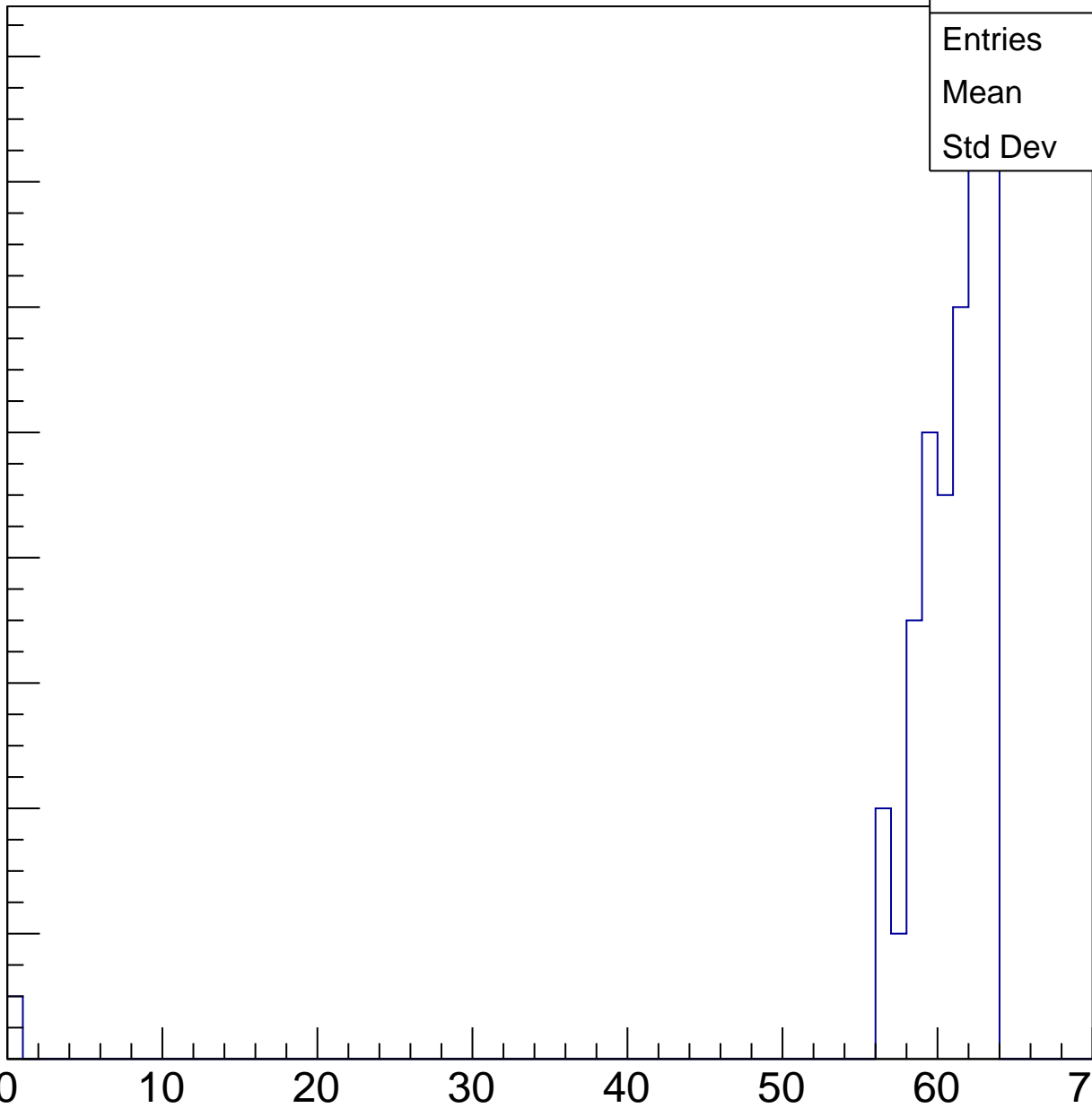
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 76 |
| Mean | 59.79 |
| Std Dev | 7.193 |

ampl



B1L001S, U26-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

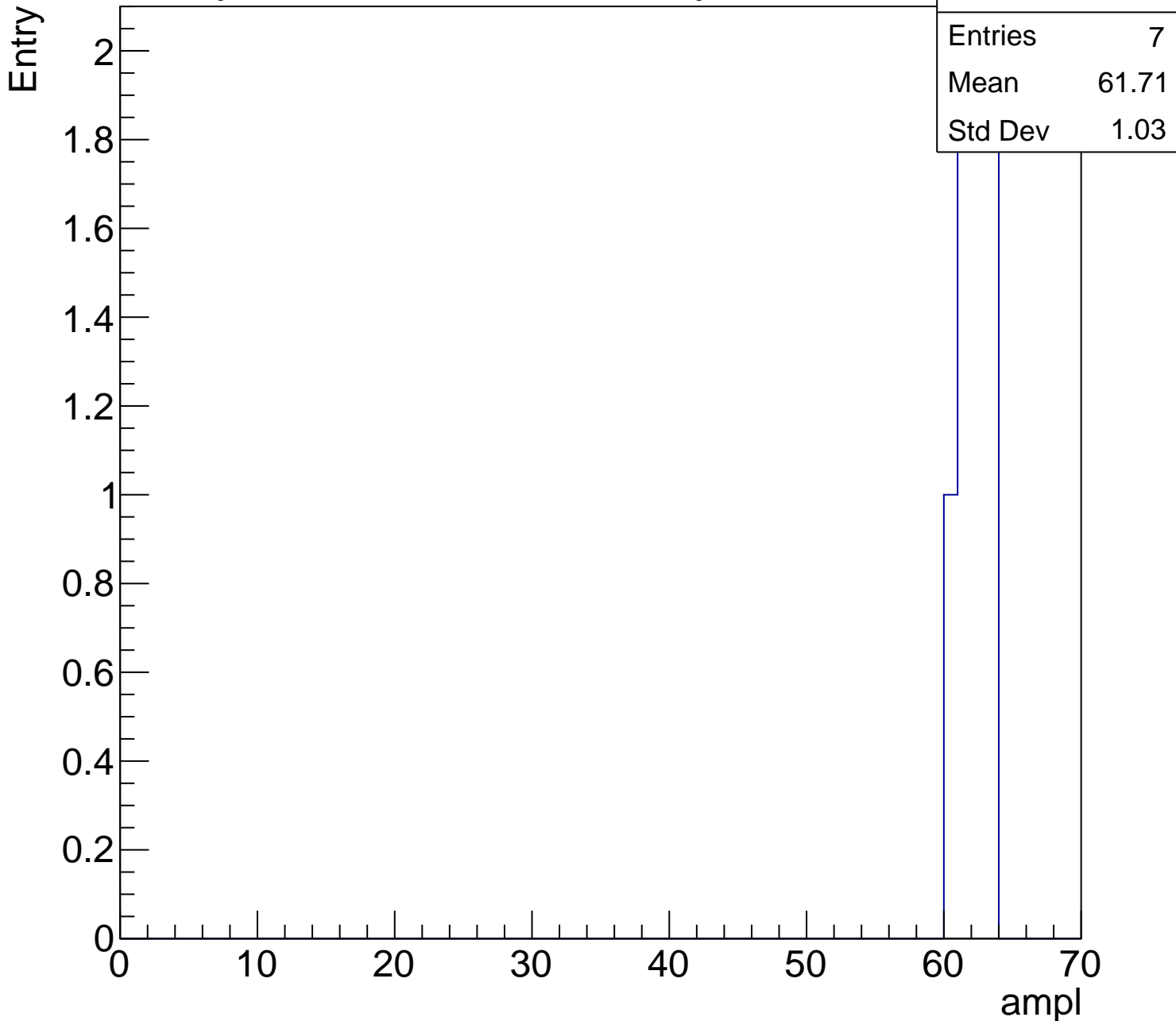
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 7 |
| Mean | 61.71 |
| Std Dev | 1.03 |

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



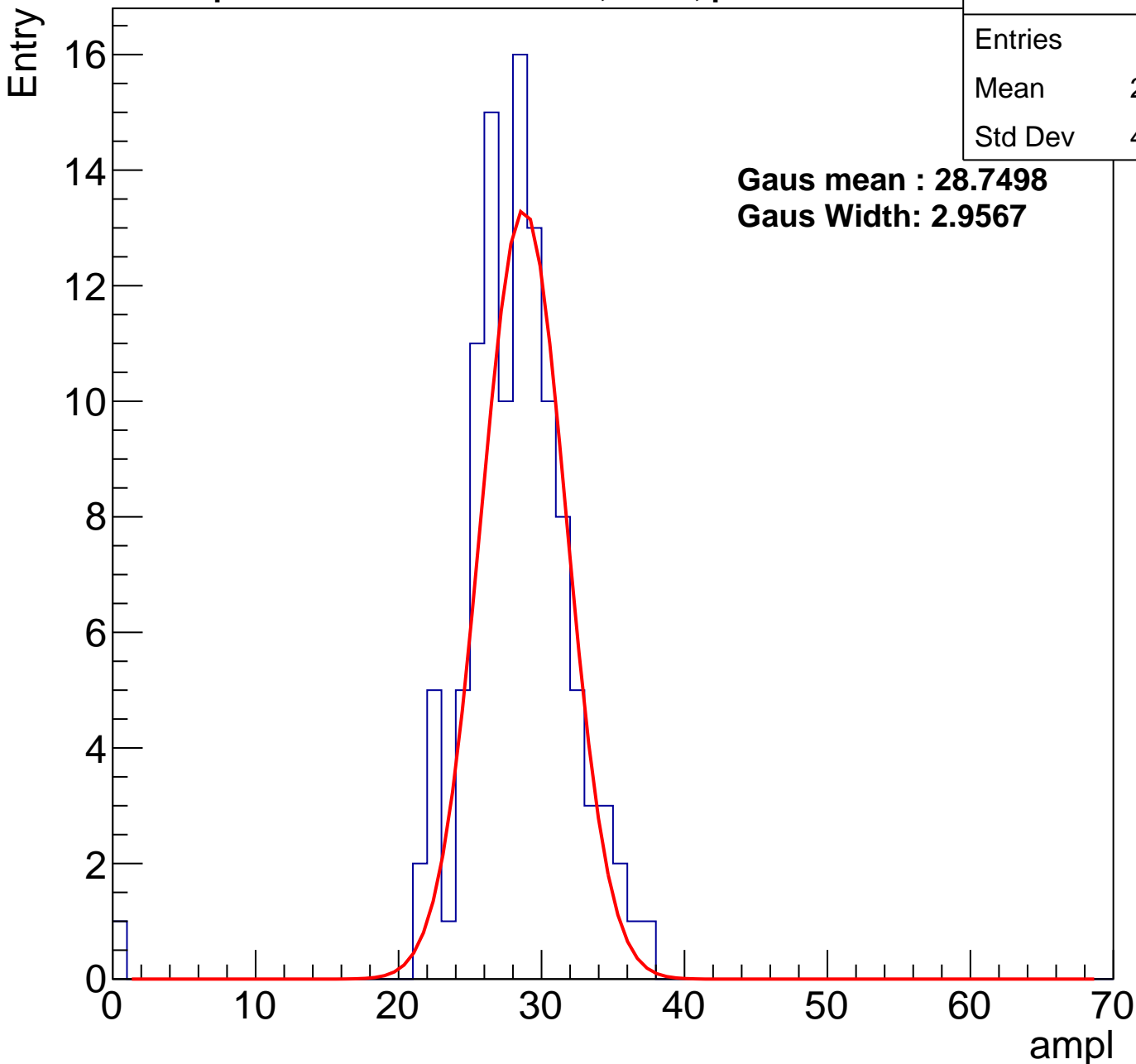
B1L001S, U26-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 27.74 |
| Std Dev | 4.183 |

Gaus mean : 28.7498

Gaus Width: 2.9567



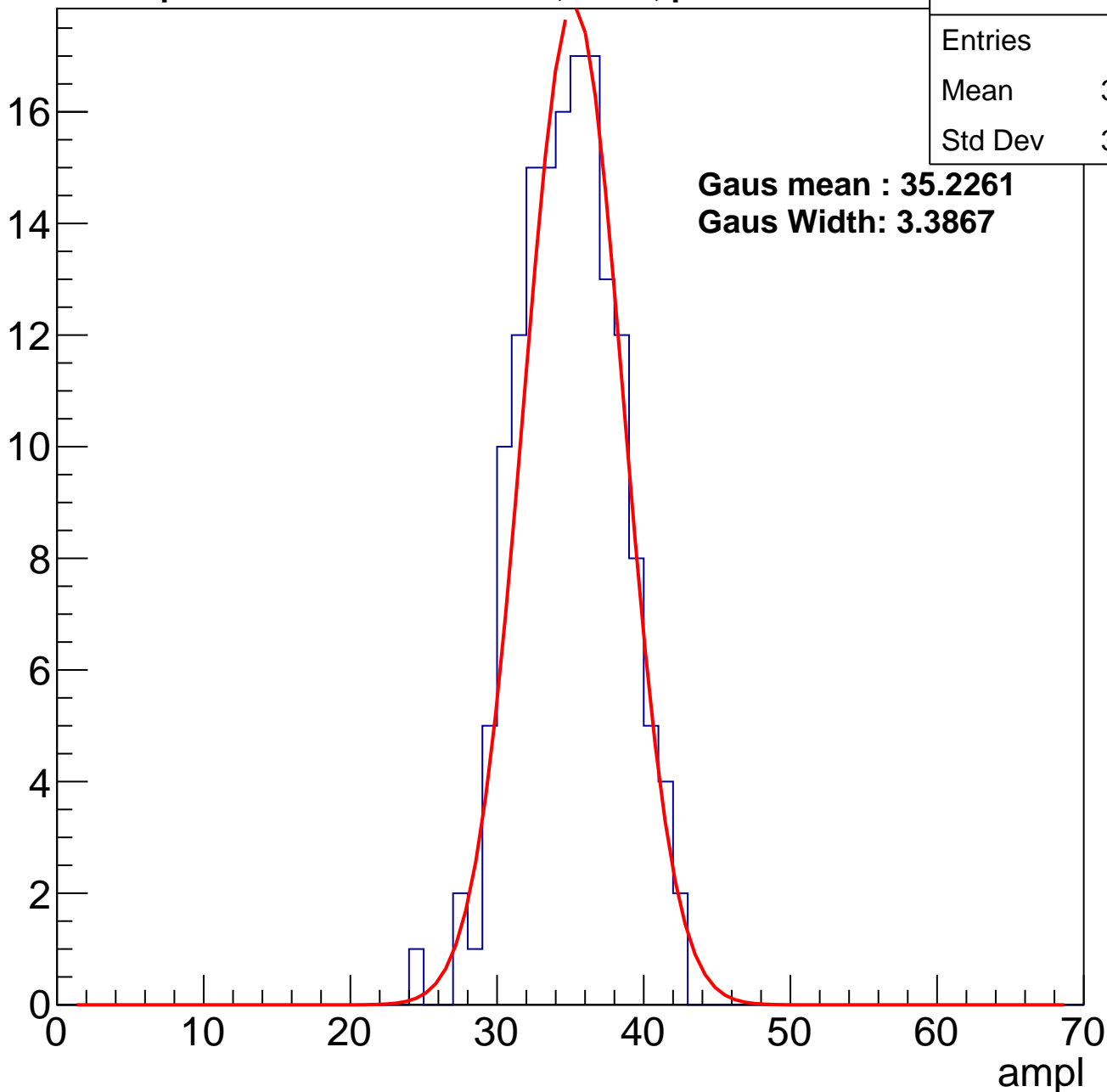
B1L001S, U26-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 155 |
| Mean | 34.49 |
| Std Dev | 3.372 |

Gaus mean : 35.2261
Gaus Width: 3.3867

Entry



B1L001S, U26-ch24, adc2

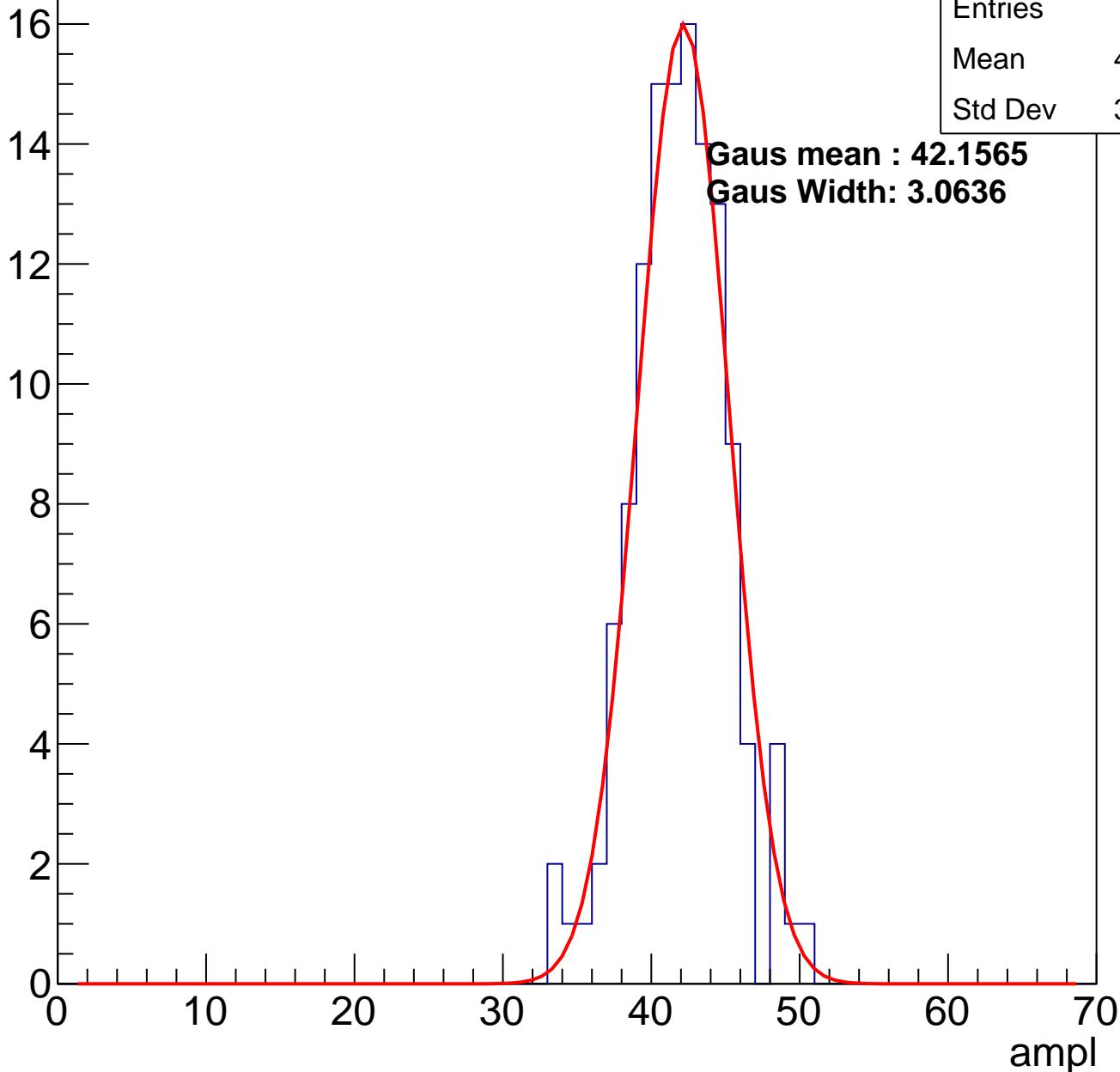
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 41.47 |
| Std Dev | 3.163 |

Gaus mean : 42.1565

Gaus Width: 3.0636

Entry

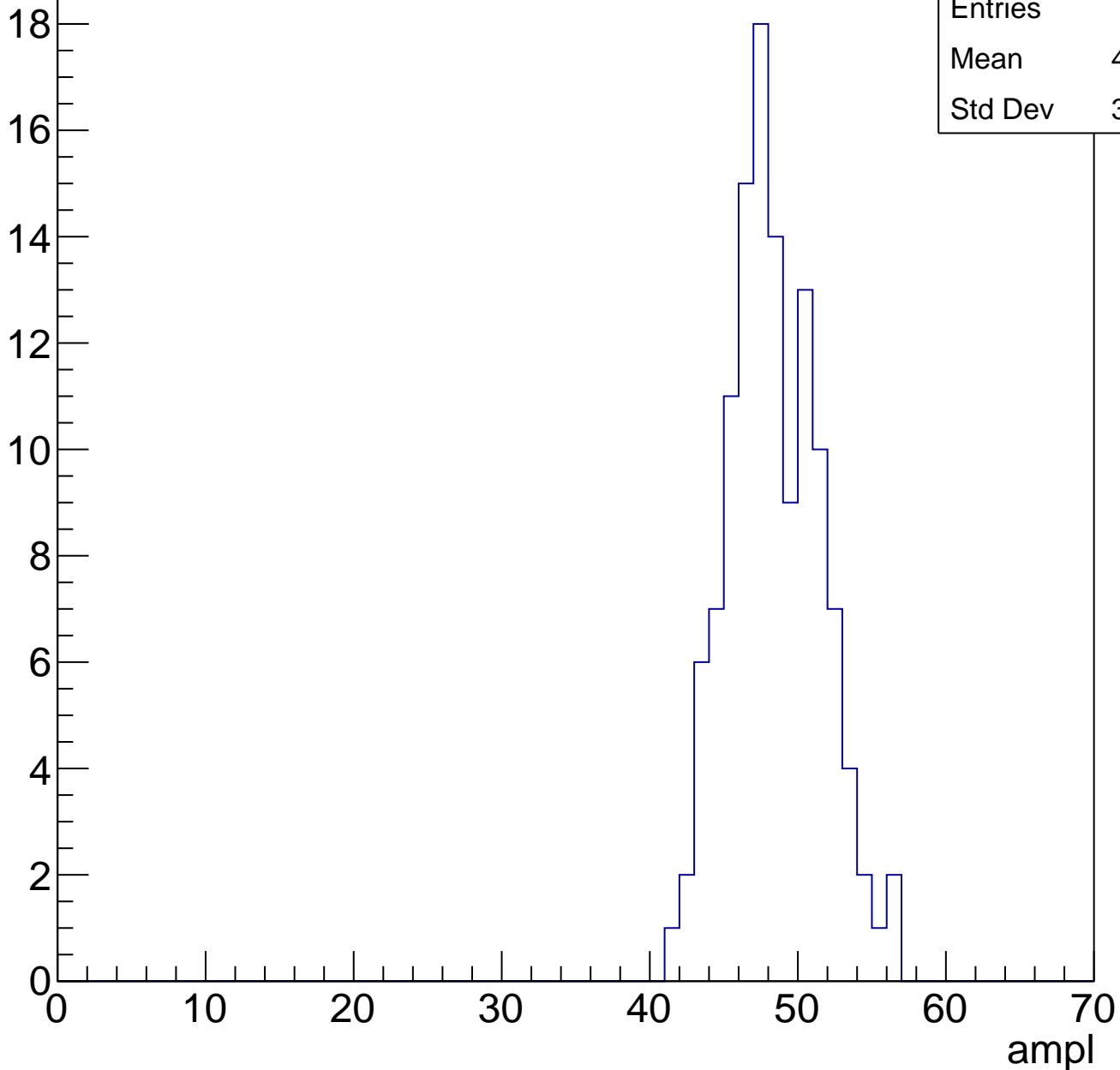


B1L001S, U26-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 47.92 |
| Std Dev | 3.098 |

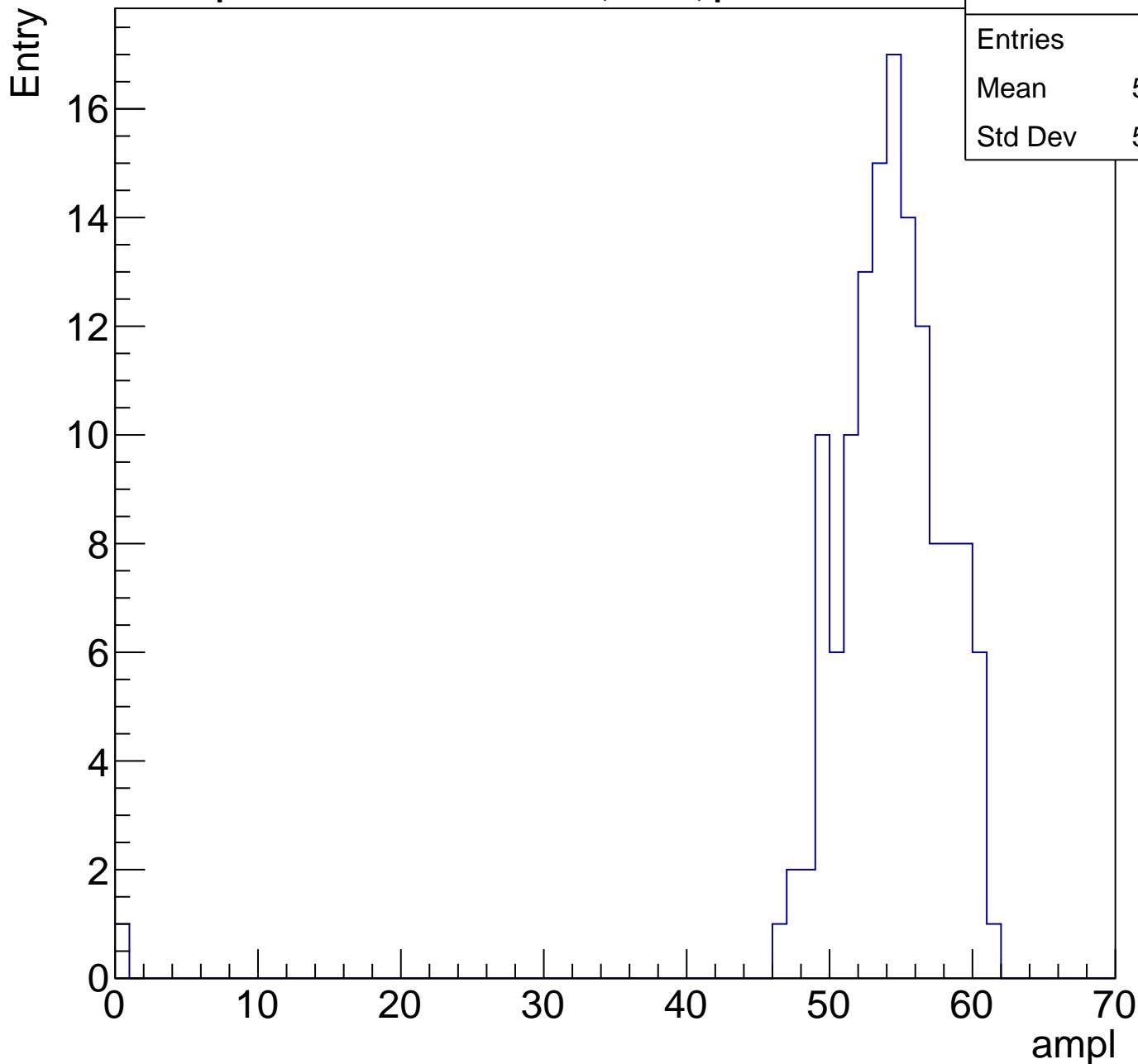
Entry



B1L001S, U26-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 53.58 |
| Std Dev | 5.702 |



B1L001S, U26-ch24, adc5

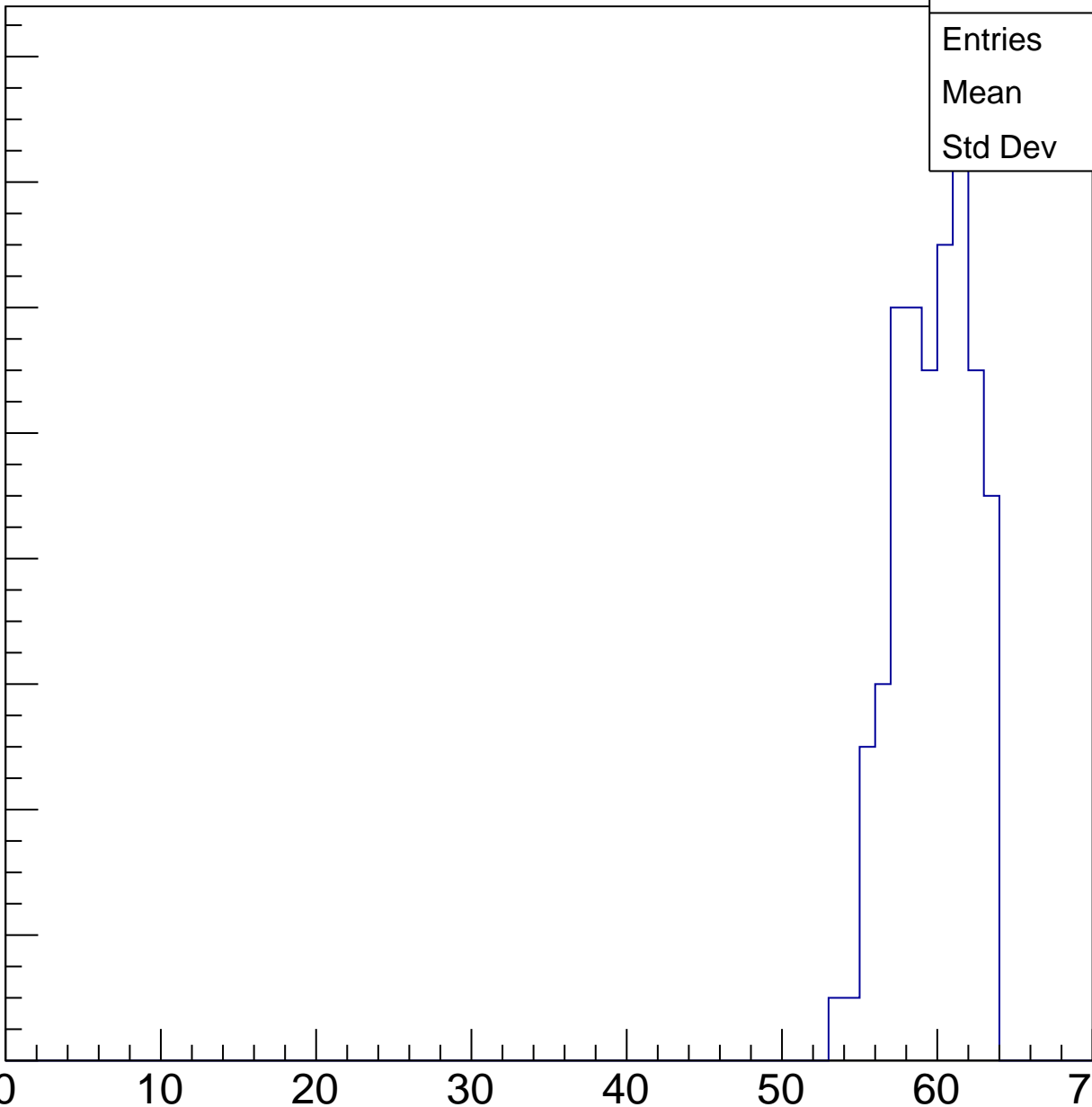
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 97 |
| Mean | 59.3 |
| Std Dev | 2.416 |

ampl

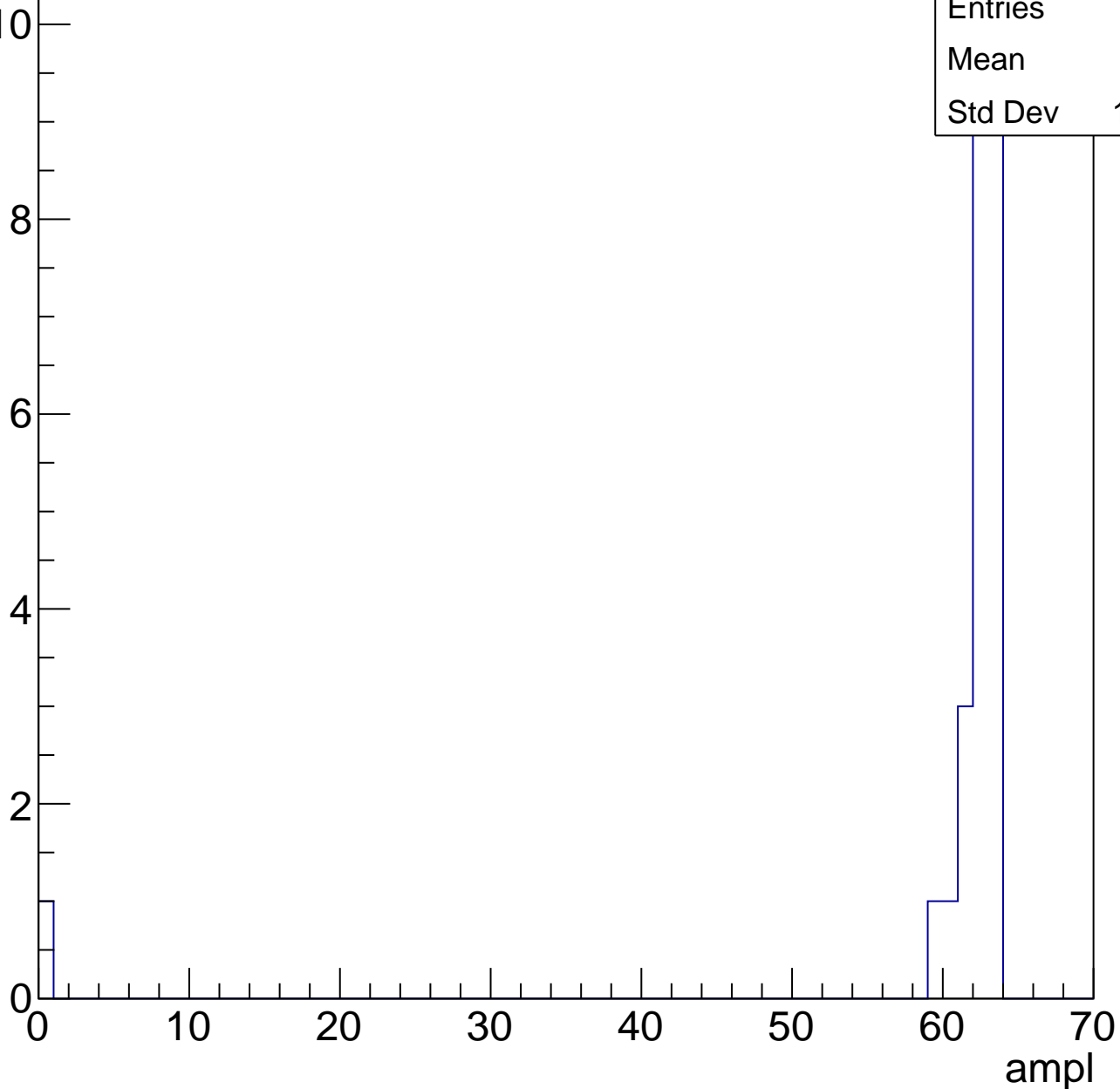


B1L001S, U26-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 25 |
| Mean | 59.6 |
| Std Dev | 12.21 |



B1L001S, U26-ch24, adc7

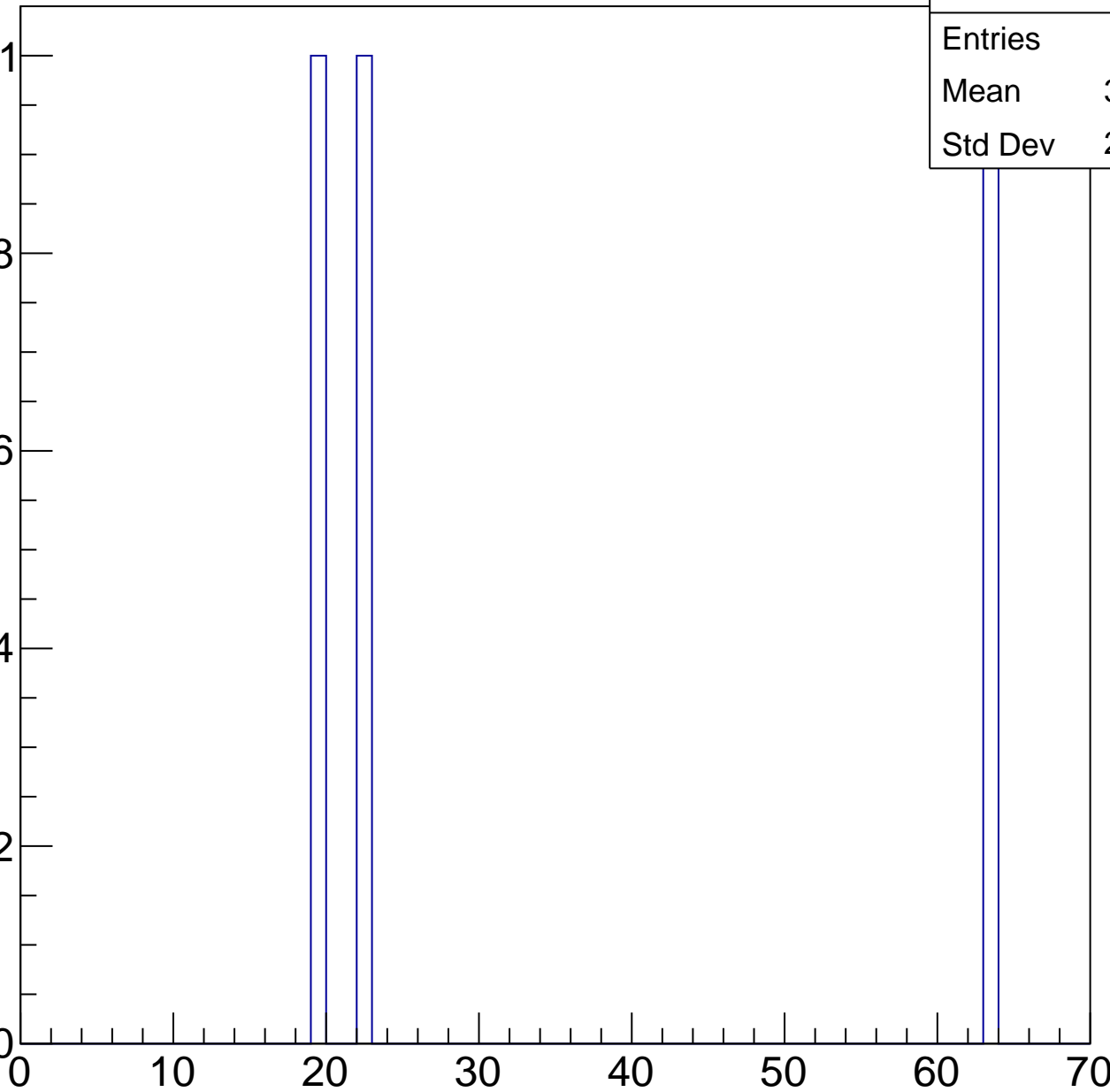
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 3 |
| Mean | 34.67 |
| Std Dev | 20.07 |

ampl



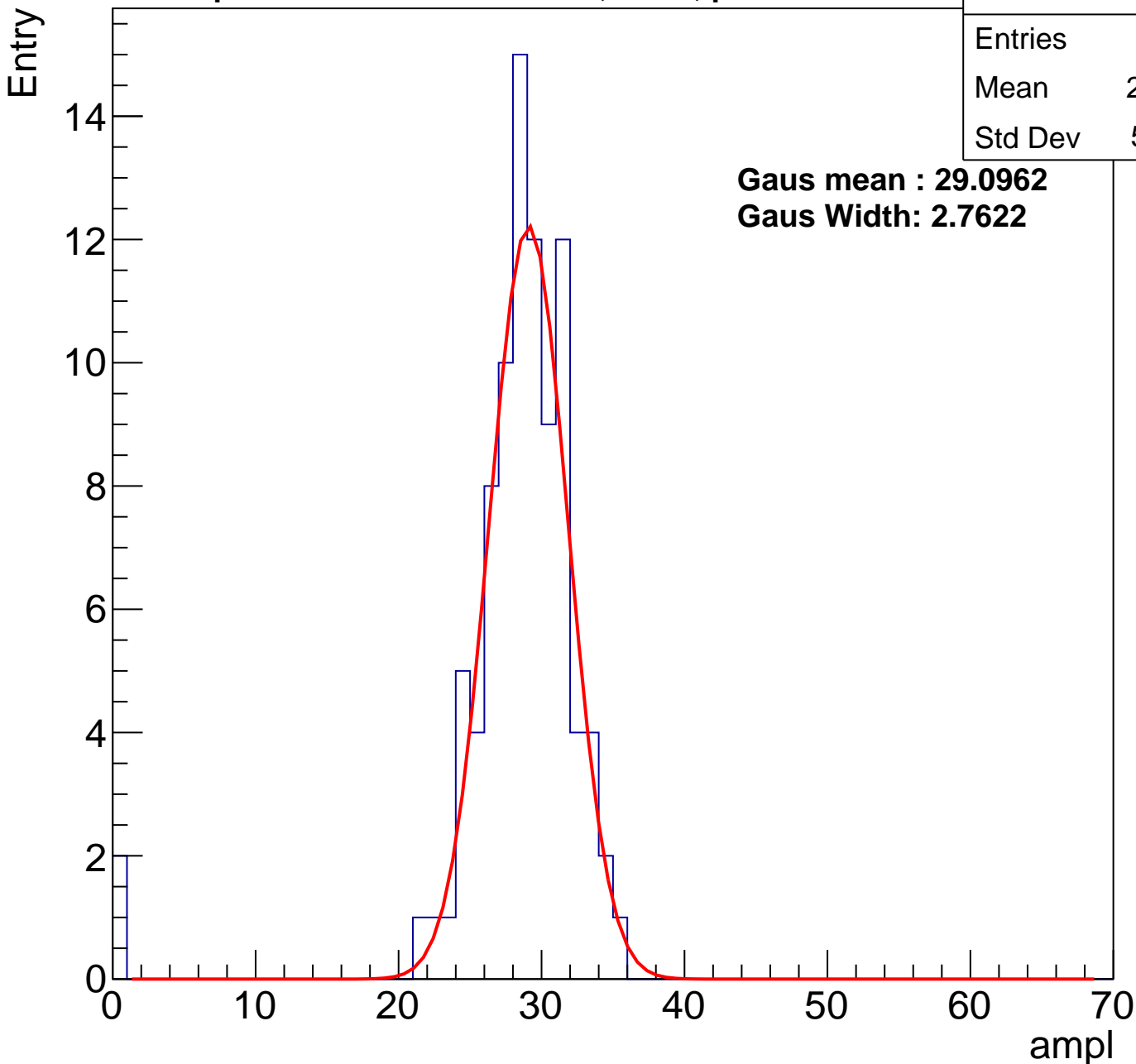
B1L001S, U26-ch25, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 27.88 |
| Std Dev | 5.001 |

Gaus mean : 29.0962

Gaus Width: 2.7622



B1L001S, U26-ch25, adc1

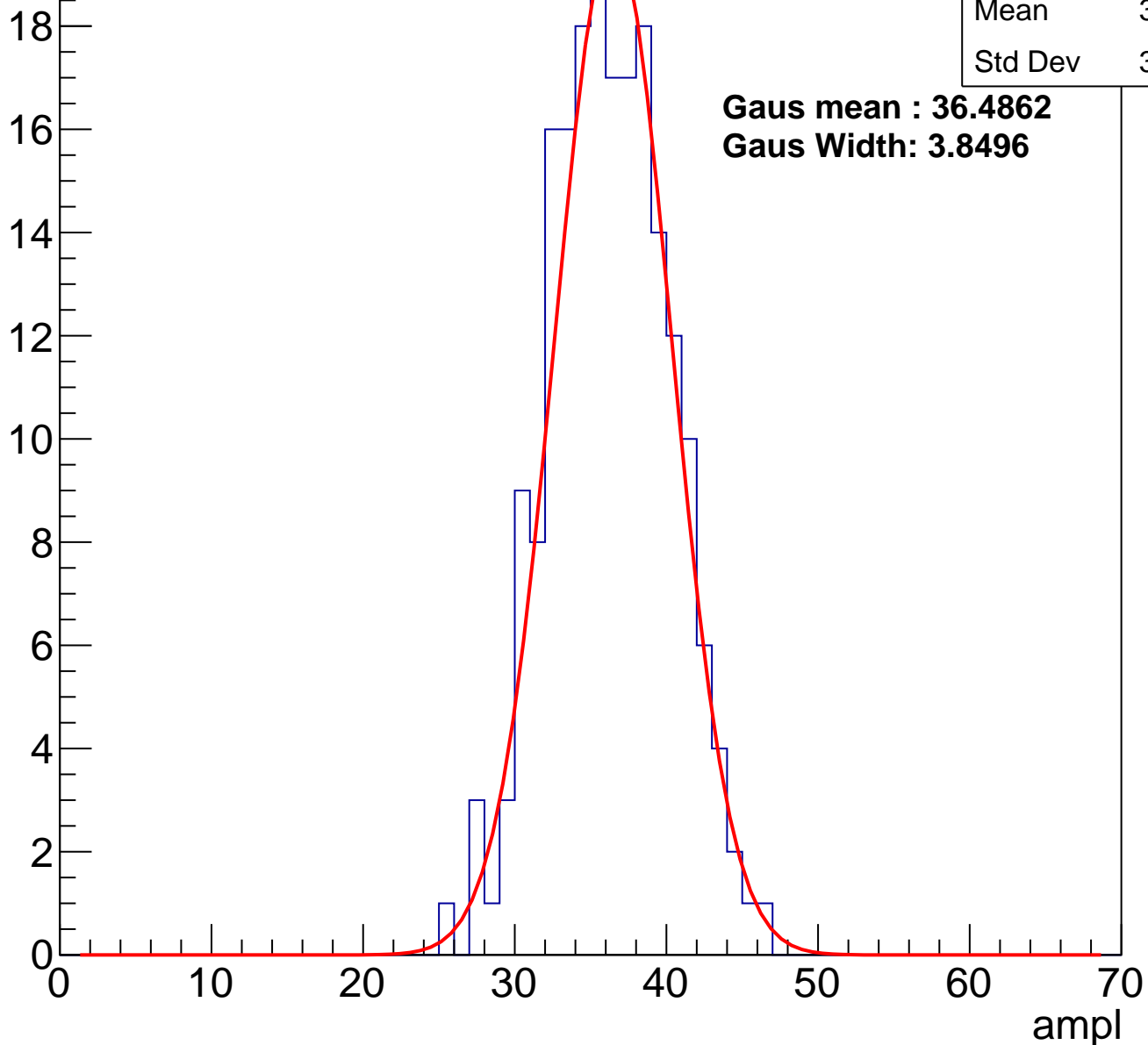
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 196 |
| Mean | 35.82 |
| Std Dev | 3.887 |

Gaus mean : 36.4862

Gaus Width: 3.8496

Entry



B1L001S, U26-ch25, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 110 |
| Mean | 43.22 |
| Std Dev | 3.195 |

Gaus mean : 43.8689

Gaus Width: 3.2904

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

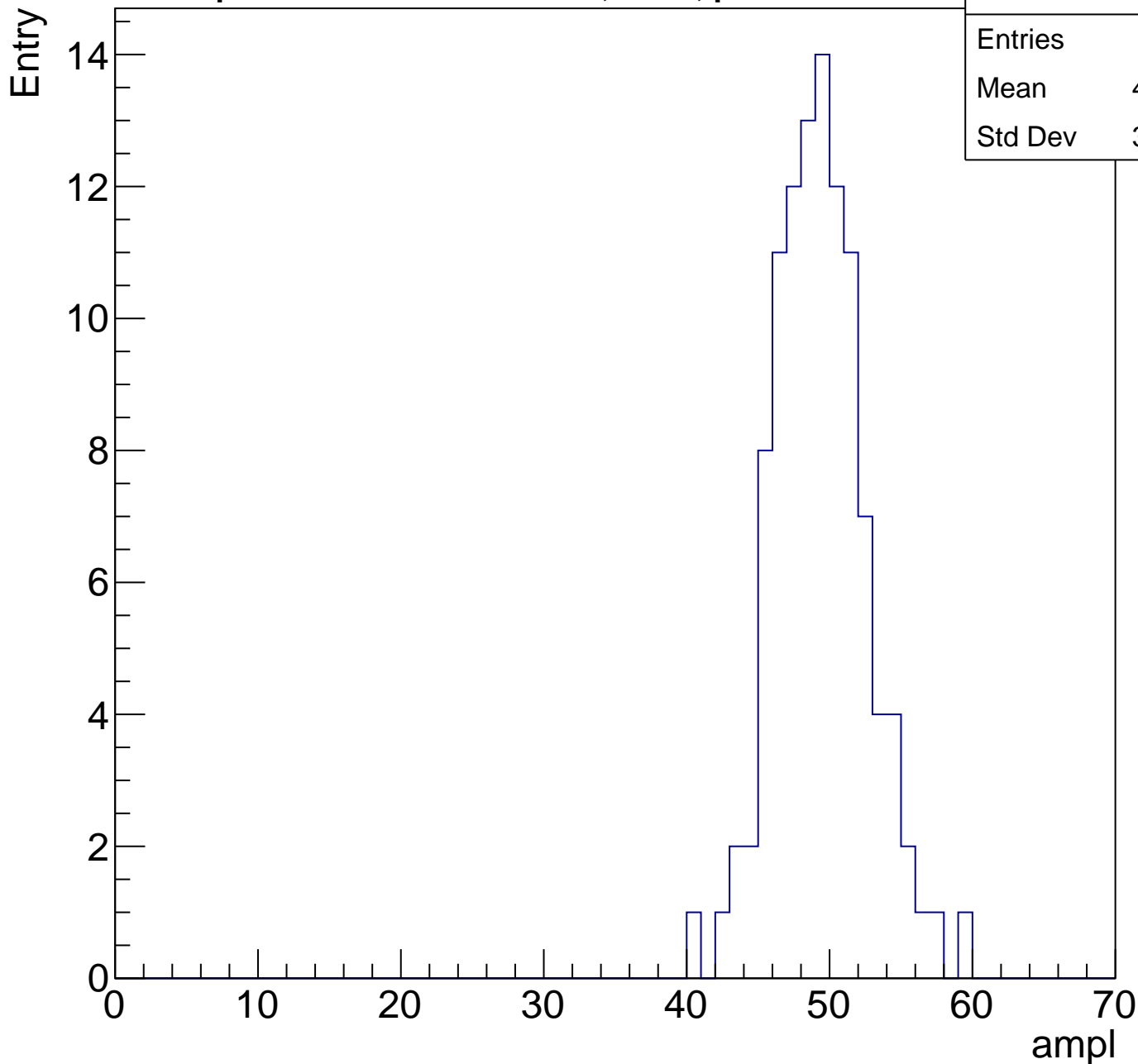
60

70

B1L001S, U26-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 107 |
| Mean | 48.89 |
| Std Dev | 3.216 |



B1L001S, U26-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 54.8 |
| Std Dev | 3.201 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

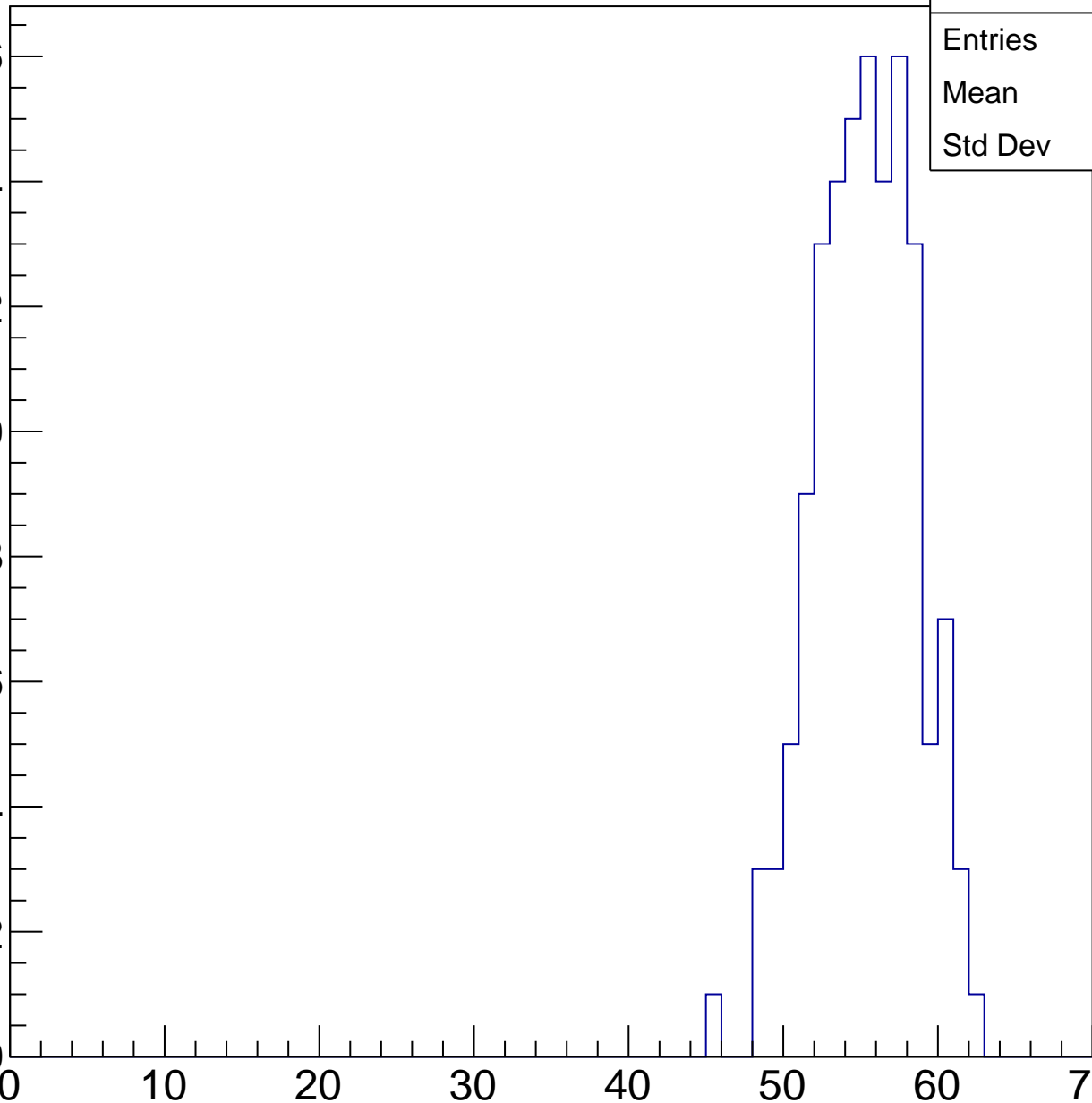
40

50

60

70

ampl



B1L001S, U26-ch25, adc5

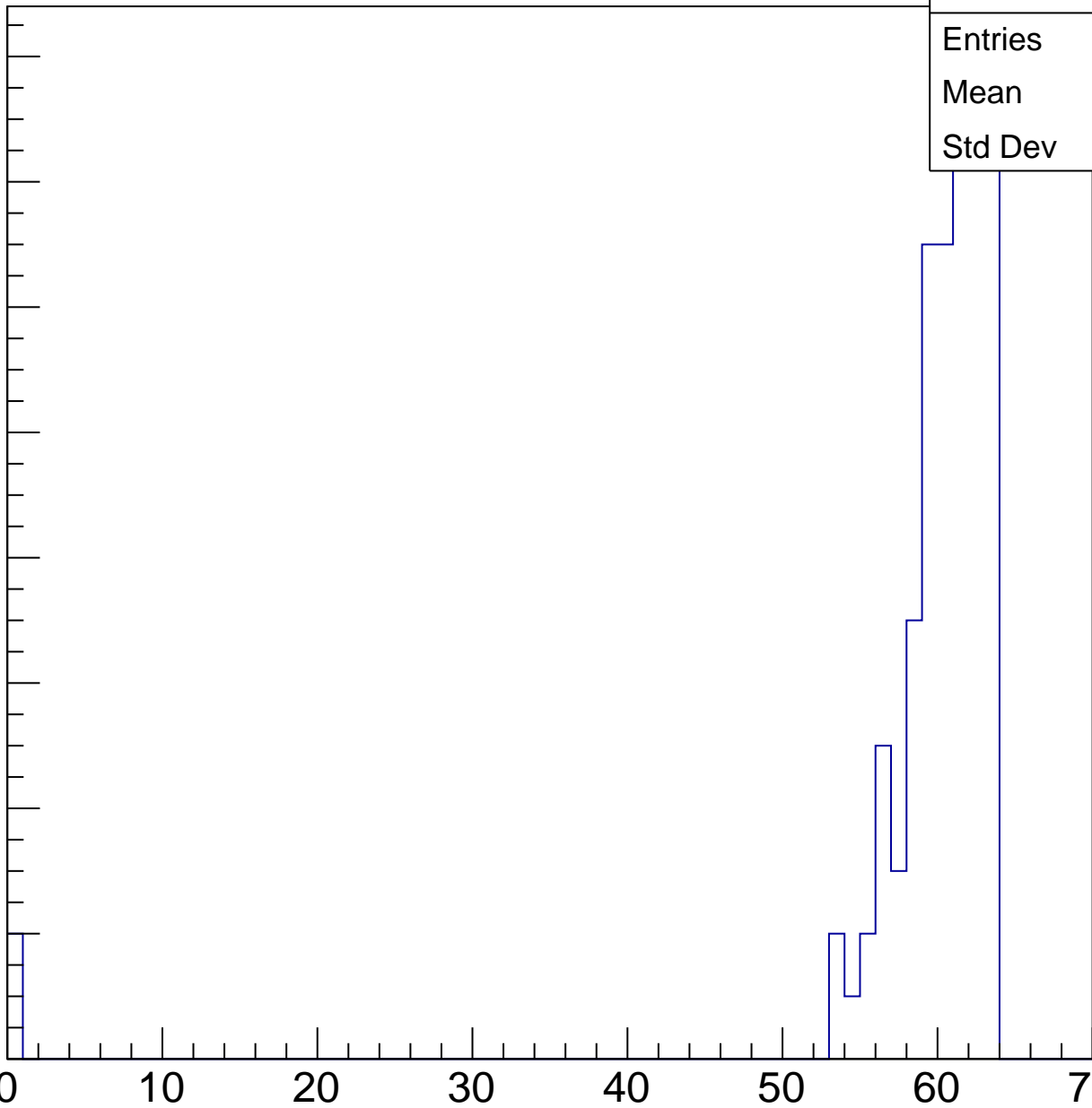
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 58.81 |
| Std Dev | 8.952 |

ampl

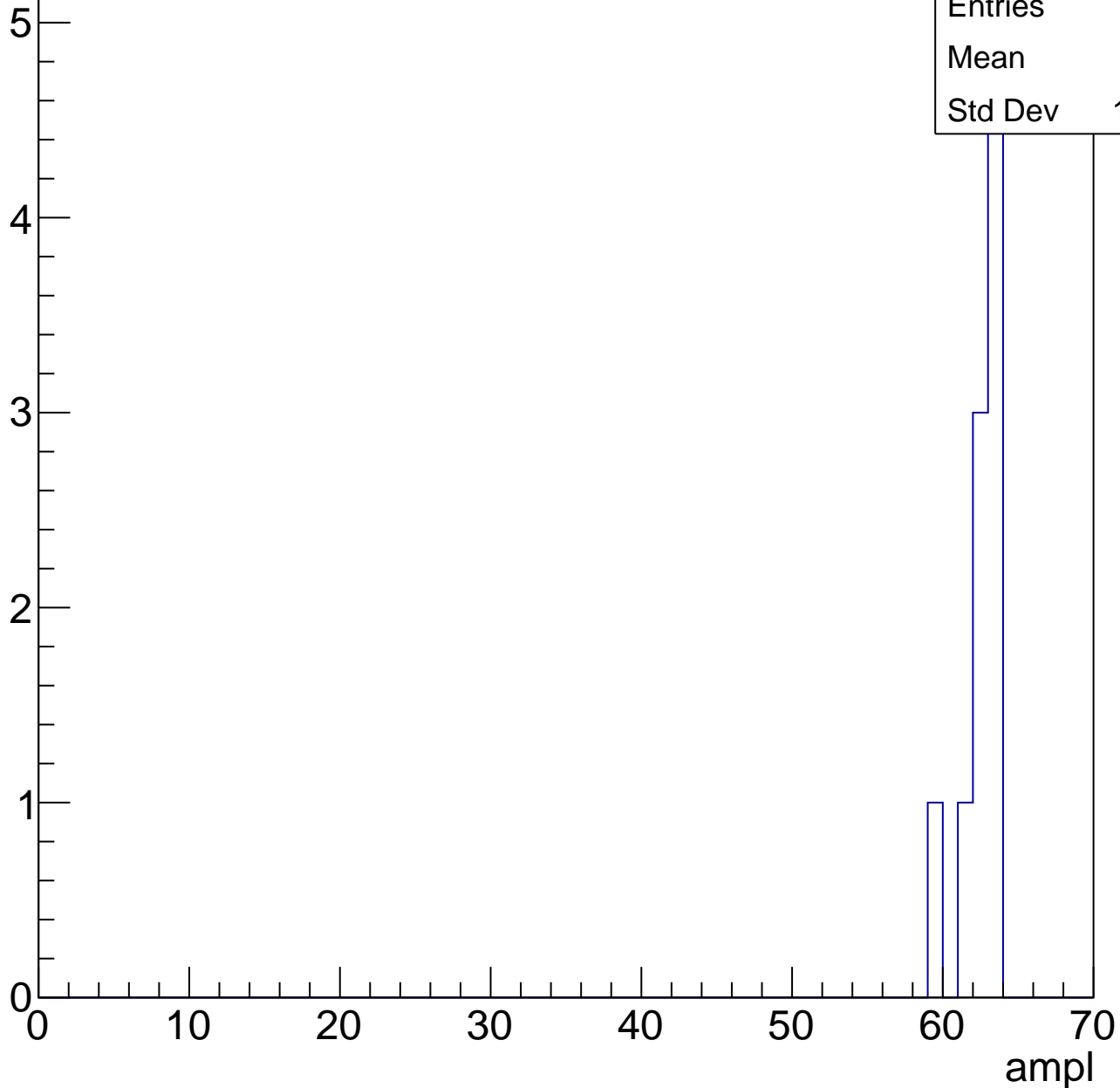


B1L001S, U26-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 62.1 |
| Std Dev | 1.221 |



B1L001S, U26-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch26, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 29.73 |
| Std Dev | 4.04 |

Gaus mean : 30.3286

Gaus Width: 2.9327

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

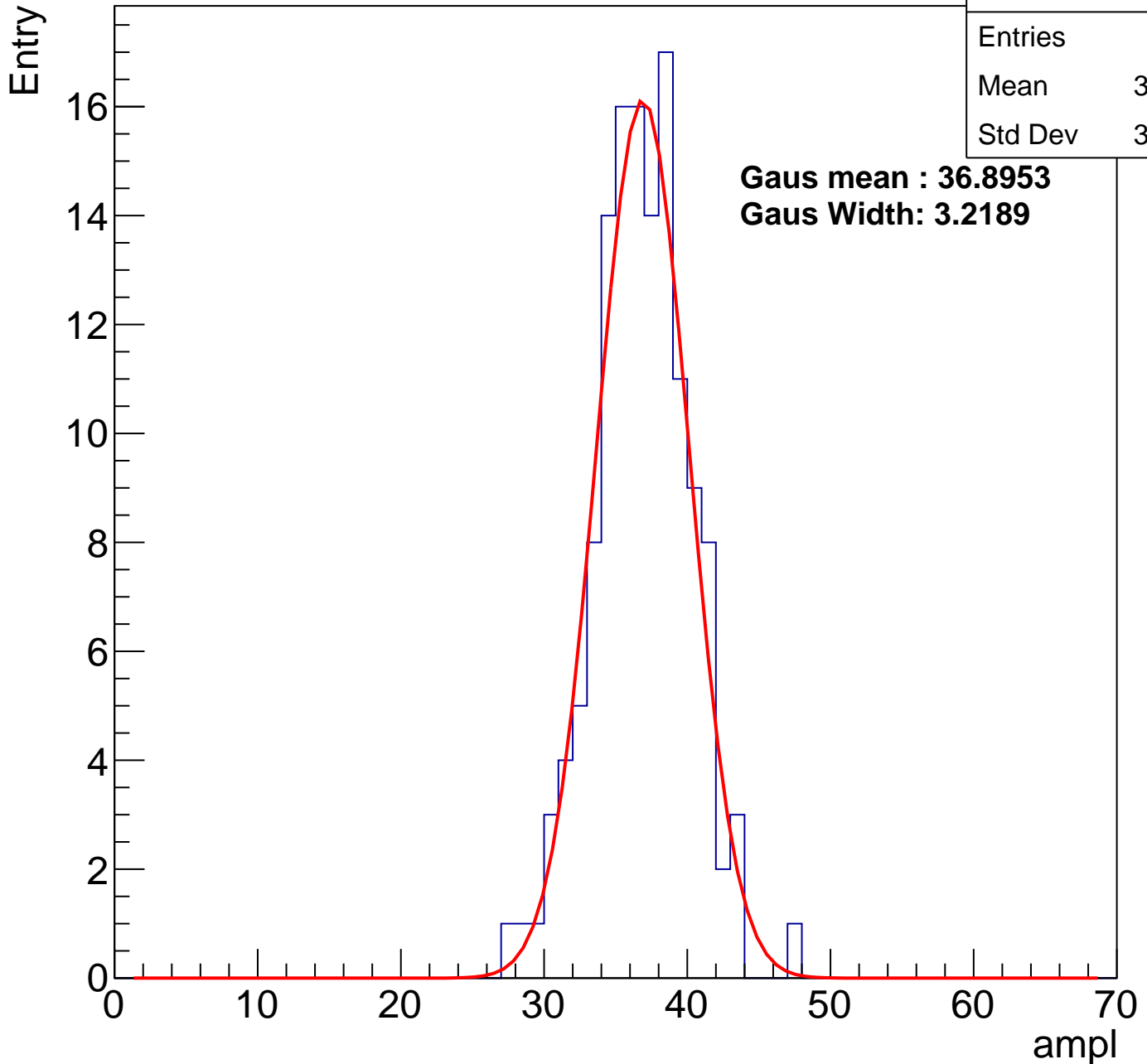
70

B1L001S, U26-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 36.38 |
| Std Dev | 3.314 |

Gaus mean : 36.8953
Gaus Width: 3.2189



B1L001S, U26-ch26, adc2

calib_packv5_042523_0143.root, FC#2, port C2

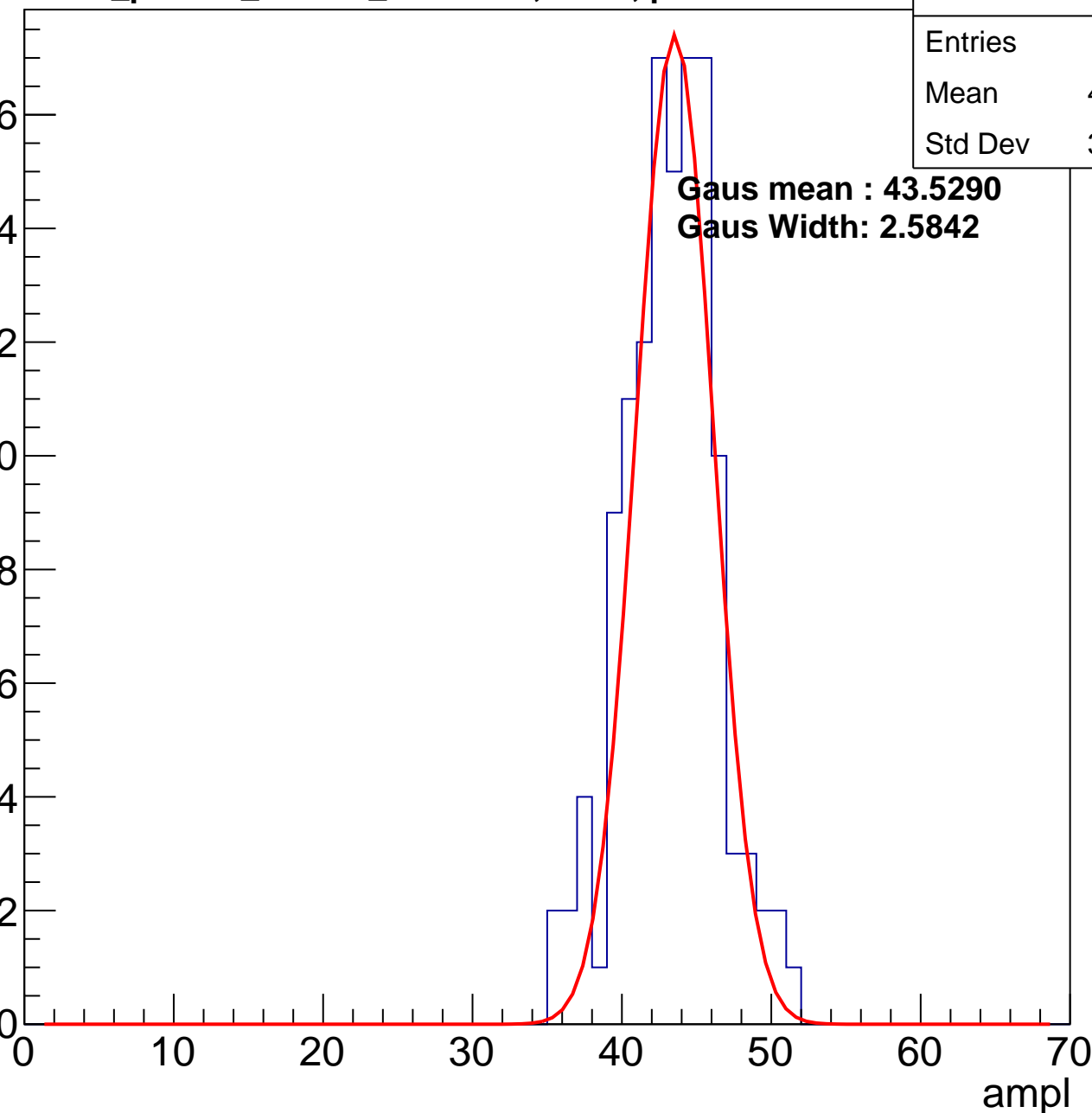
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 128 |
| Mean | 42.79 |
| Std Dev | 3.099 |

Gaus mean : 43.5290

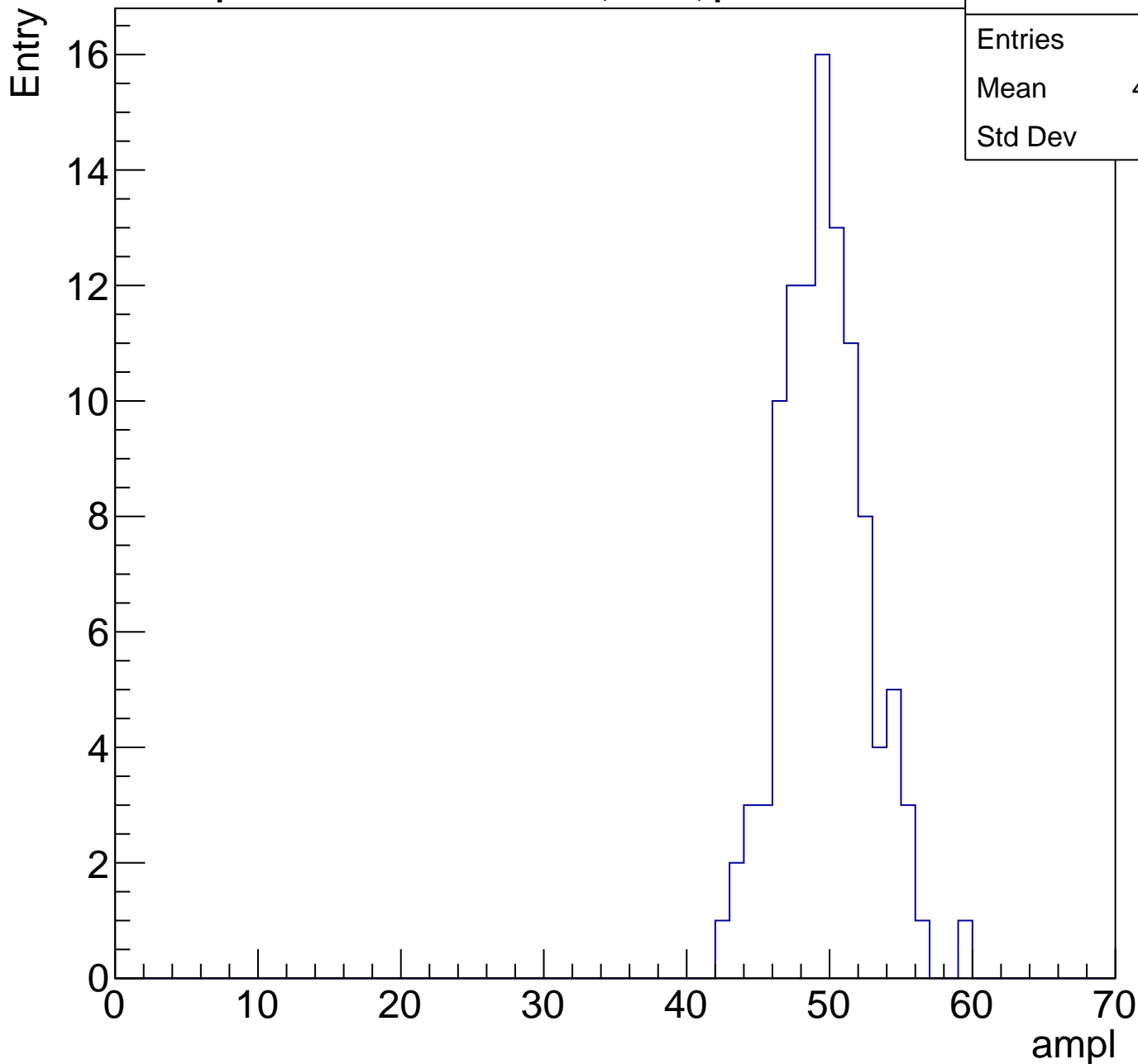
Gaus Width: 2.5842



B1L001S, U26-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

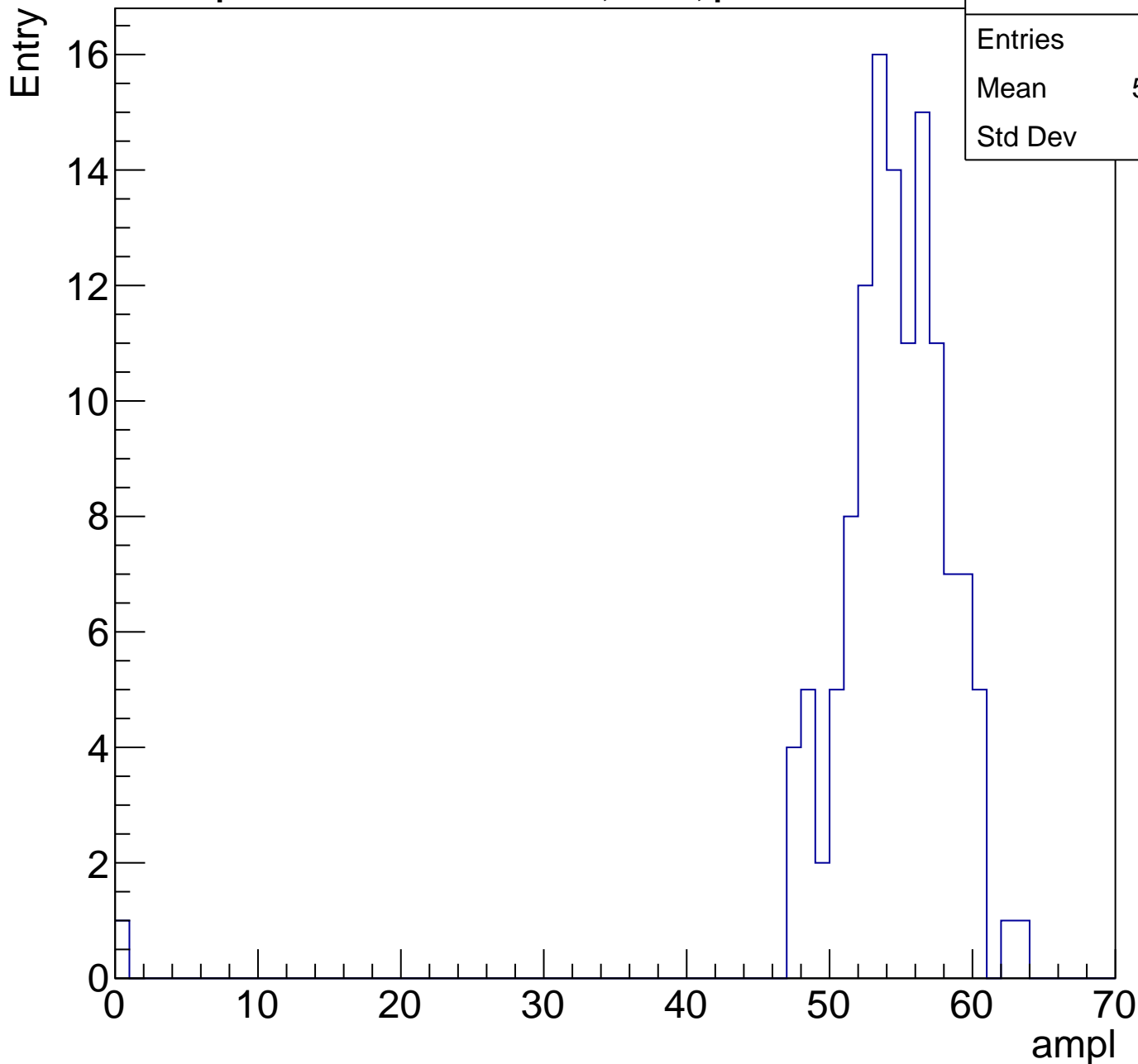
| | |
|---------|-------|
| Entries | 105 |
| Mean | 49.22 |
| Std Dev | 3.03 |



B1L001S, U26-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 53.82 |
| Std Dev | 5.89 |



B1L001S, U26-ch26, adc5

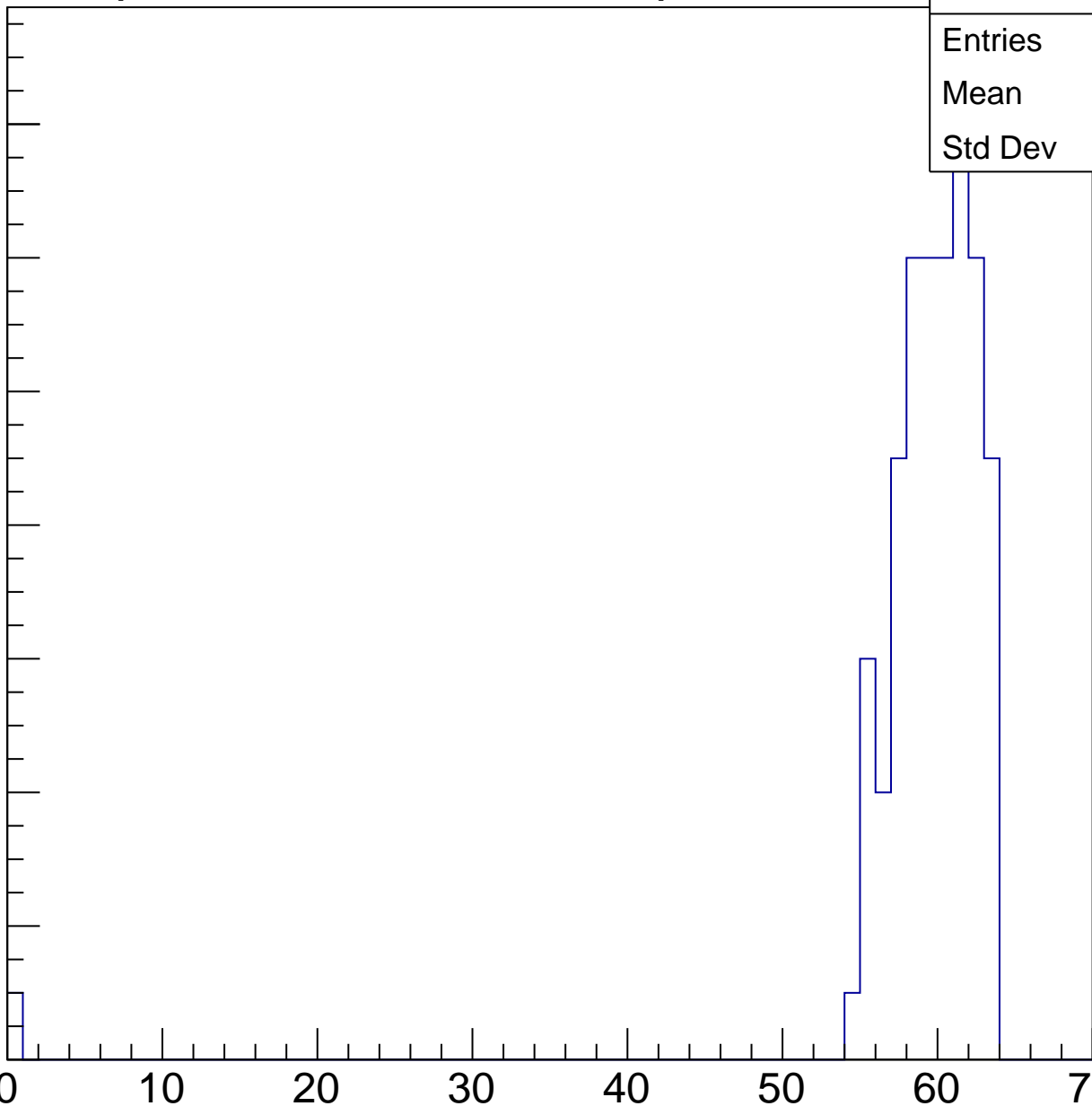
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 93 |
| Mean | 58.83 |
| Std Dev | 6.564 |

ampl

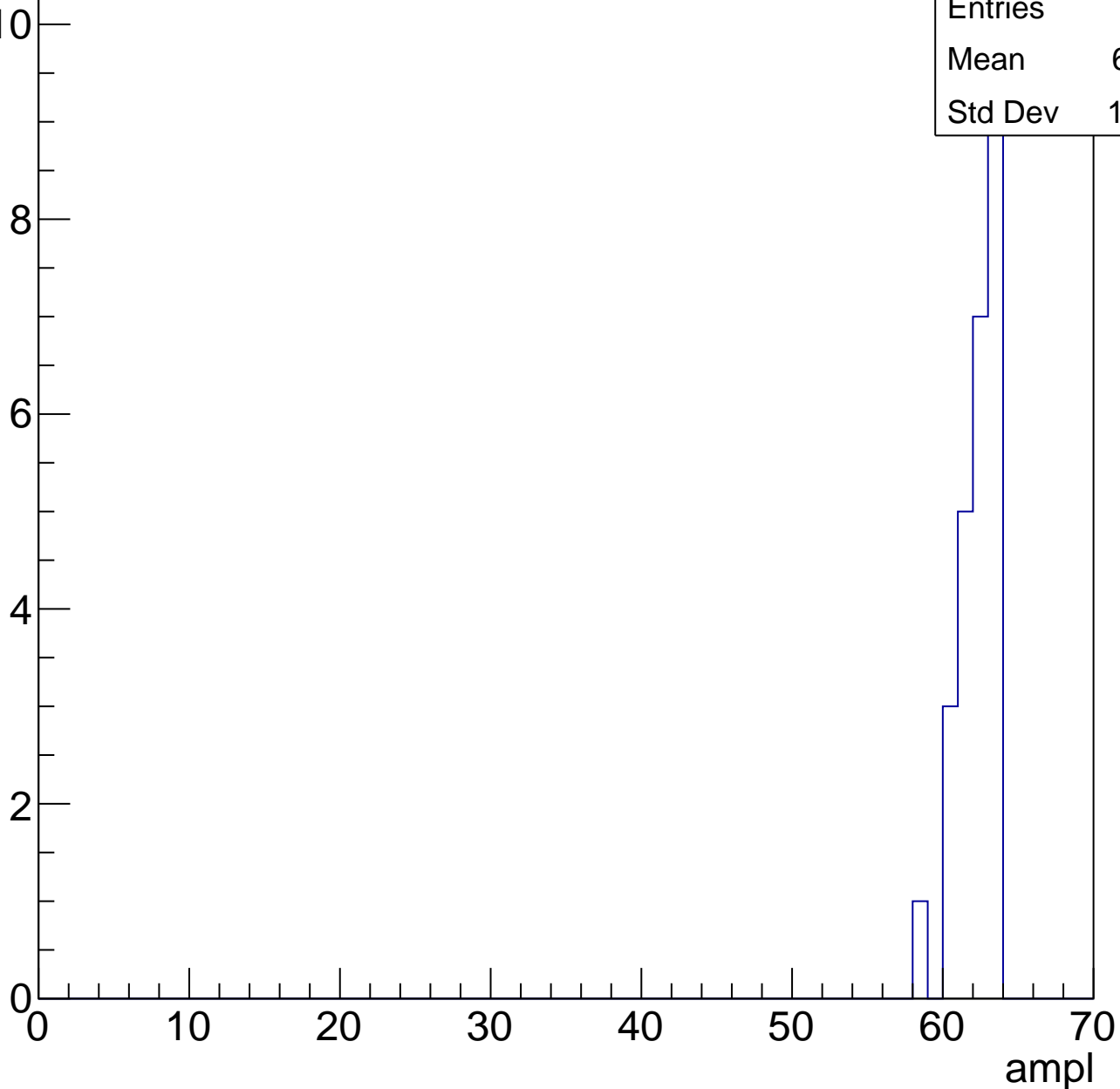


B1L001S, U26-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 61.81 |
| Std Dev | 1.272 |



B1L001S, U26-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-----|
| Entries | 2 |
| Mean | 9.5 |
| Std Dev | 9.5 |

B1L001S, U26-ch27, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 29.53 |
| Std Dev | 3.485 |

Gaus mean : 29.8992

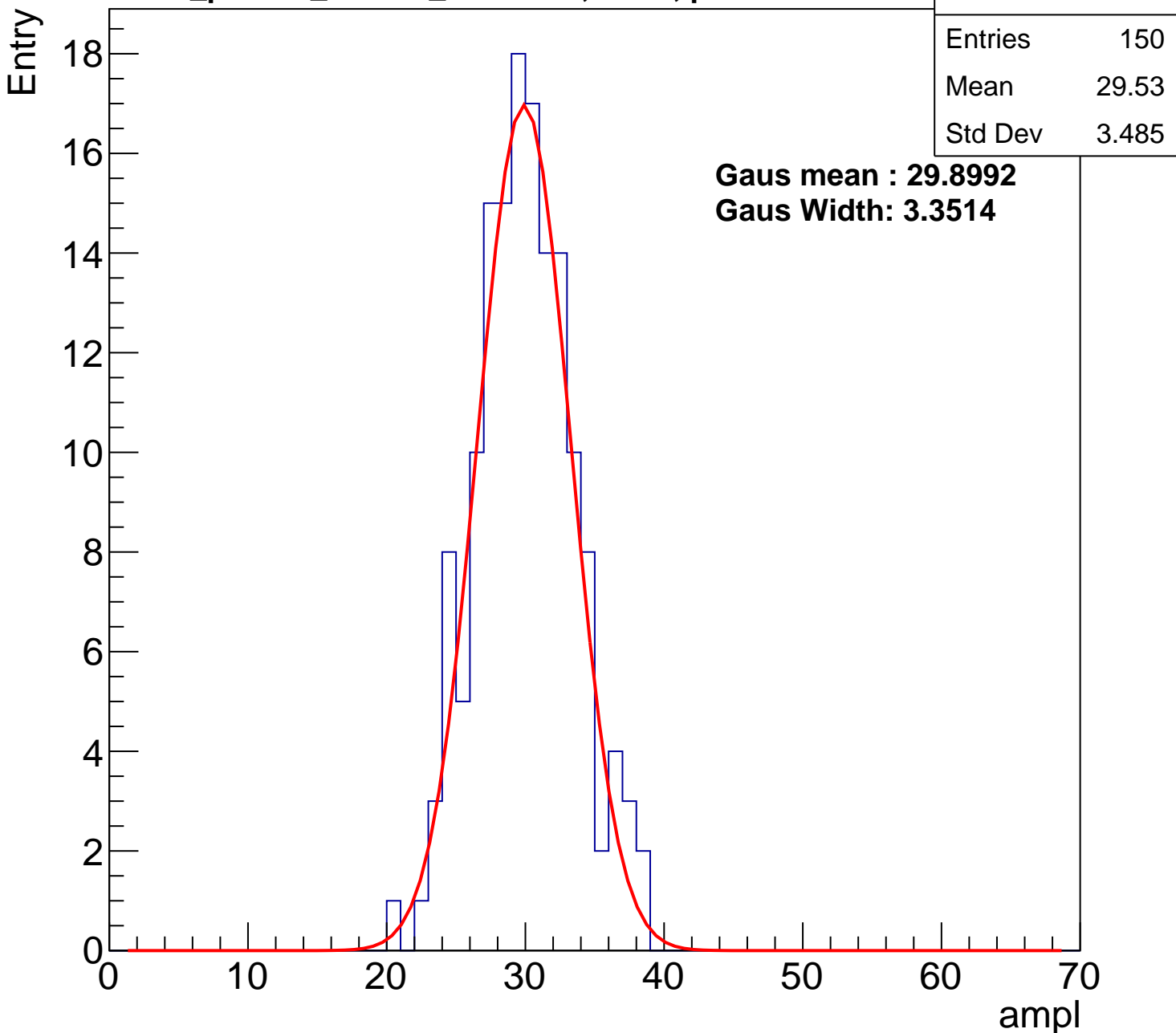
Gaus Width: 3.3514

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch27, adc1

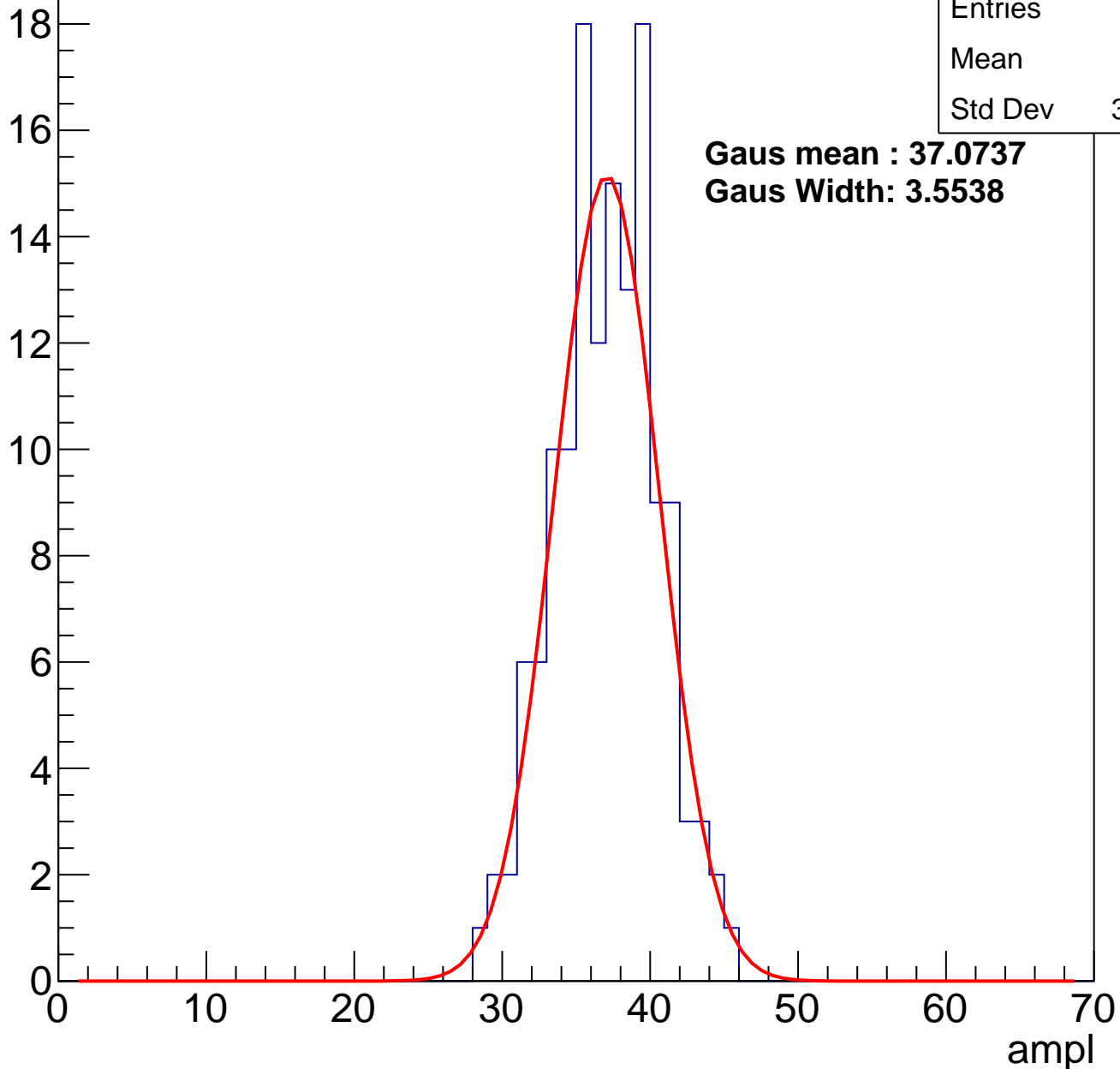
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 36.6 |
| Std Dev | 3.422 |

Gaus mean : 37.0737

Gaus Width: 3.5538

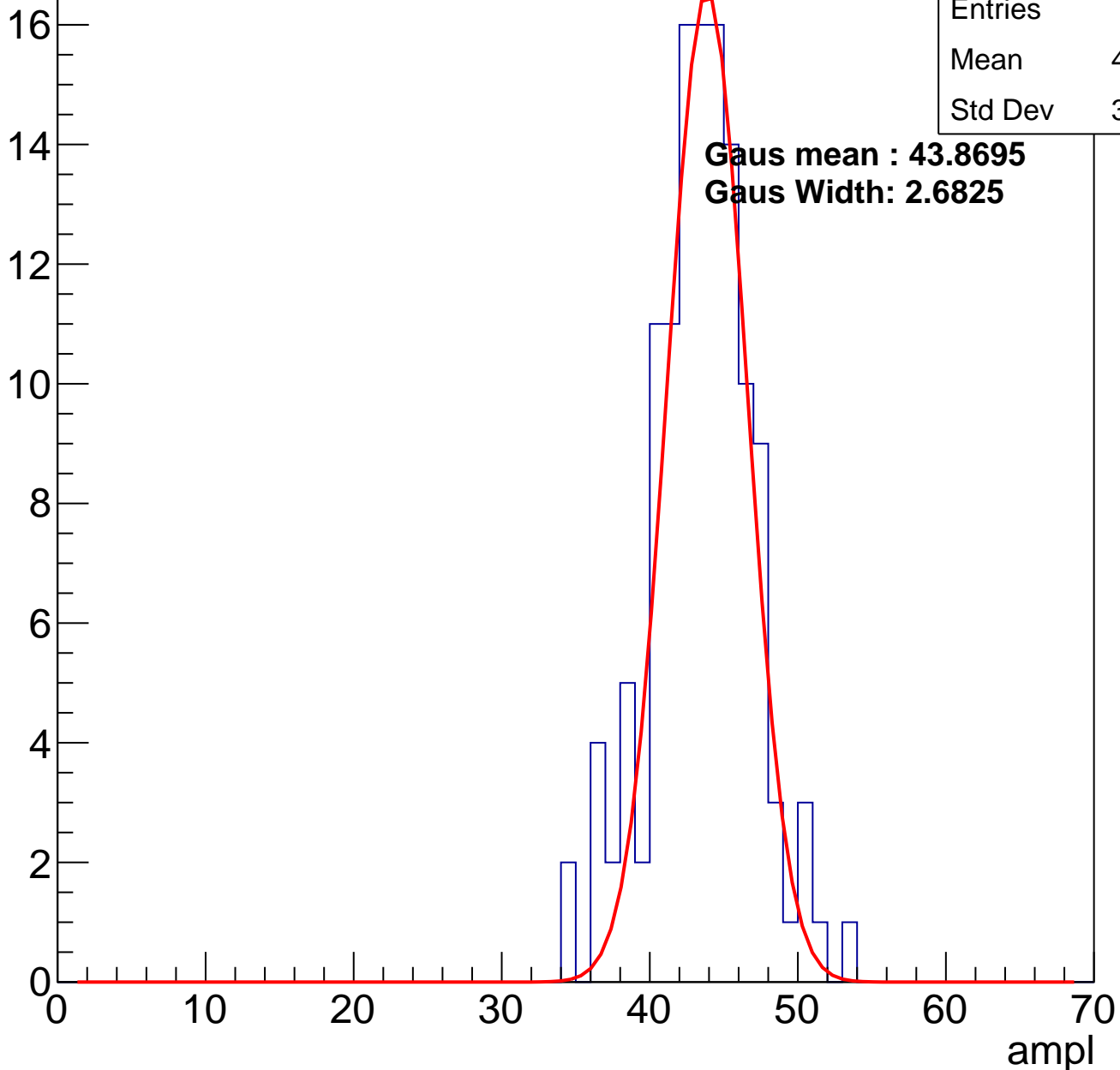
Entry



B1L001S, U26-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

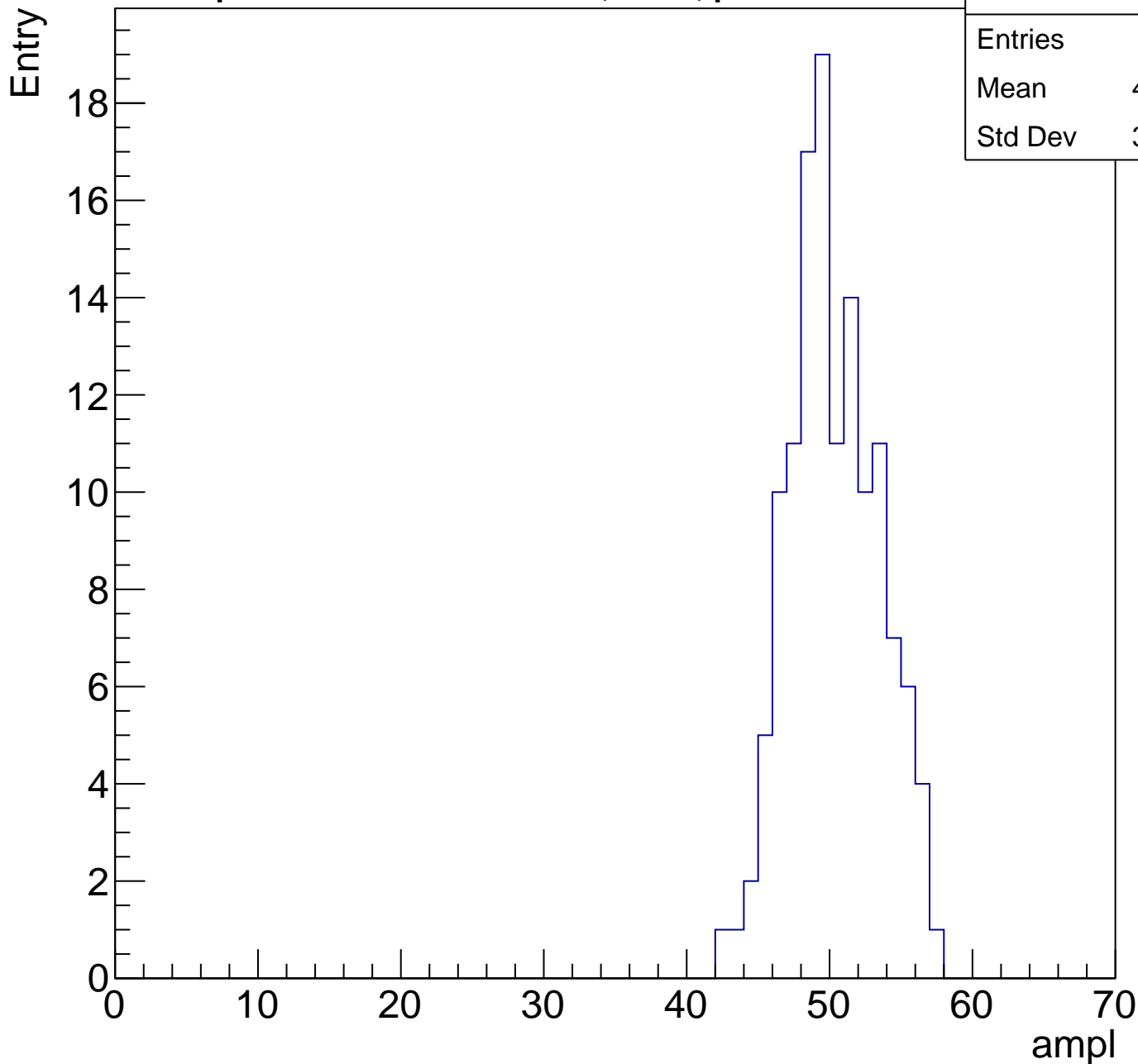
Entry



B1L001S, U26-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

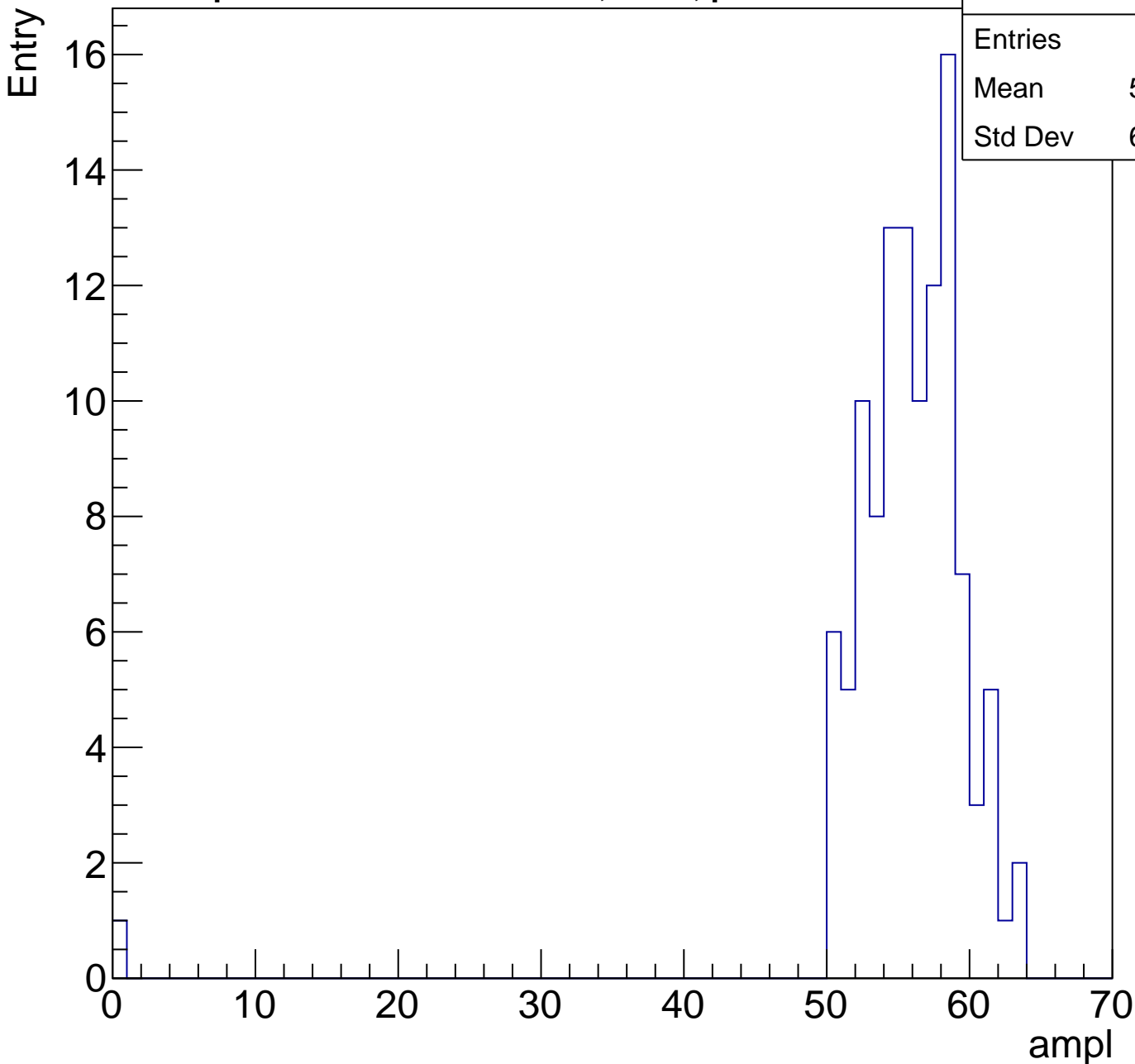
| | |
|---------|-------|
| Entries | 130 |
| Mean | 49.83 |
| Std Dev | 3.114 |



B1L001S, U26-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 55.12 |
| Std Dev | 6.076 |



B1L001S, U26-ch27, adc5

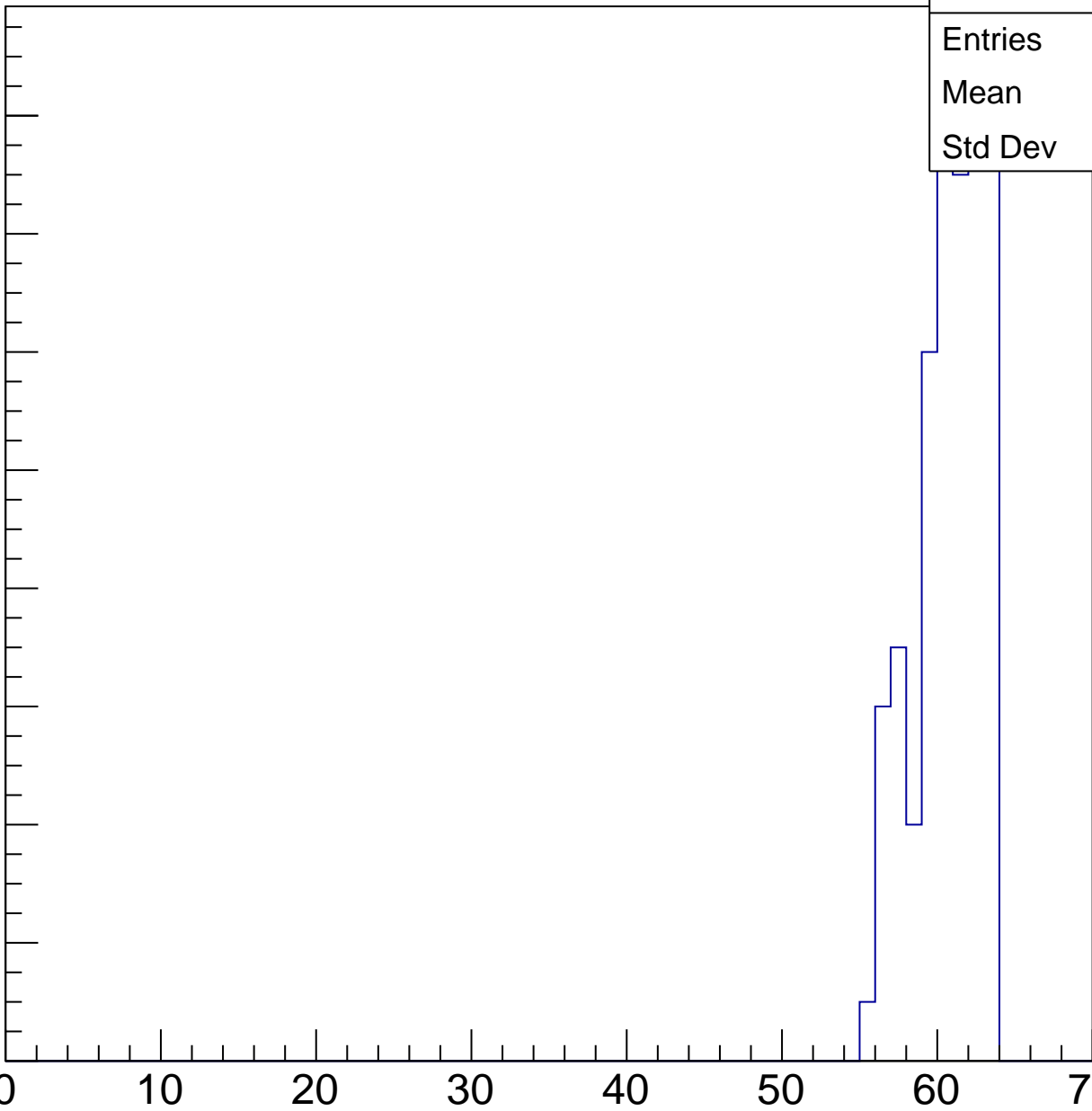
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 96 |
| Mean | 60.31 |
| Std Dev | 2.138 |

ampl



B1L001S, U26-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

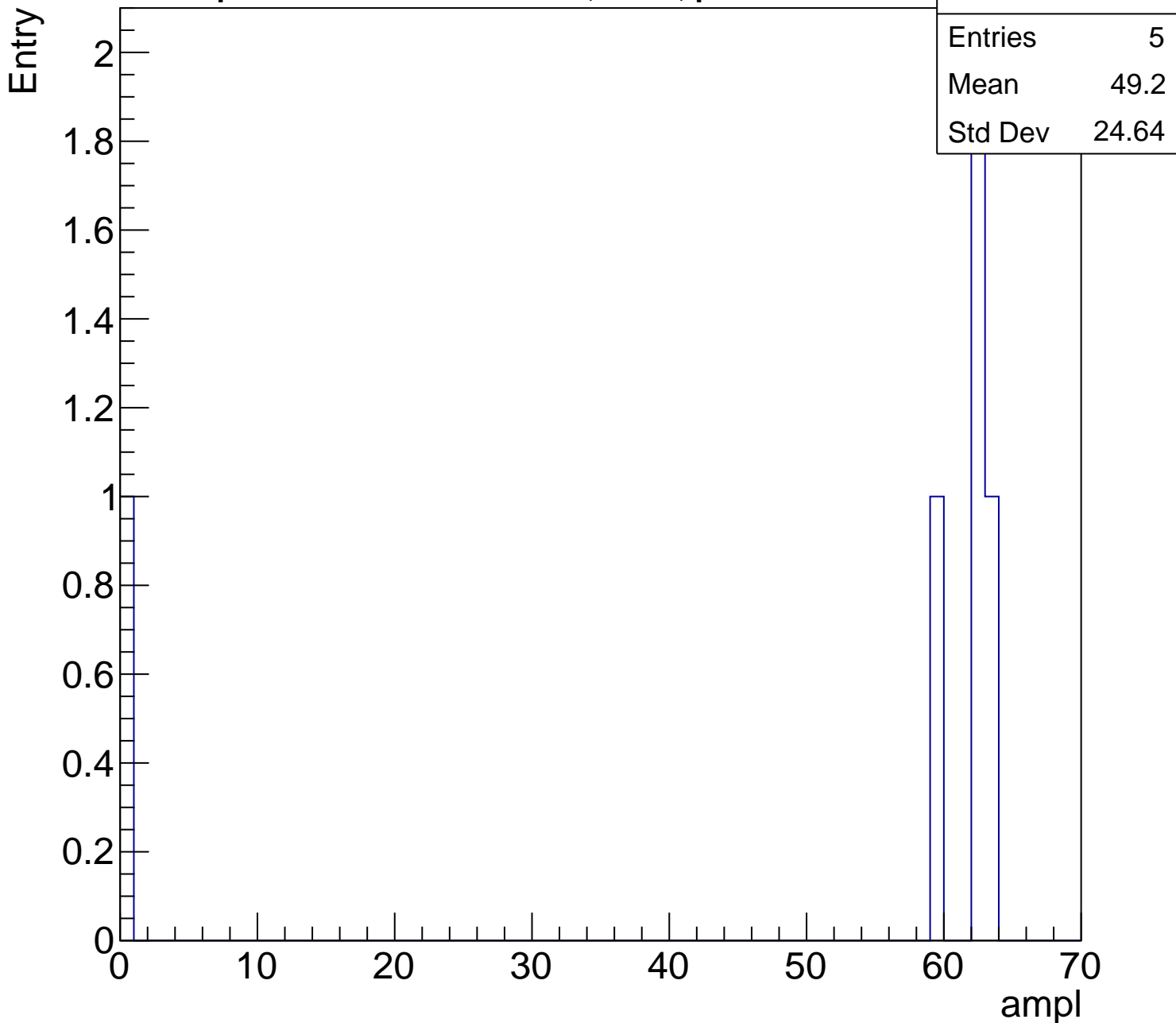
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 5 |
| Mean | 49.2 |
| Std Dev | 24.64 |

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch28, adc0

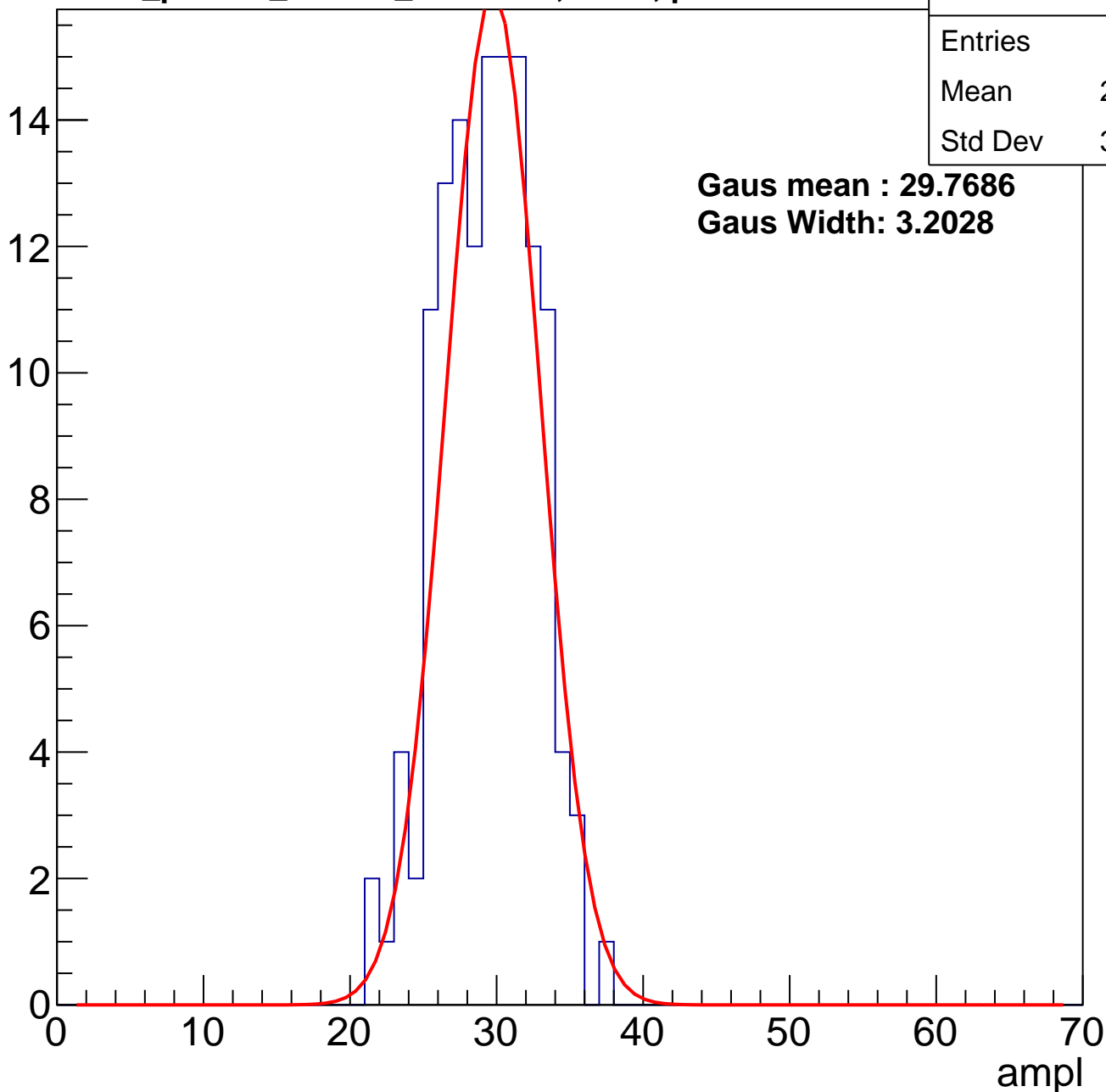
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 28.93 |
| Std Dev | 3.177 |

Gaus mean : 29.7686

Gaus Width: 3.2028

Entry



B1L001S, U26-ch28, adc1

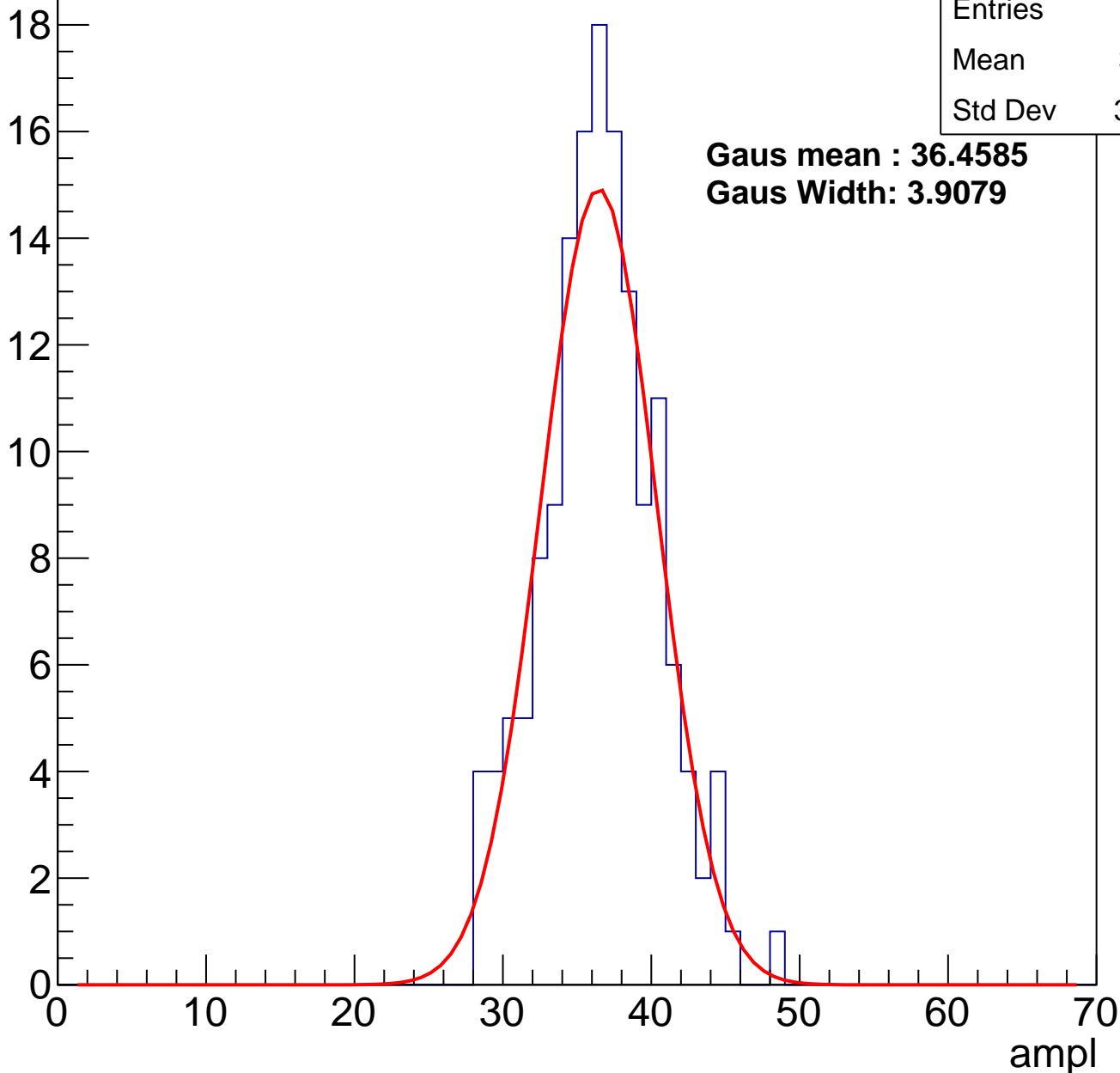
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 36.11 |
| Std Dev | 3.859 |

Gaus mean : 36.4585

Gaus Width: 3.9079

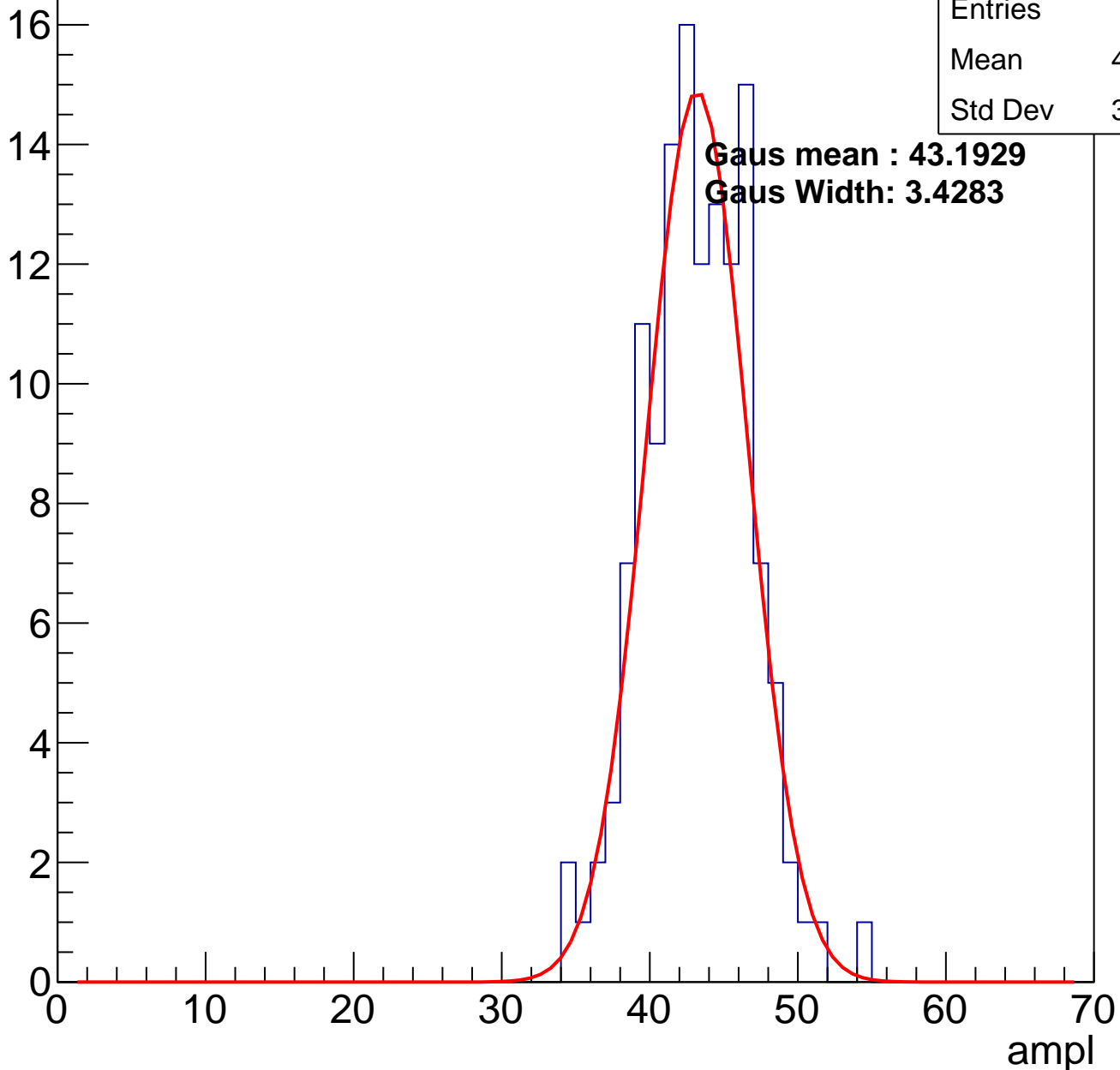
Entry



B1L001S, U26-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

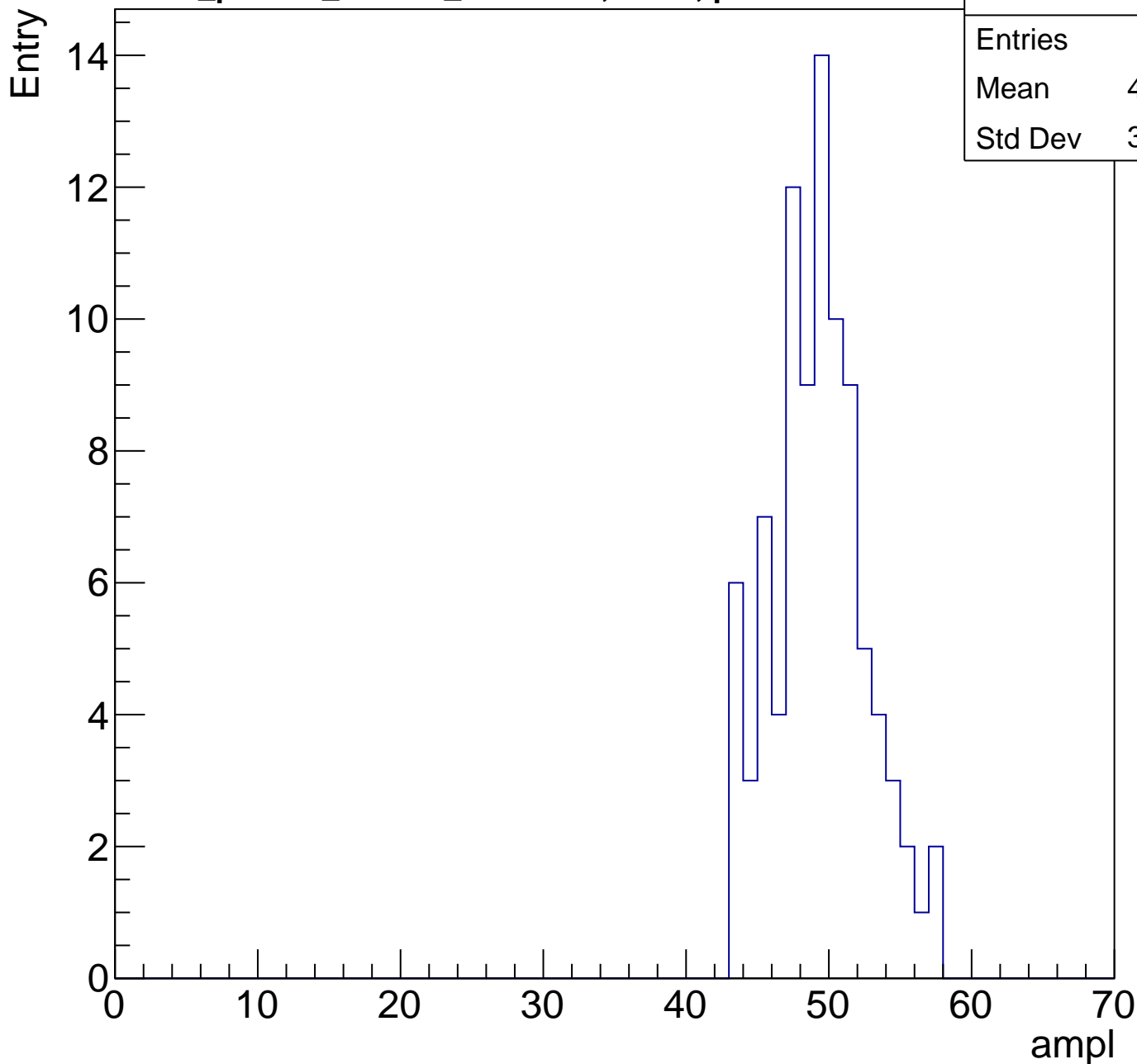
Entry



B1L001S, U26-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

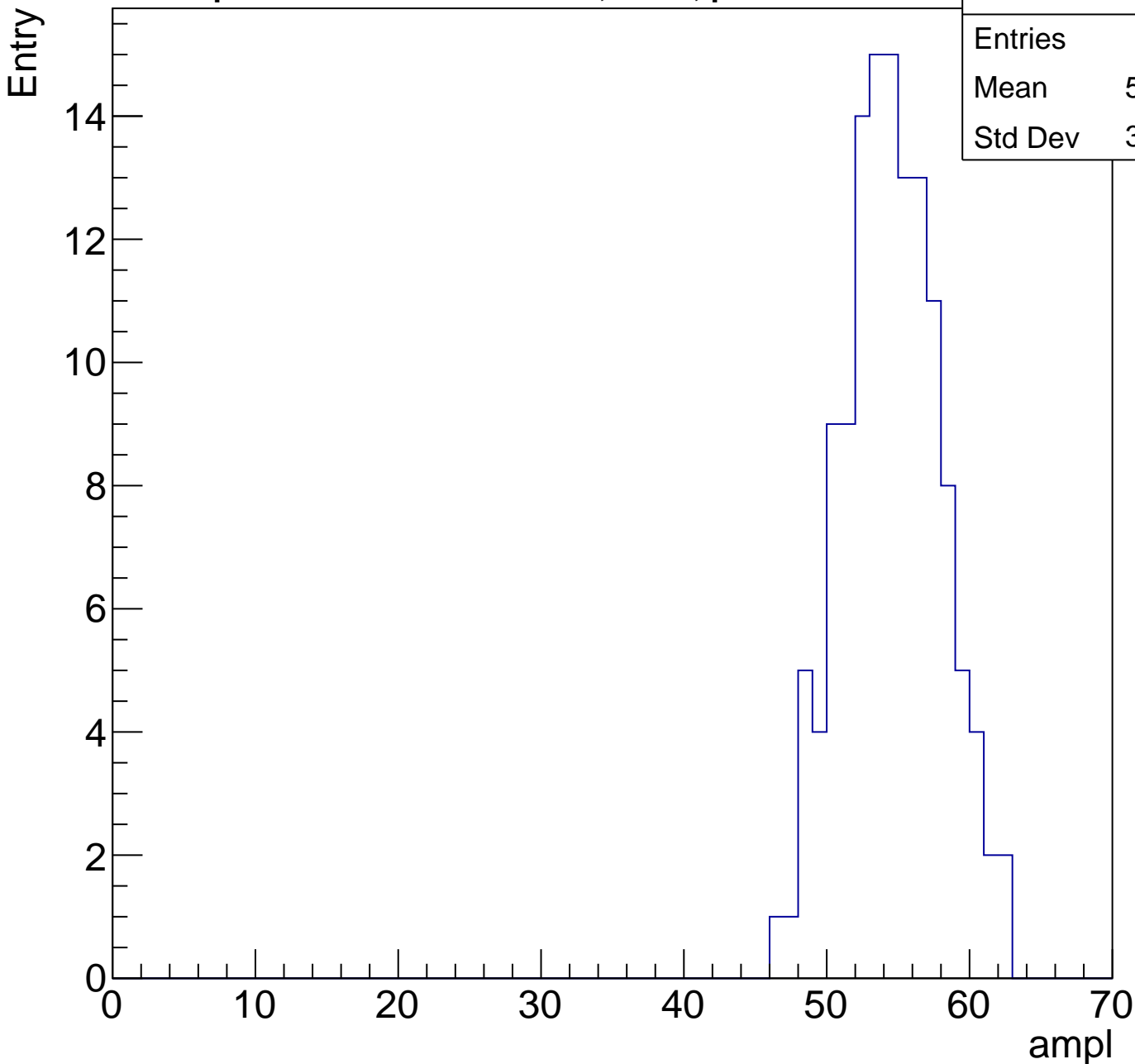
| | |
|---------|-------|
| Entries | 91 |
| Mean | 48.84 |
| Std Dev | 3.269 |



B1L001S, U26-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

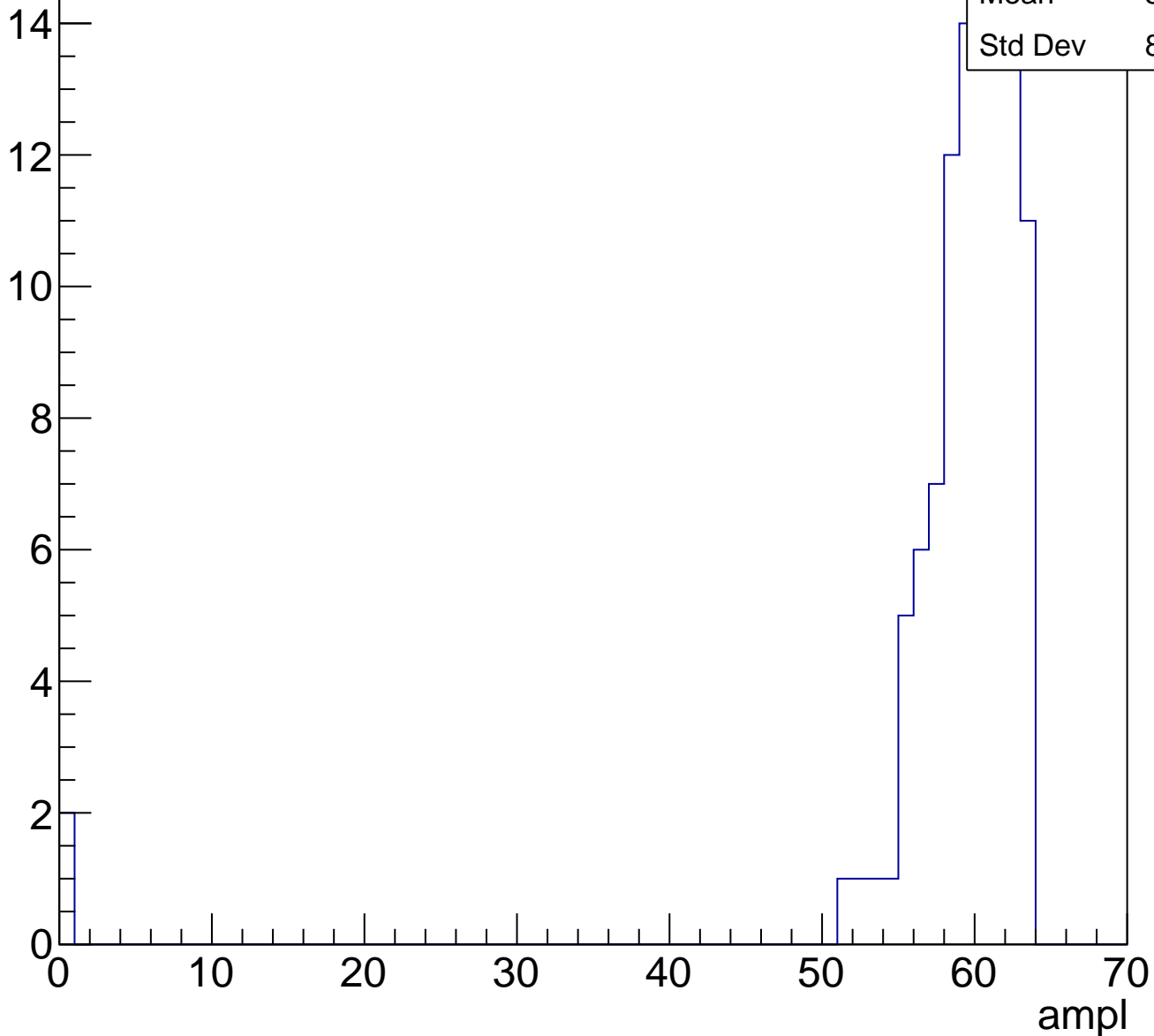
| | |
|---------|-------|
| Entries | 131 |
| Mean | 54.09 |
| Std Dev | 3.355 |



B1L001S, U26-ch28, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch28, adc6

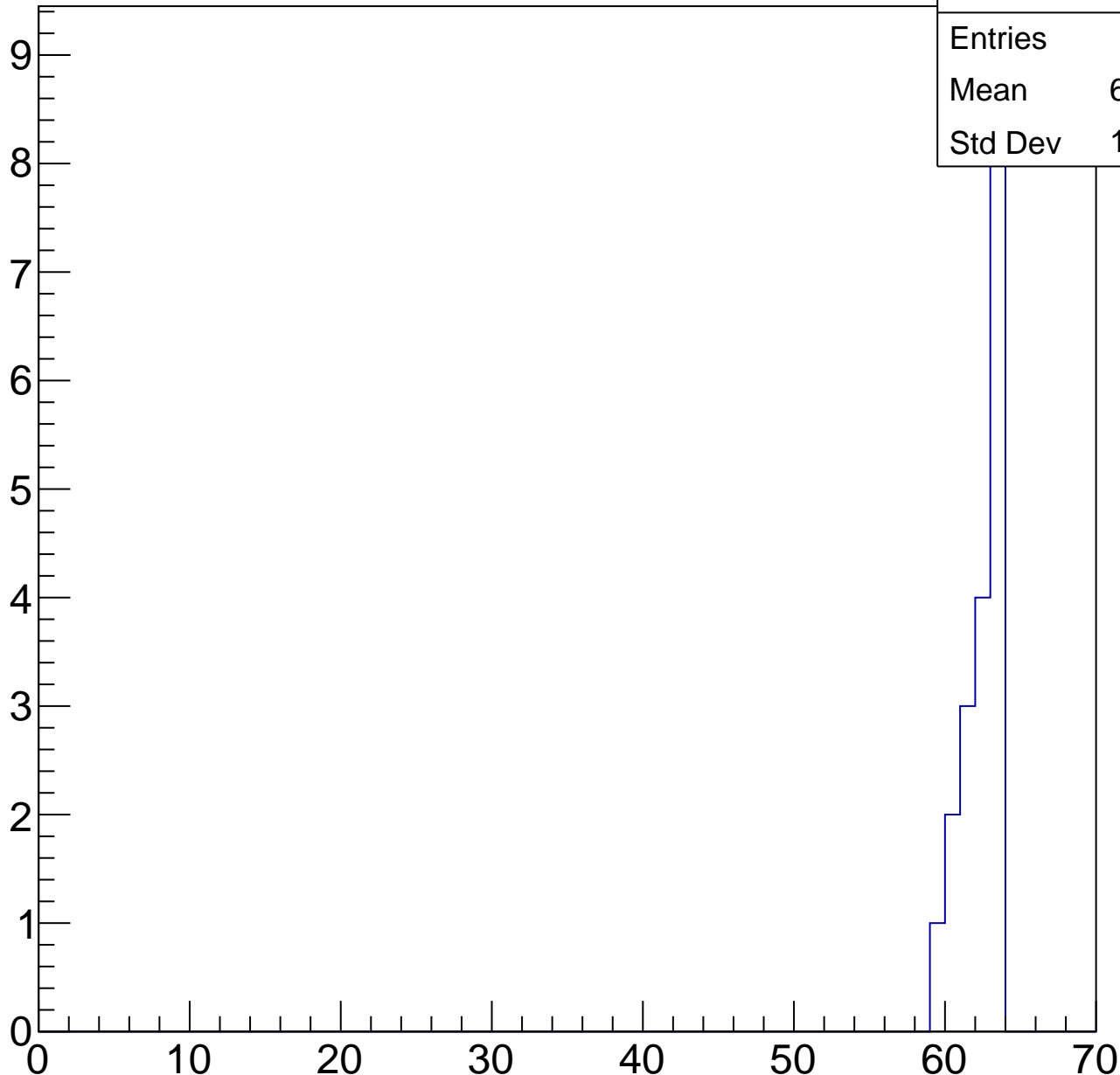
calib_packv5_042523_0143.root, FC#2, port C2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 19 |
| Mean | 61.95 |
| Std Dev | 1.234 |

ampl



B1L001S, U26-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 130 |
| Mean | 29.36 |
| Std Dev | 3.201 |

Gaus mean : 29.5657

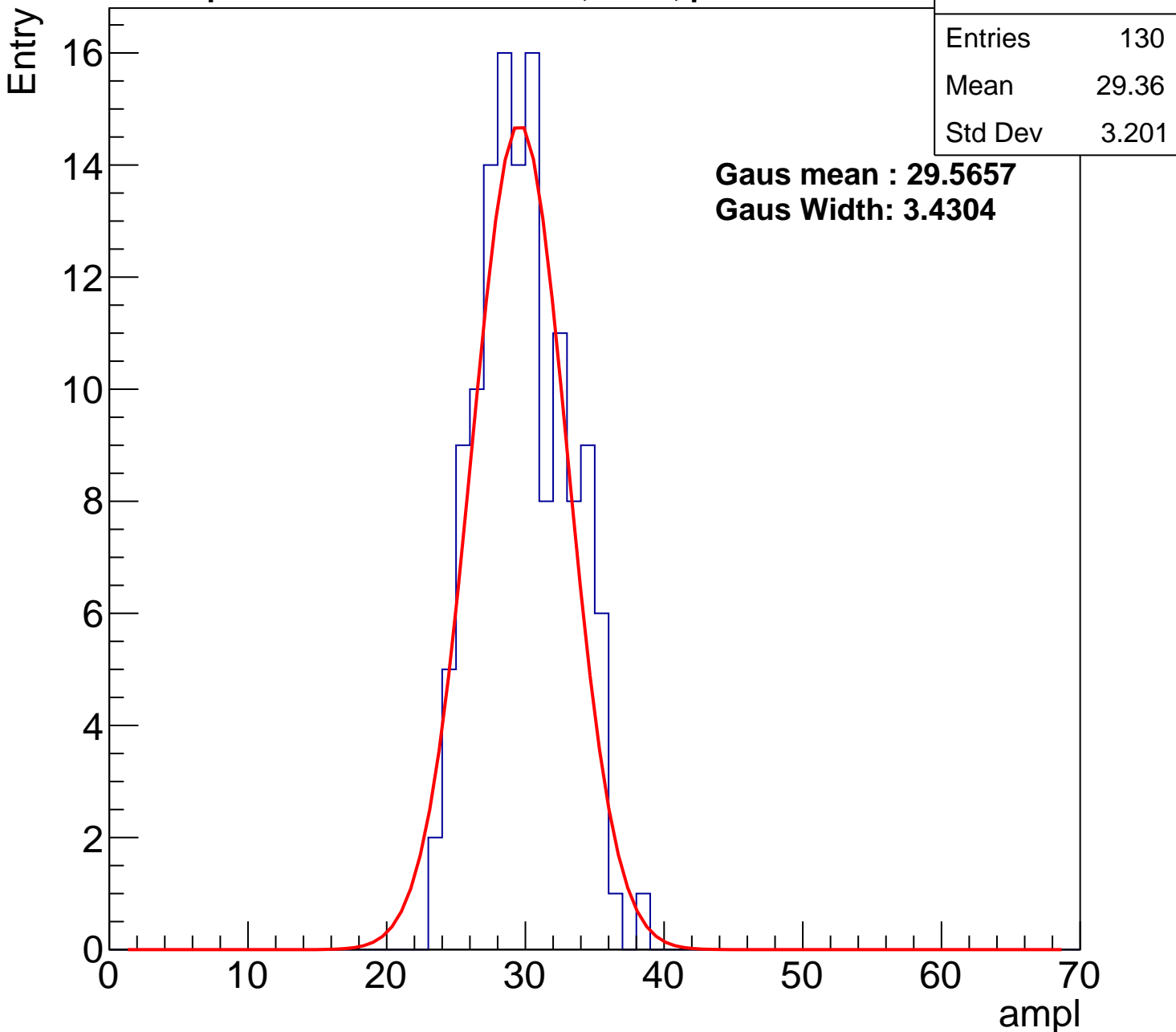
Gaus Width: 3.4304

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch29, adc1

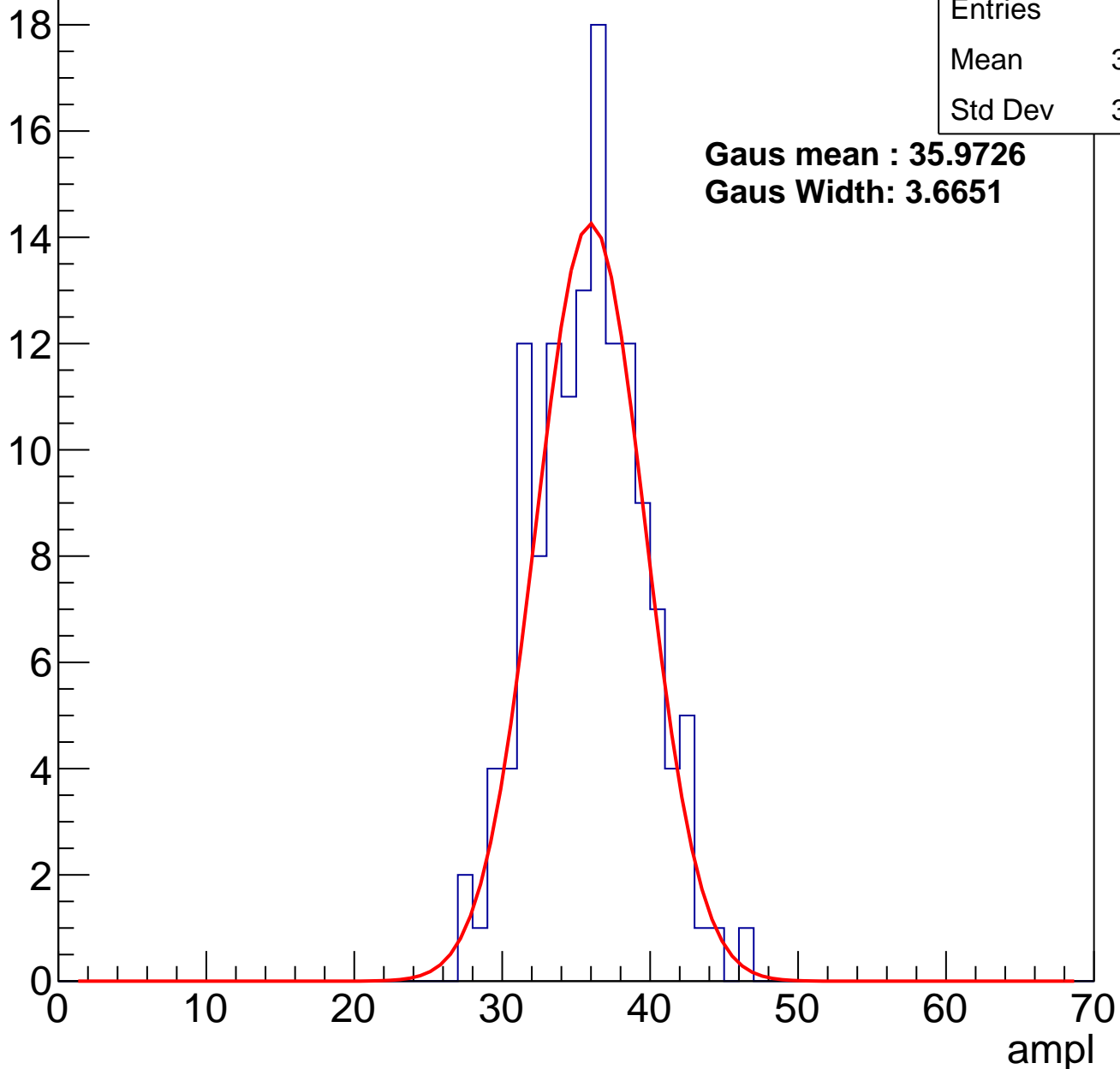
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 137 |
| Mean | 35.45 |
| Std Dev | 3.689 |

Gaus mean : 35.9726

Gaus Width: 3.6651

Entry



B1L001S, U26-ch29, adc2

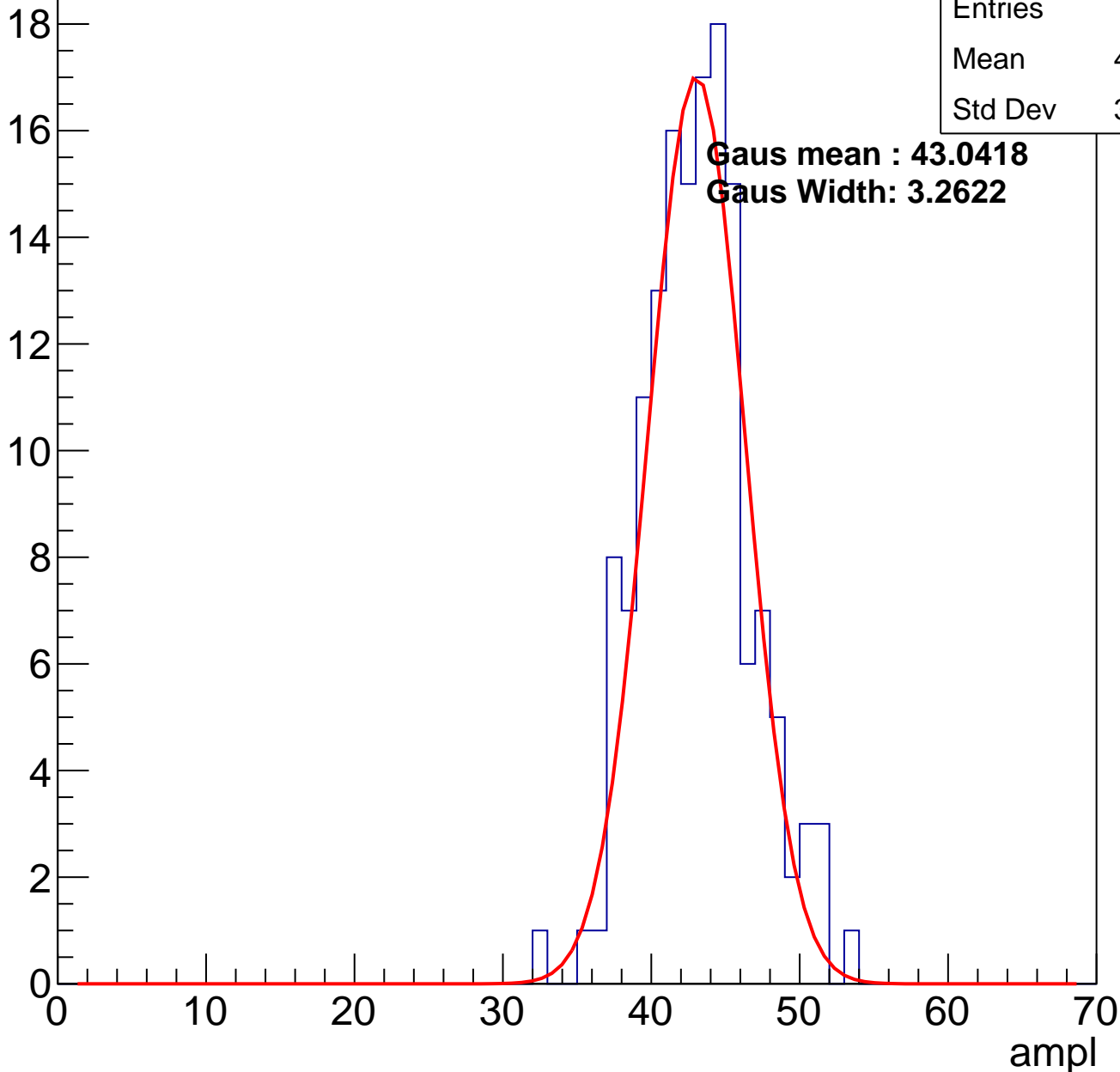
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 42.65 |
| Std Dev | 3.587 |

Gaus mean : 43.0418

Gaus Width: 3.2622

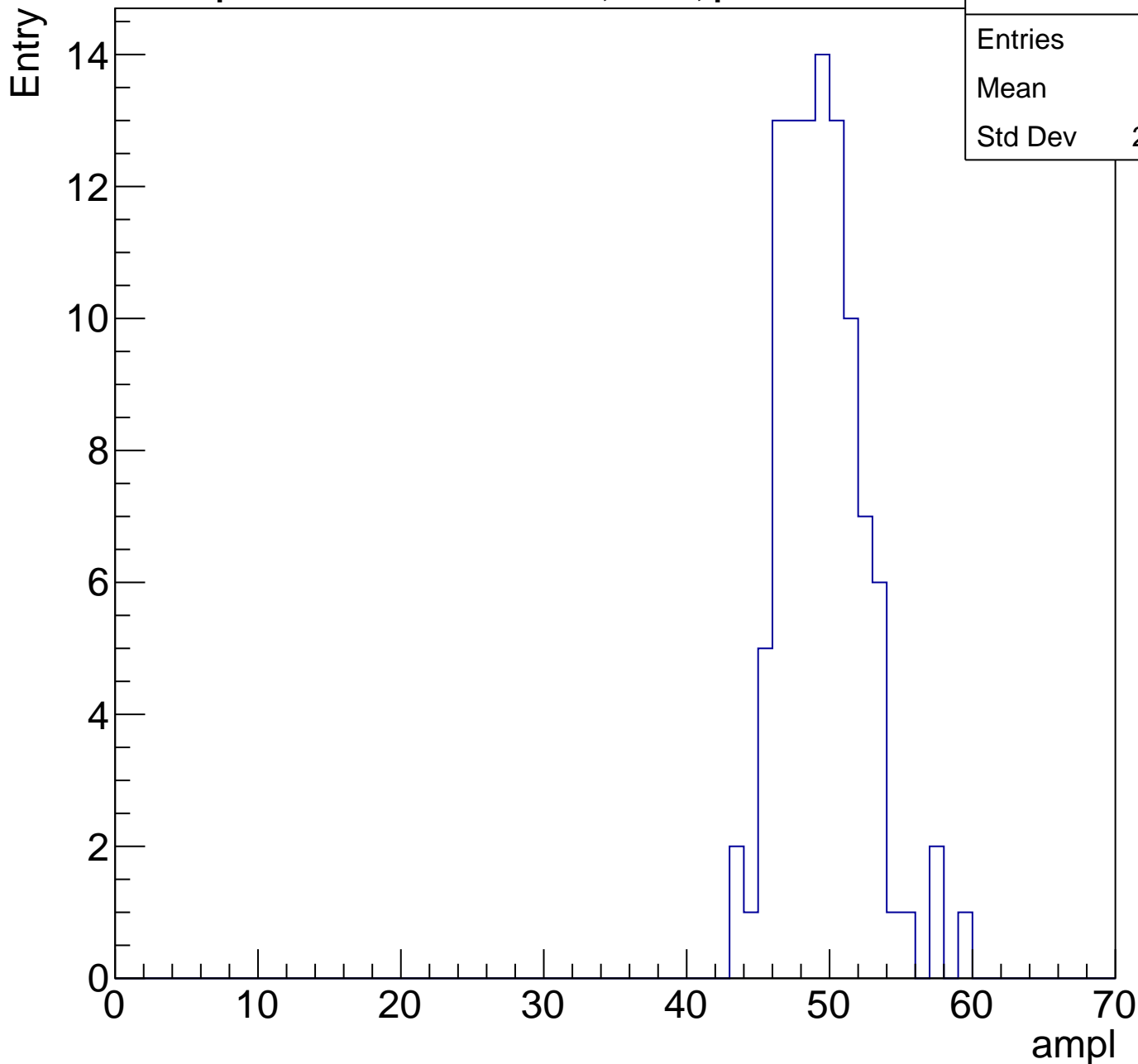
Entry



B1L001S, U26-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

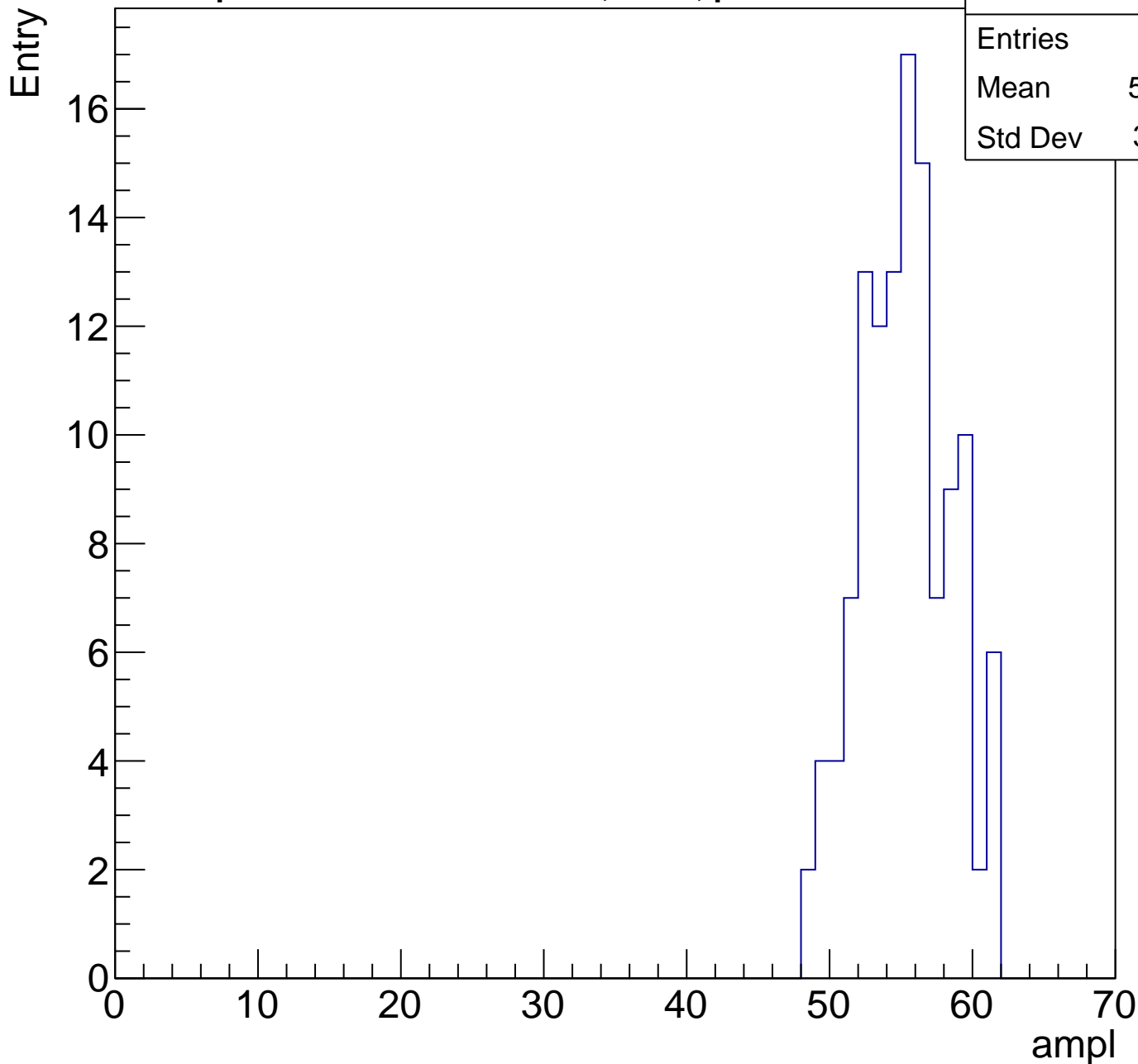
| | |
|---------|-------|
| Entries | 102 |
| Mean | 49 |
| Std Dev | 2.904 |



B1L001S, U26-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 54.83 |
| Std Dev | 3.121 |



B1L001S, U26-ch29, adc5

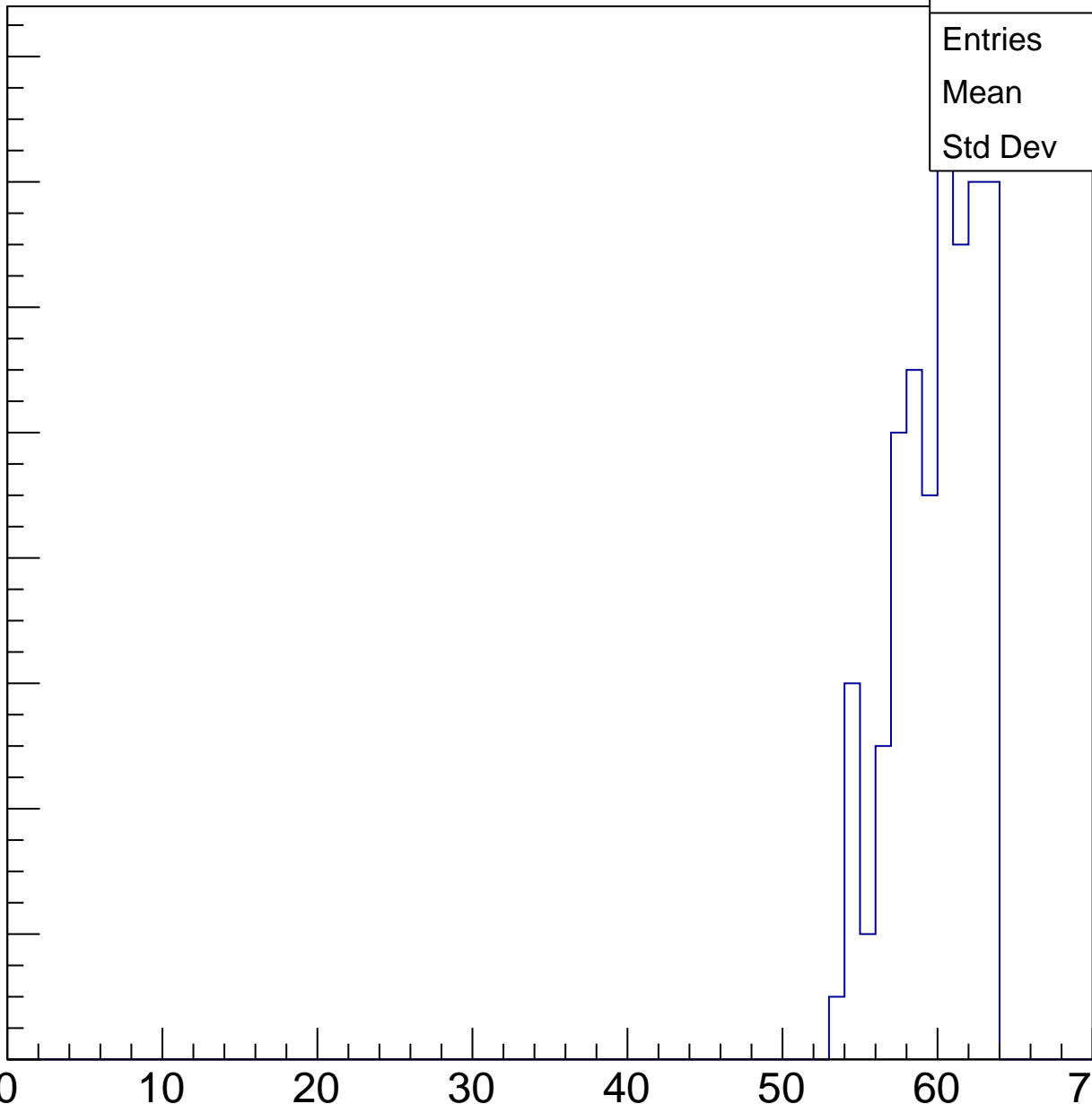
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 101 |
| Mean | 59.5 |
| Std Dev | 2.646 |

ampl

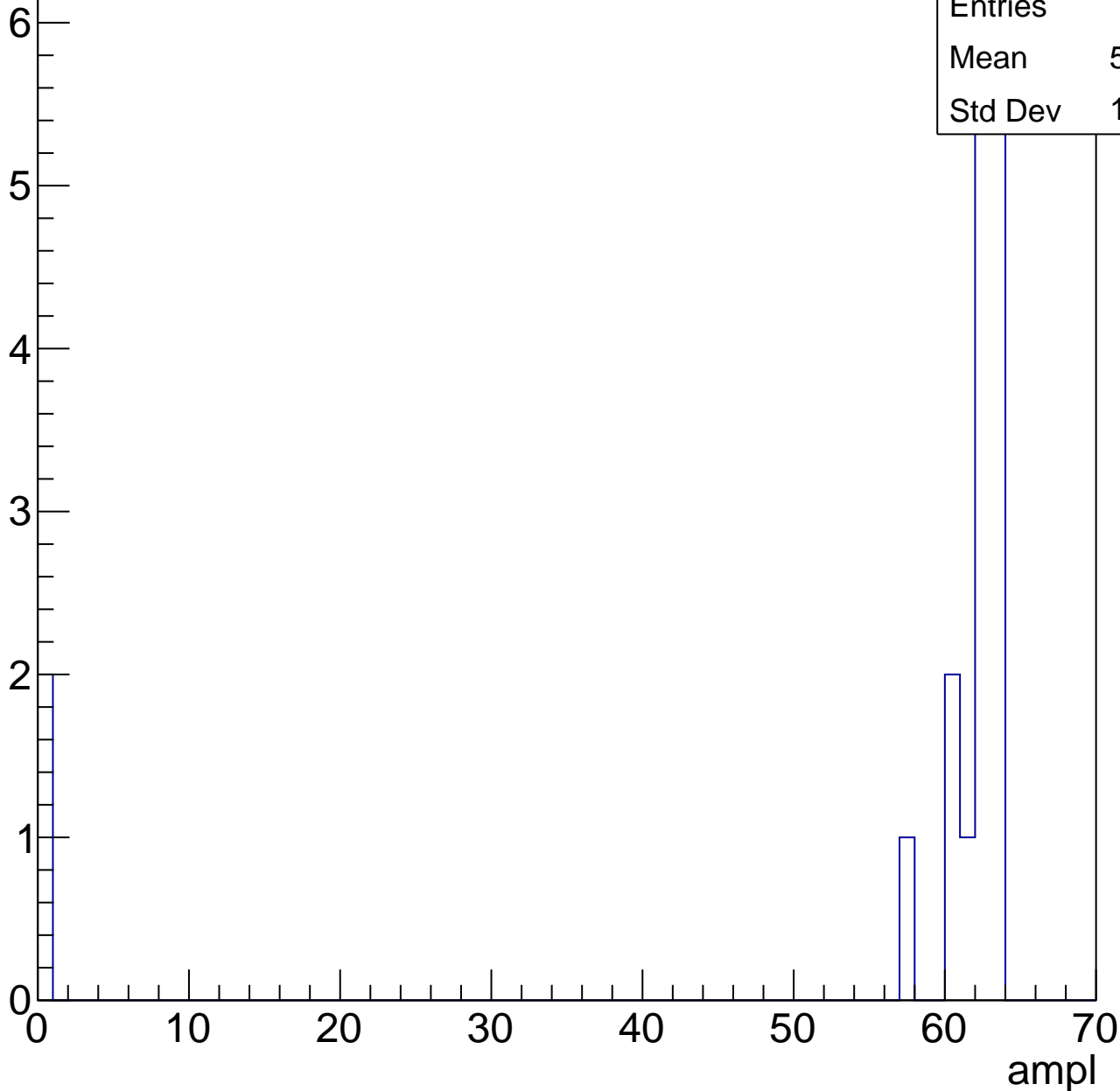


B1L001S, U26-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 54.89 |
| Std Dev | 19.46 |



B1L001S, U26-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch30, adc0

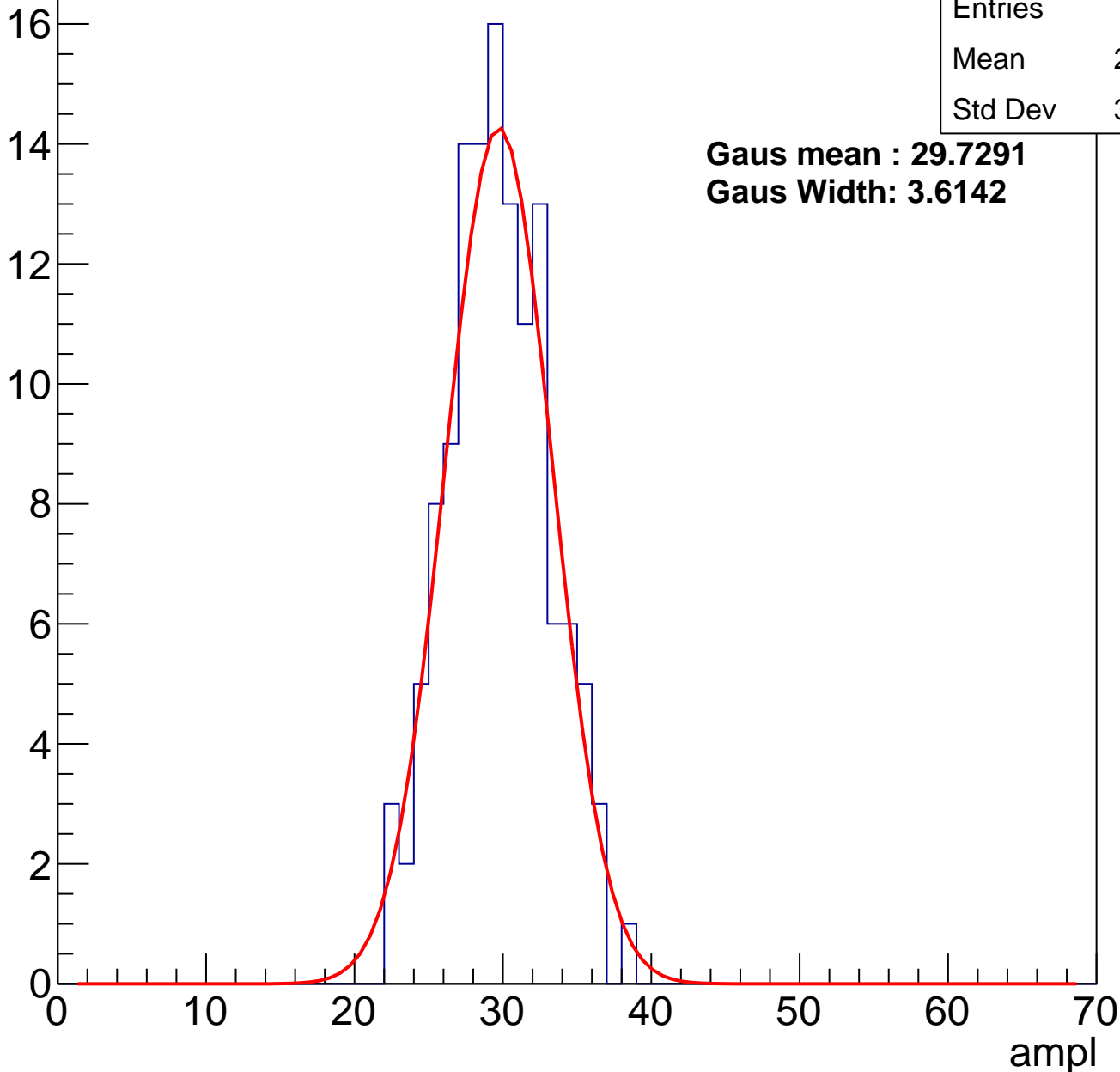
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 29.22 |
| Std Dev | 3.346 |

Gaus mean : 29.7291

Gaus Width: 3.6142

Entry



B1L001S, U26-ch30, adc1

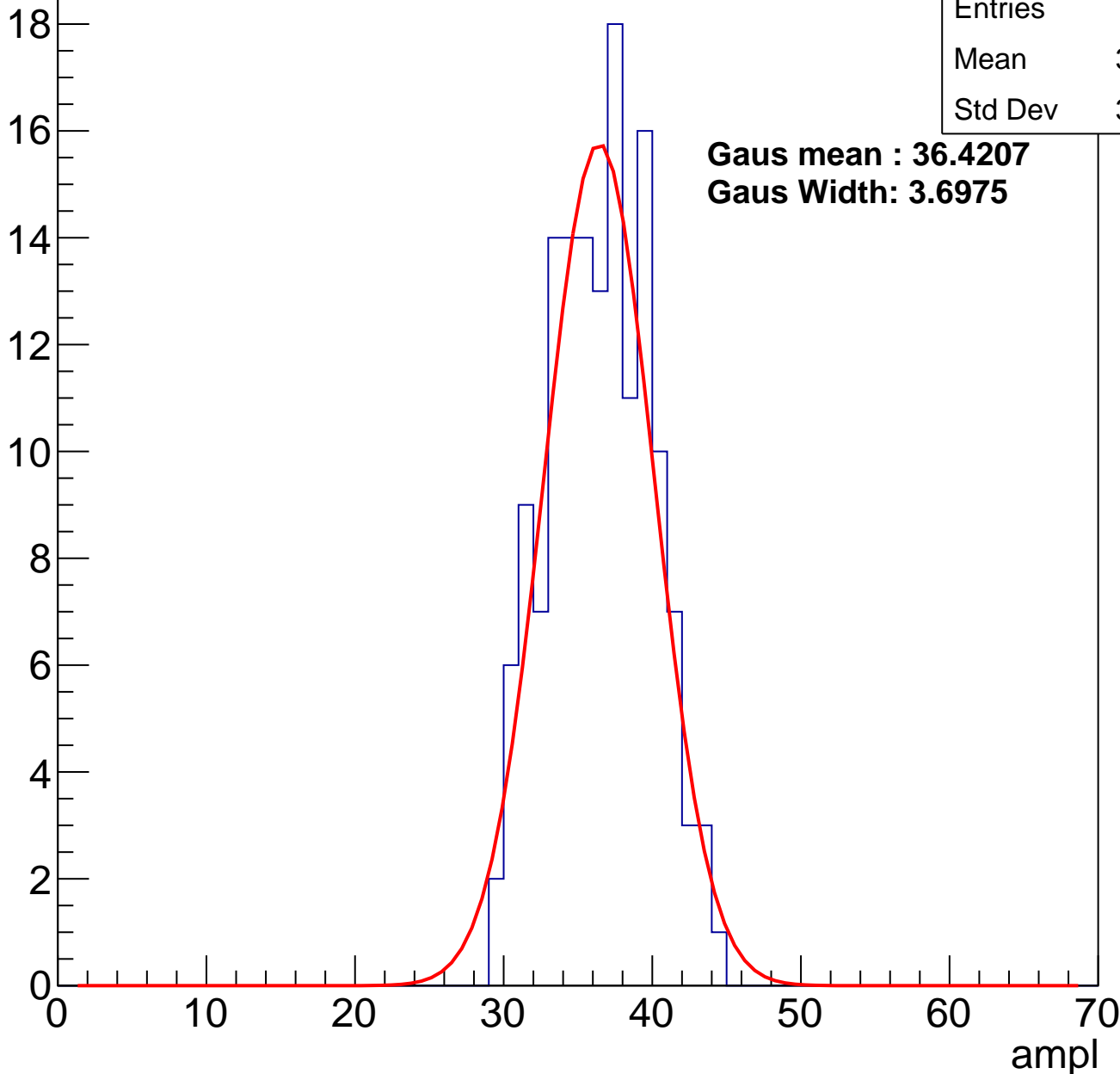
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 36.02 |
| Std Dev | 3.396 |

Gaus mean : 36.4207

Gaus Width: 3.6975

Entry



B1L001S, U26-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

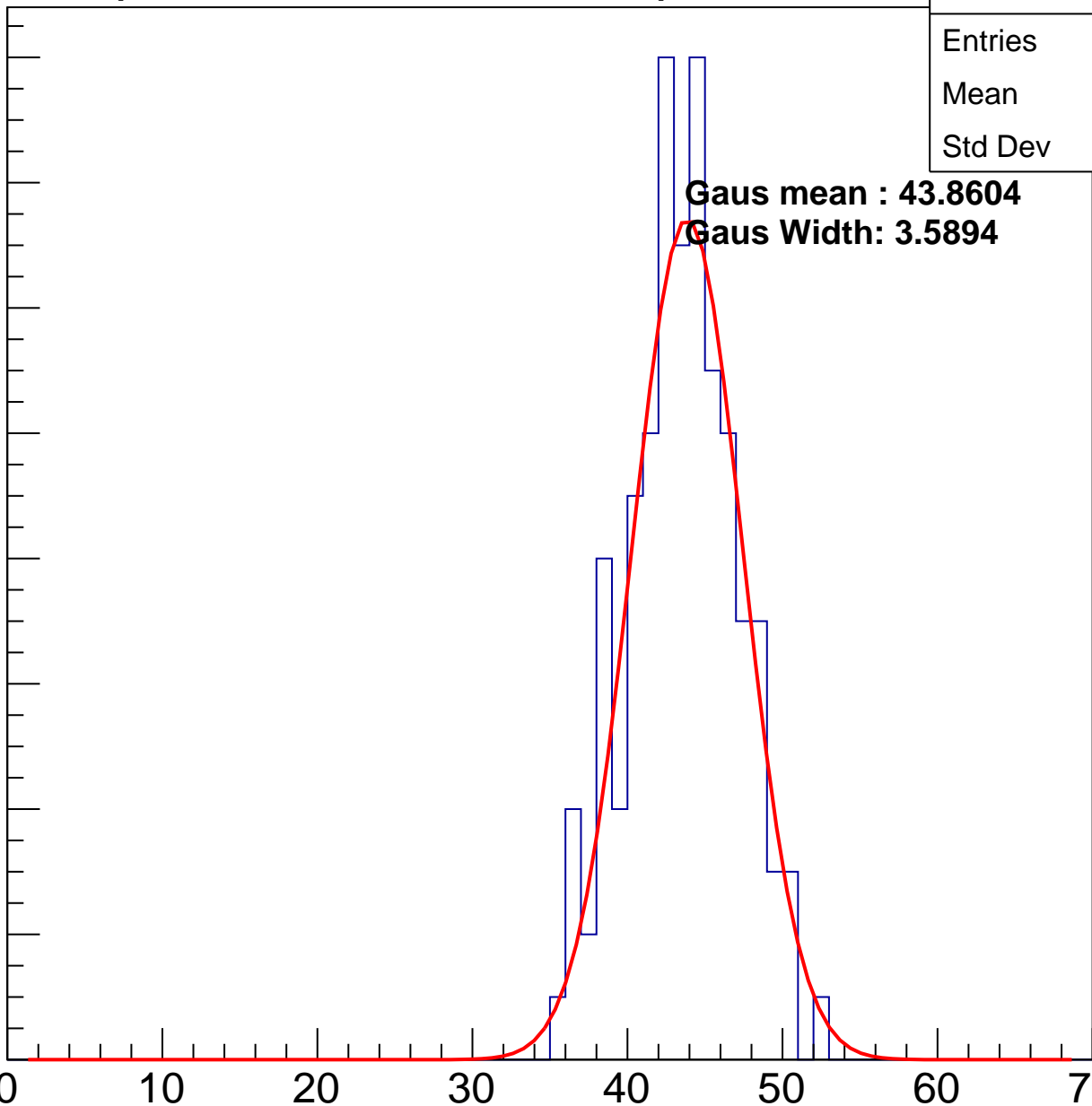
16
14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 125 |
| Mean | 43.1 |
| Std Dev | 3.47 |

Gaus mean : 43.8604

Gaus Width: 3.5894

ampl



B1L001S, U26-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

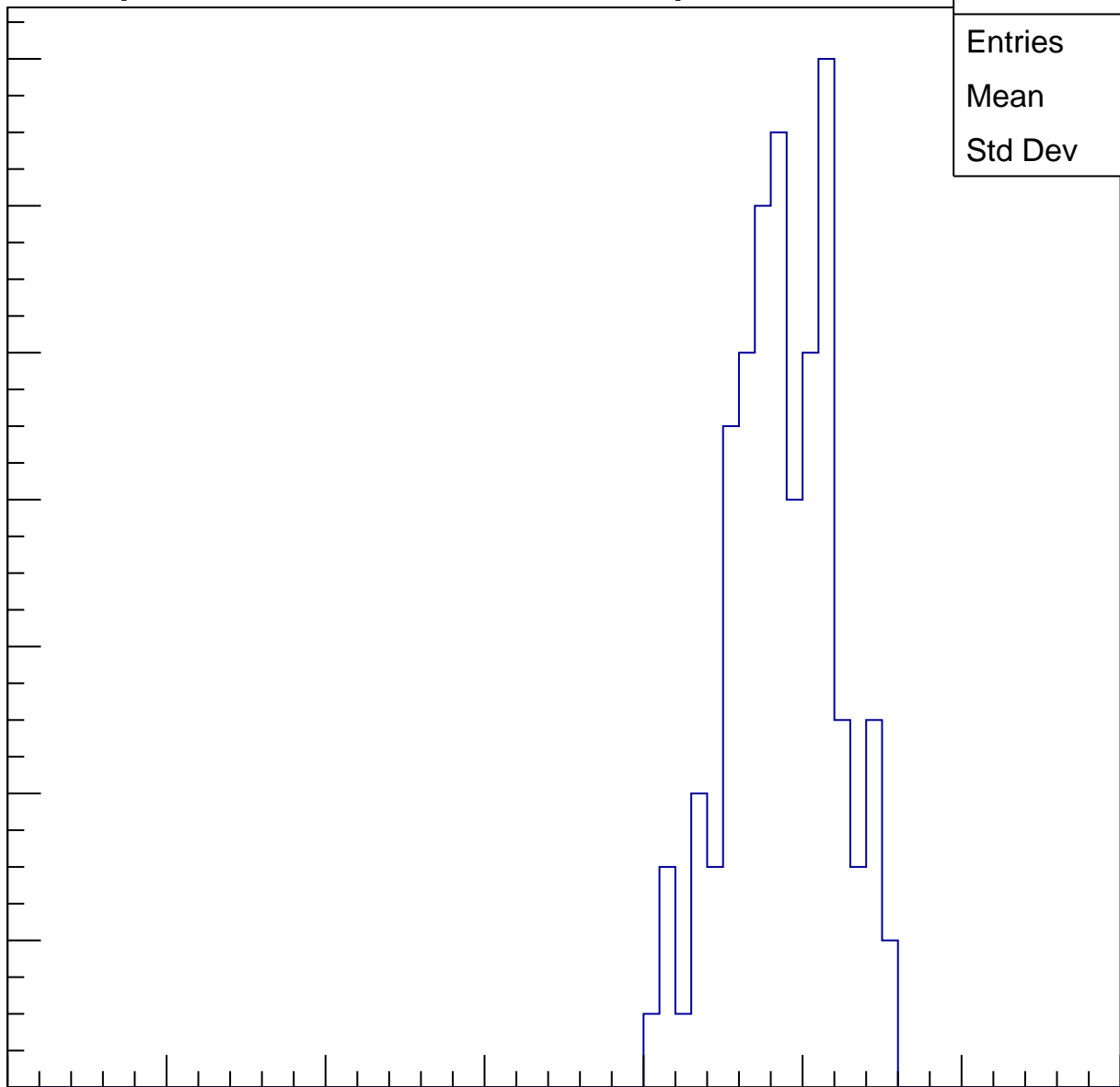
| | |
|---------|-------|
| Entries | 103 |
| Mean | 48.22 |
| Std Dev | 3.312 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

145

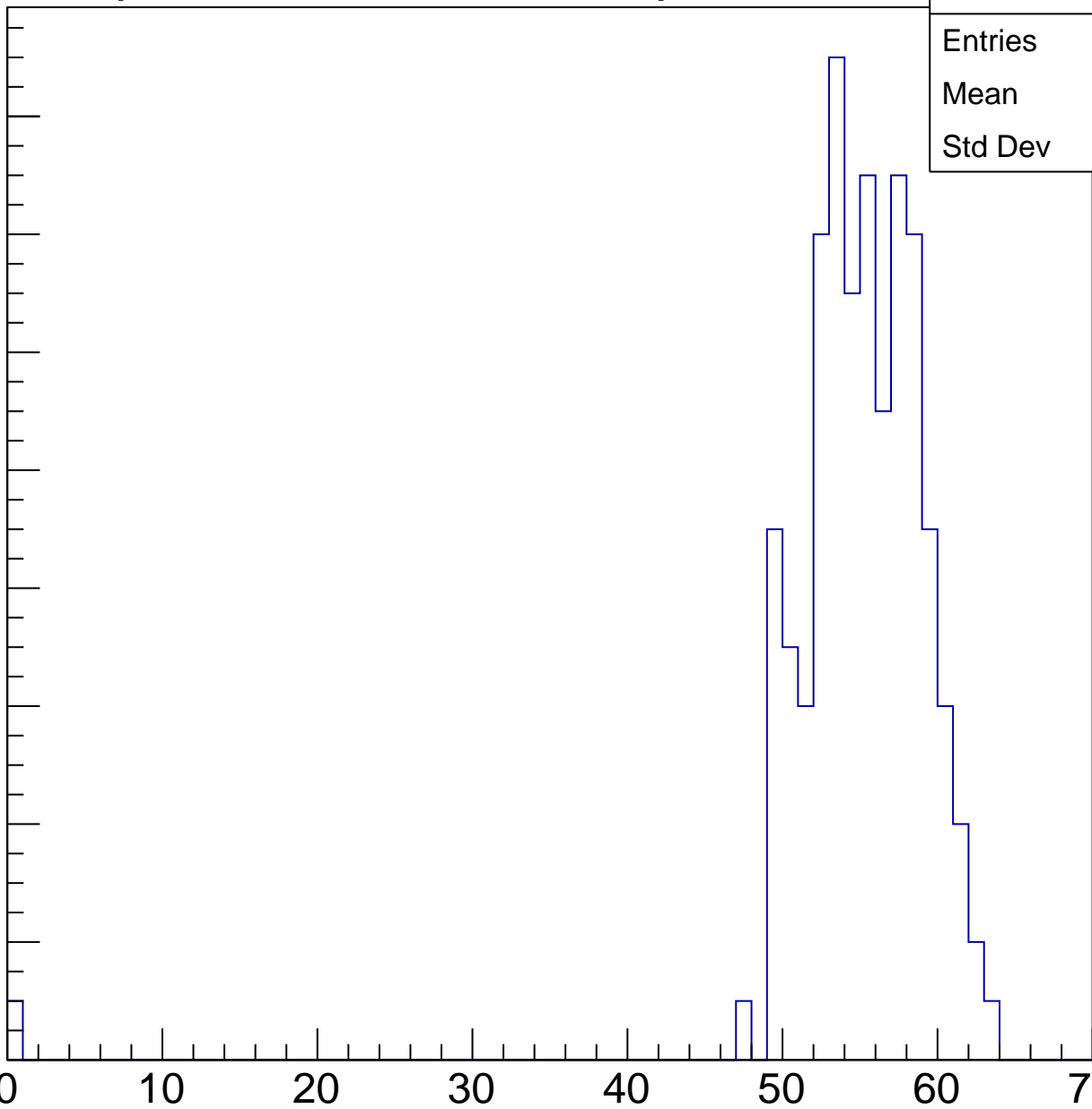
Mean

54.52

Std Dev

5.666

ampl



B1L001S, U26-ch30, adc5

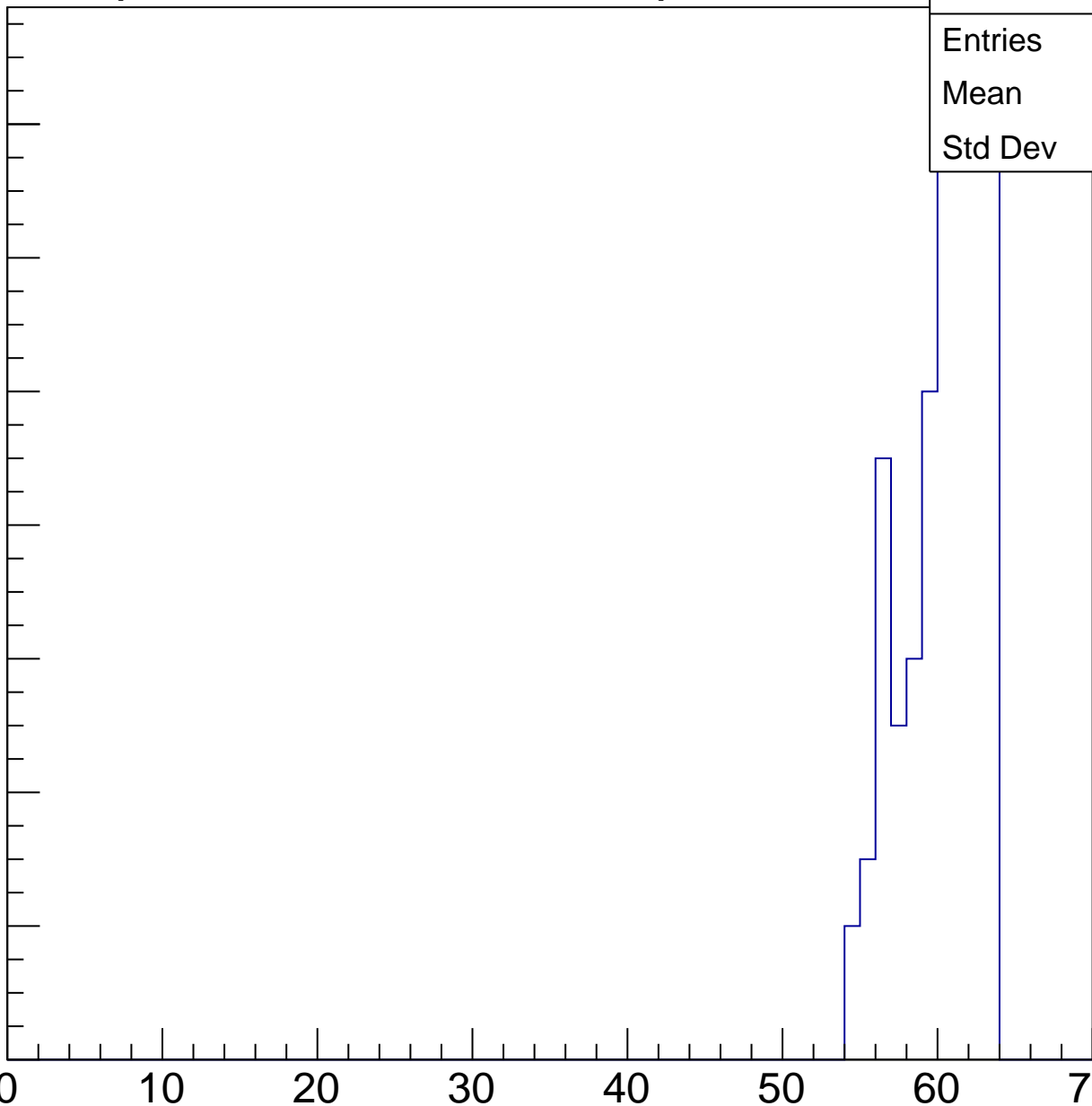
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 92 |
| Mean | 59.85 |
| Std Dev | 2.484 |

ampl

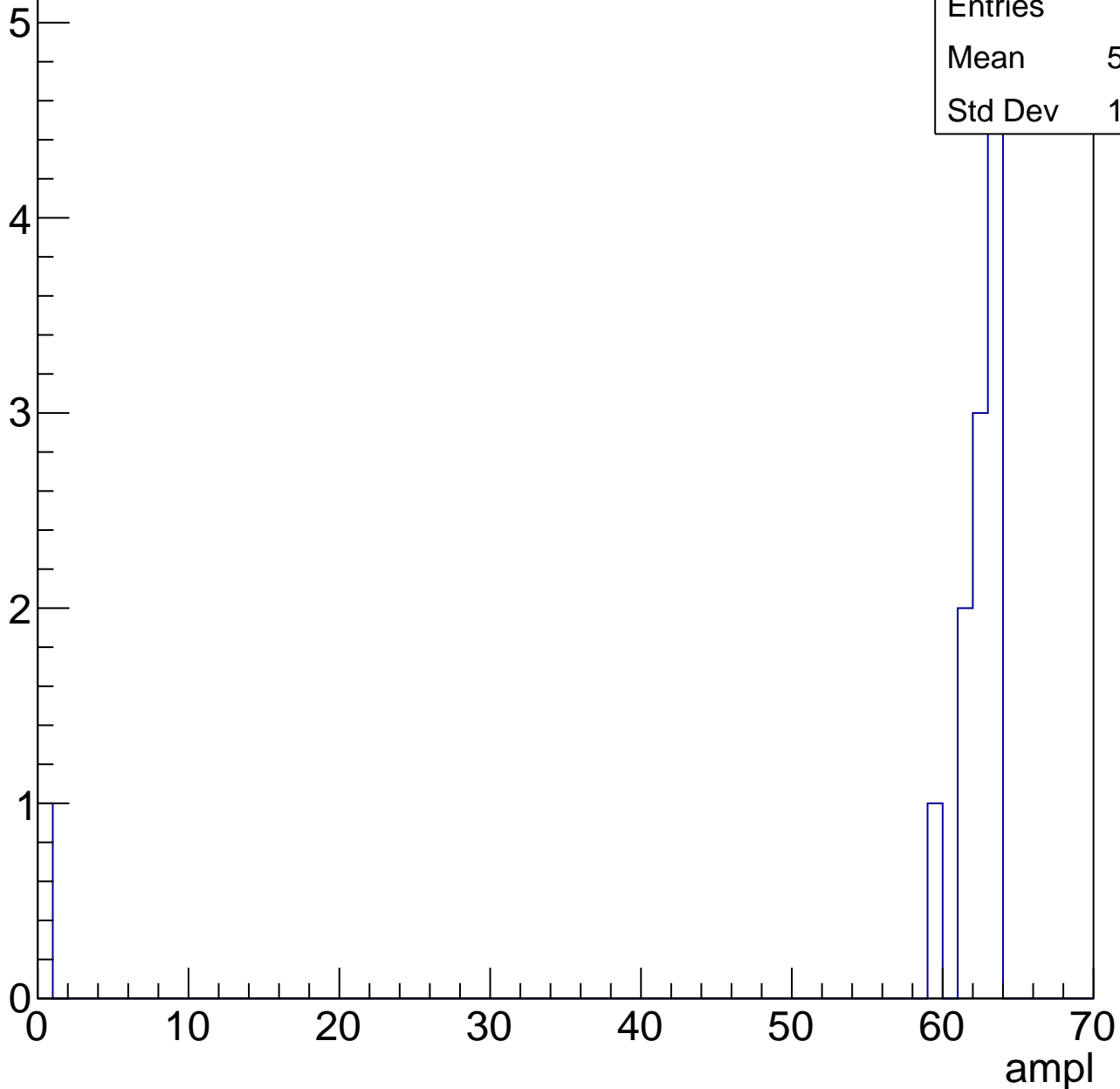


B1L001S, U26-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 56.83 |
| Std Dev | 17.17 |



B1L001S, U26-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

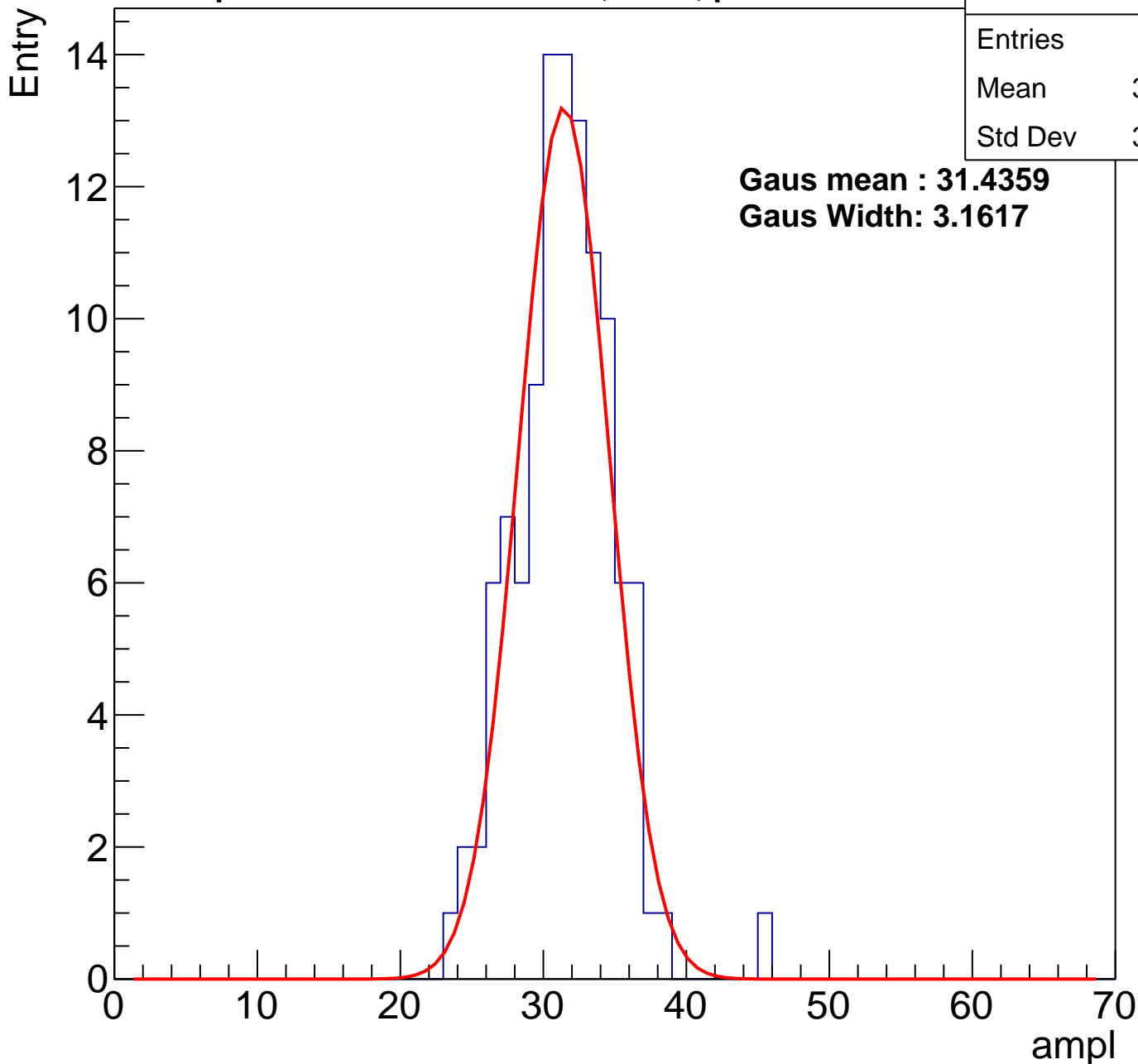
B1L001S, U26-ch31, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 110 |
| Mean | 31.04 |
| Std Dev | 3.403 |

Gaus mean : 31.4359

Gaus Width: 3.1617



B1L001S, U26-ch31, adc1

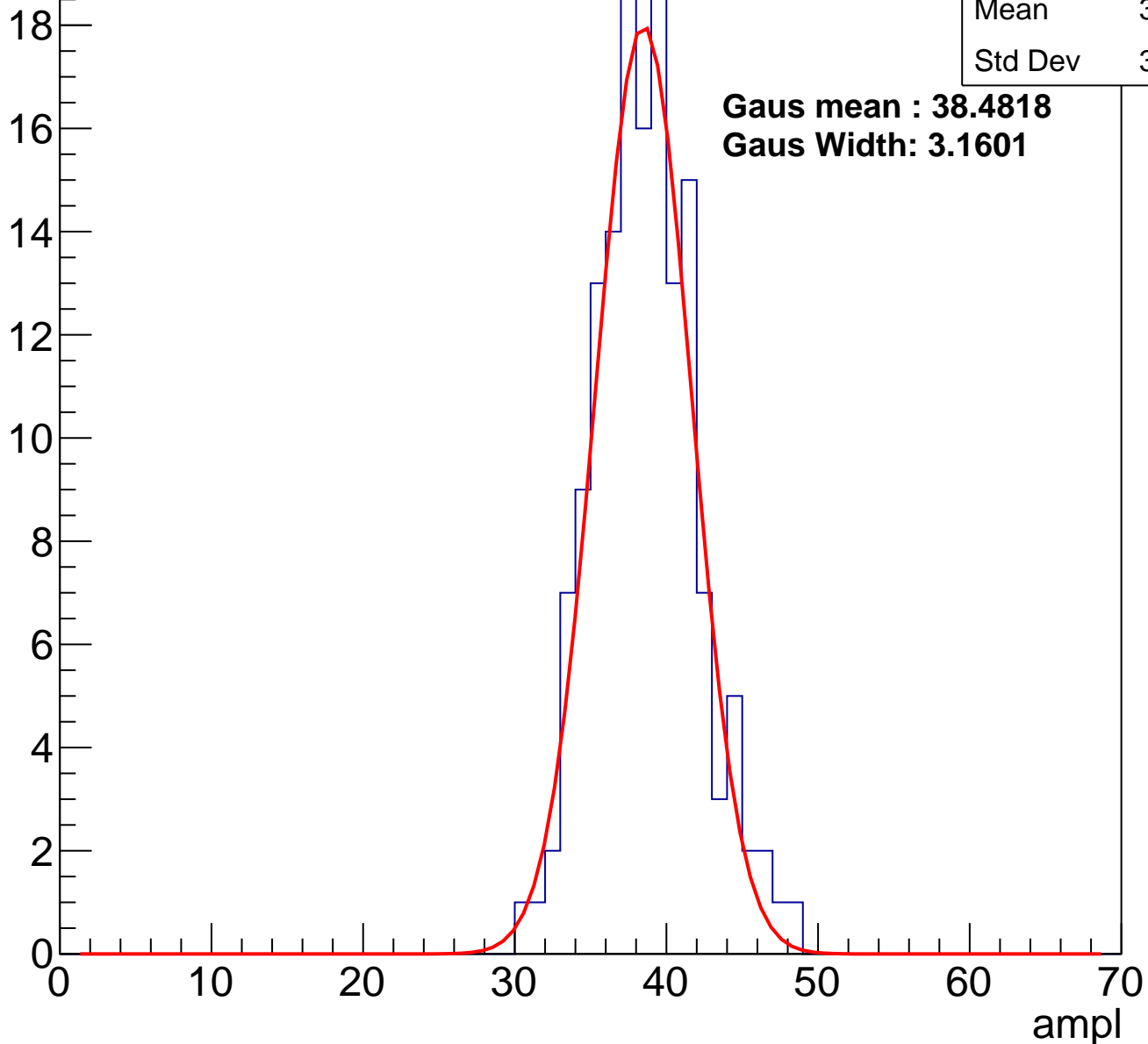
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 38.19 |
| Std Dev | 3.337 |

Gaus mean : 38.4818

Gaus Width: 3.1601

Entry



B1L001S, U26-ch31, adc2

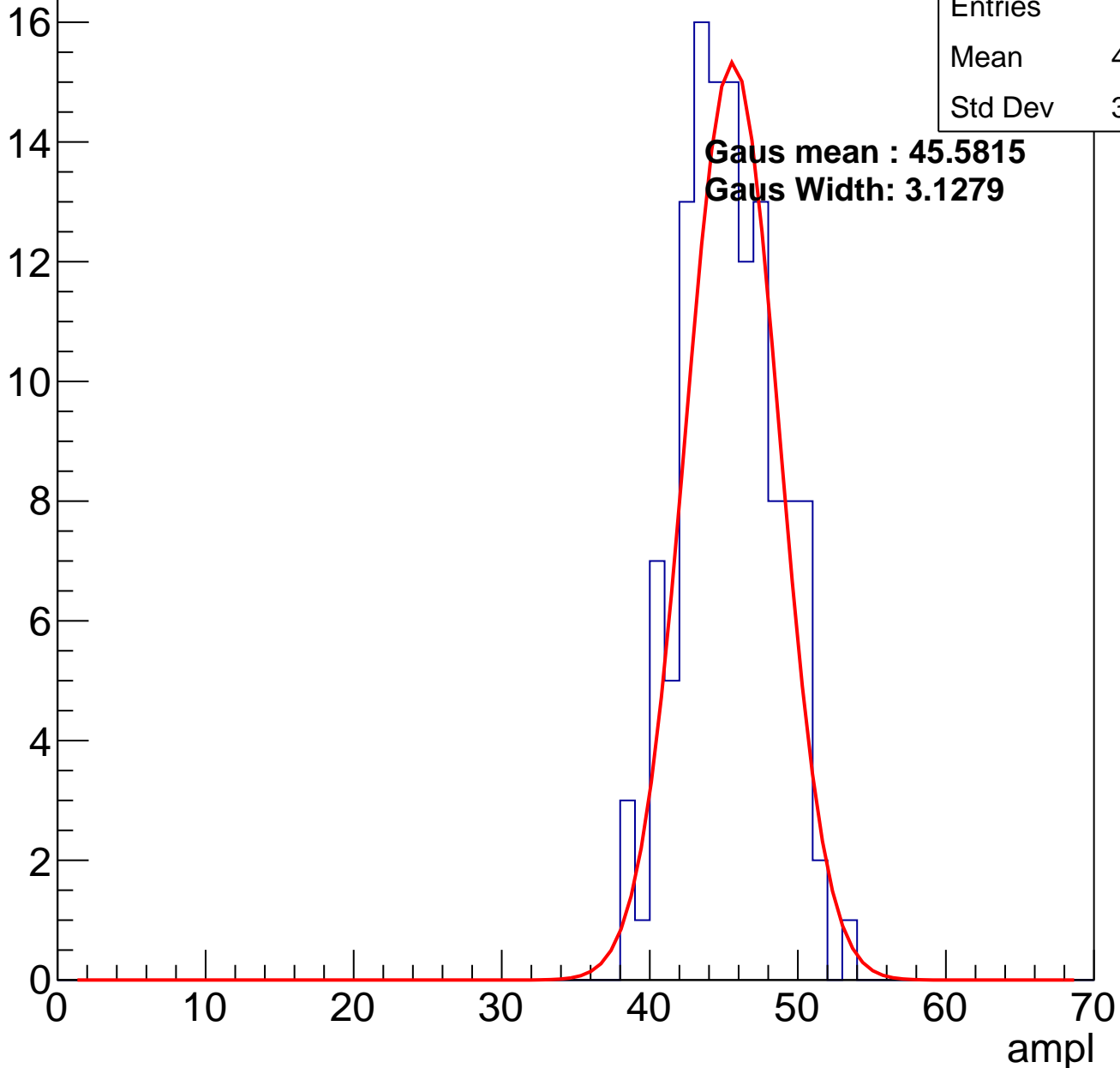
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 44.89 |
| Std Dev | 3.115 |

Gaus mean : 45.5815

Gaus Width: 3.1279

Entry



B1L001S, U26-ch31, adc3

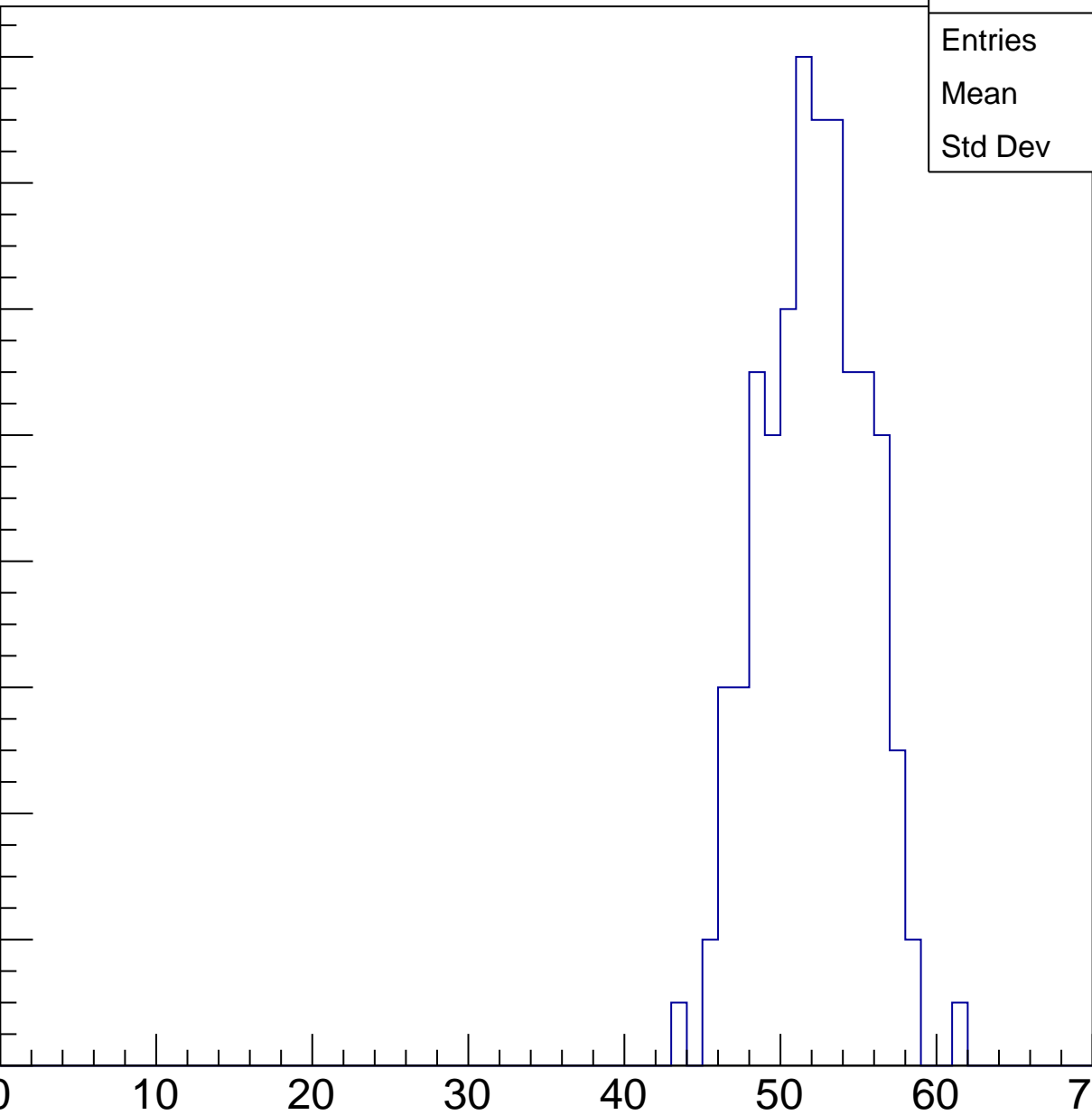
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 51.65 |
| Std Dev | 3.295 |

ampl



B1L001S, U26-ch31, adc4

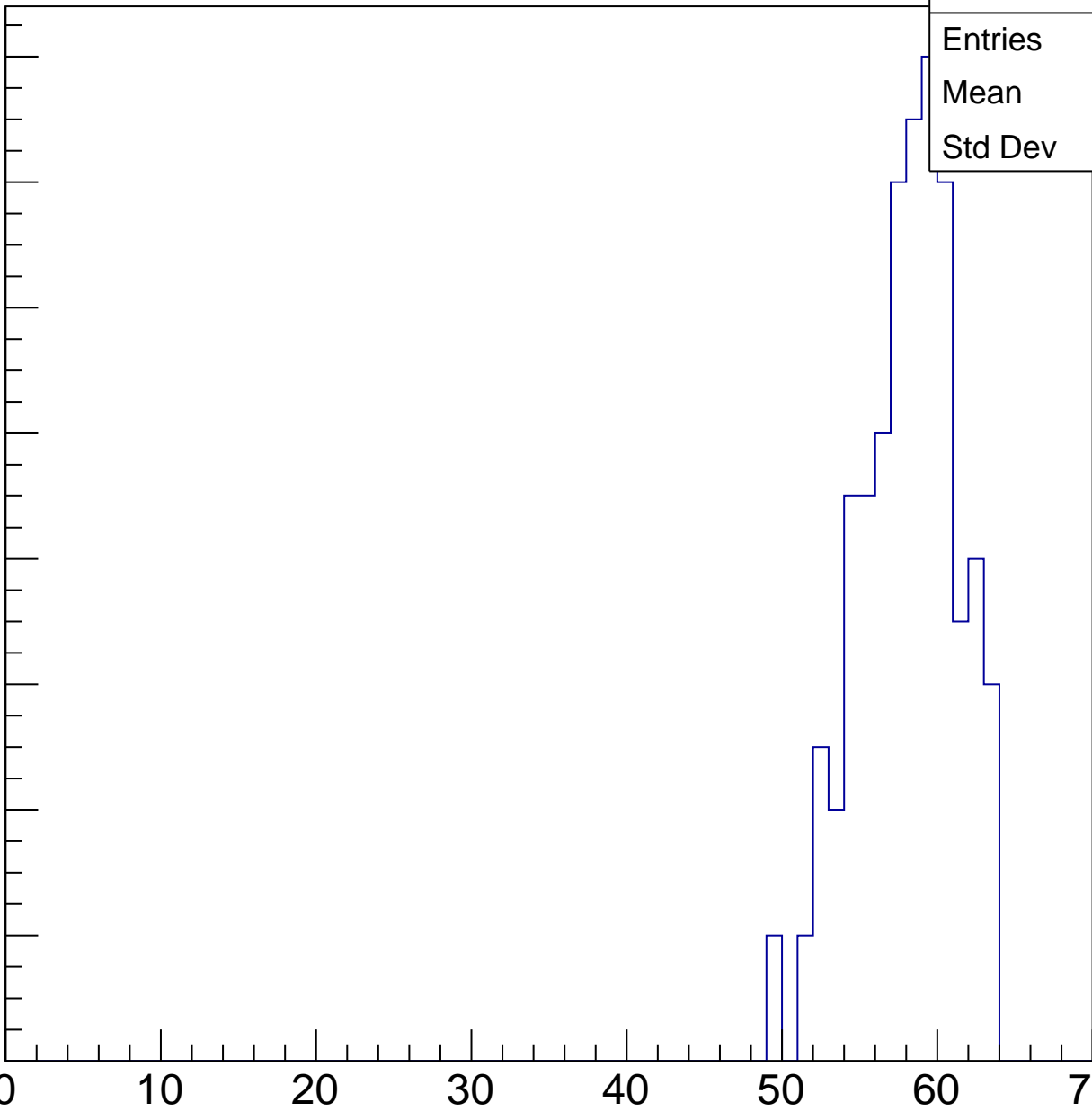
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 121 |
| Mean | 57.57 |
| Std Dev | 3.162 |

ampl



B1L001S, U26-ch31, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

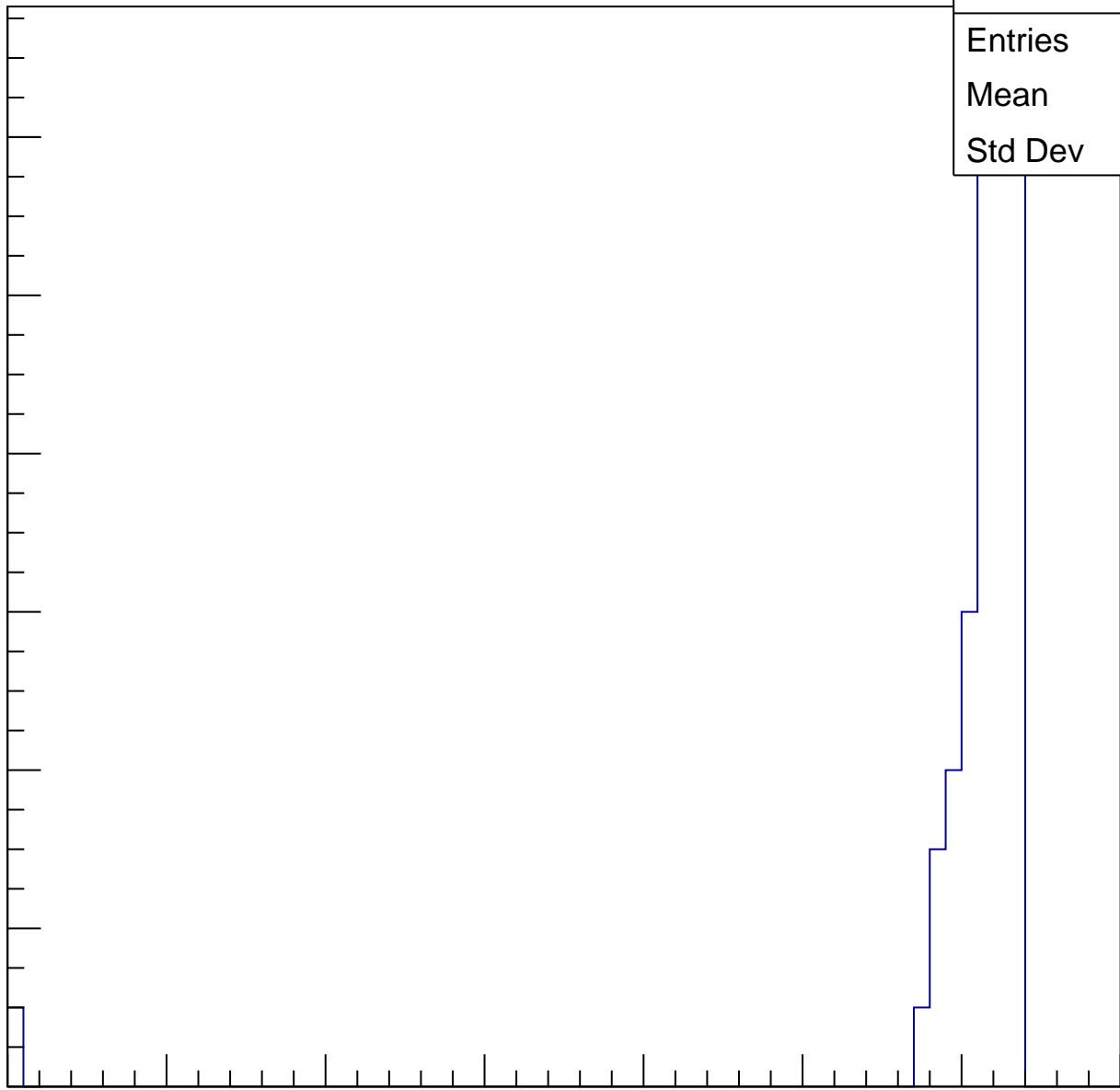
2

0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 60.04 |
| Std Dev | 8.551 |

ampl

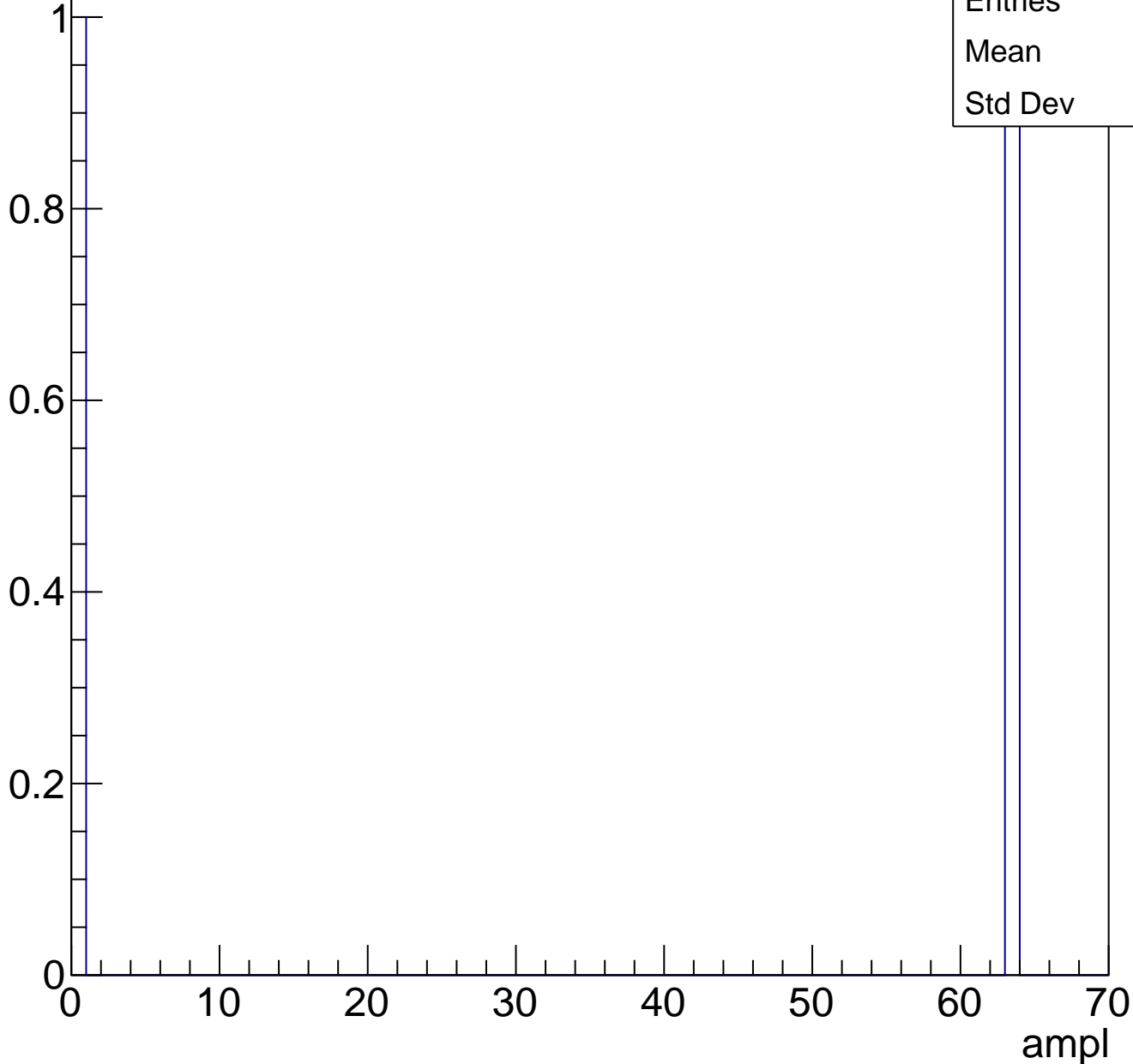
0 10 20 30 40 50 60 70



B1L001S, U26-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

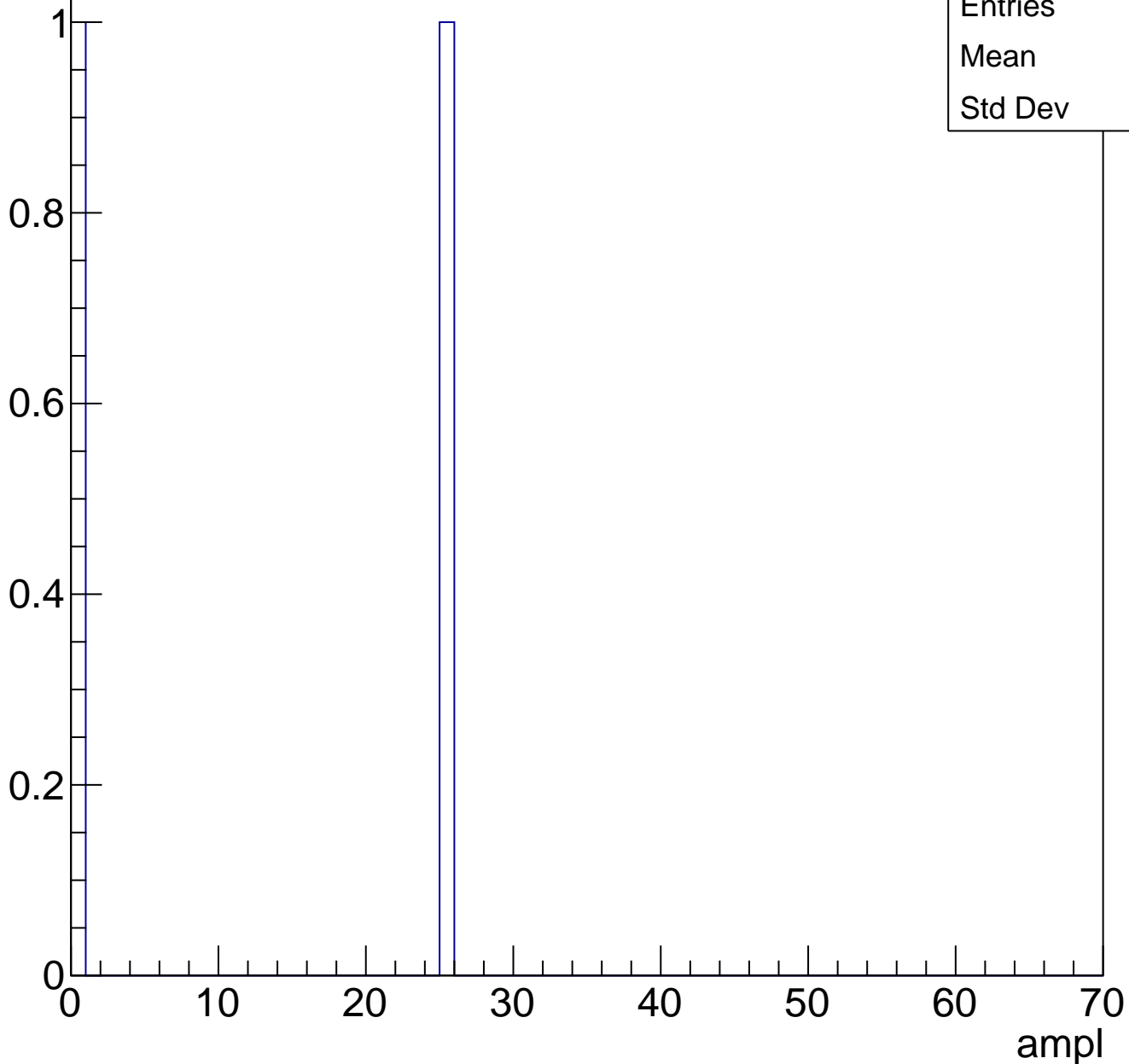
Entry



B1L001S, U26-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 12.5 |
| Std Dev | 12.5 |

B1L001S, U26-ch32, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|------|
| Entries | 140 |
| Mean | 30.4 |
| Std Dev | 3.34 |

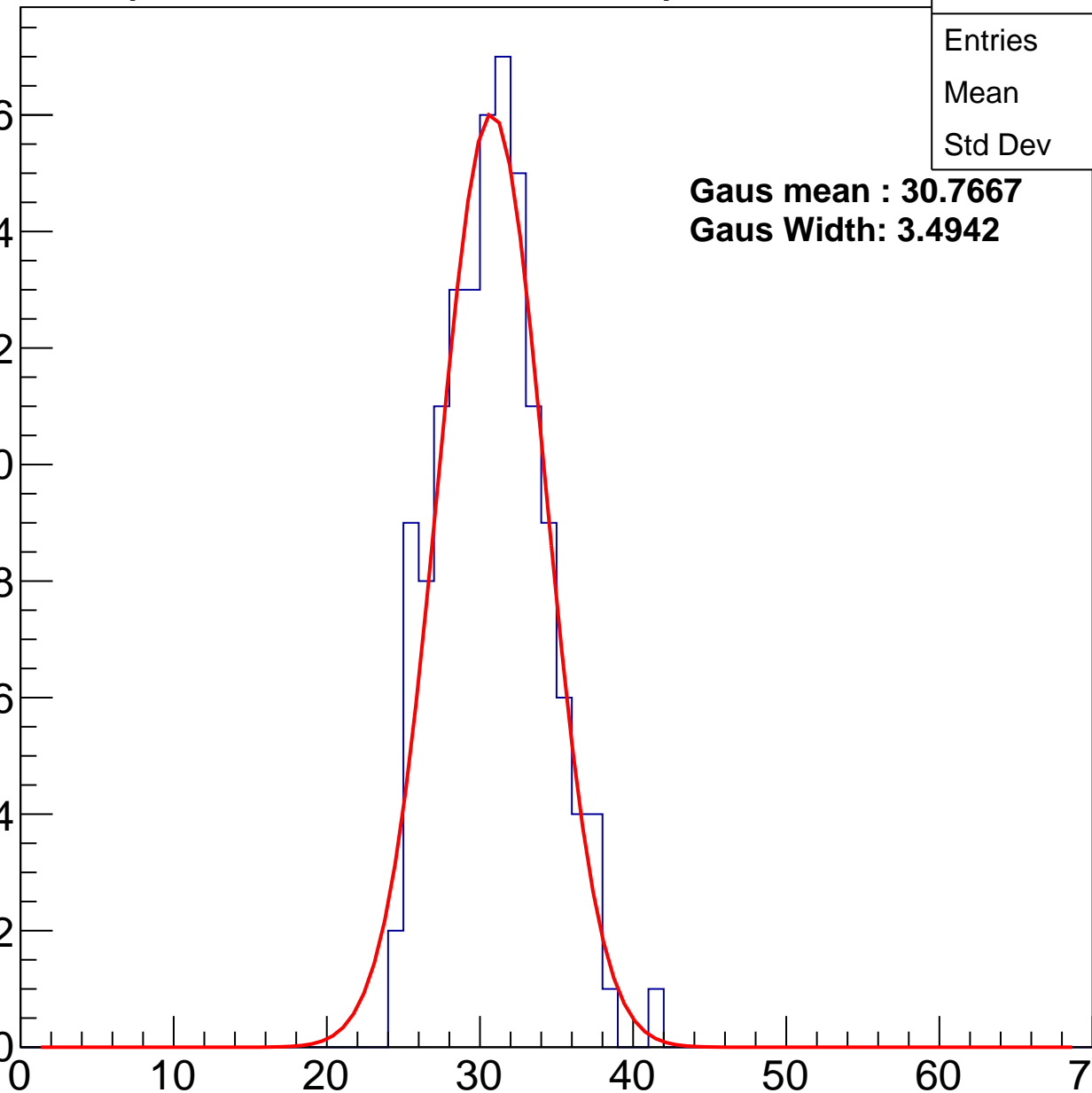
Gaus mean : 30.7667

Gaus Width: 3.4942

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.78 |
| Std Dev | 2.968 |

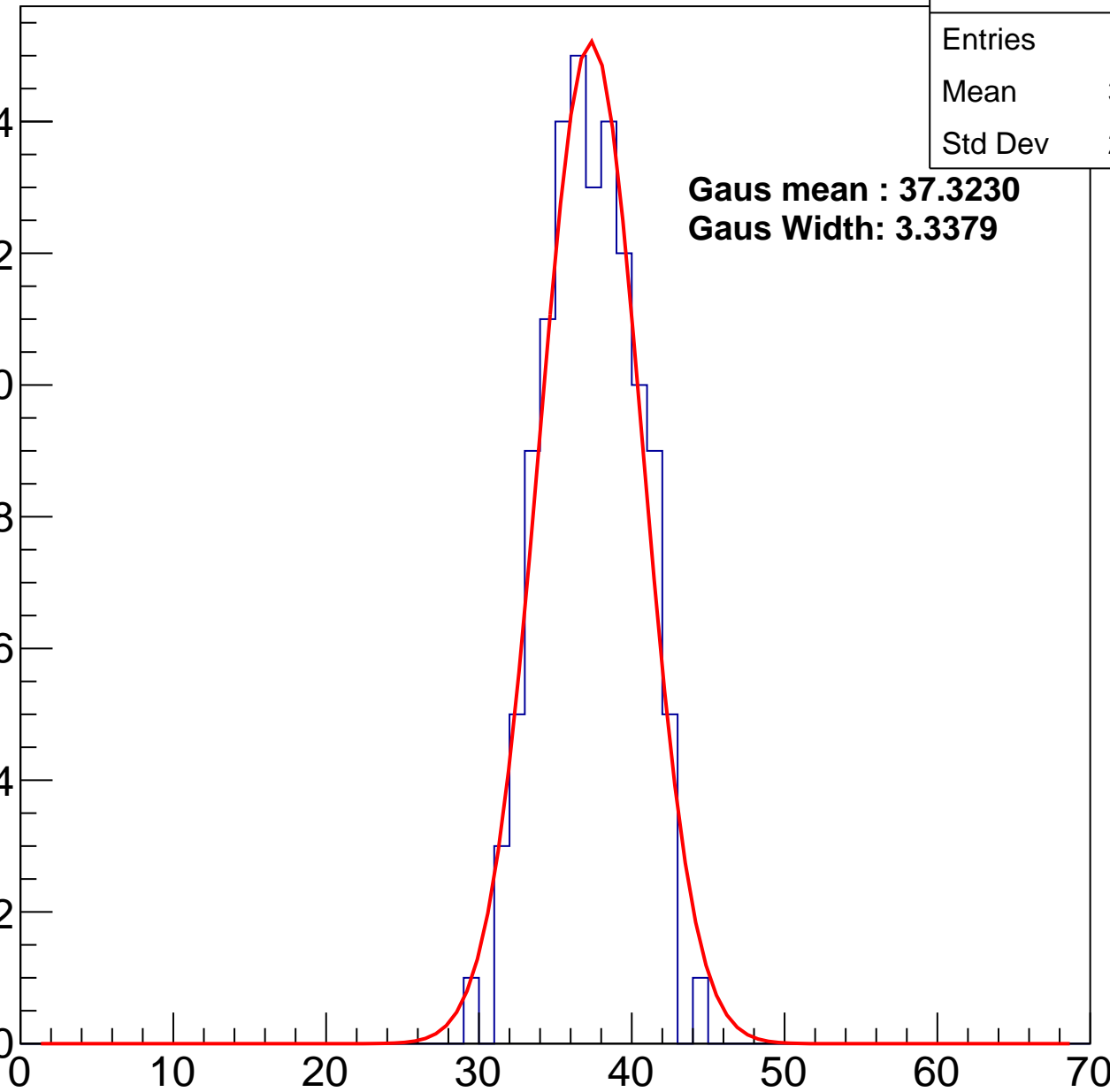
Gaus mean : 37.3230

Gaus Width: 3.3379

Entry

14
12
10
8
6
4
2
0

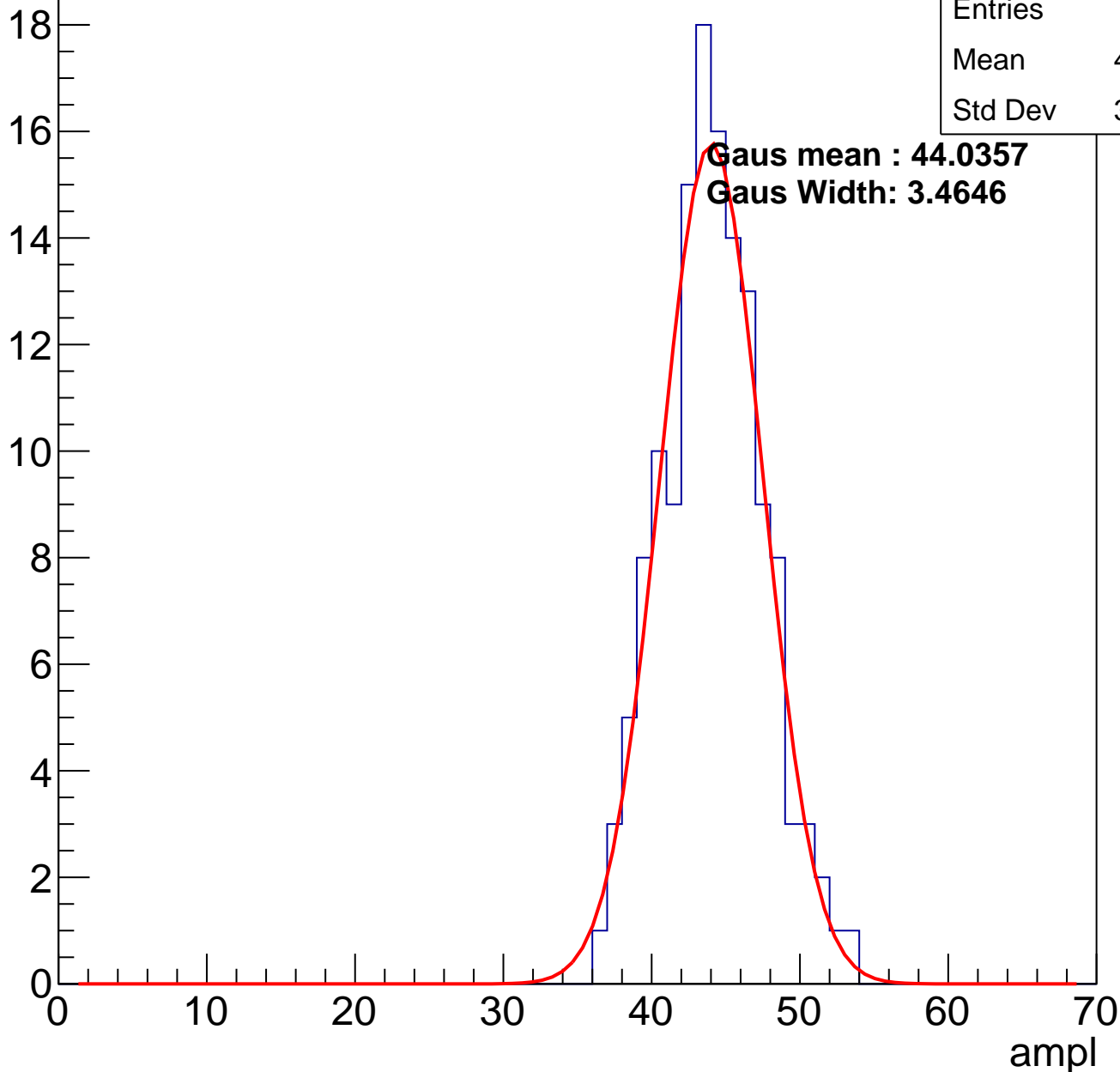
ampl



B1L001S, U26-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 50.25 |
| Std Dev | 3.388 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

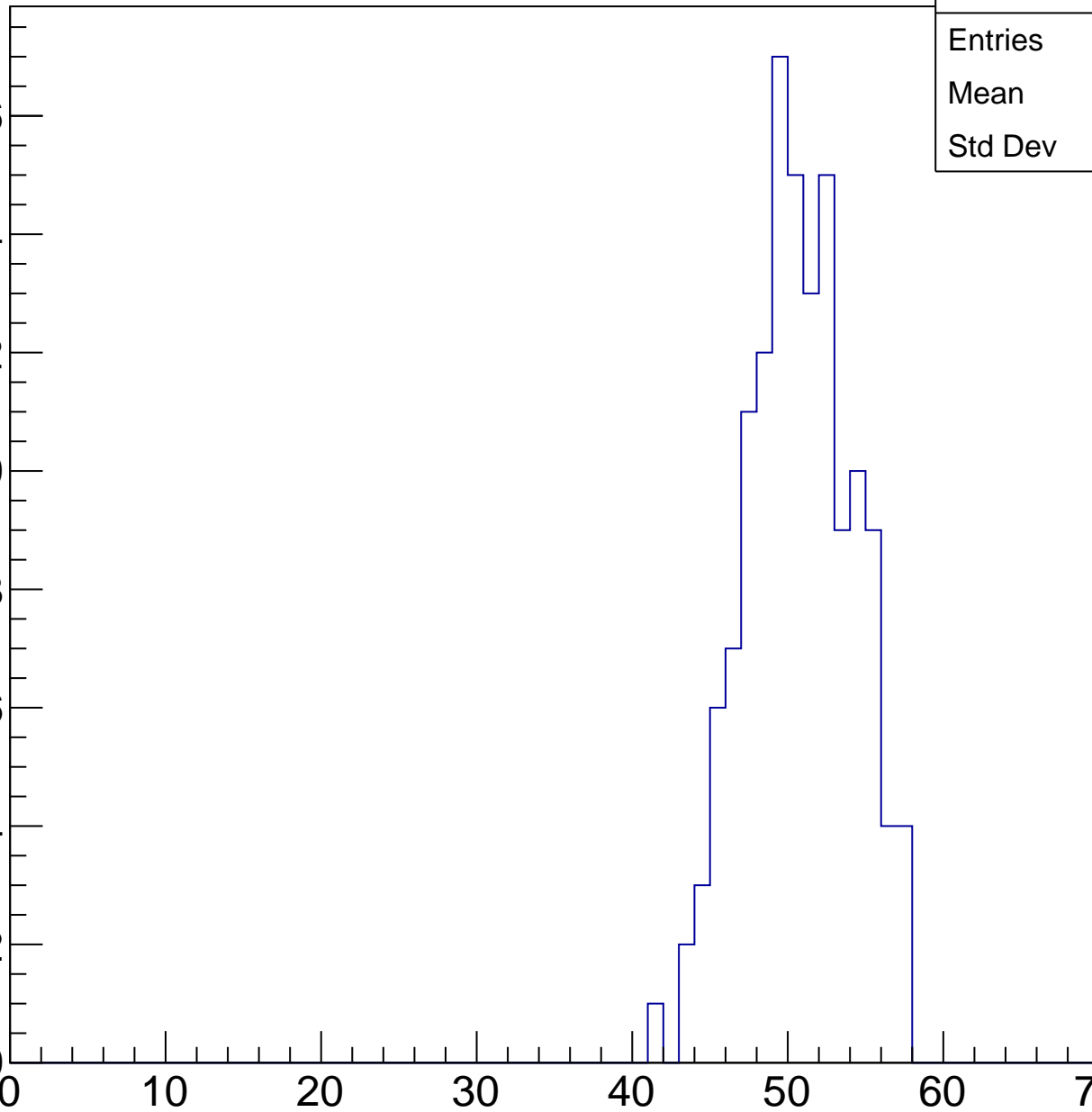
40

50

60

70

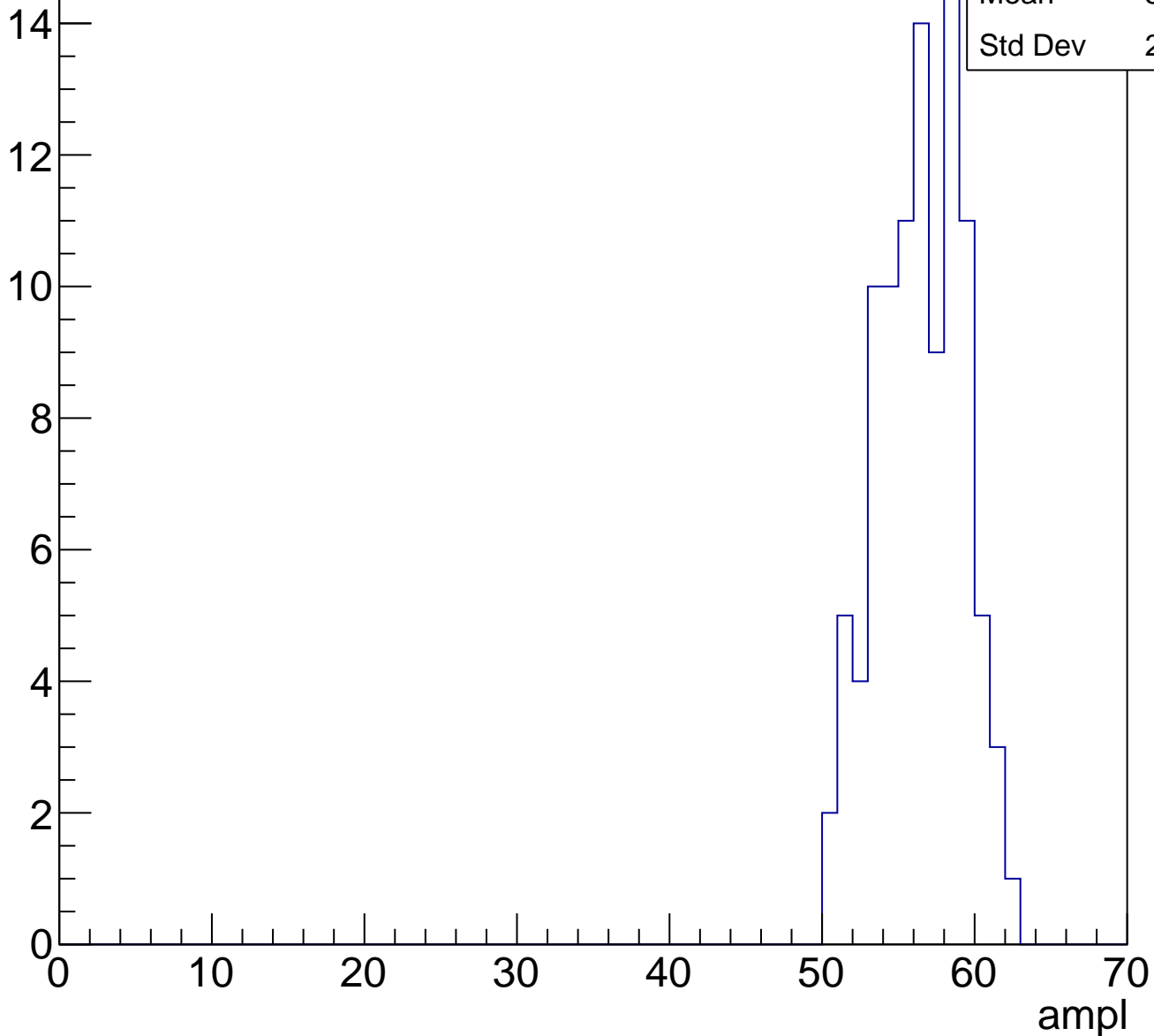
ampl



B1L001S, U26-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch32, adc5

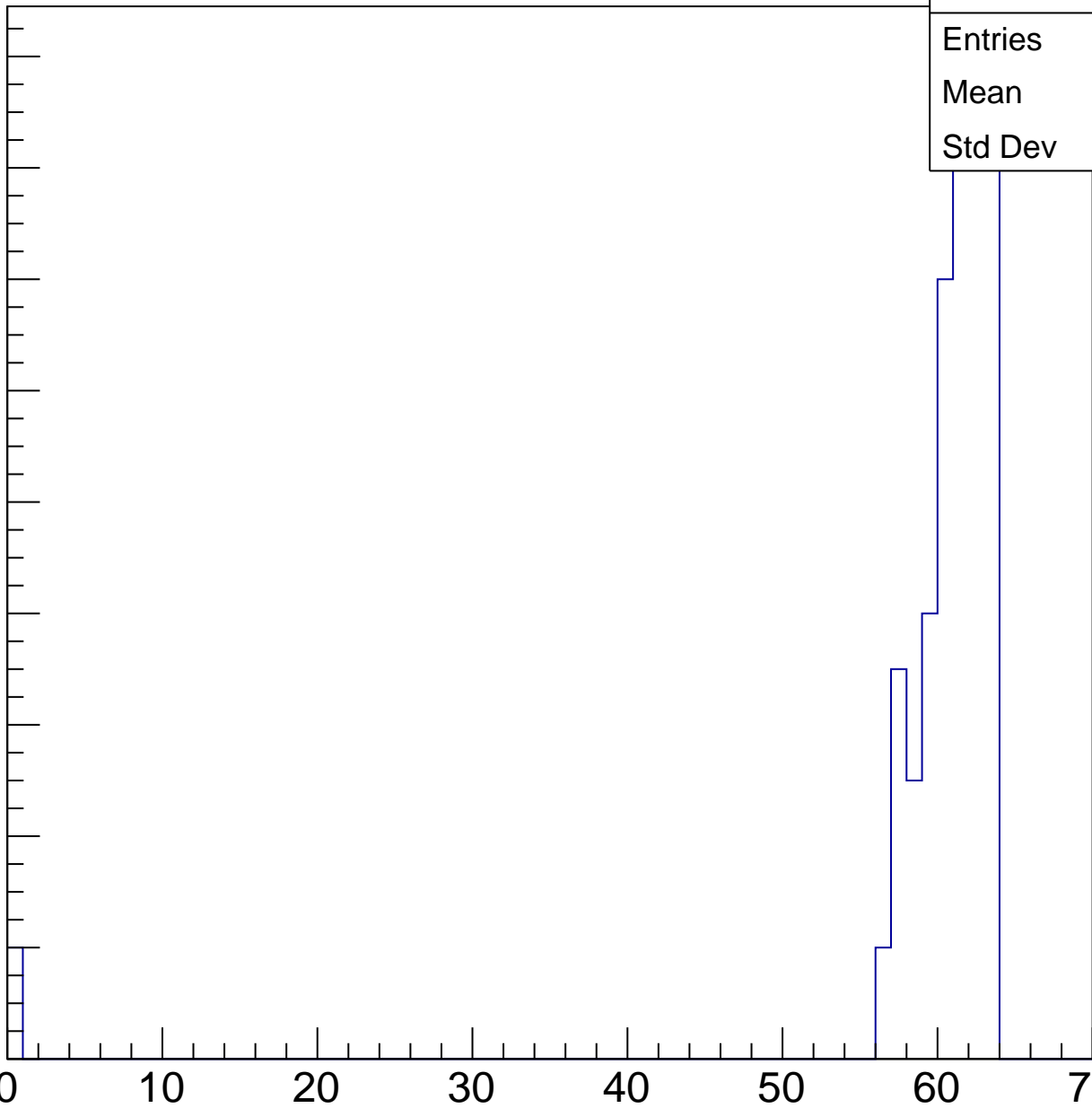
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 88 |
| Mean | 59.24 |
| Std Dev | 9.234 |

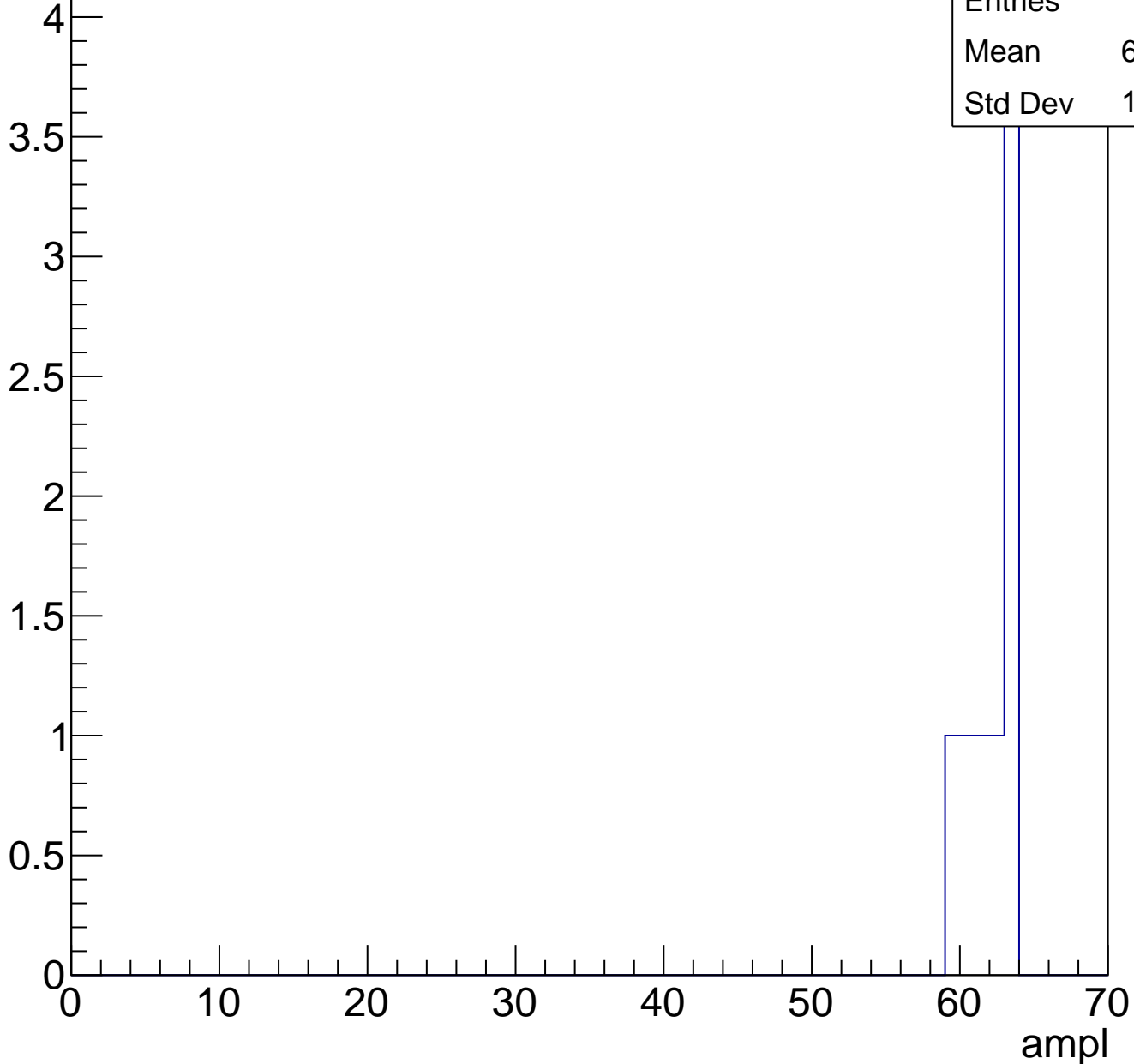
ampl



B1L001S, U26-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 29.85 |
| Std Dev | 3.286 |

Gaus mean : 30.4643

Gaus Width: 3.2980

Entry

14

12

10

8

6

4

2

0

0

10

20

30

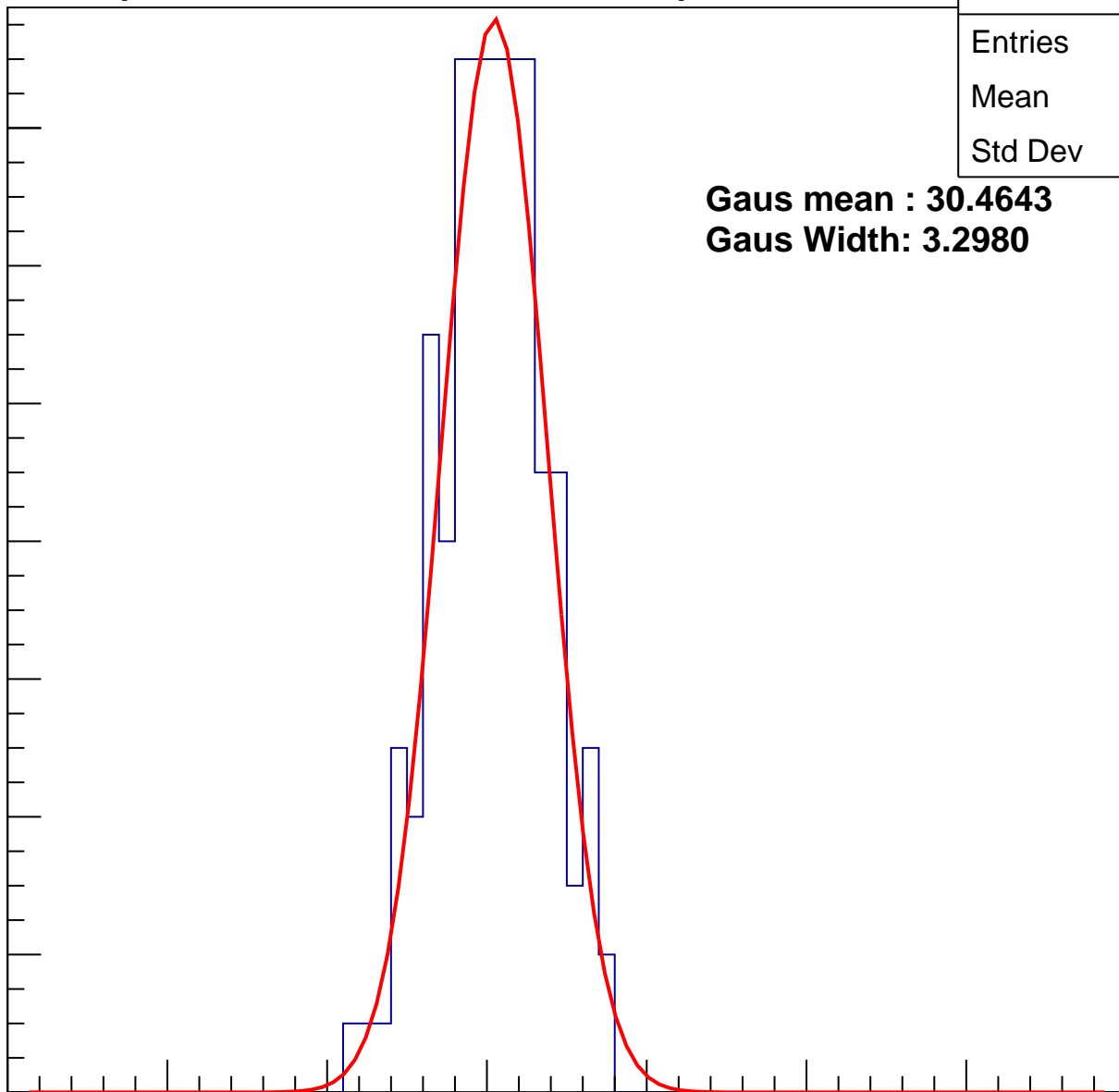
40

50

60

70

ampl



B1L001S, U26-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

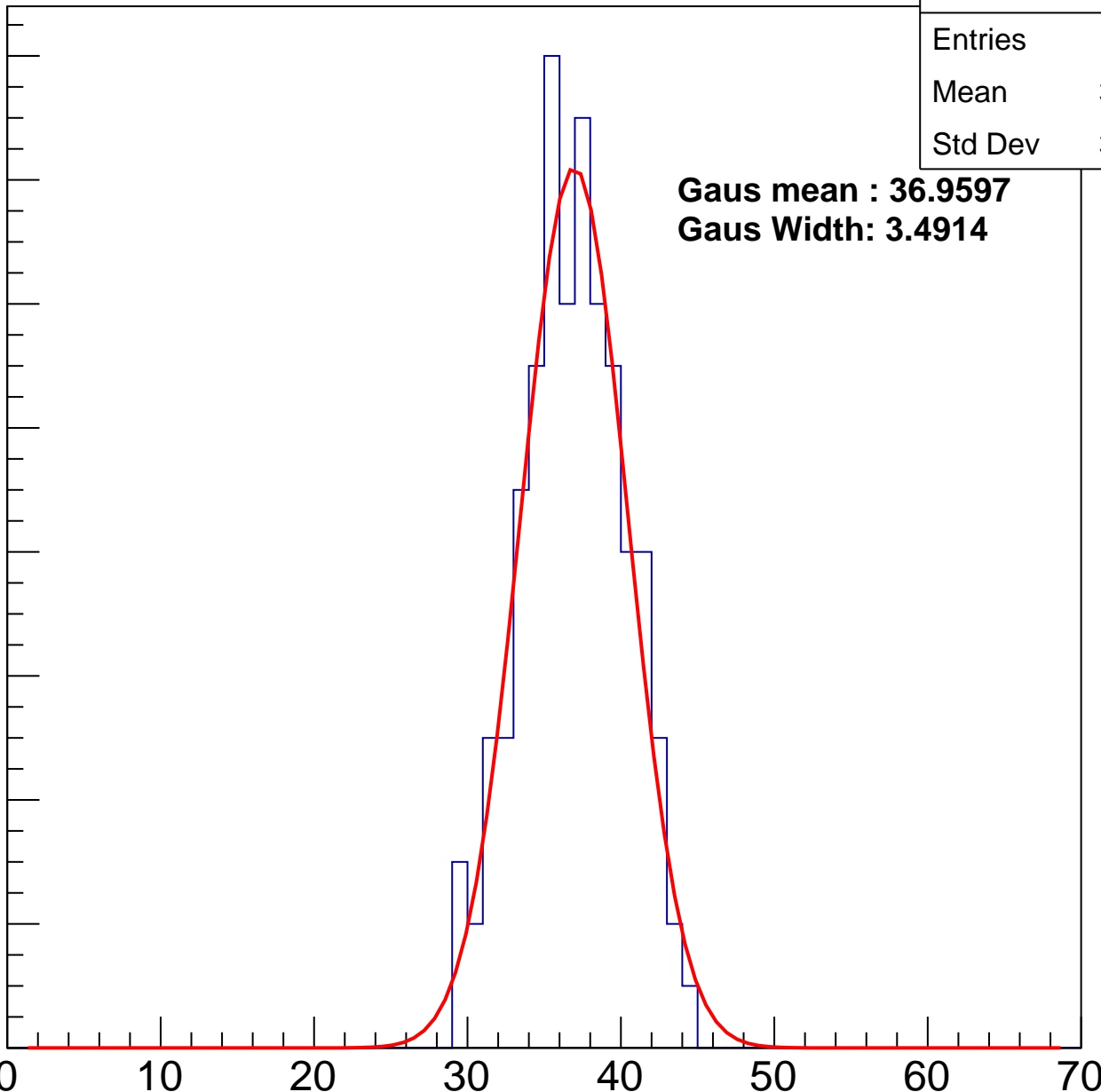
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 125 |
| Mean | 36.42 |
| Std Dev | 3.316 |

Gaus mean : 36.9597

Gaus Width: 3.4914

ampl



B1L001S, U26-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

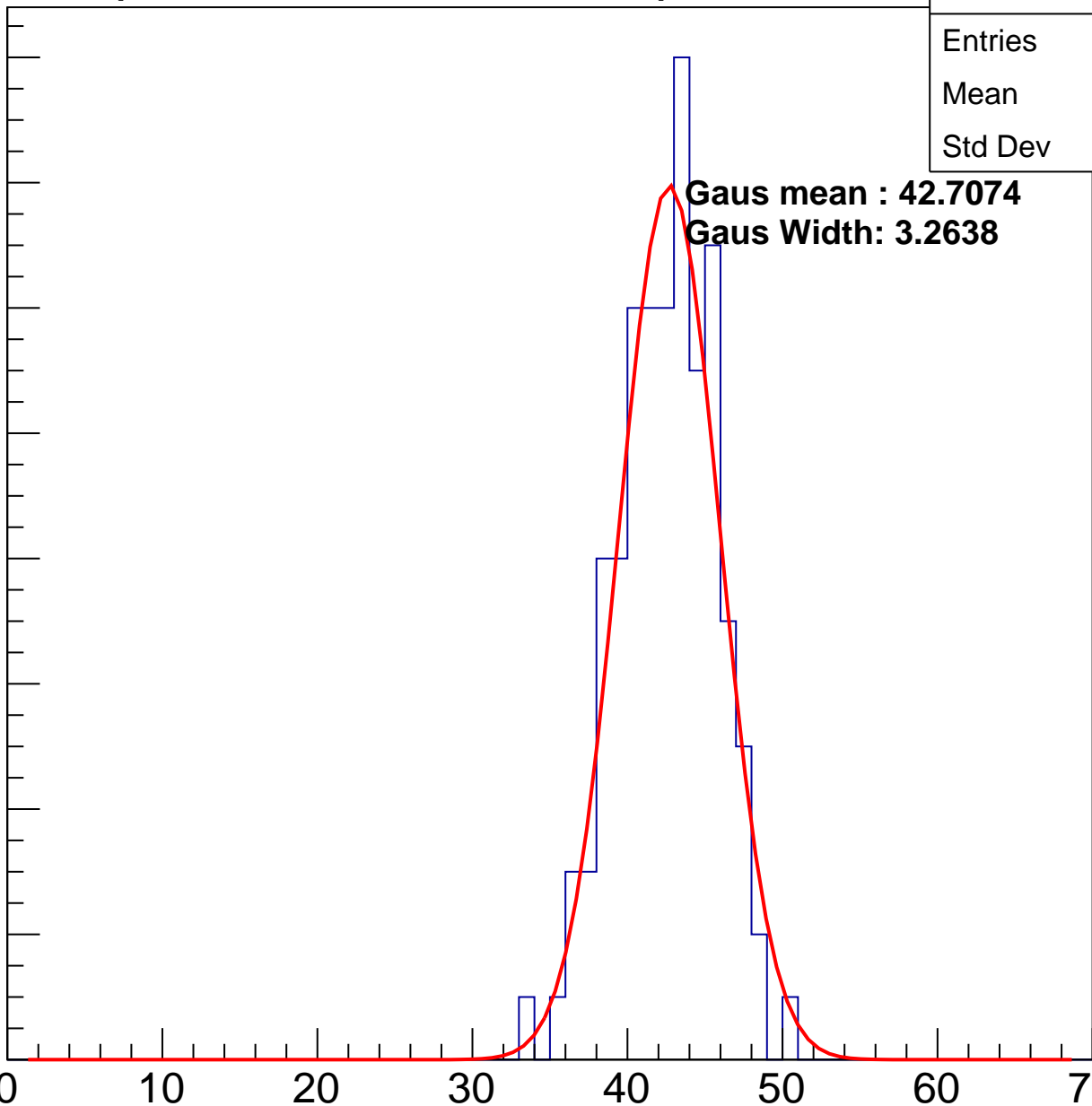
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 115 |
| Mean | 42.08 |
| Std Dev | 3.124 |

Gaus mean : 42.7074

Gaus Width: 3.2638

ampl

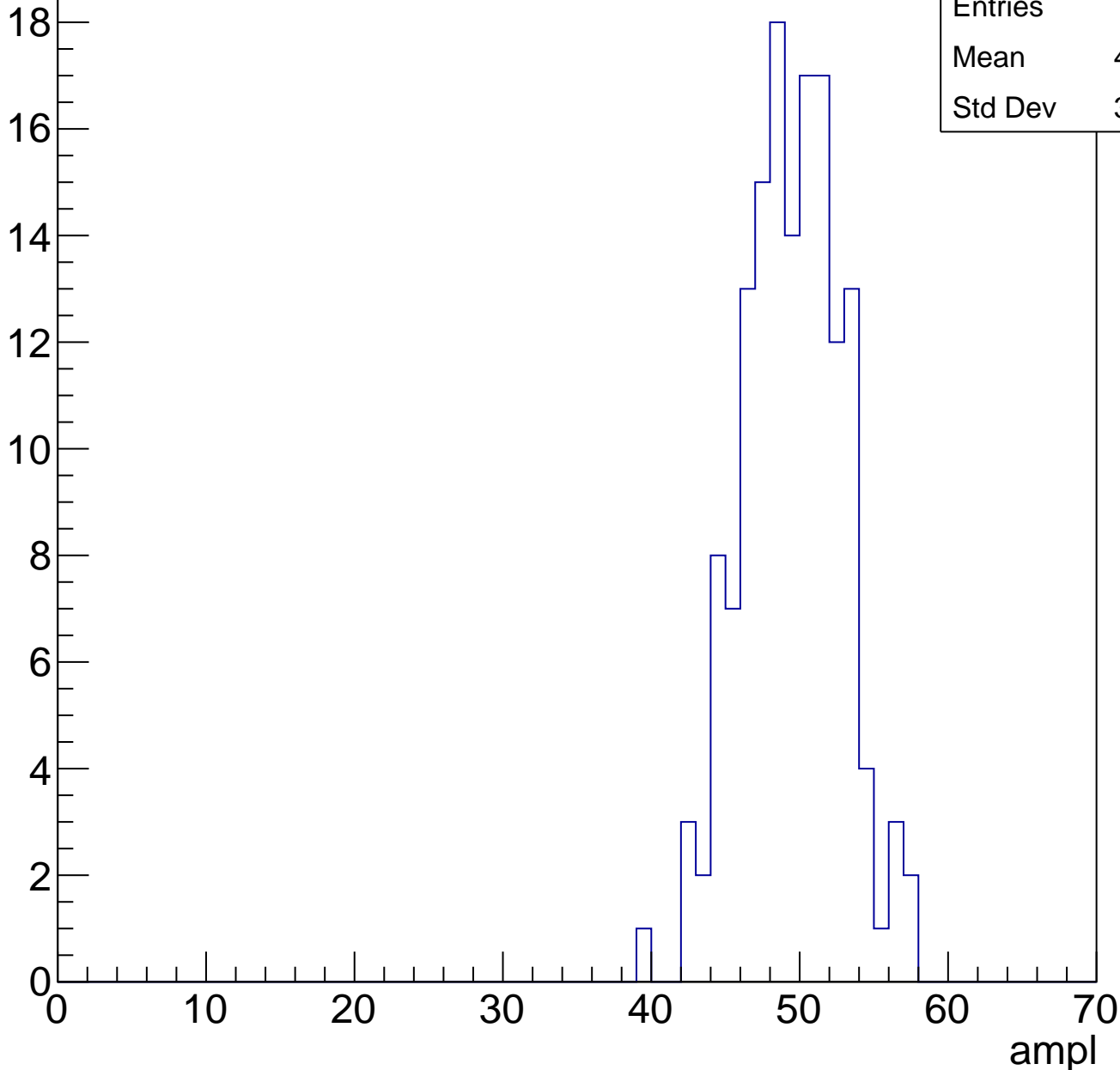


B1L001S, U26-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 49.03 |
| Std Dev | 3.304 |

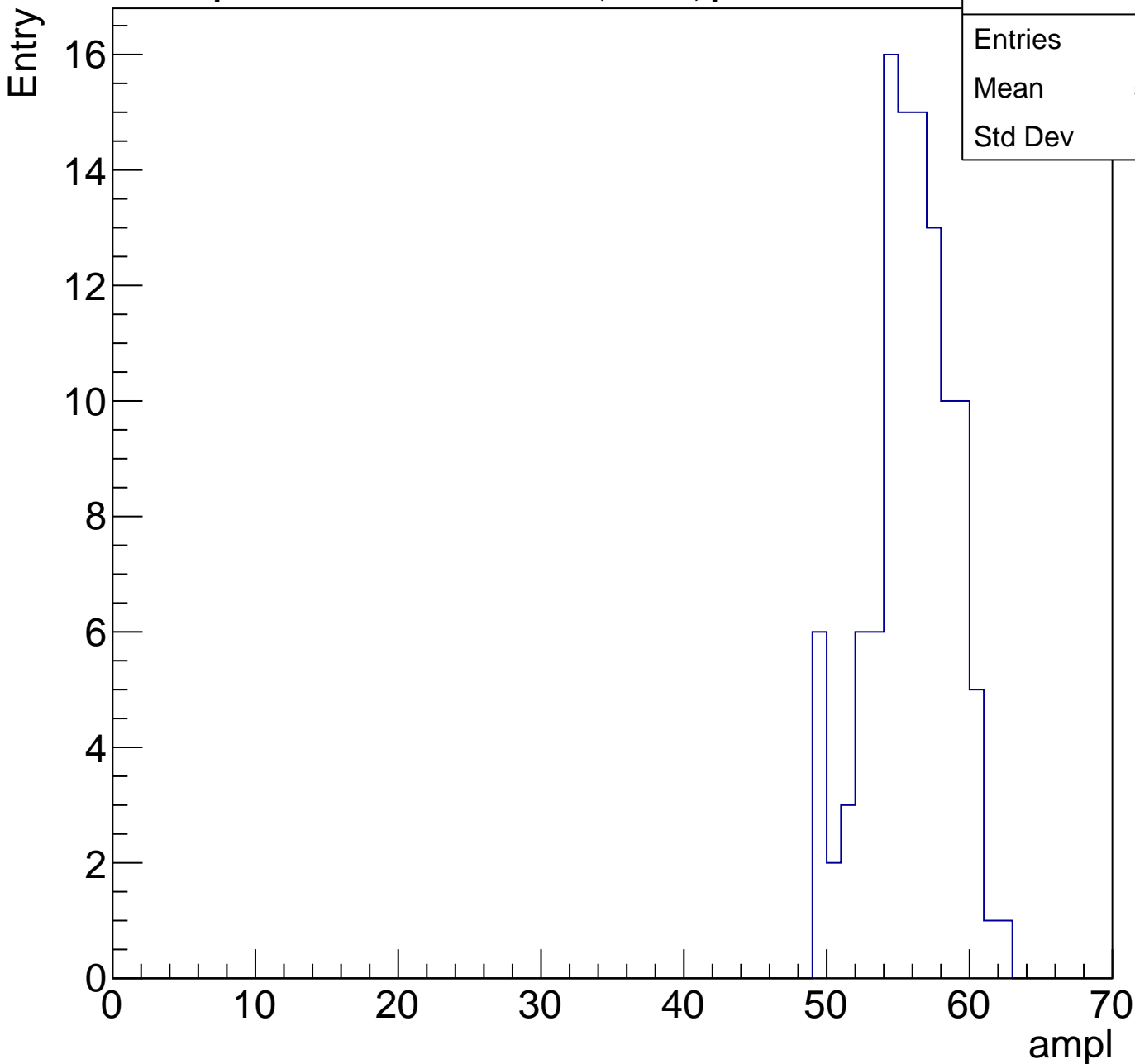
Entry



B1L001S, U26-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 109 |
| Mean | 55.41 |
| Std Dev | 2.9 |



B1L001S, U26-ch33, adc5

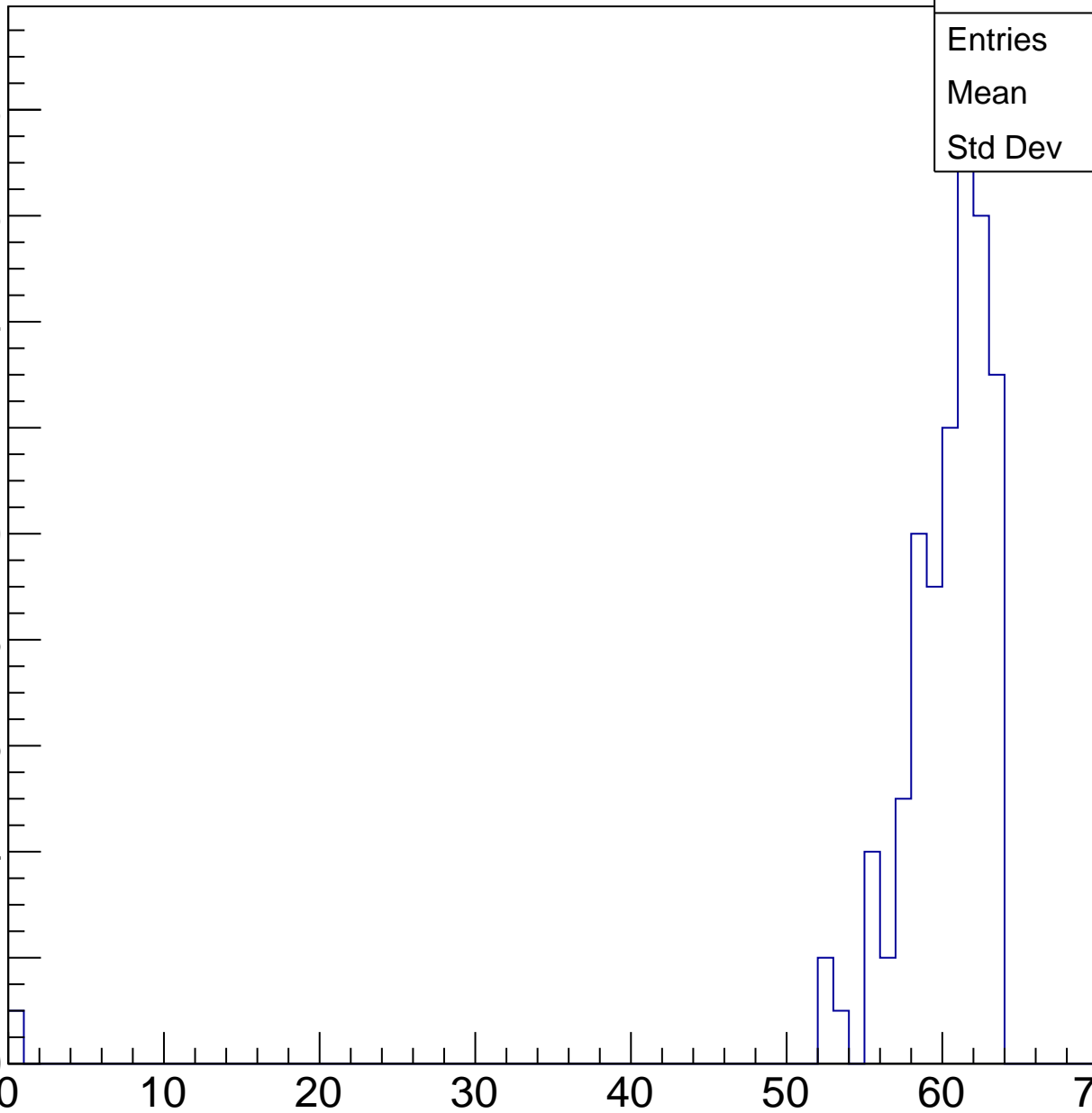
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 94 |
| Mean | 59.31 |
| Std Dev | 6.651 |

ampl

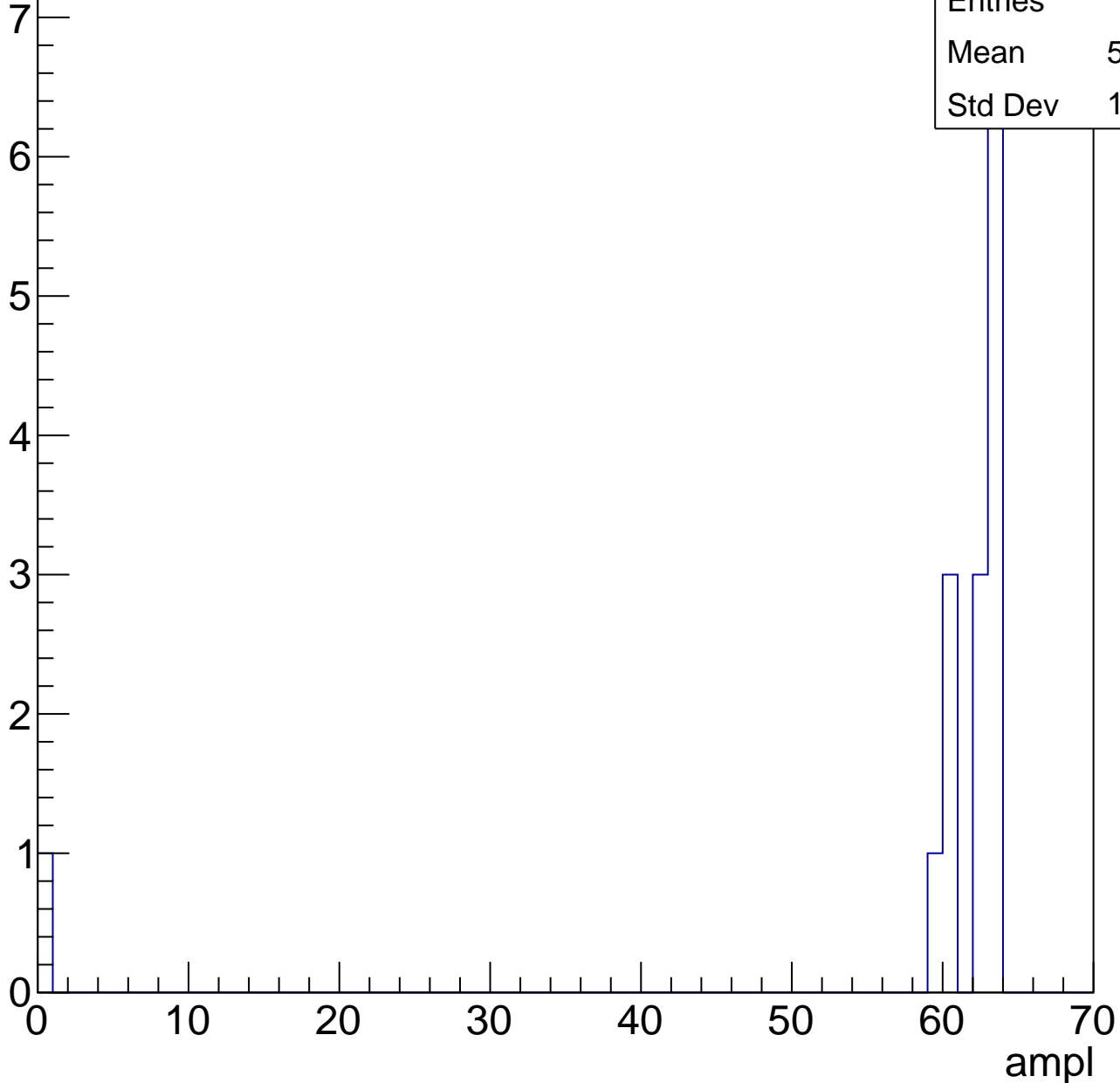


B1L001S, U26-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

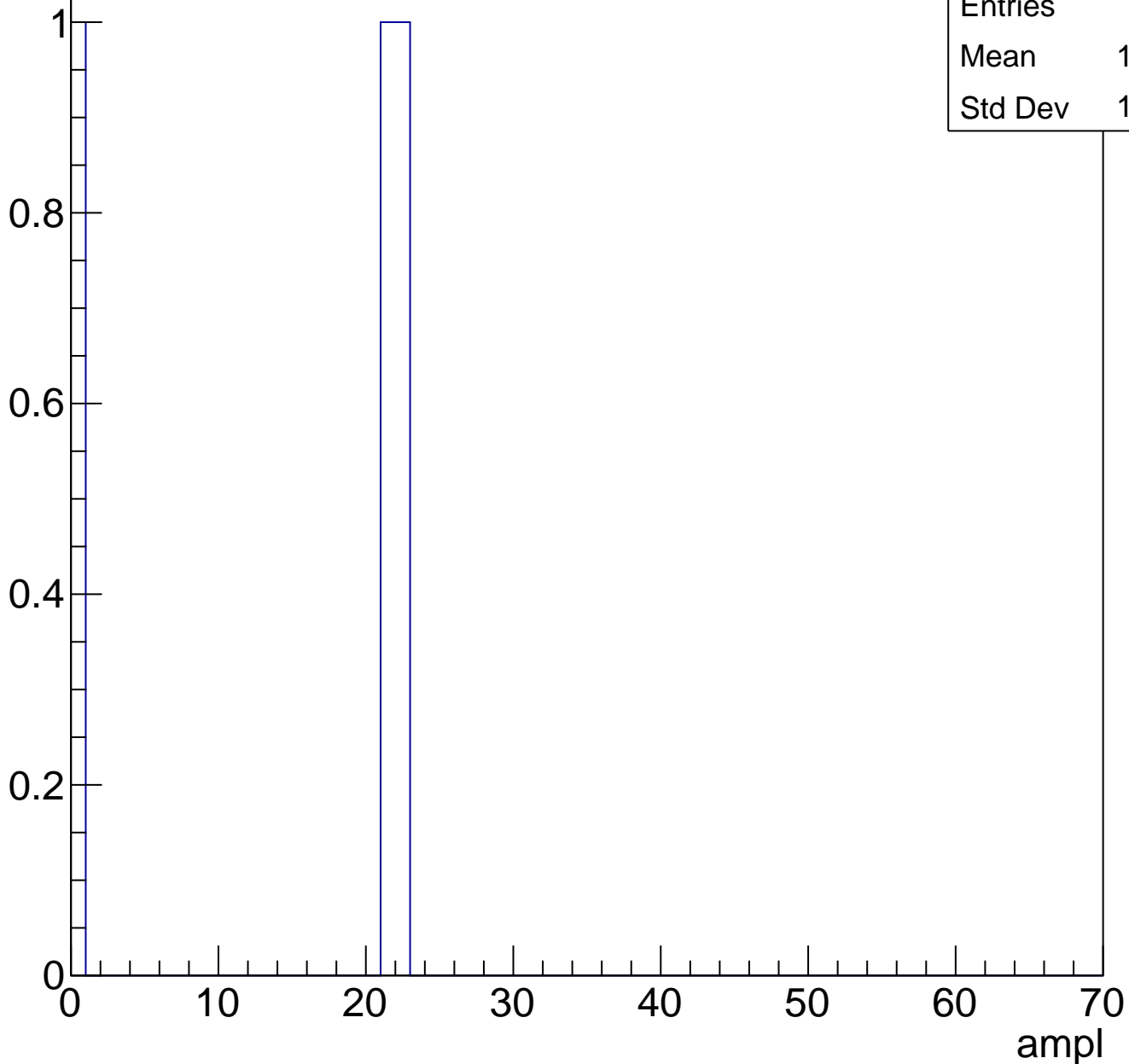
| | |
|---------|-------|
| Entries | 15 |
| Mean | 57.73 |
| Std Dev | 15.49 |



B1L001S, U26-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 14.33 |
| Std Dev | 10.14 |

B1L001S, U26-ch34, adc0

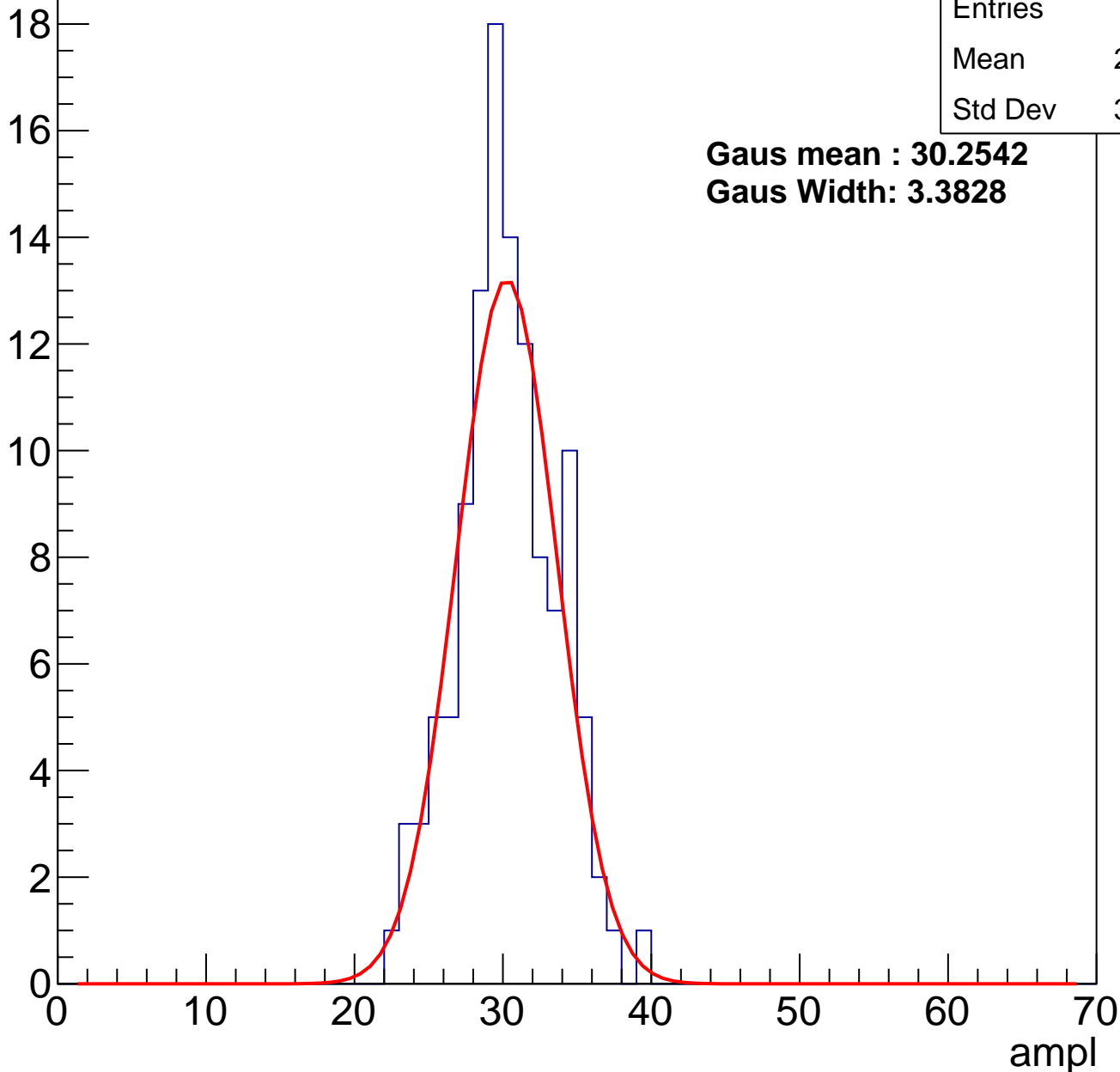
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 29.82 |
| Std Dev | 3.288 |

Gaus mean : 30.2542

Gaus Width: 3.3828

Entry



B1L001S, U26-ch34, adc1

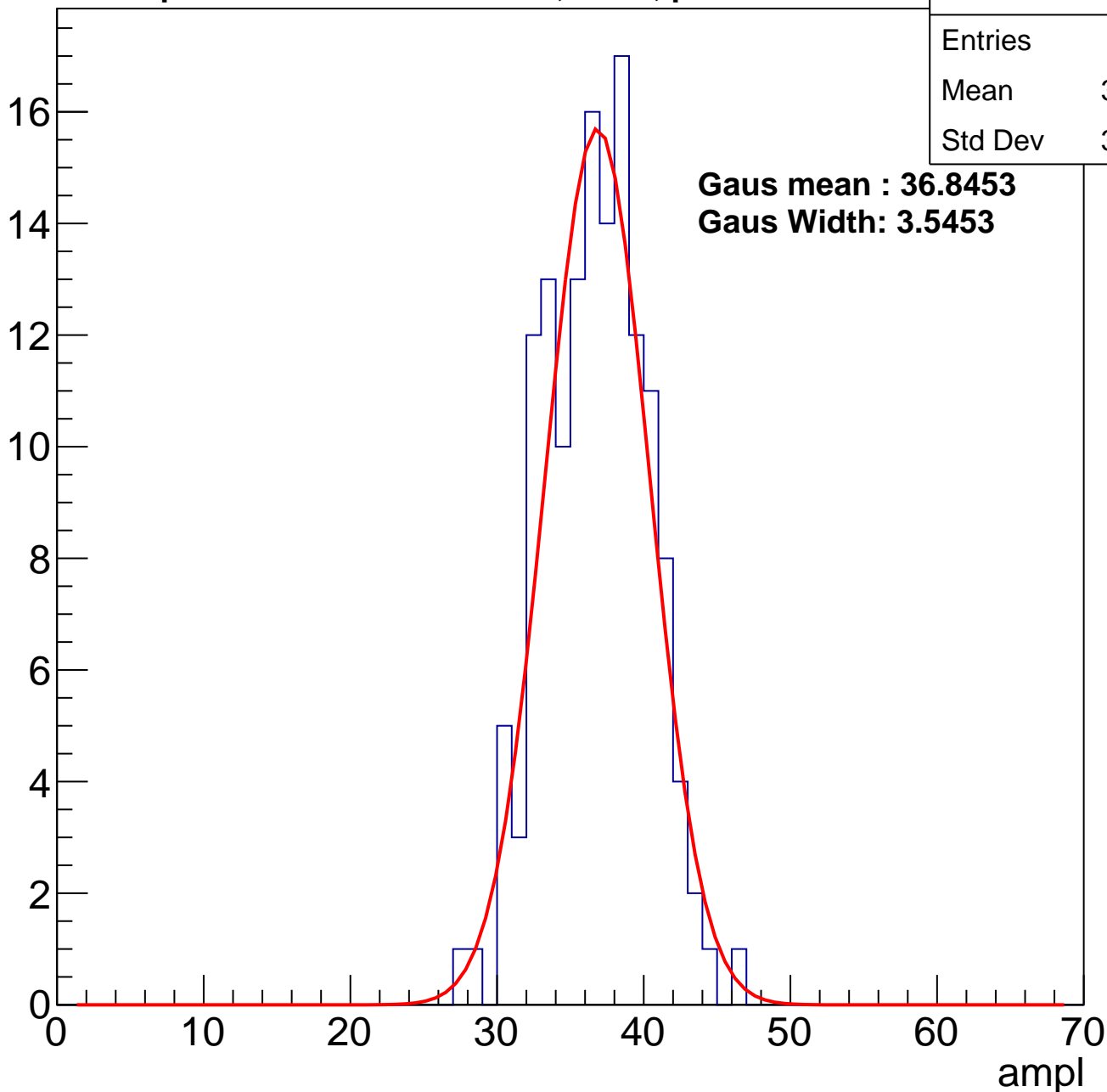
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 144 |
| Mean | 36.29 |
| Std Dev | 3.458 |

Gaus mean : 36.8453

Gaus Width: 3.5453

Entry



B1L001S, U26-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 130 |
| Mean | 43.02 |
| Std Dev | 3.326 |

Gaus mean : 43.7010

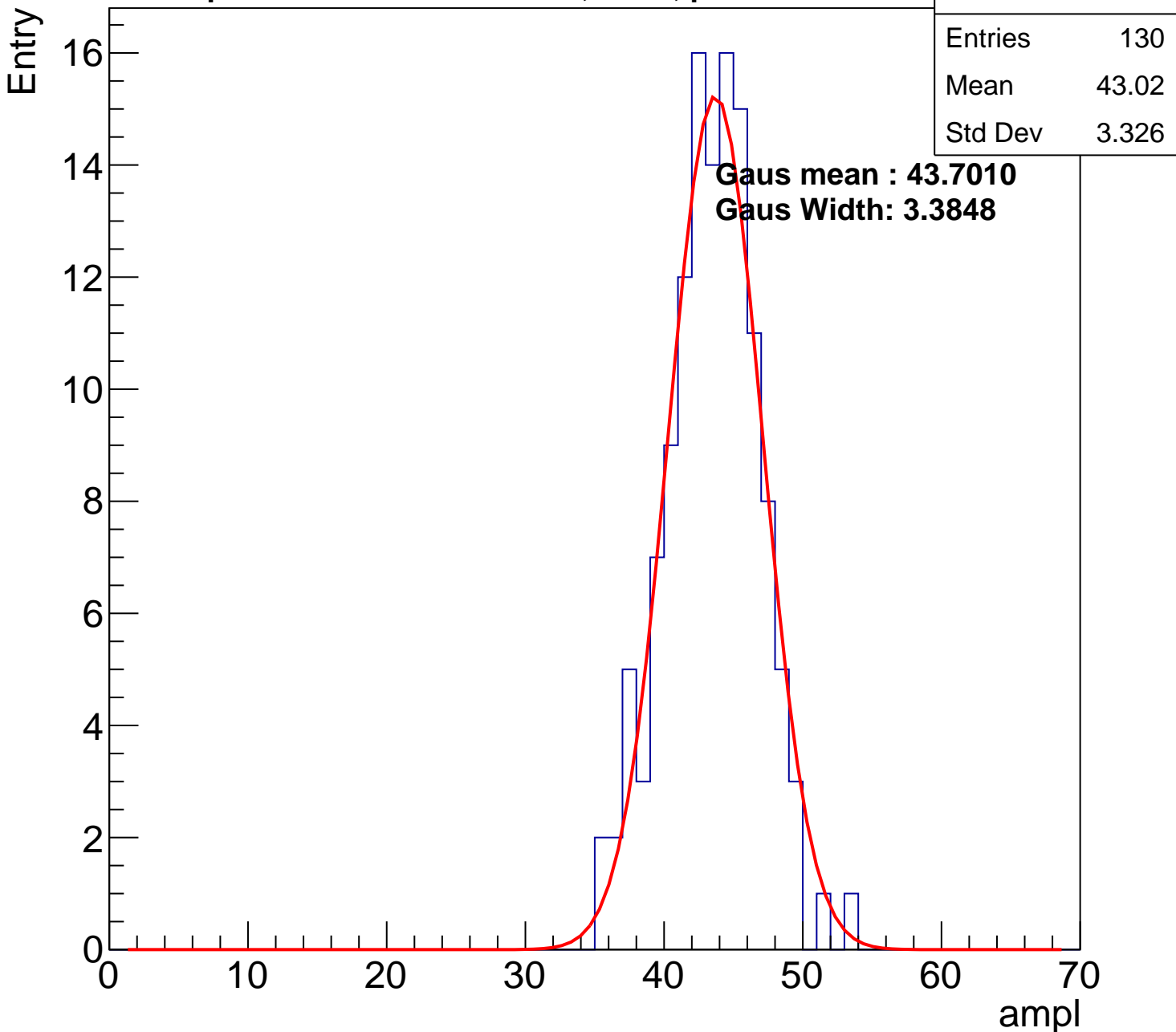
Gaus Width: 3.3848

Entry

16
14
12
10
8
6
4
2
0

ampl

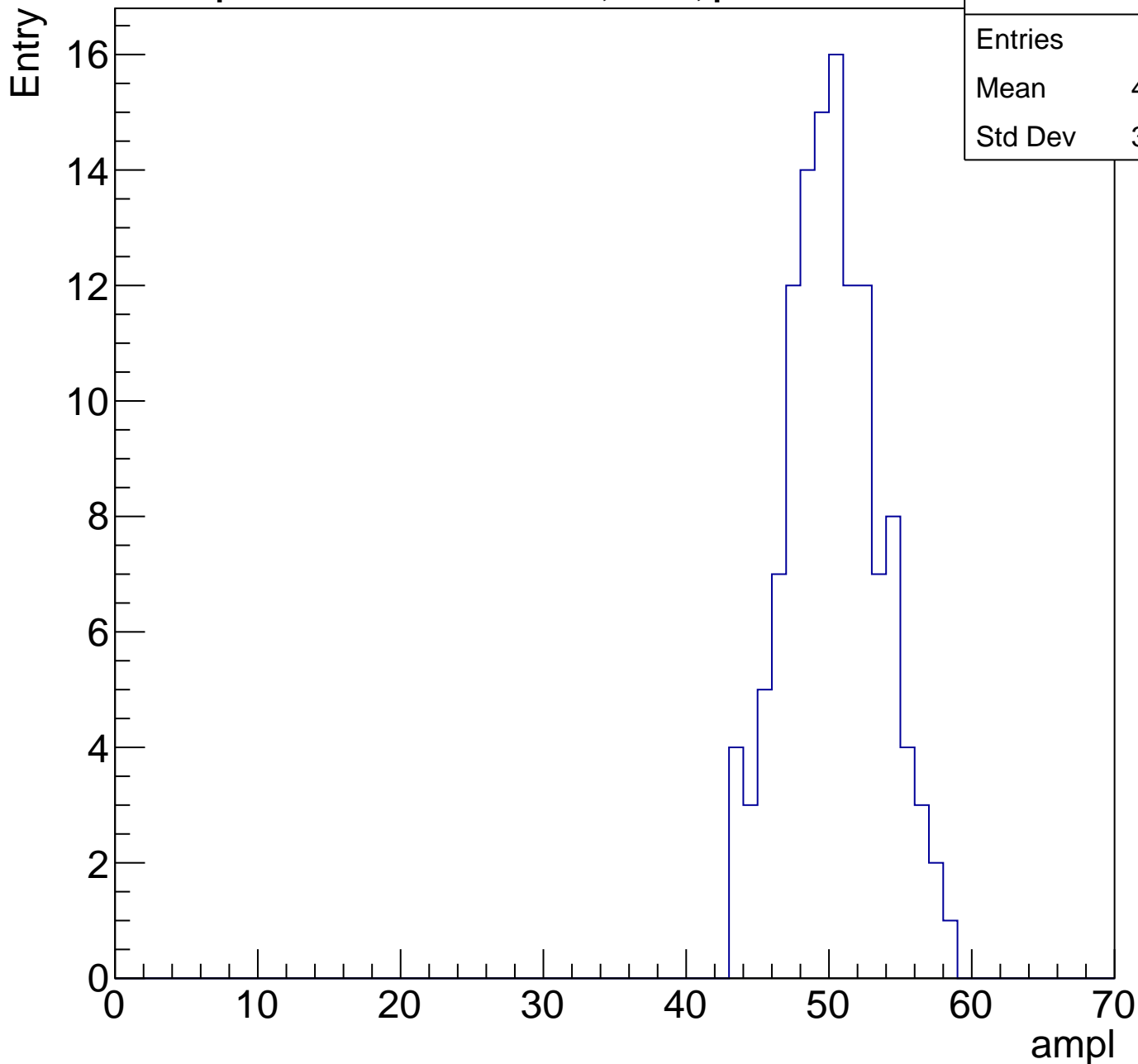
0 10 20 30 40 50 60 70



B1L001S, U26-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

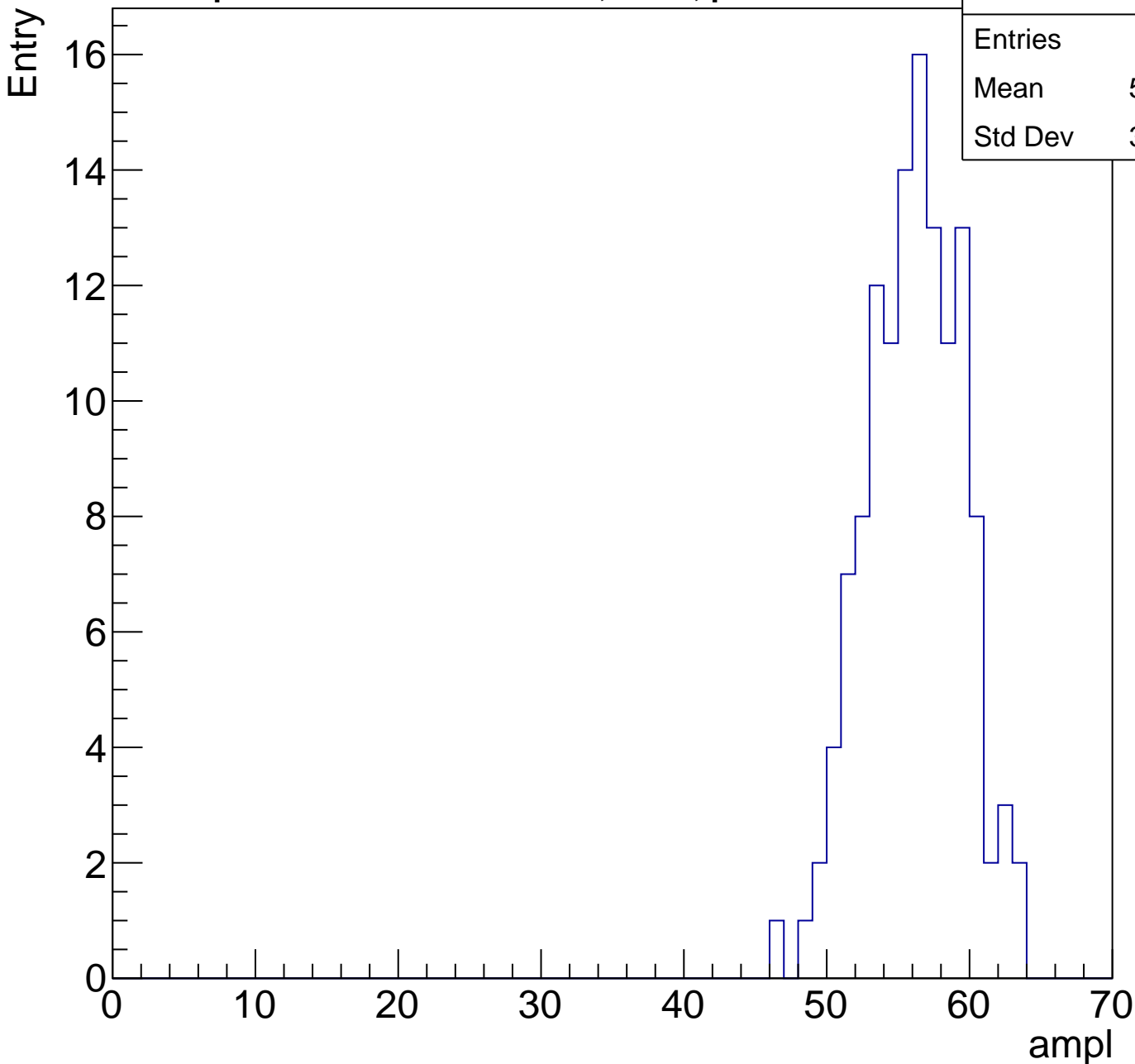
| | |
|---------|-------|
| Entries | 125 |
| Mean | 49.77 |
| Std Dev | 3.267 |



B1L001S, U26-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 55.63 |
| Std Dev | 3.326 |



B1L001S, U26-ch34, adc5

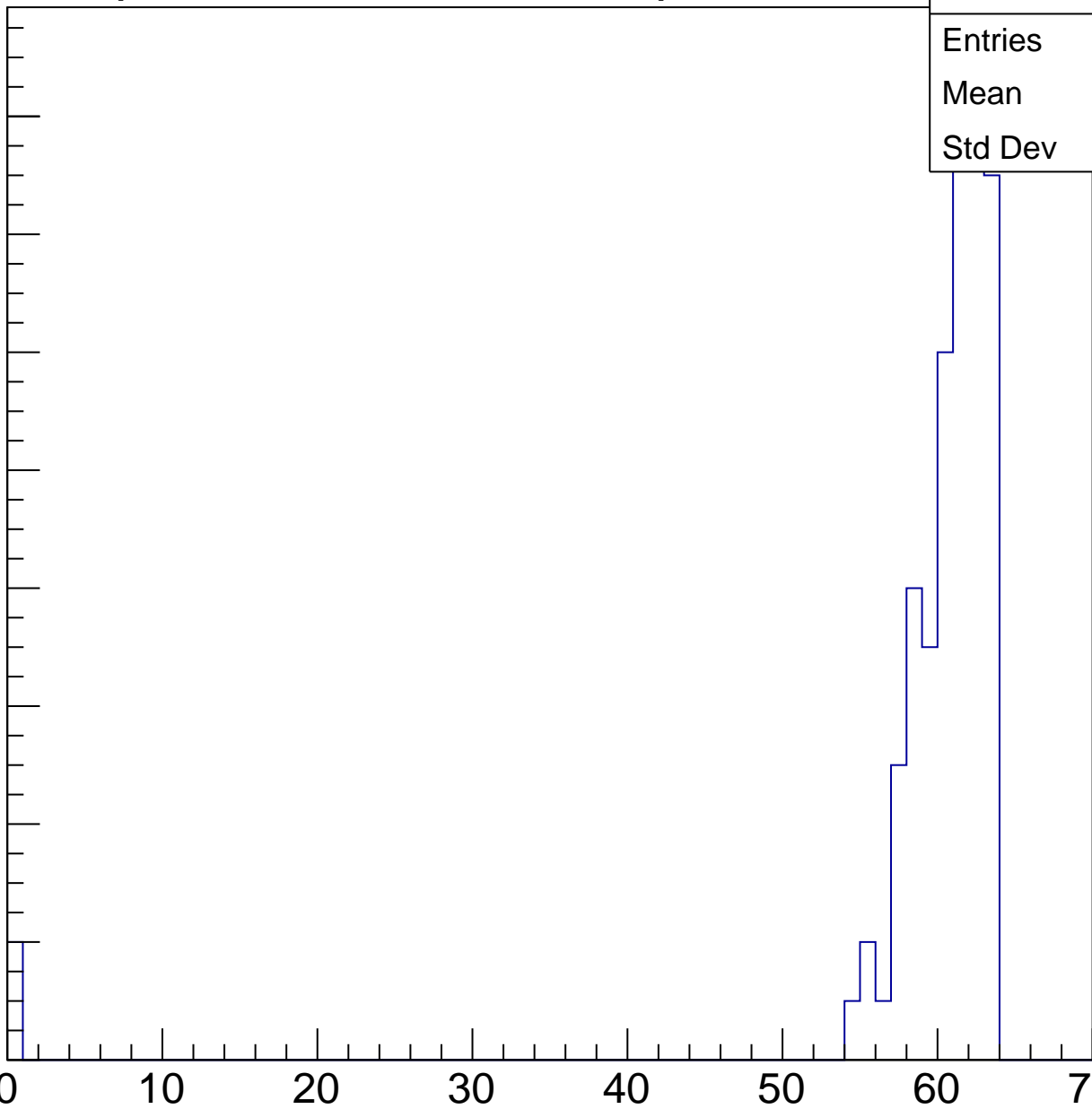
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 86 |
| Mean | 59.02 |
| Std Dev | 9.351 |

ampl



B1L001S, U26-ch34, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch35, adc0

calib_packv5_042523_0143.root, FC#2, port C2

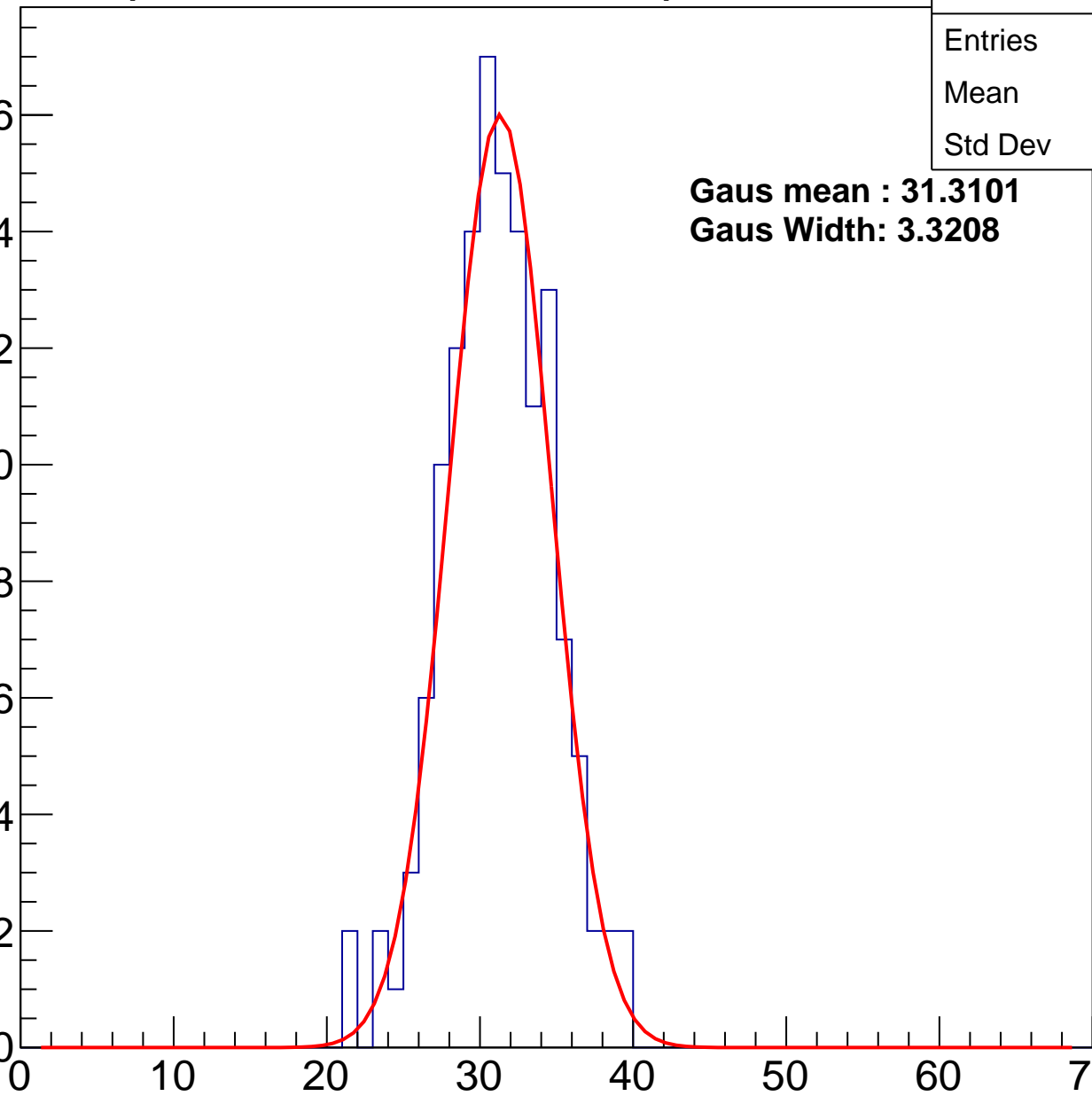
| | |
|---------|-------|
| Entries | 138 |
| Mean | 30.7 |
| Std Dev | 3.482 |

Gaus mean : 31.3101
Gaus Width: 3.3208

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch35, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 37.9 |
| Std Dev | 3.646 |

Gaus mean : 38.3445

Gaus Width: 4.1532

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

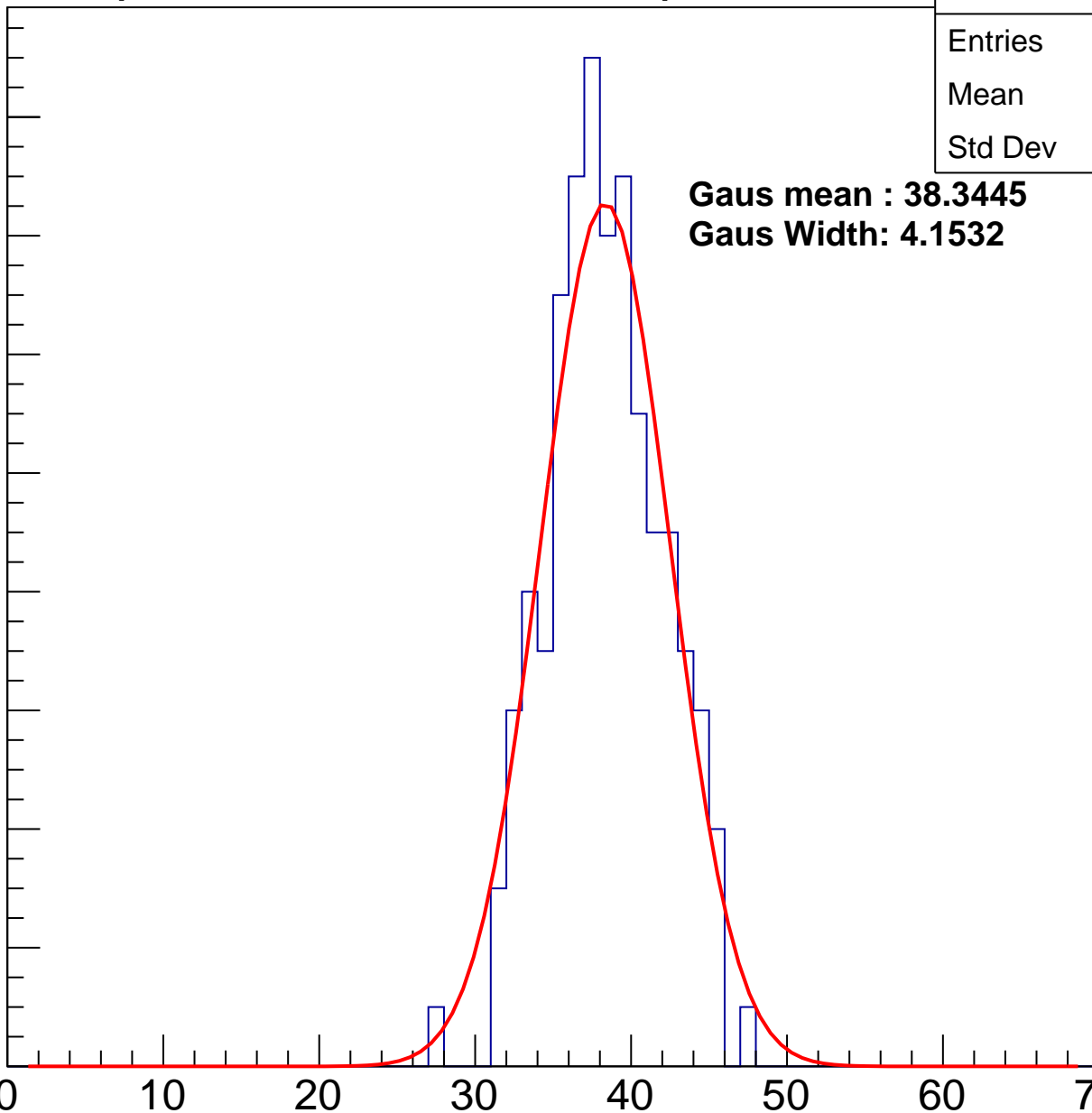
40

50

60

70

ampl



B1L001S, U26-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

138

Mean

44.86

Std Dev

3.574

Gaus mean : 45.0987

Gaus Width: 3.4903

0

10

20

30

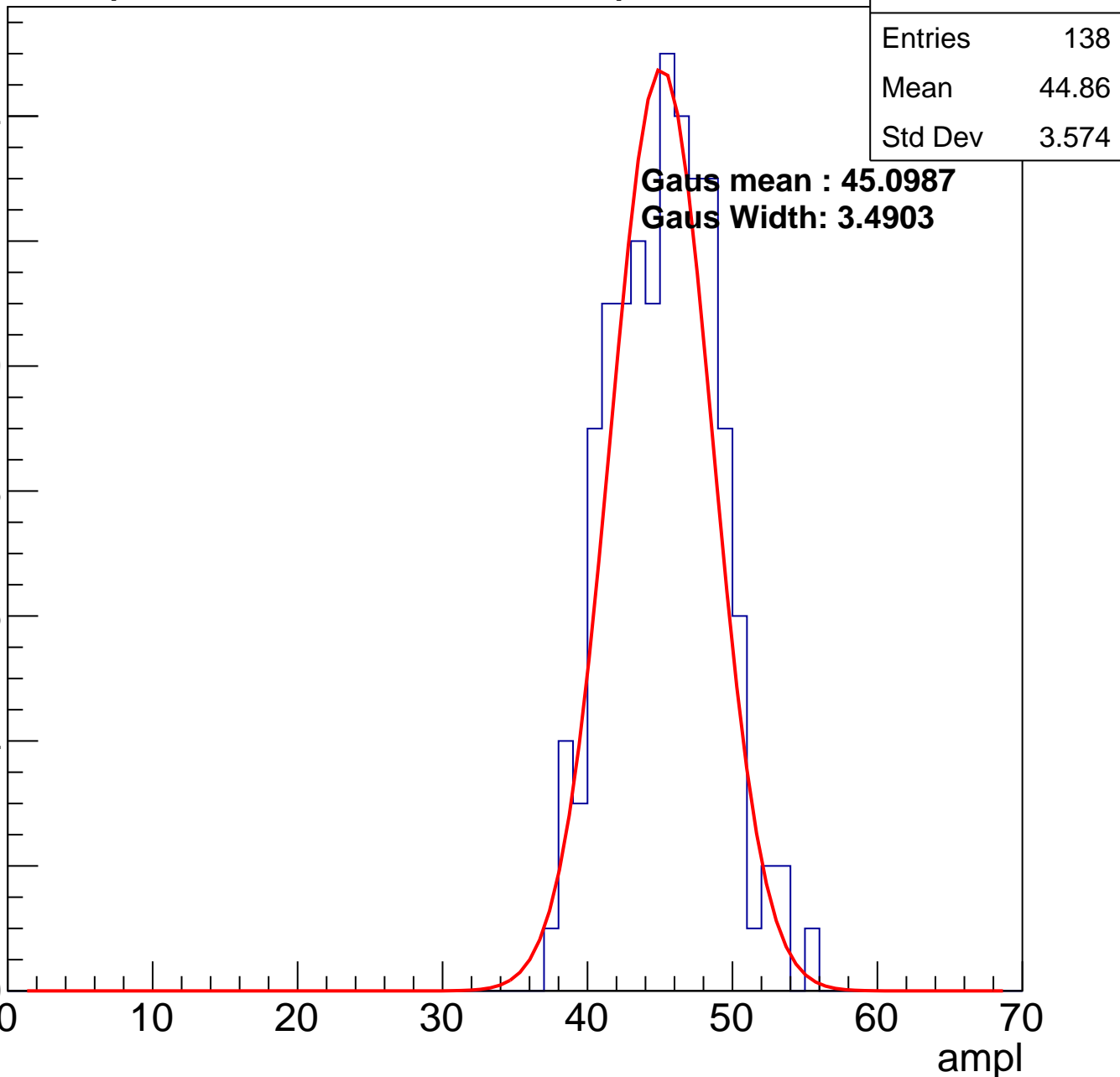
40

50

60

70

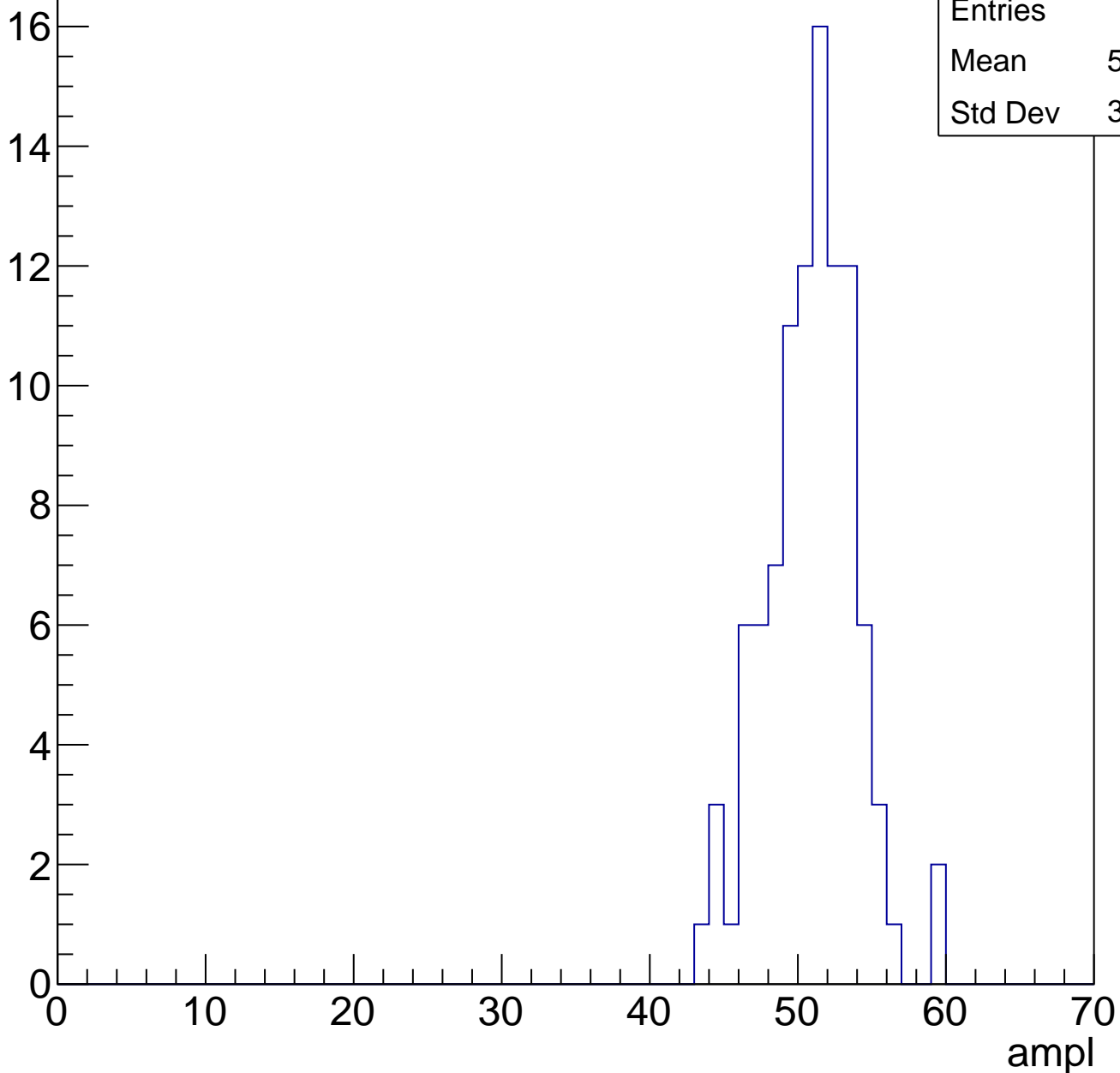
ampl



B1L001S, U26-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

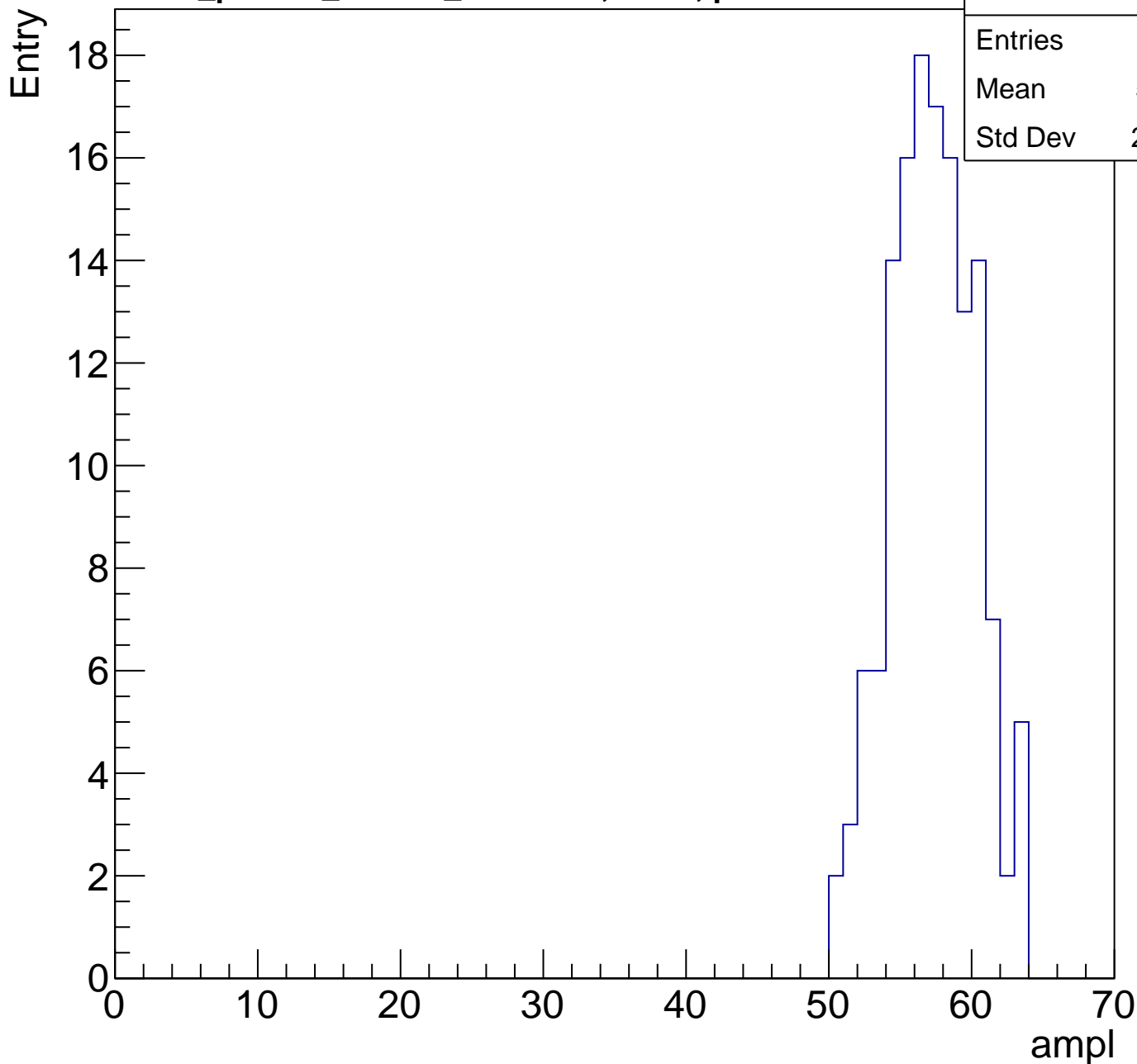
Entry



B1L001S, U26-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 56.81 |
| Std Dev | 2.927 |



B1L001S, U26-ch35, adc5

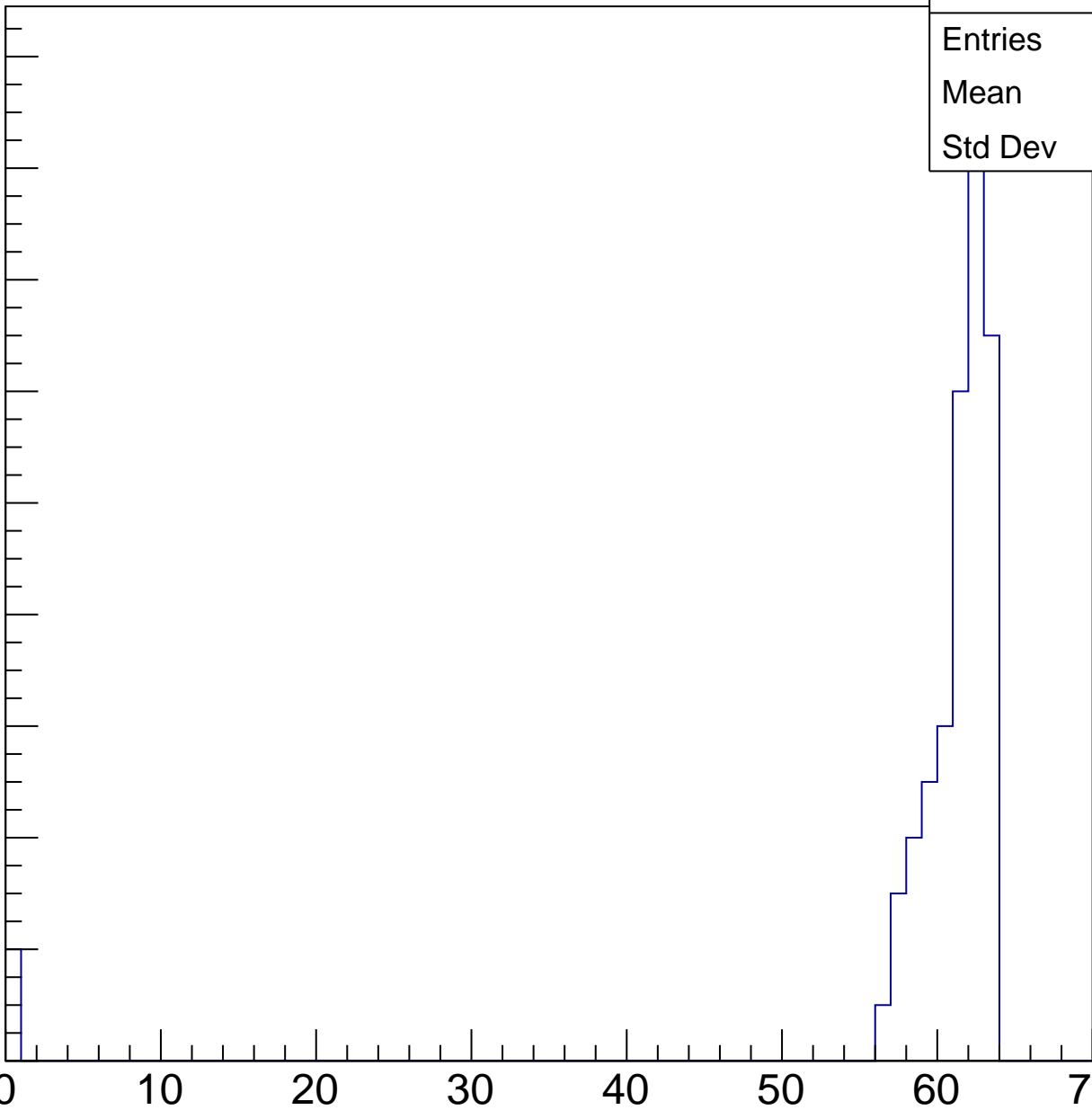
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 59.08 |
| Std Dev | 10.76 |

ampl



B1L001S, U26-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

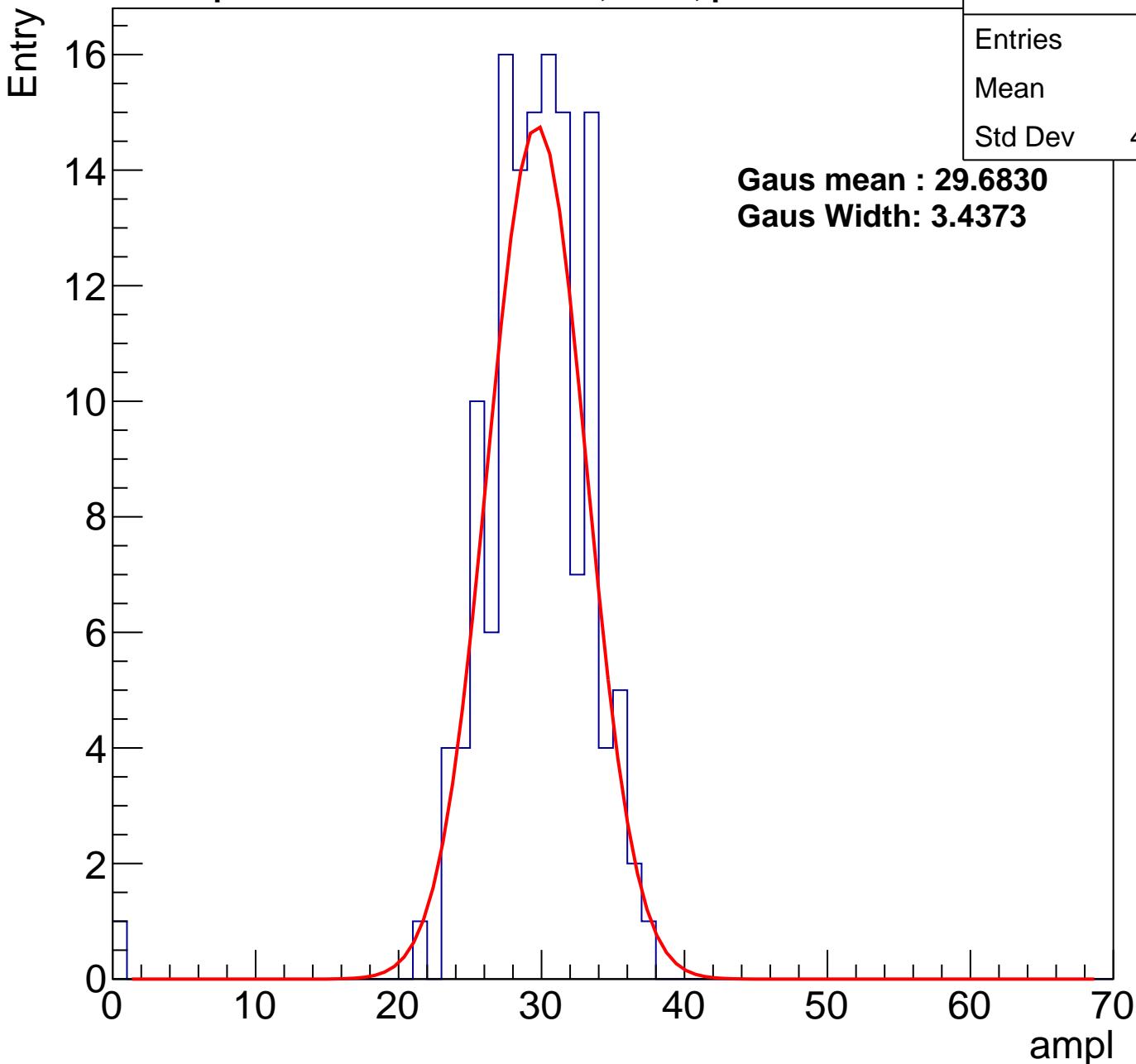
B1L001S, U26-ch36, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 29.1 |
| Std Dev | 4.077 |

Gaus mean : 29.6830

Gaus Width: 3.4373



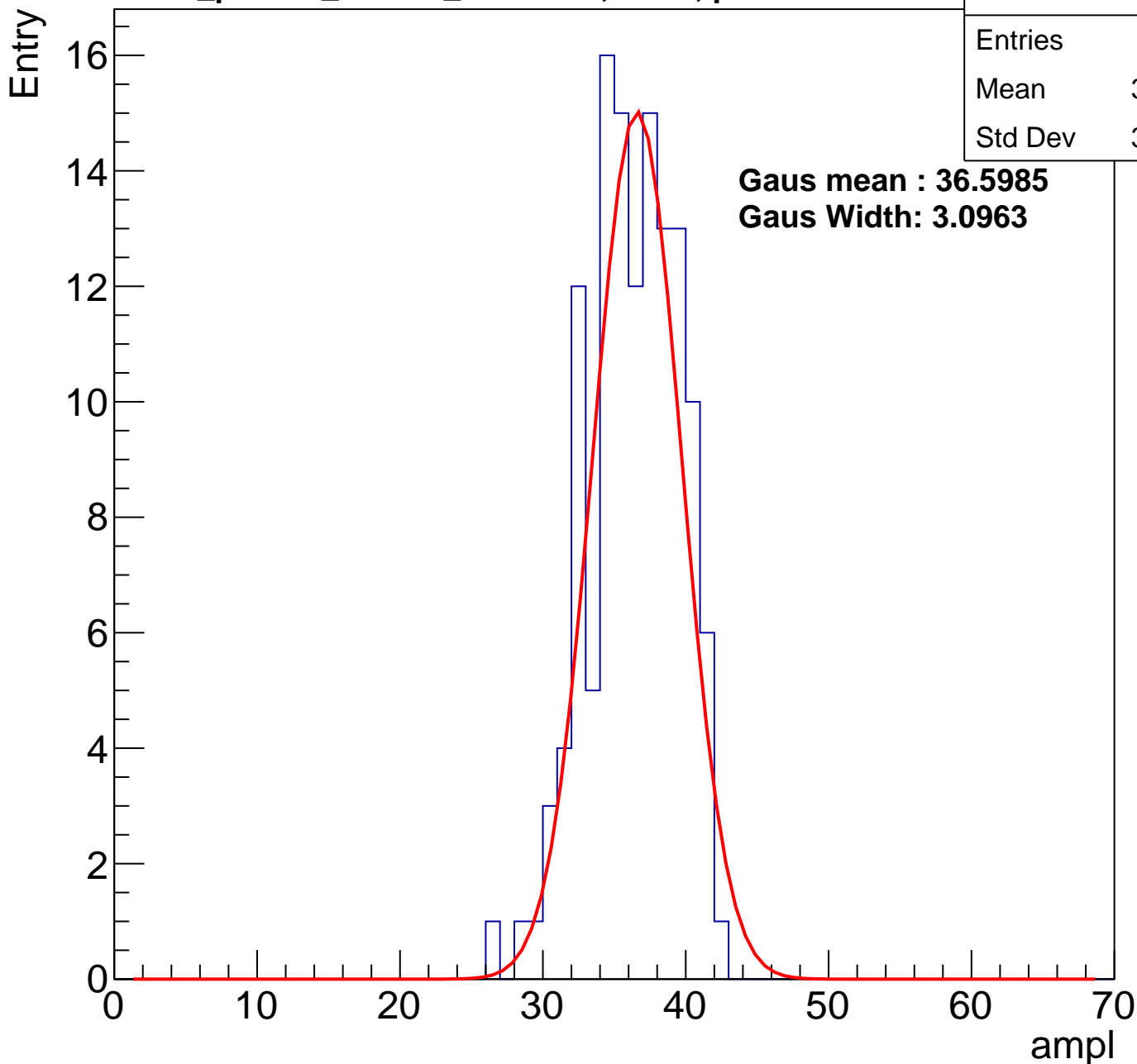
B1L001S, U26-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 35.87 |
| Std Dev | 3.143 |

Gaus mean : 36.5985

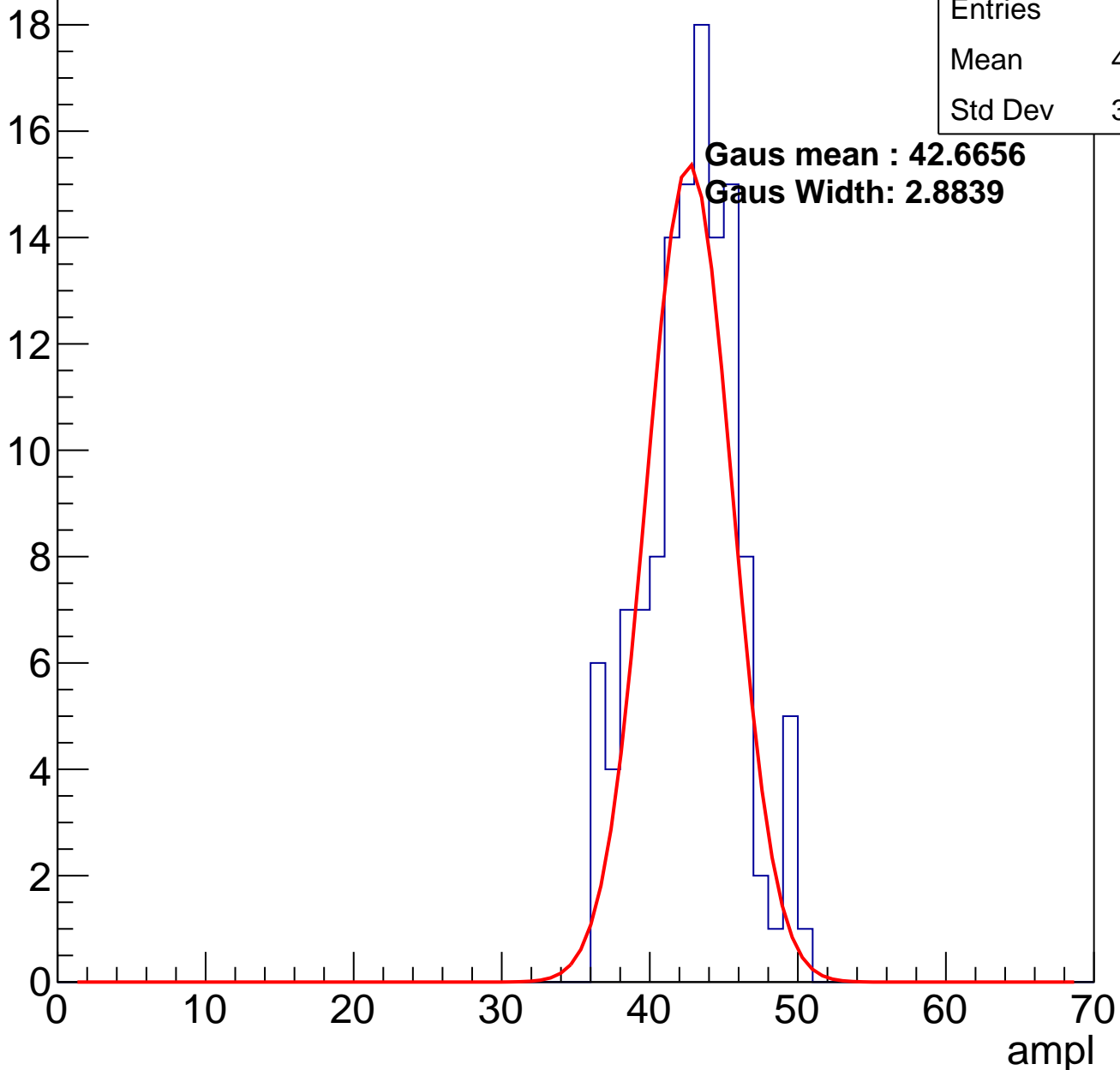
Gaus Width: 3.0963



B1L001S, U26-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

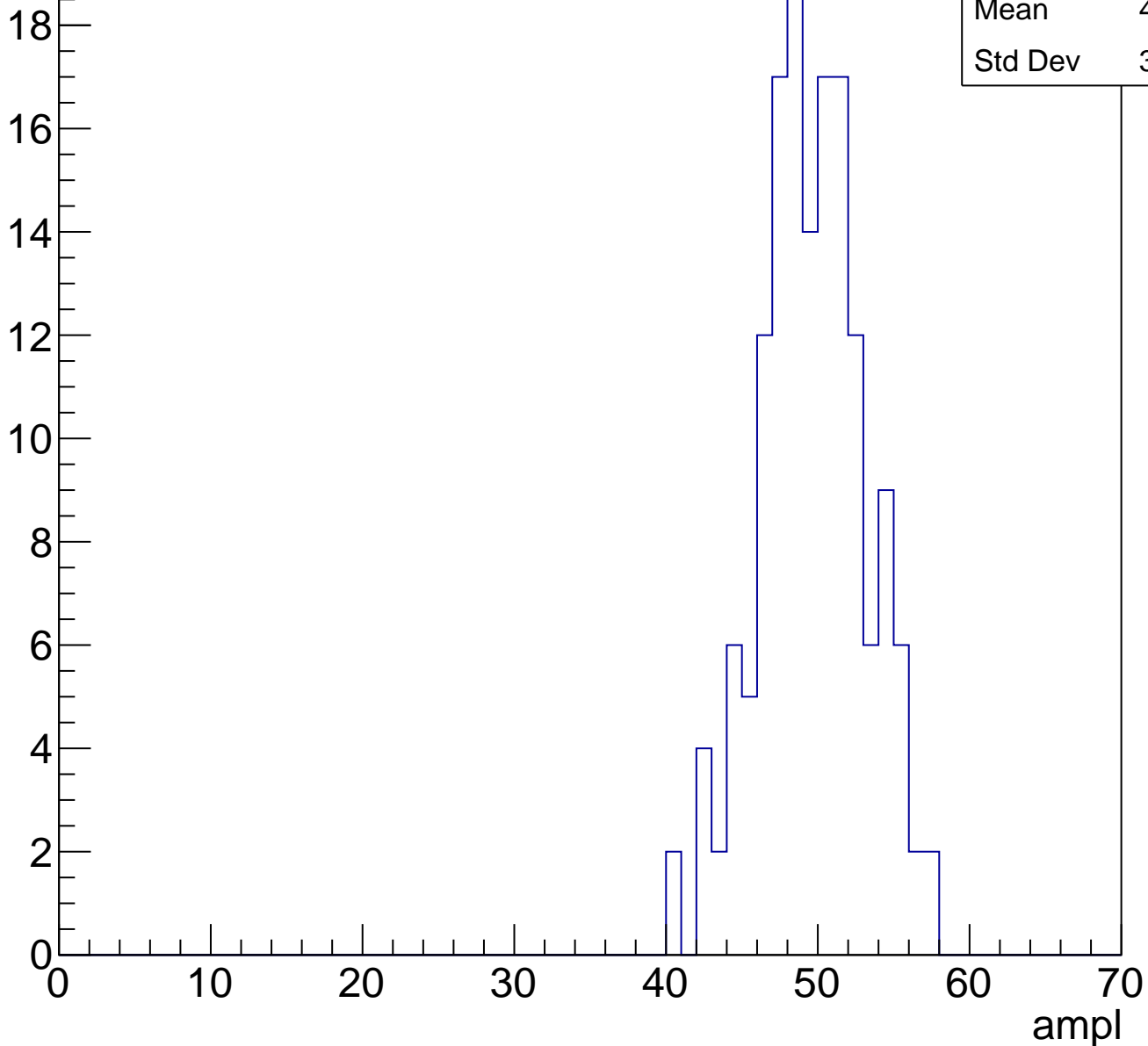


B1L001S, U26-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 49.16 |
| Std Dev | 3.448 |

Entry



B1L001S, U26-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

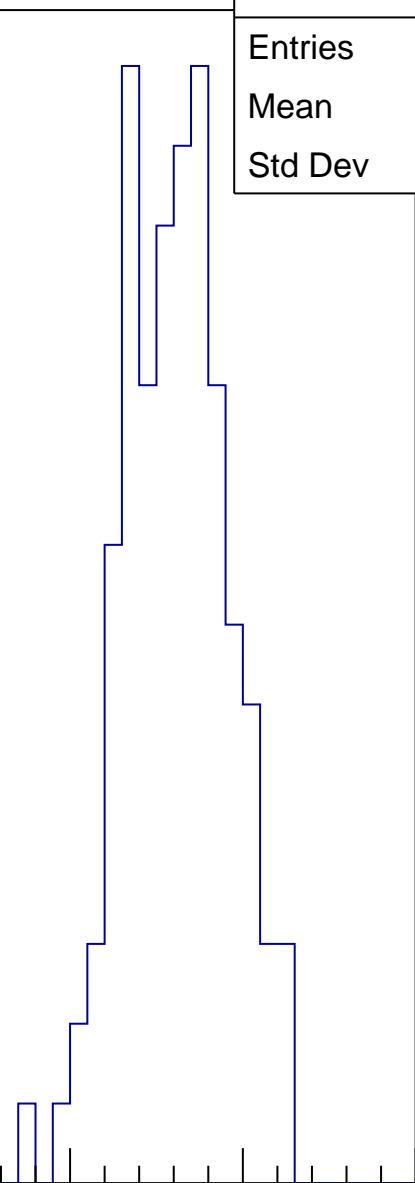
| | |
|---------|-------|
| Entries | 107 |
| Mean | 55.65 |
| Std Dev | 3.005 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch36, adc5

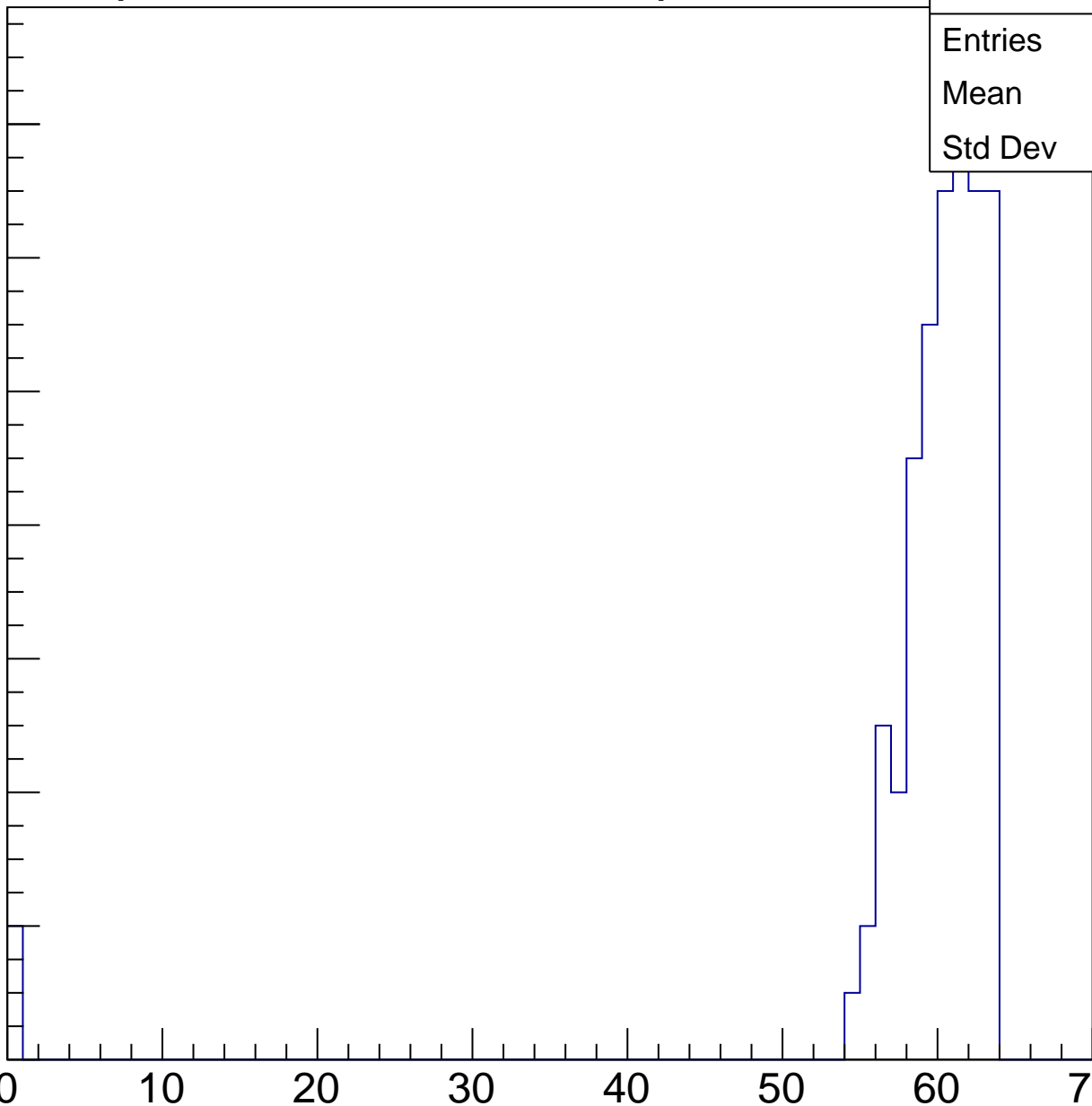
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 88 |
| Mean | 58.67 |
| Std Dev | 9.218 |

ampl

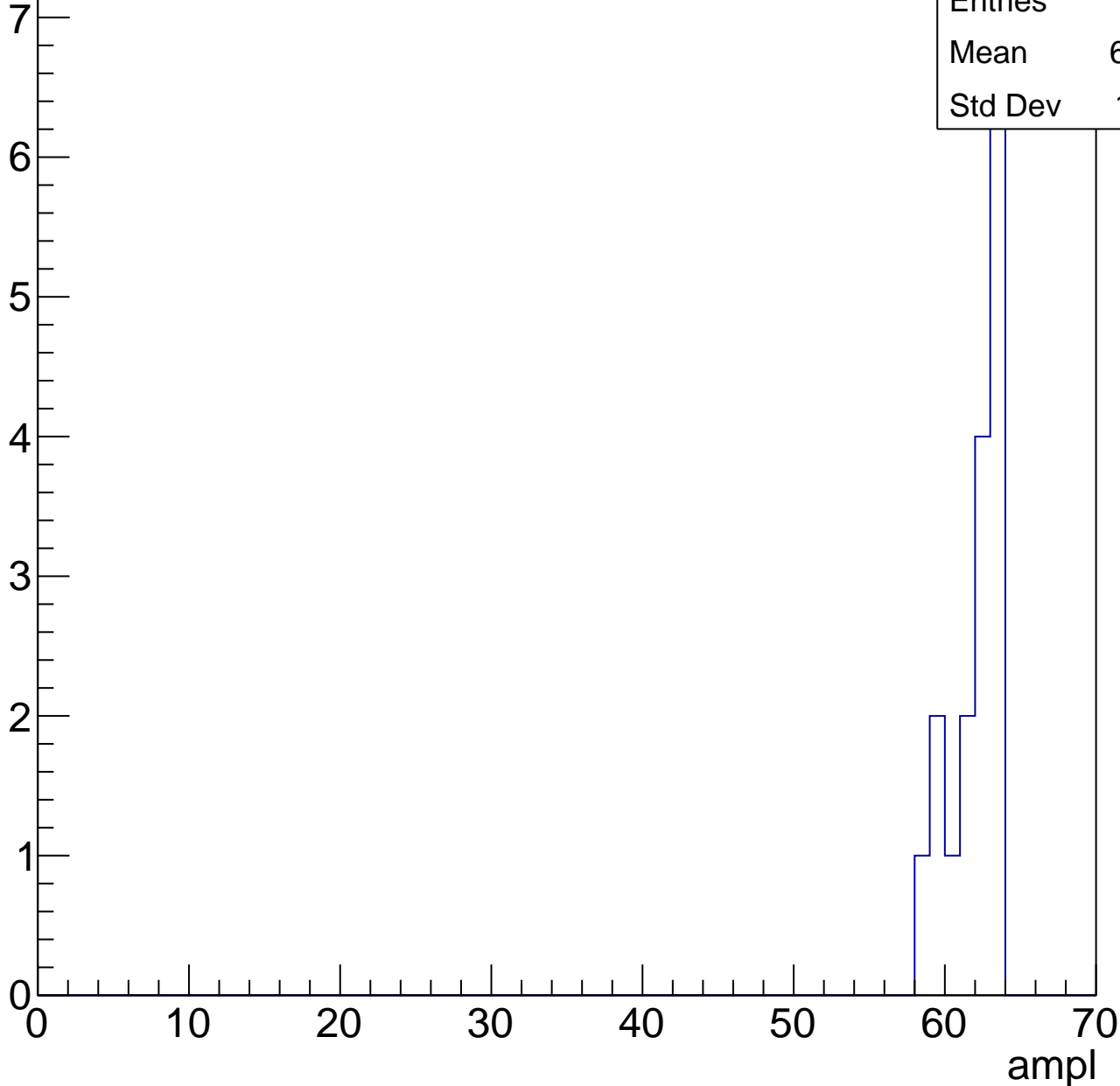


B1L001S, U26-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.59 |
| Std Dev | 1.611 |



B1L001S, U26-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch37, adc0

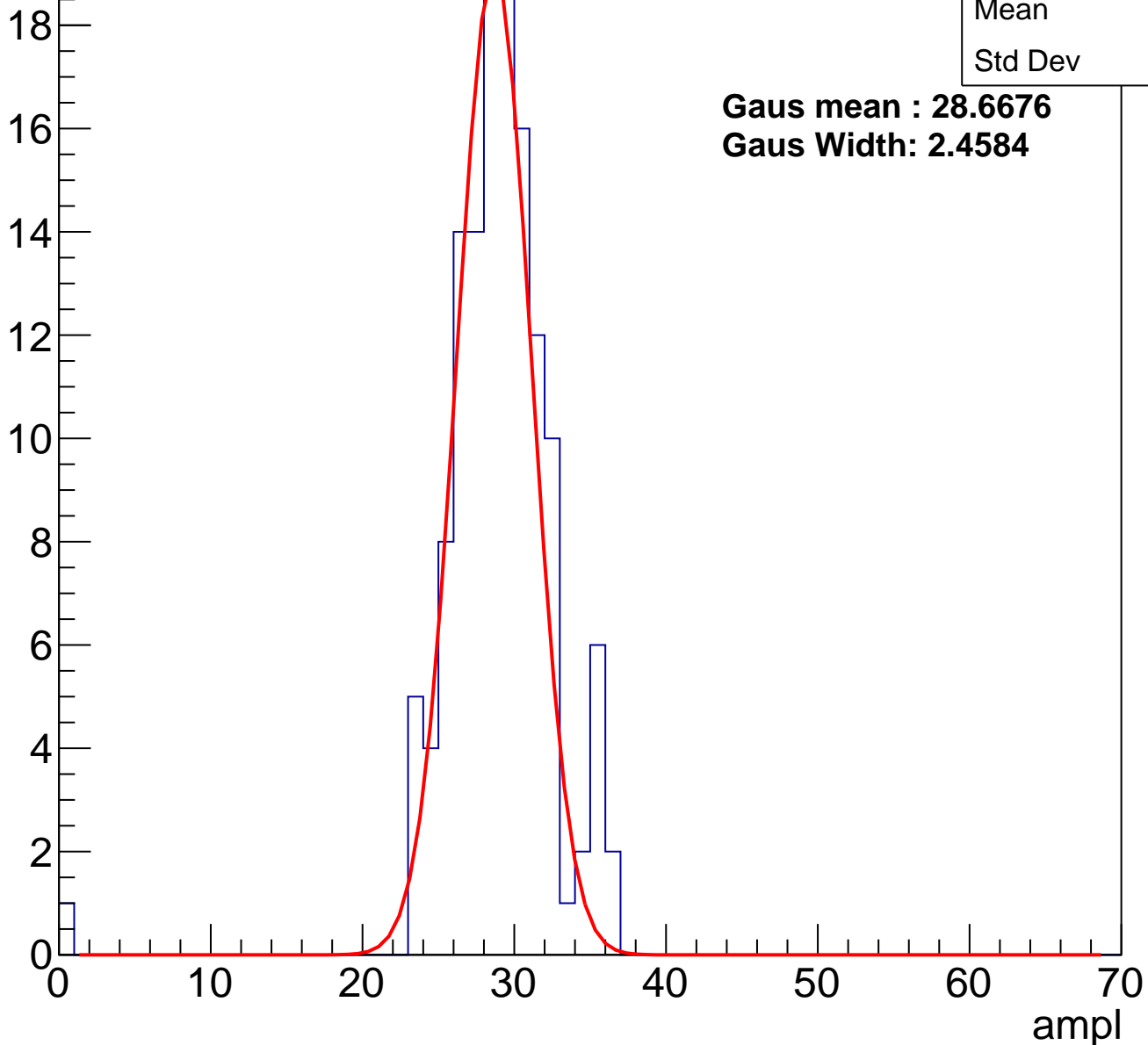
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|------|
| Entries | 133 |
| Mean | 28.5 |
| Std Dev | 3.83 |

Gaus mean : 28.6676

Gaus Width: 2.4584

Entry



B1L001S, U26-ch37, adc1

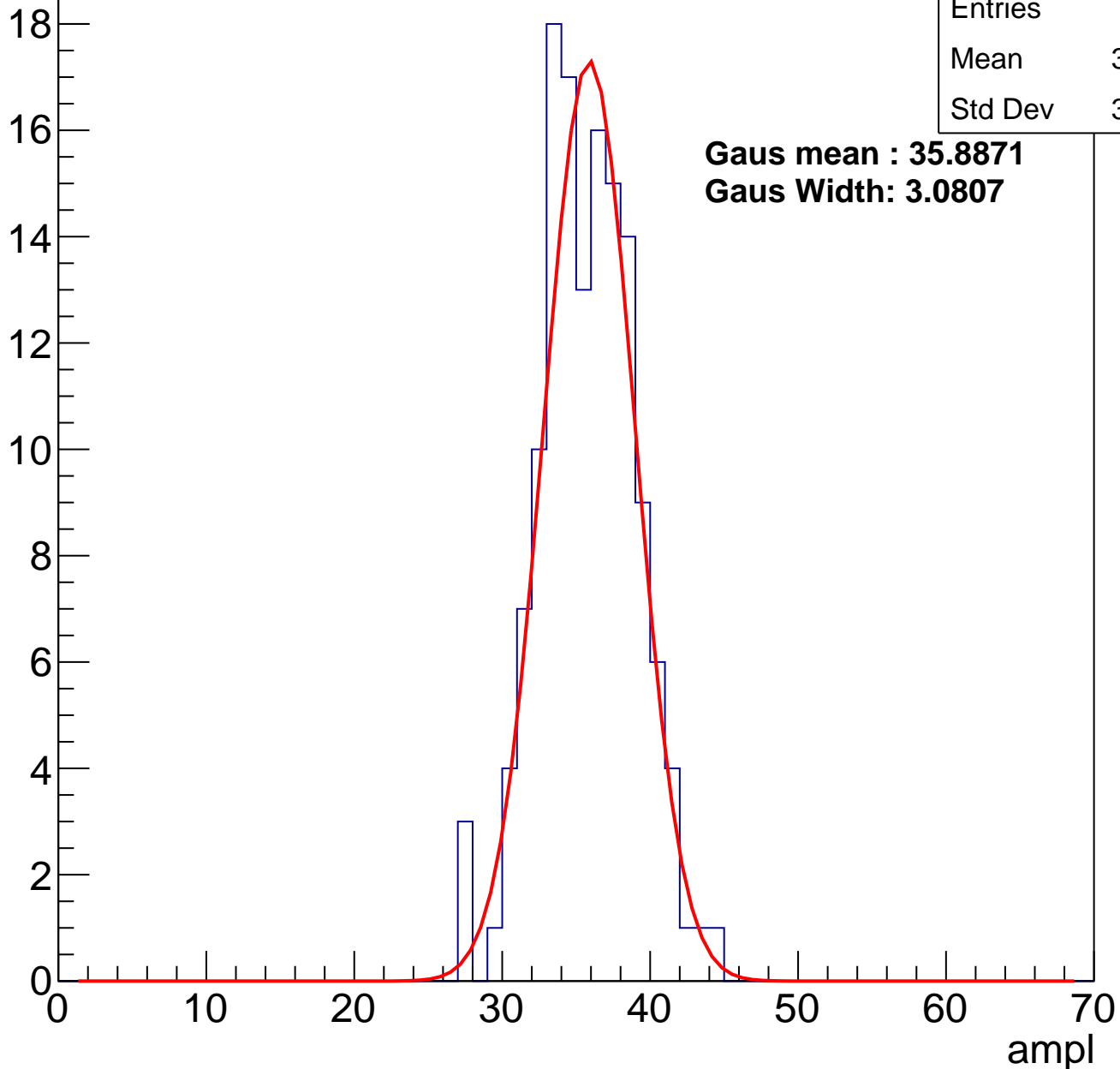
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 35.29 |
| Std Dev | 3.208 |

Gaus mean : 35.8871

Gaus Width: 3.0807

Entry



B1L001S, U26-ch37, adc2

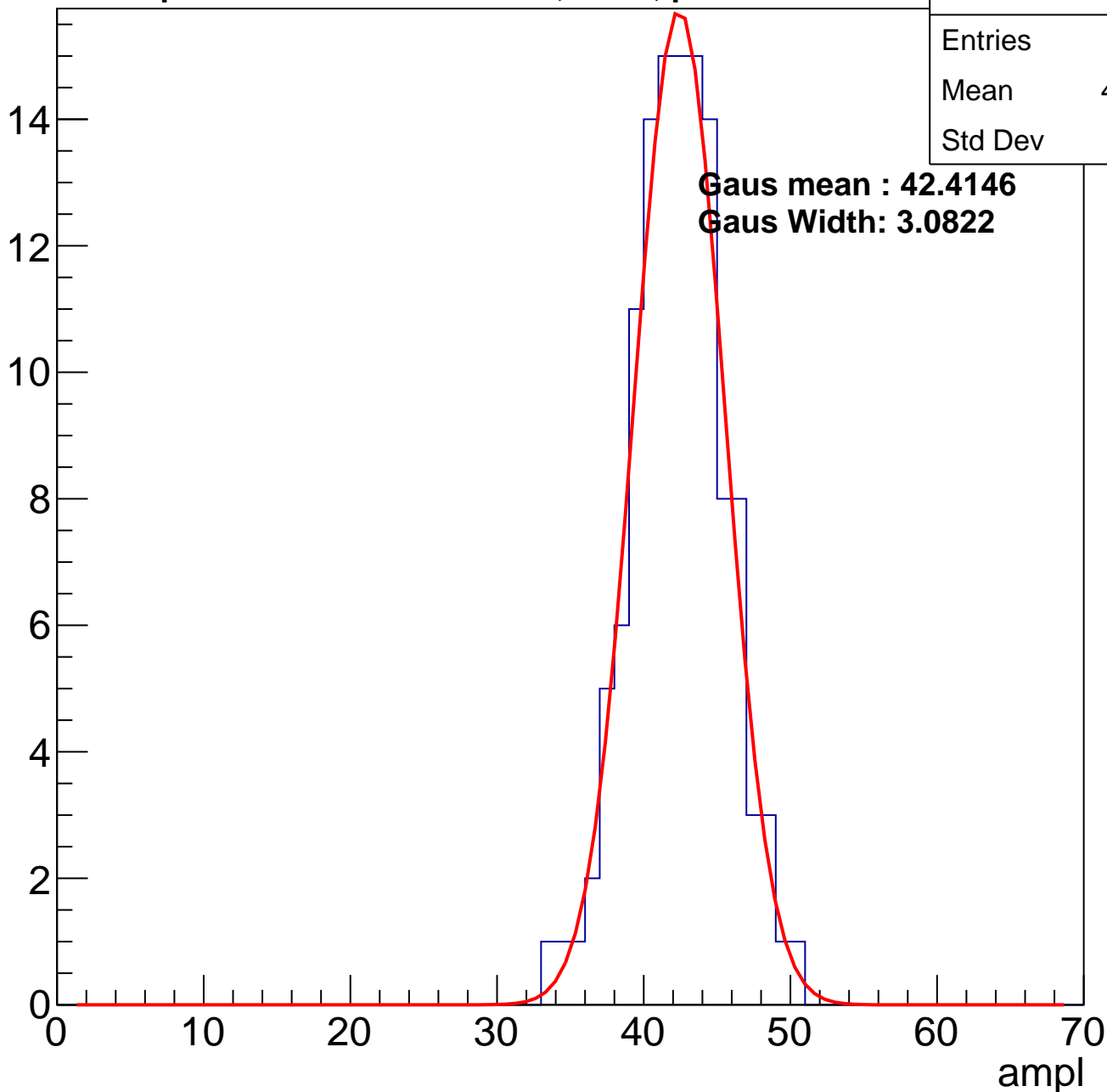
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 41.89 |
| Std Dev | 3.14 |

Gaus mean : 42.4146

Gaus Width: 3.0822

Entry



B1L001S, U26-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

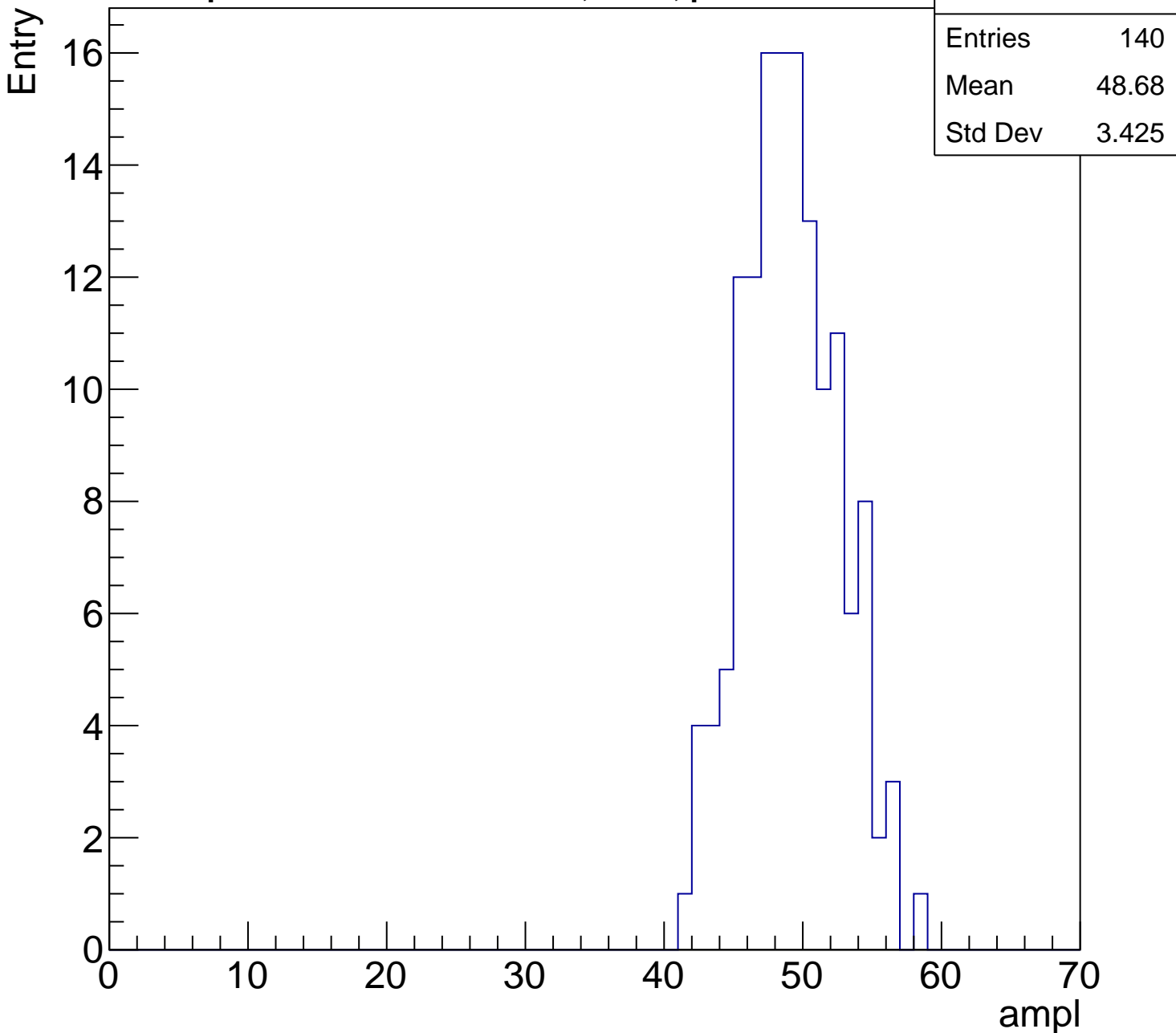
| | |
|---------|-------|
| Entries | 140 |
| Mean | 48.68 |
| Std Dev | 3.425 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

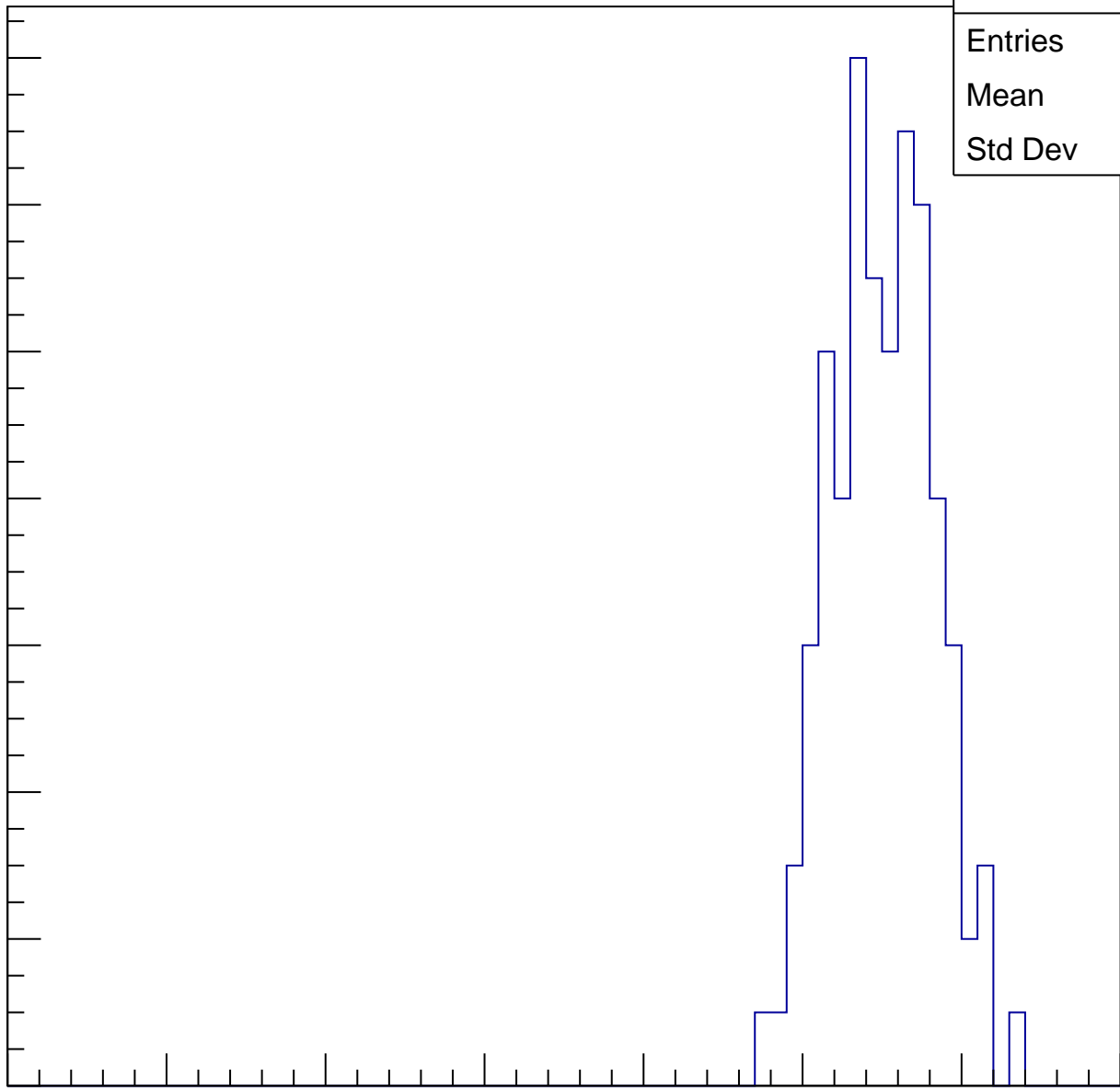
| | |
|---------|-------|
| Entries | 109 |
| Mean | 54.59 |
| Std Dev | 3.18 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch37, adc5

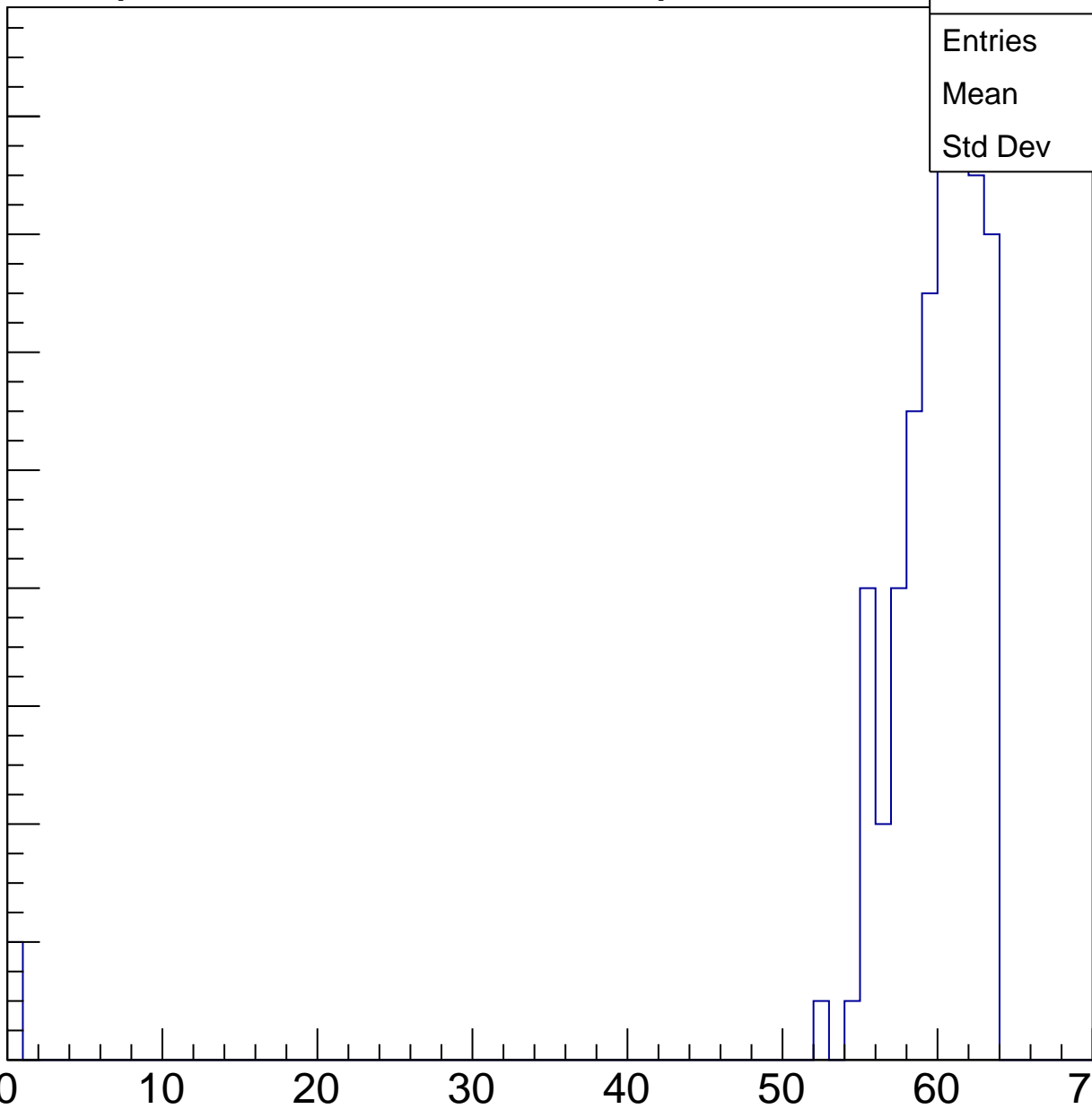
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 110 |
| Mean | 58.54 |
| Std Dev | 8.344 |

ampl

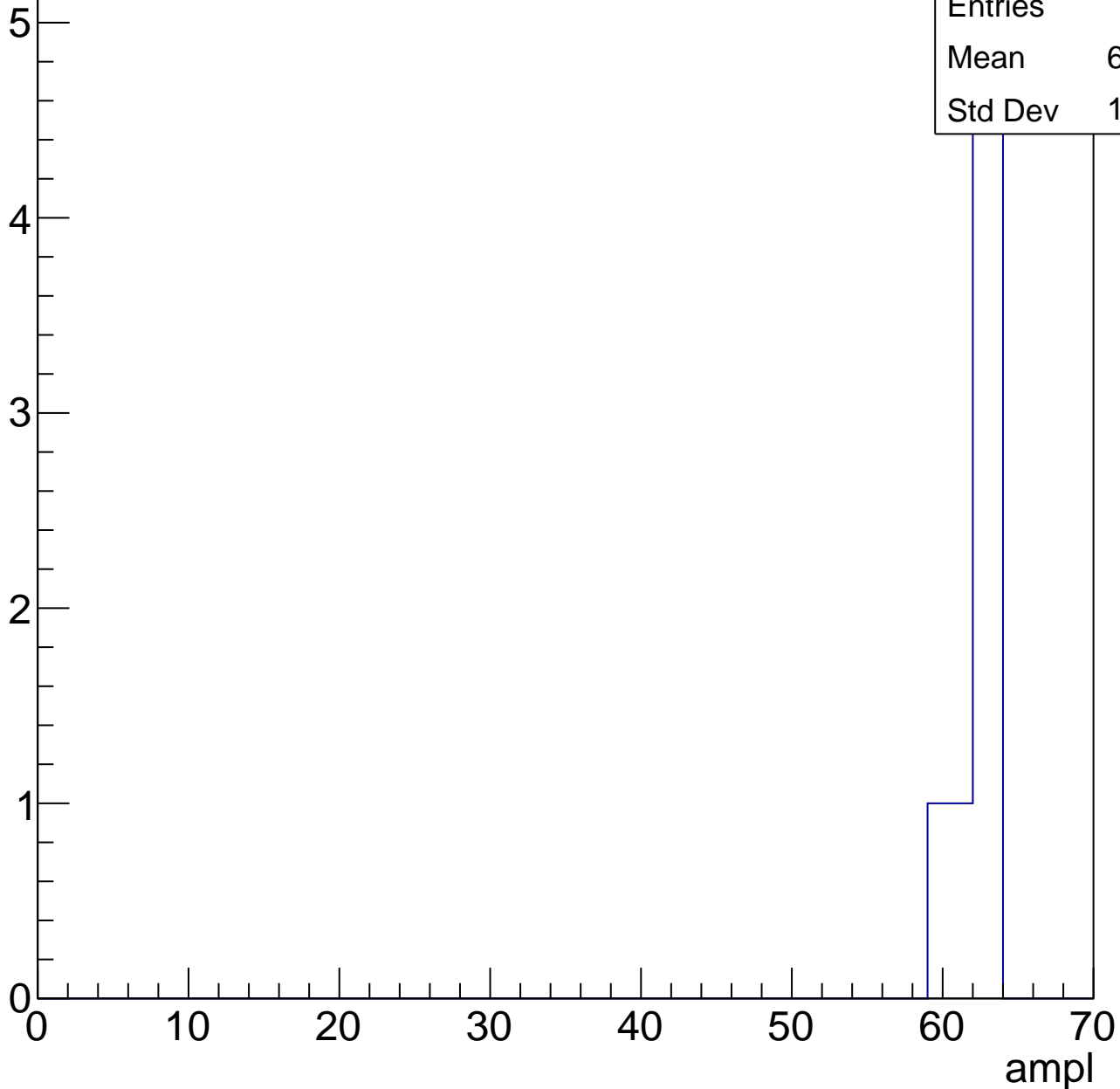


B1L001S, U26-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.92 |
| Std Dev | 1.206 |



B1L001S, U26-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 161 |
| Mean | 29.77 |
| Std Dev | 3.577 |

Gaus mean : 30.2403

Gaus Width: 3.6641

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

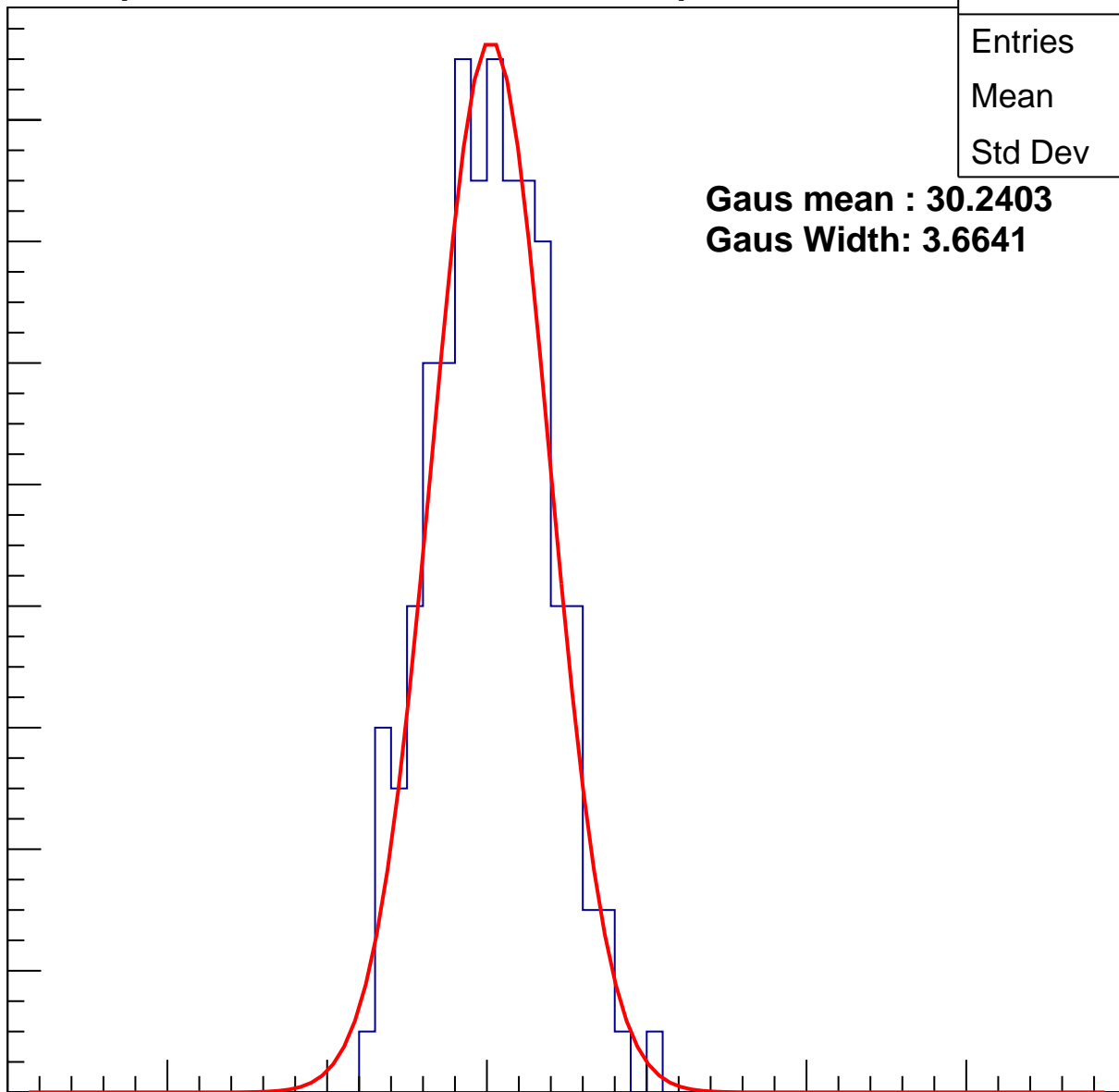
40

50

60

70

ampl



B1L001S, U26-ch38, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 36.53 |
| Std Dev | 3.397 |

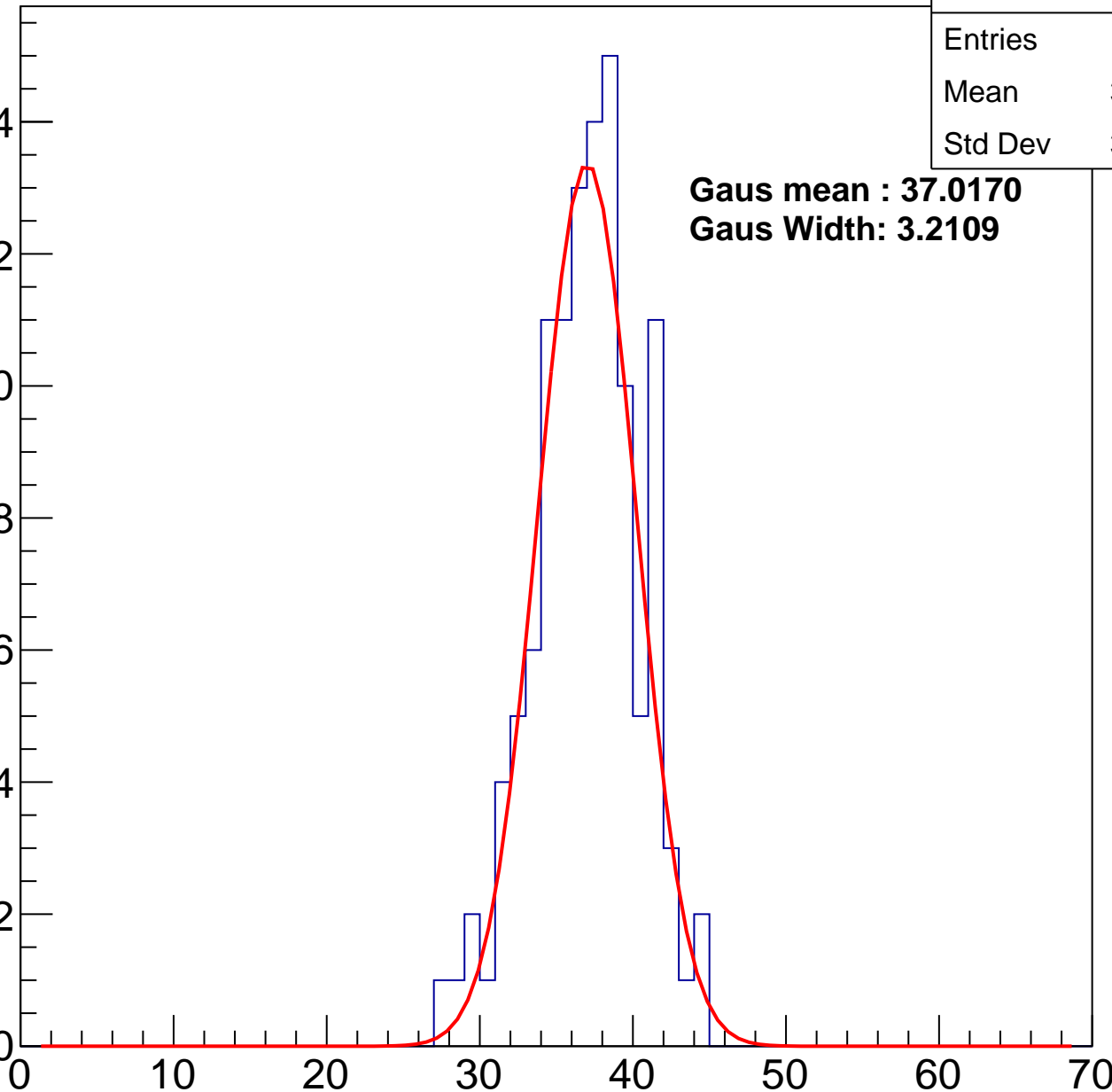
Gaus mean : 37.0170

Gaus Width: 3.2109

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 42.33 |
| Std Dev | 3.12 |

Gaus mean : 43.1007

Gaus Width: 3.2988

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

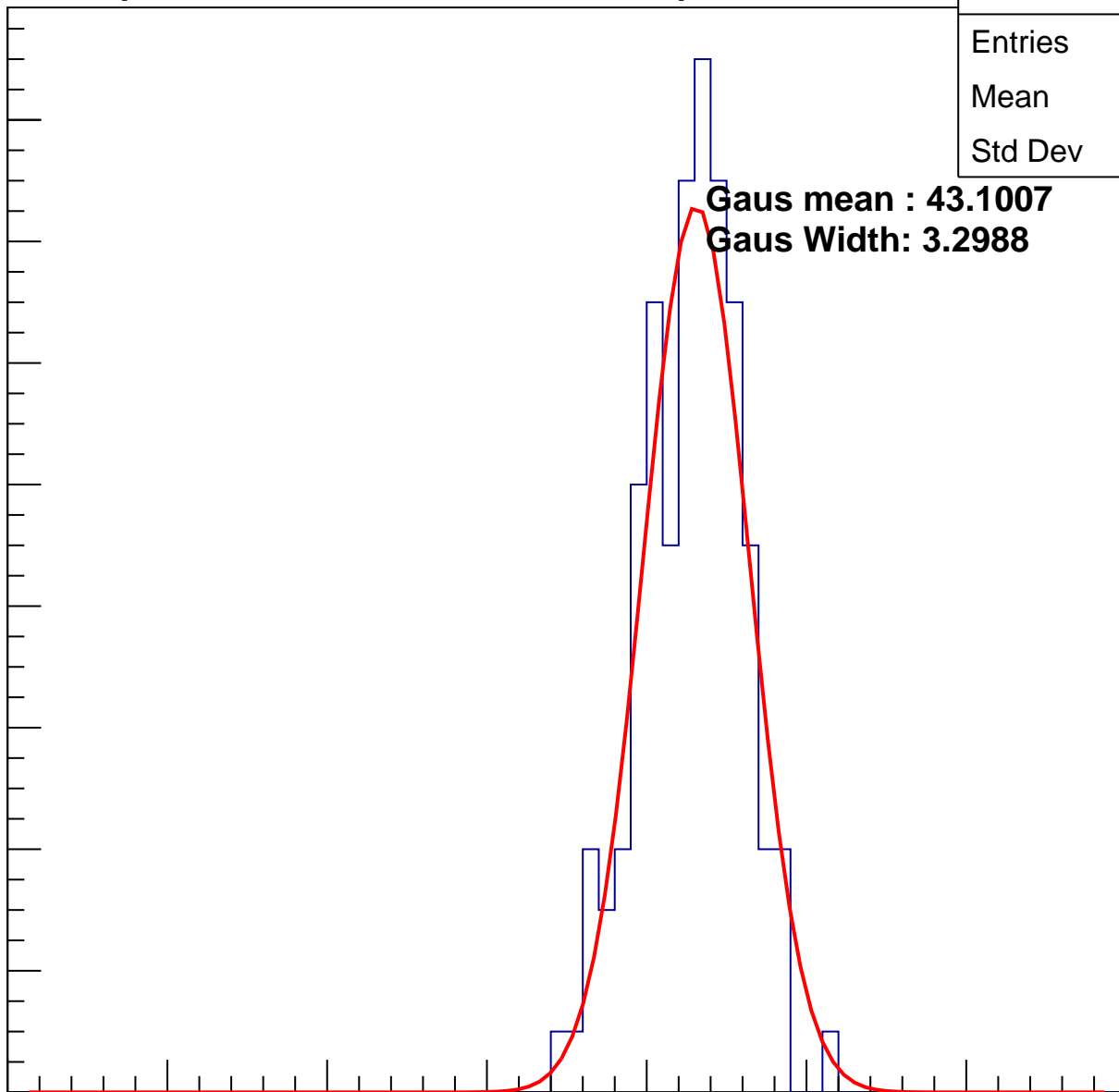
40

50

60

70

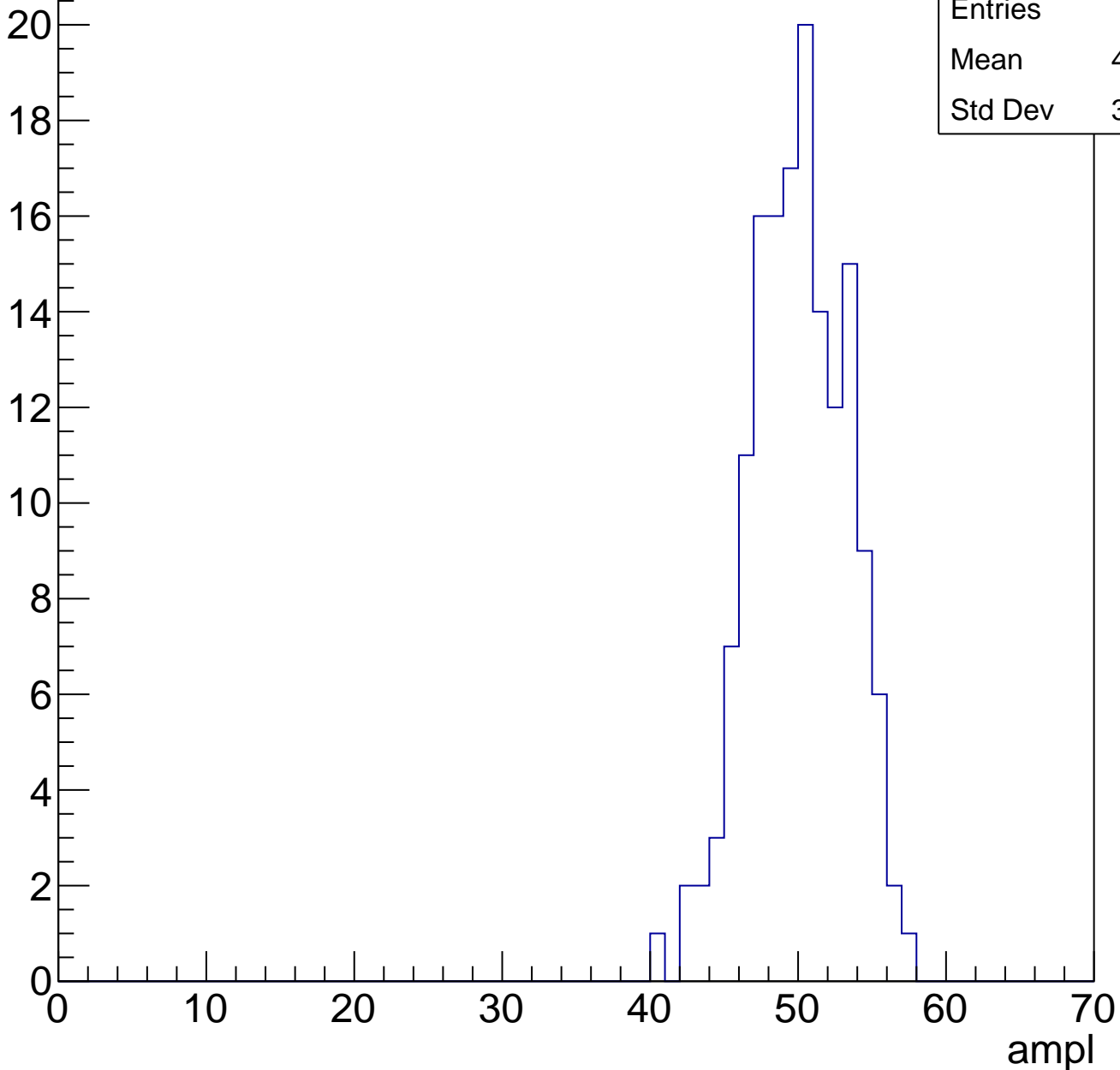
ampl



B1L001S, U26-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 98 |
| Mean | 56.01 |
| Std Dev | 3.059 |

Entry

12

10

8

6

4

2

0

0

10

20

30

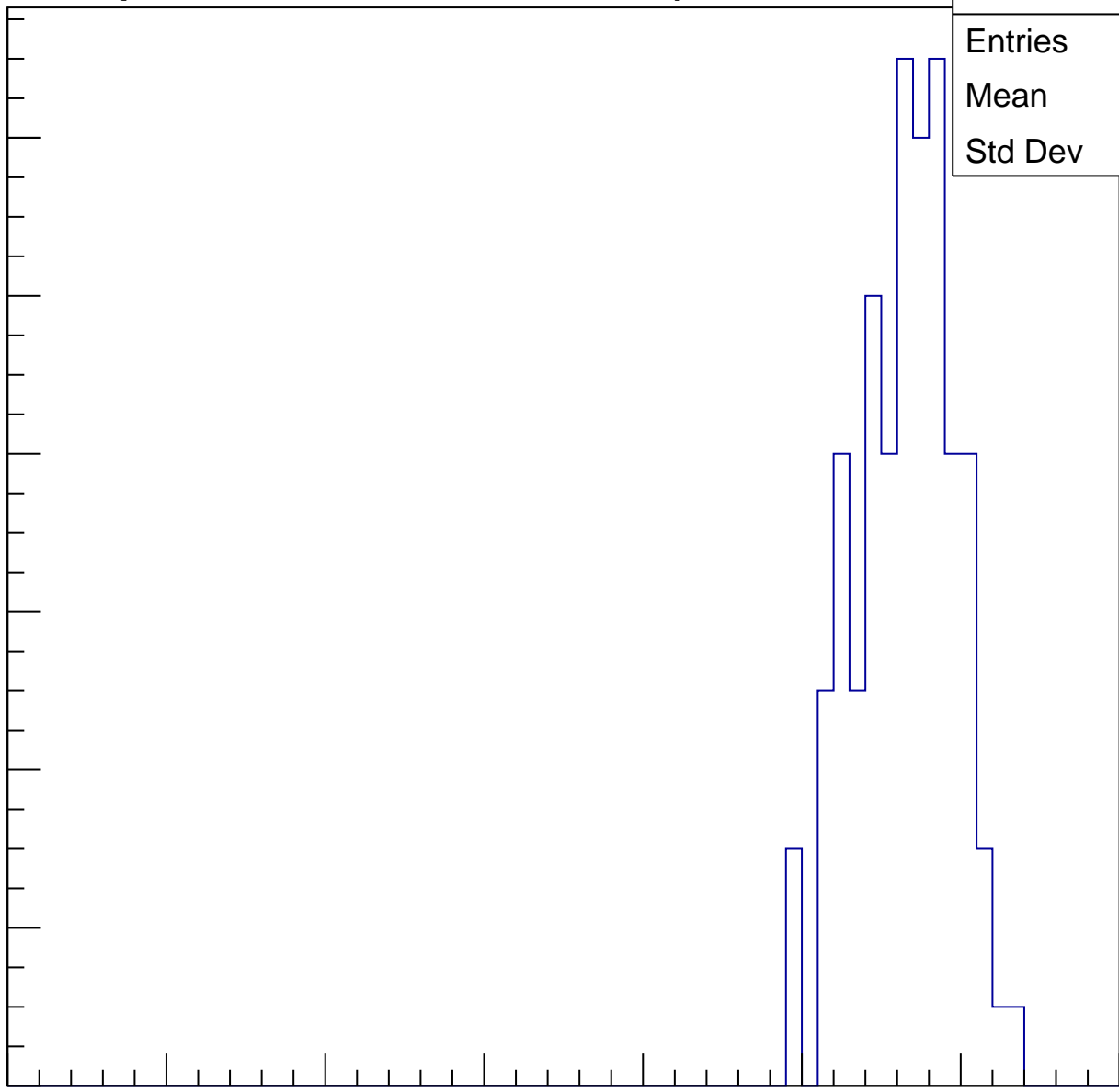
40

50

60

70

ampl



B1L001S, U26-ch38, adc5

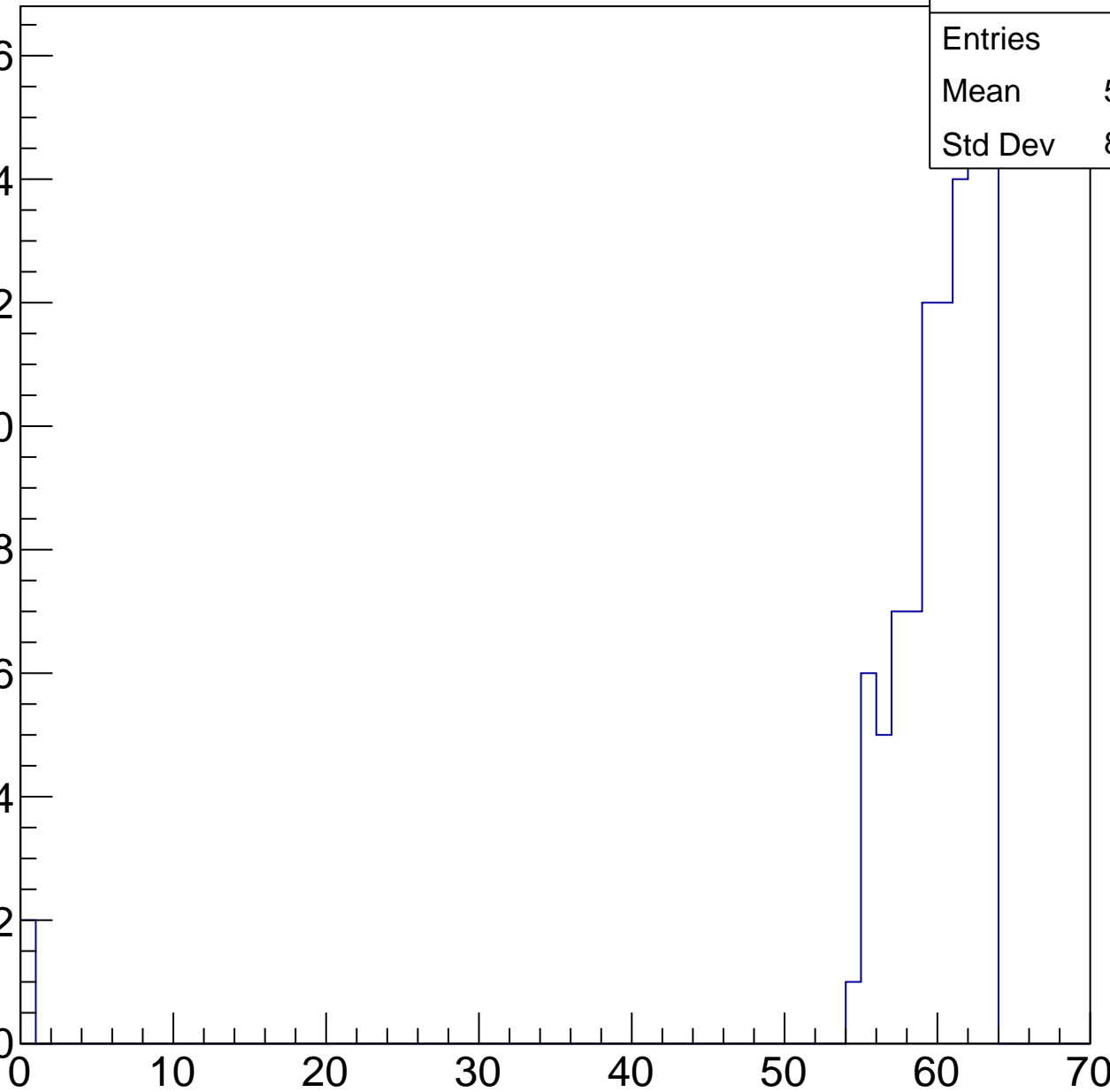
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 58.68 |
| Std Dev | 8.819 |

ampl



B1L001S, U26-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

62

Std Dev

0.8165

B1L001S, U26-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B1L001S, U26-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 30.12 |
| Std Dev | 4.028 |

Gaus mean : 30.9561

Gaus Width: 3.1282

Entry

14

12

10

8

6

4

2

0

0

10

20

30

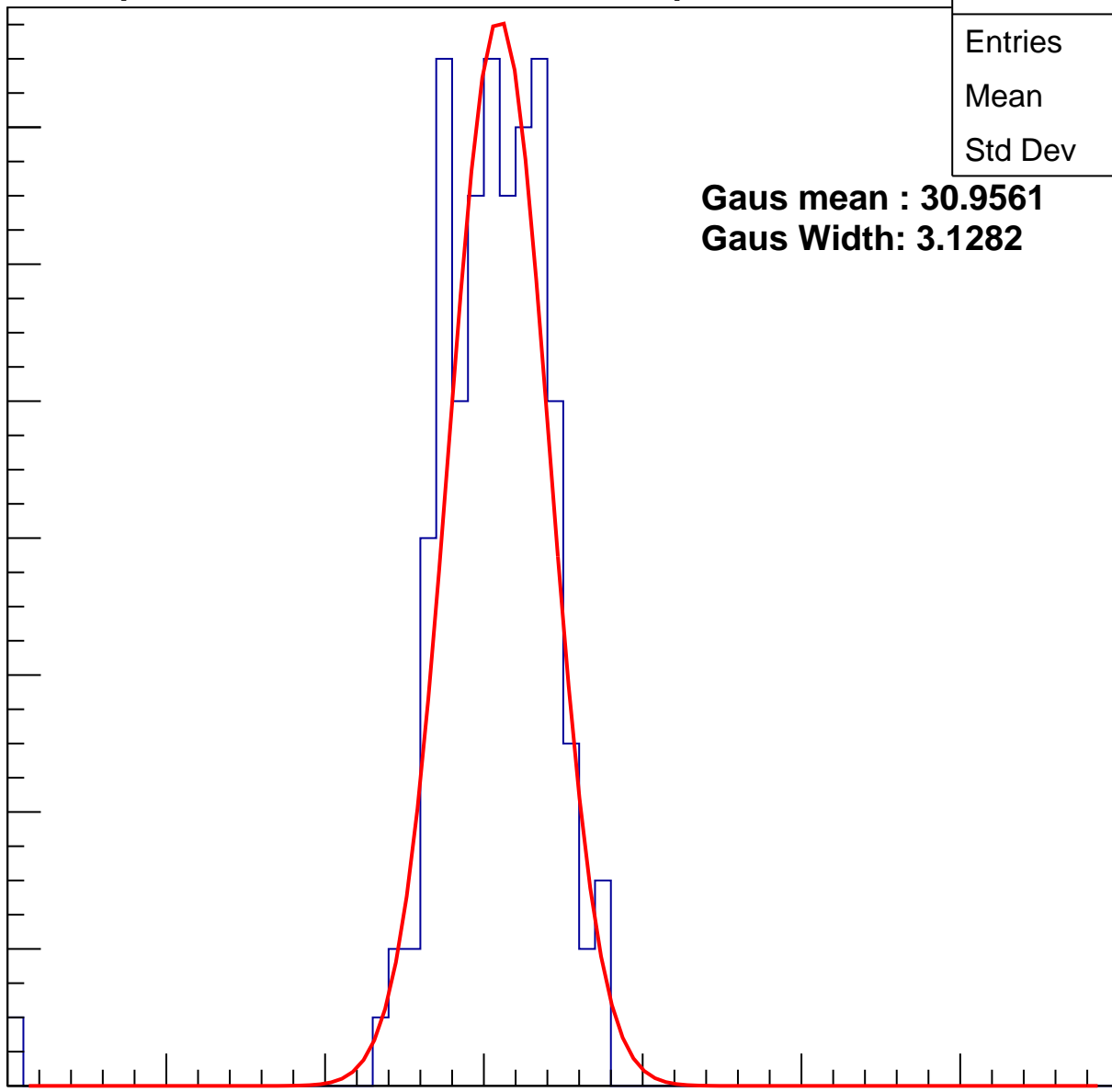
40

50

60

70

ampl



B1L001S, U26-ch39, adc1

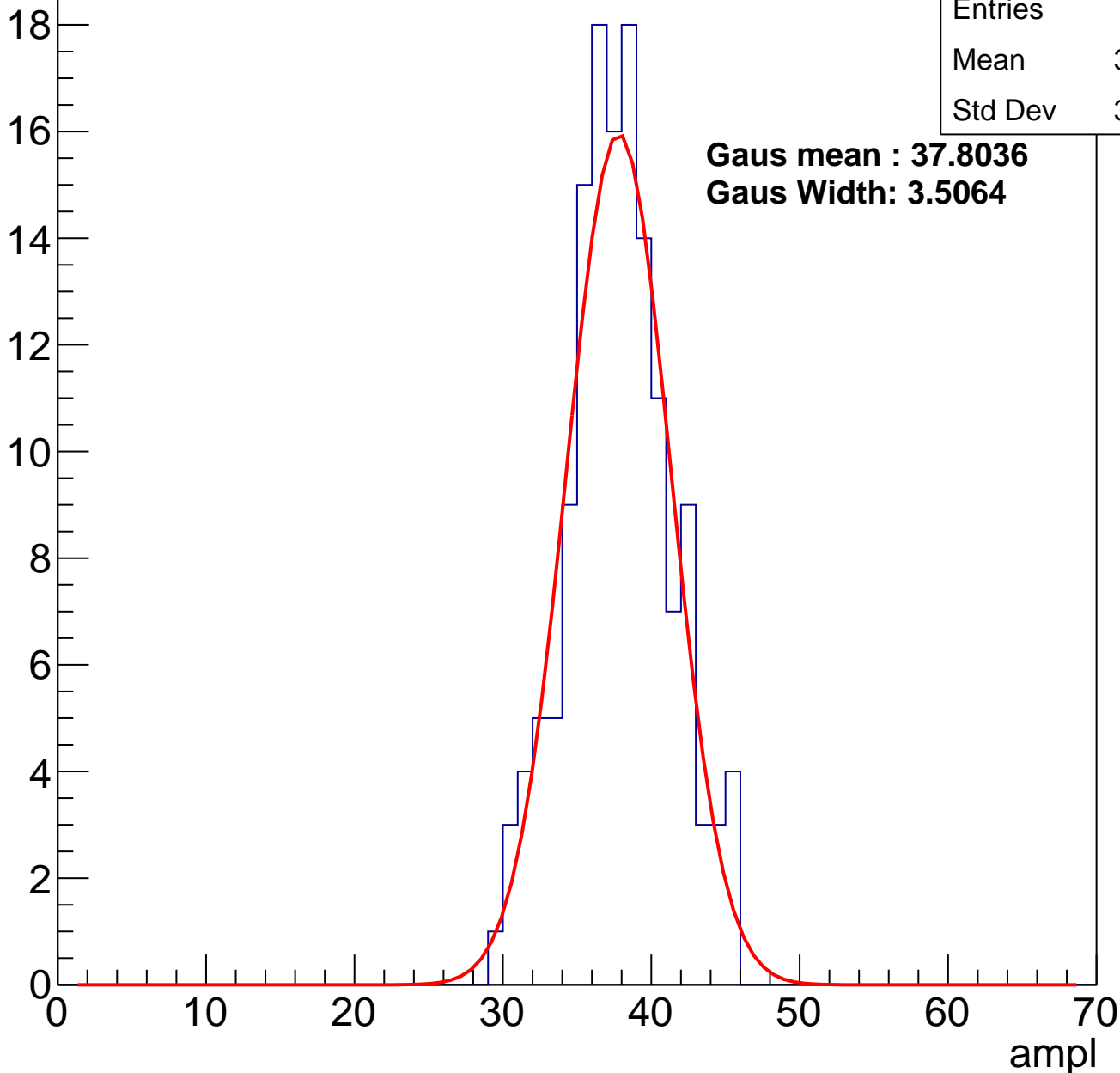
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 37.34 |
| Std Dev | 3.447 |

Gaus mean : 37.8036

Gaus Width: 3.5064

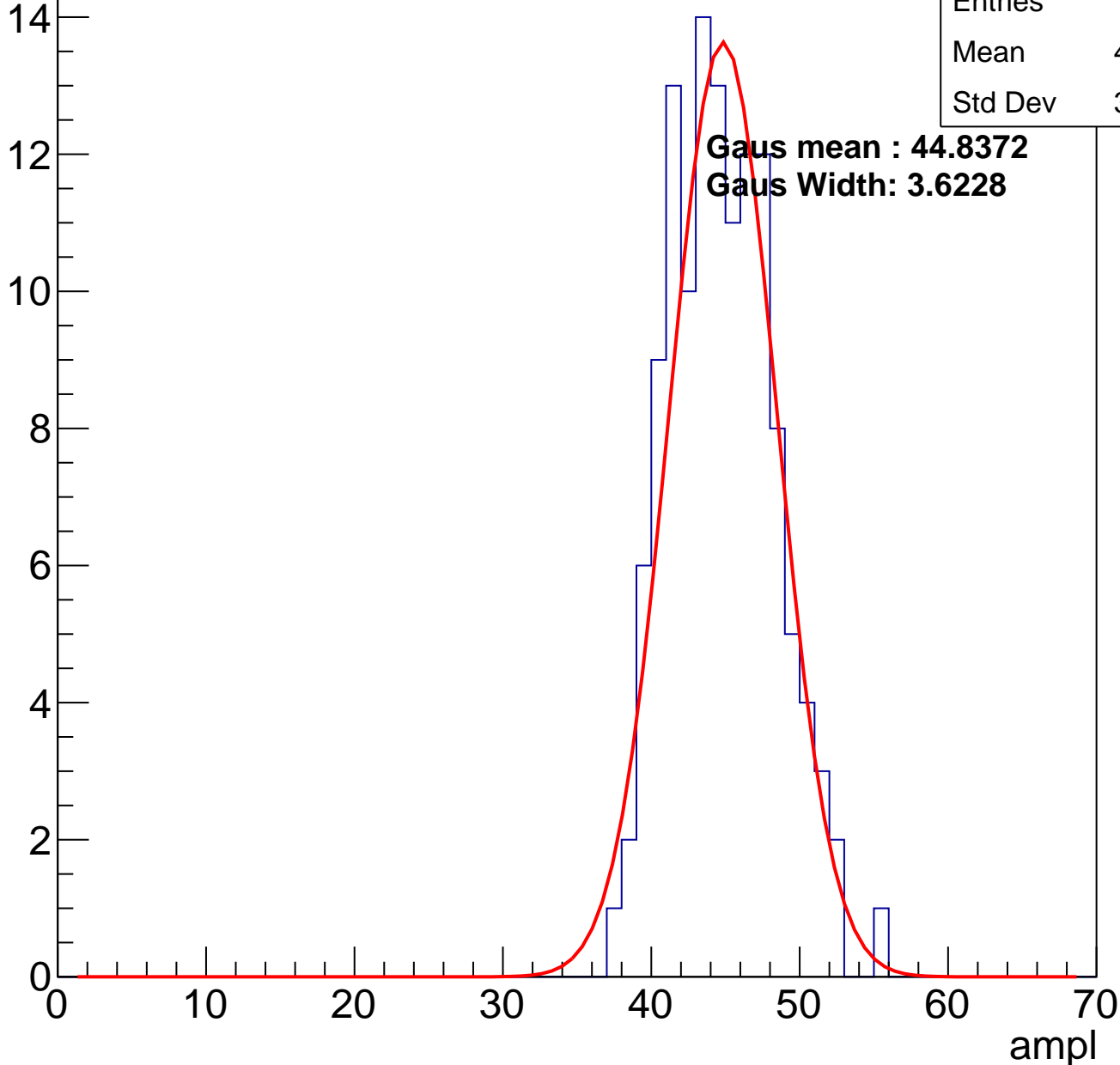
Entry



B1L001S, U26-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

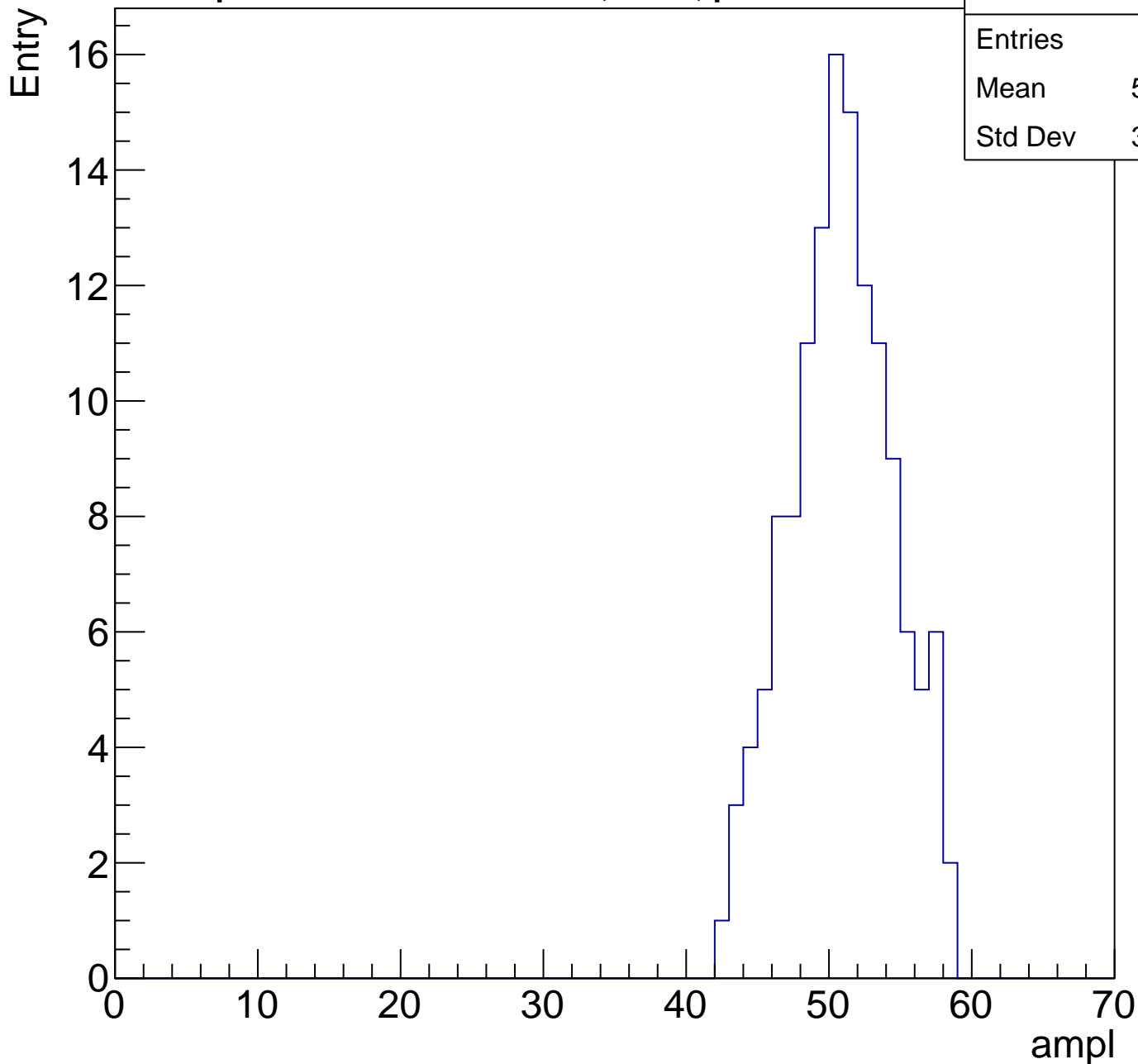
Entry



B1L001S, U26-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

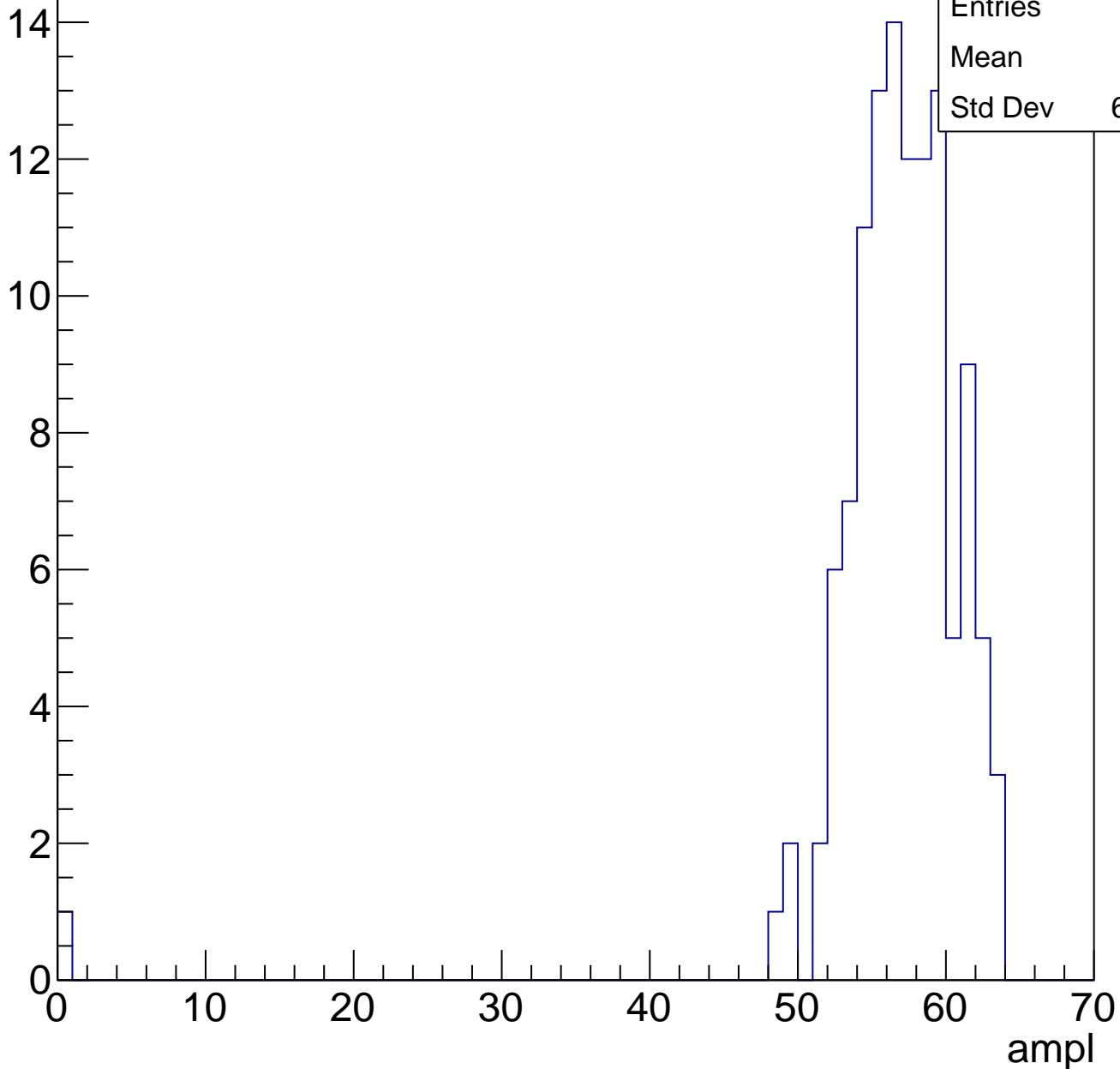
| | |
|---------|-------|
| Entries | 135 |
| Mean | 50.42 |
| Std Dev | 3.622 |



B1L001S, U26-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch39, adc5

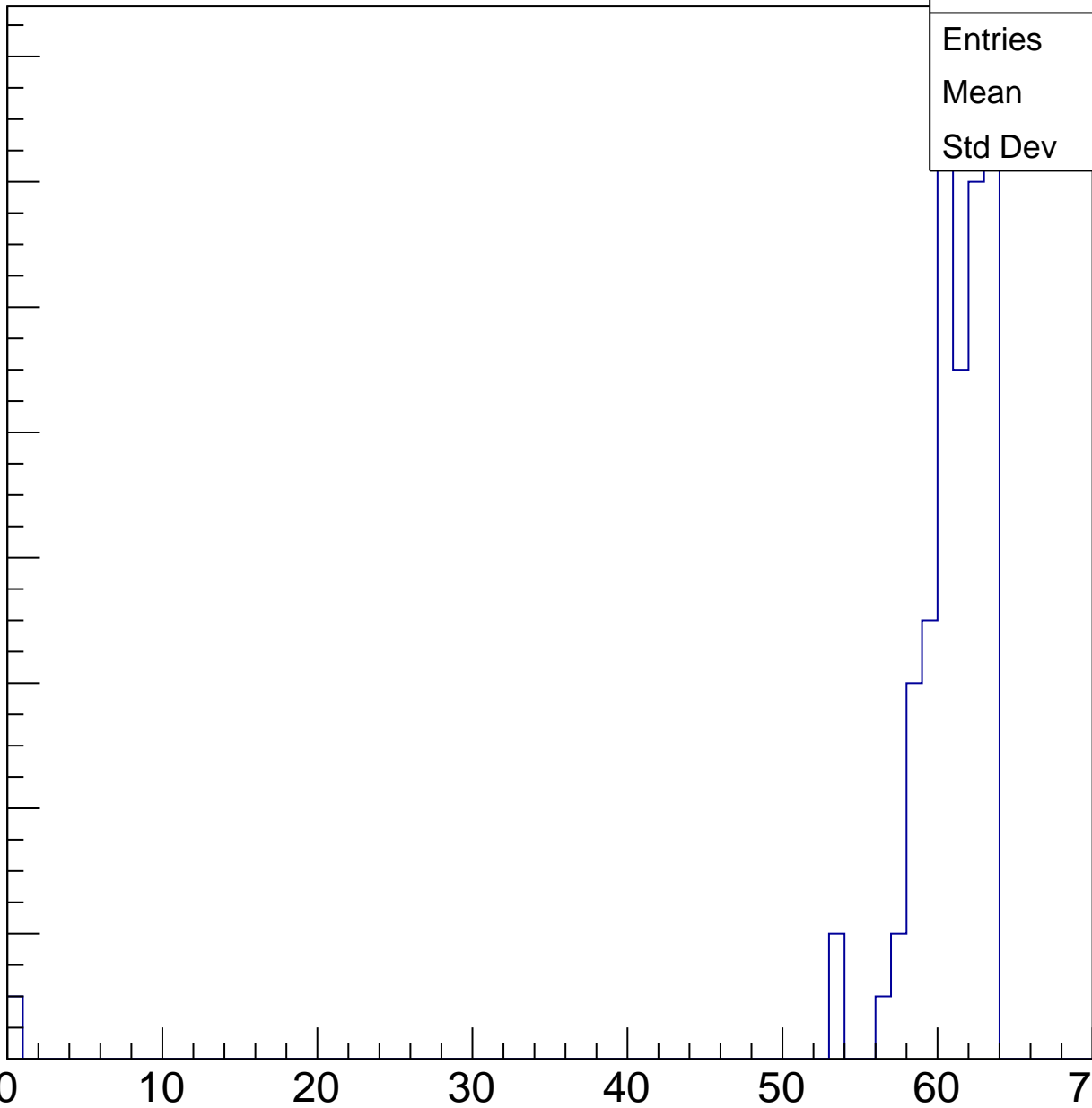
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 75 |
| Mean | 59.79 |
| Std Dev | 7.276 |

ampl



B1L001S, U26-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 12 |
| Std Dev | 12 |

B1L001S, U26-ch40, adc0

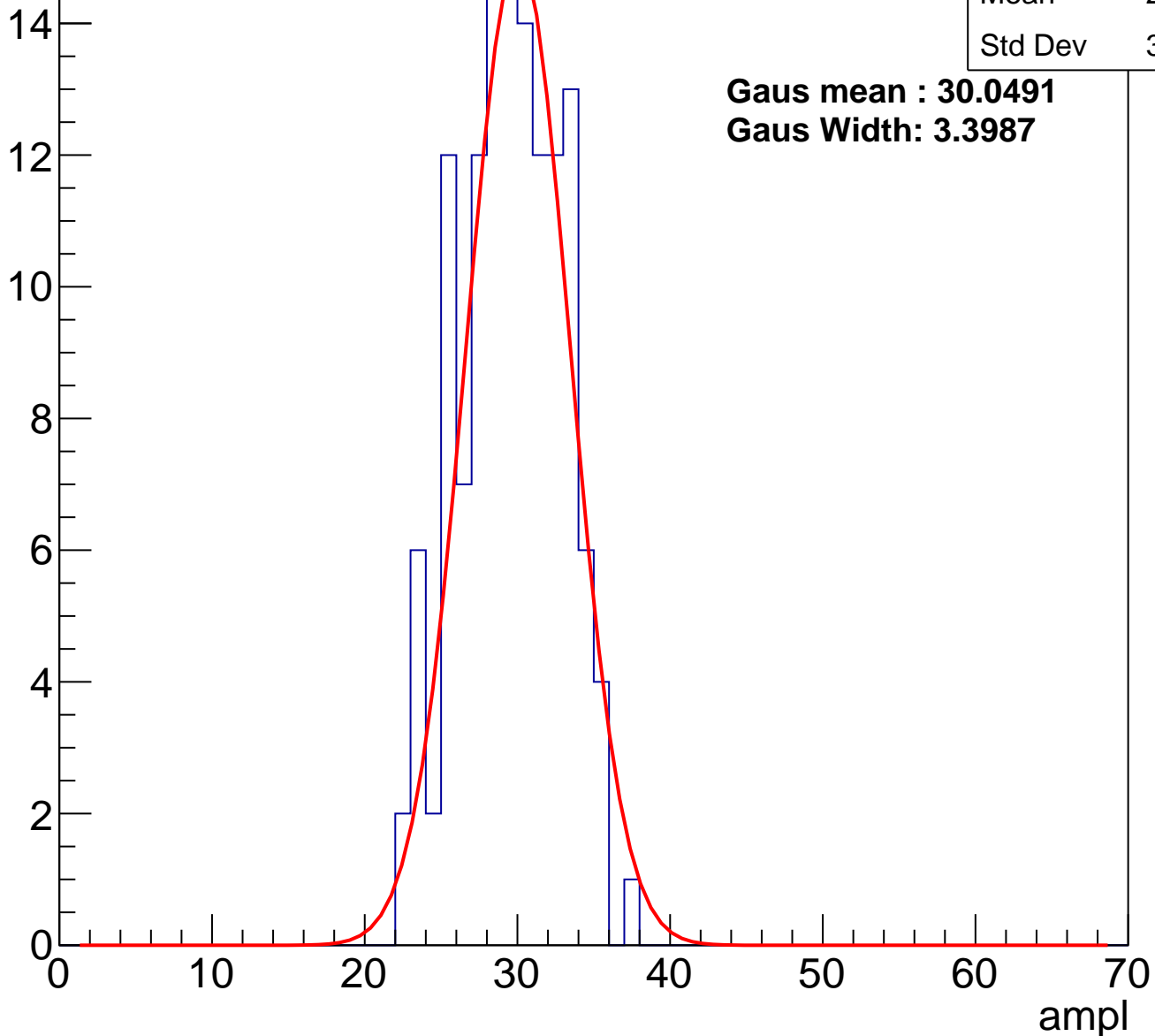
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 29.15 |
| Std Dev | 3.262 |

Gaus mean : 30.0491

Gaus Width: 3.3987

Entry



B1L001S, U26-ch40, adc1

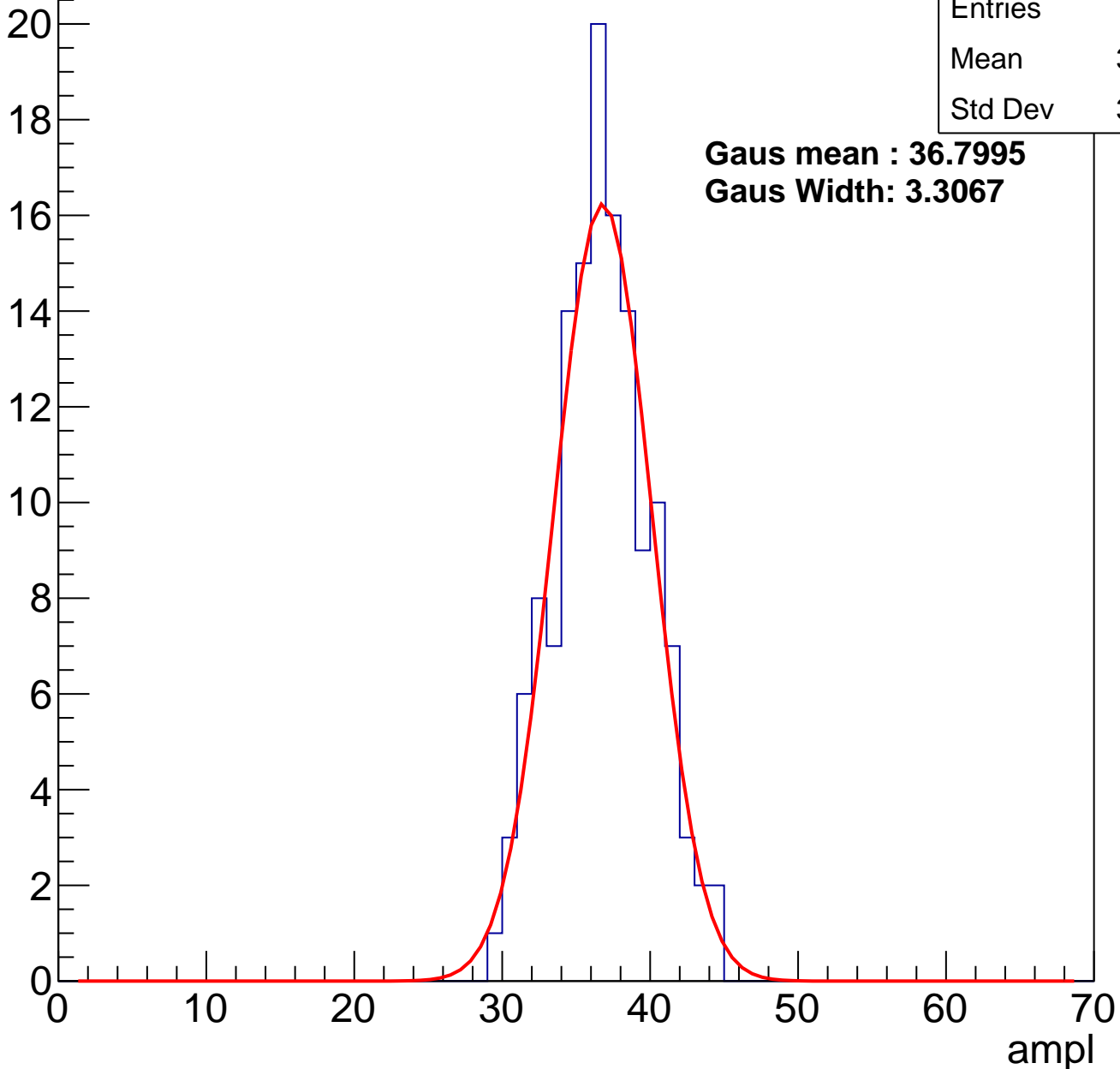
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 137 |
| Mean | 36.31 |
| Std Dev | 3.171 |

Gaus mean : 36.7995

Gaus Width: 3.3067

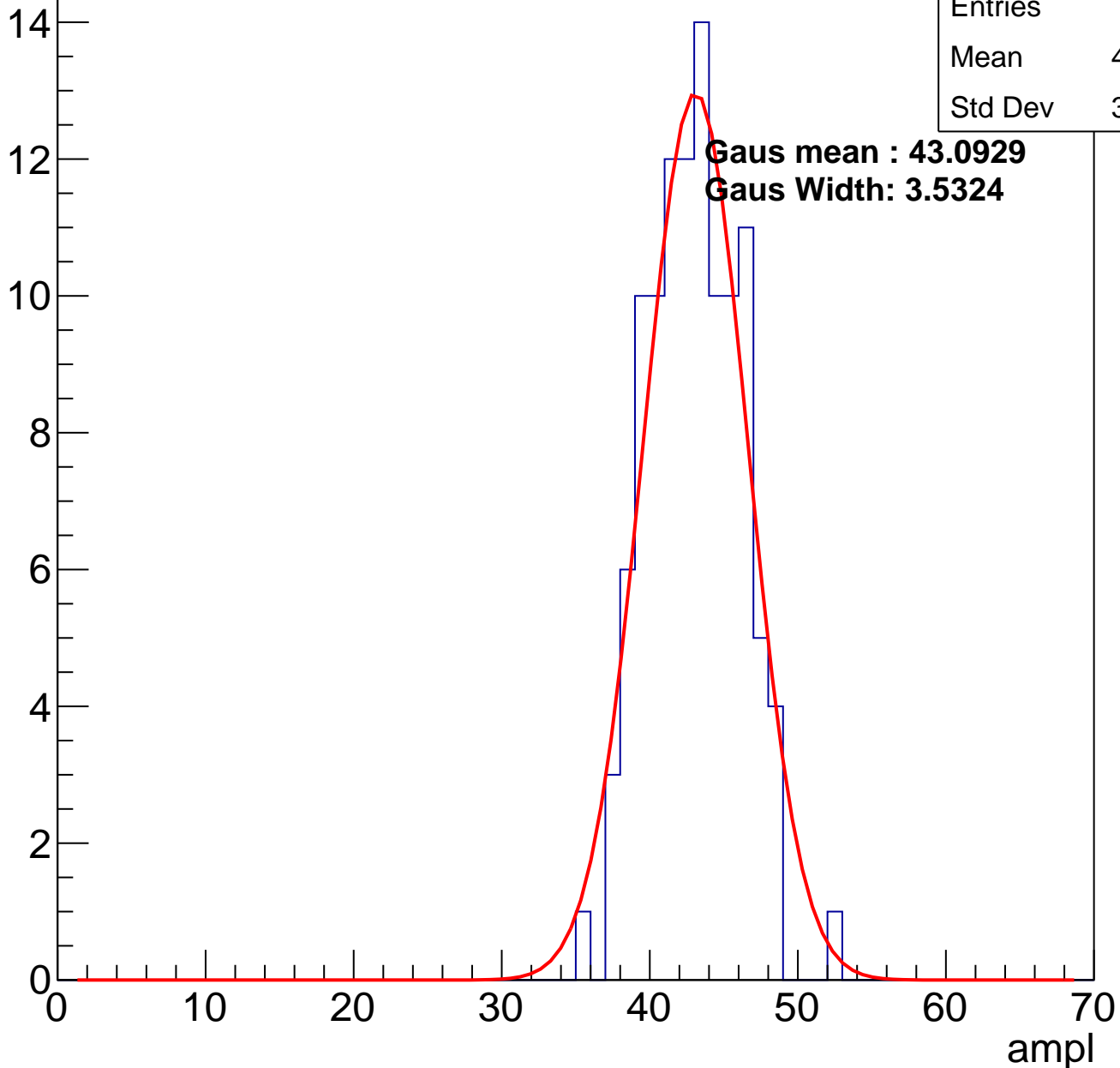
Entry



B1L001S, U26-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

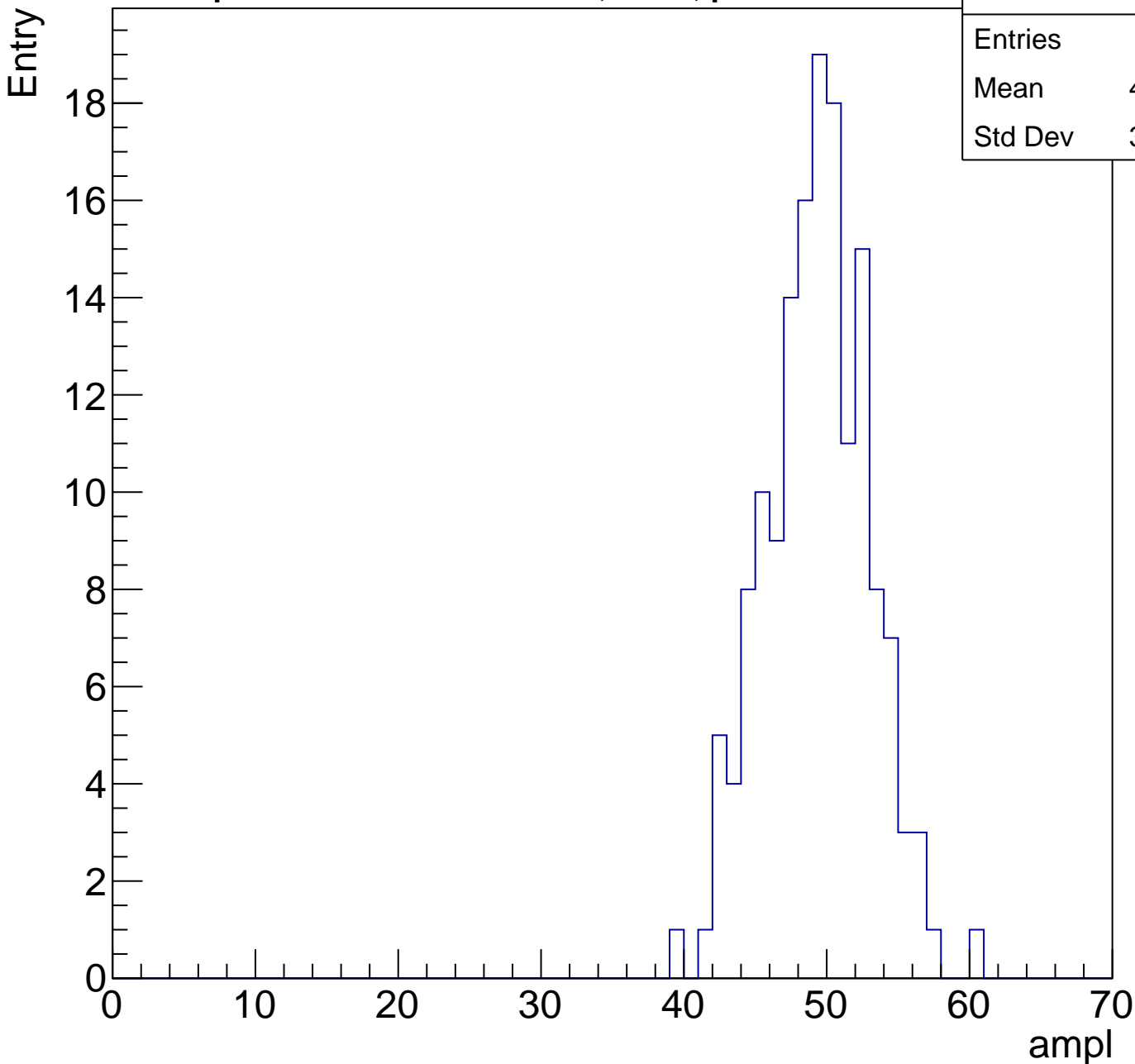
Entry



B1L001S, U26-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

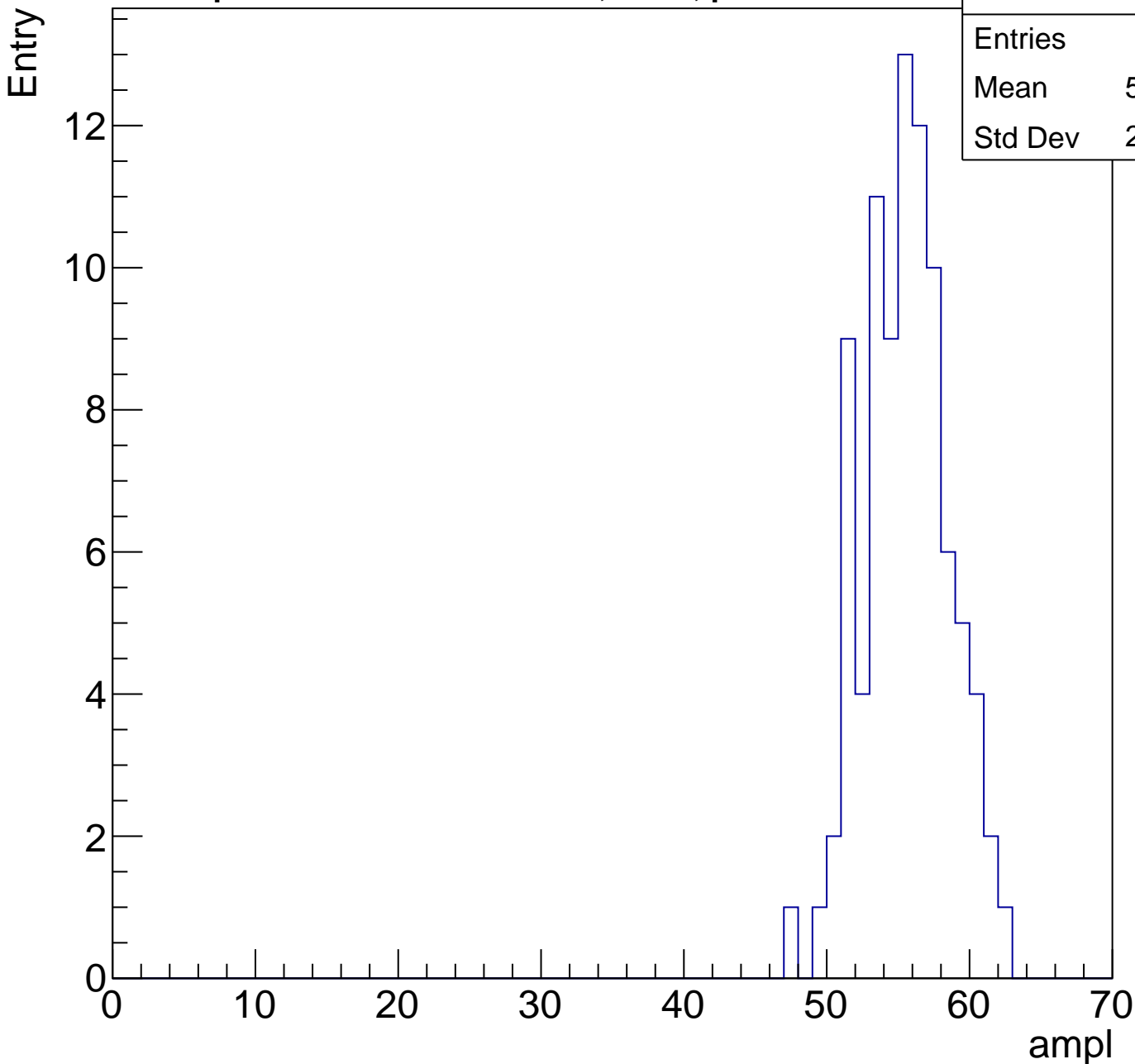
| | |
|---------|-------|
| Entries | 154 |
| Mean | 48.88 |
| Std Dev | 3.607 |



B1L001S, U26-ch40, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 90 |
| Mean | 55.07 |
| Std Dev | 2.962 |



B1L001S, U26-ch40, adc5

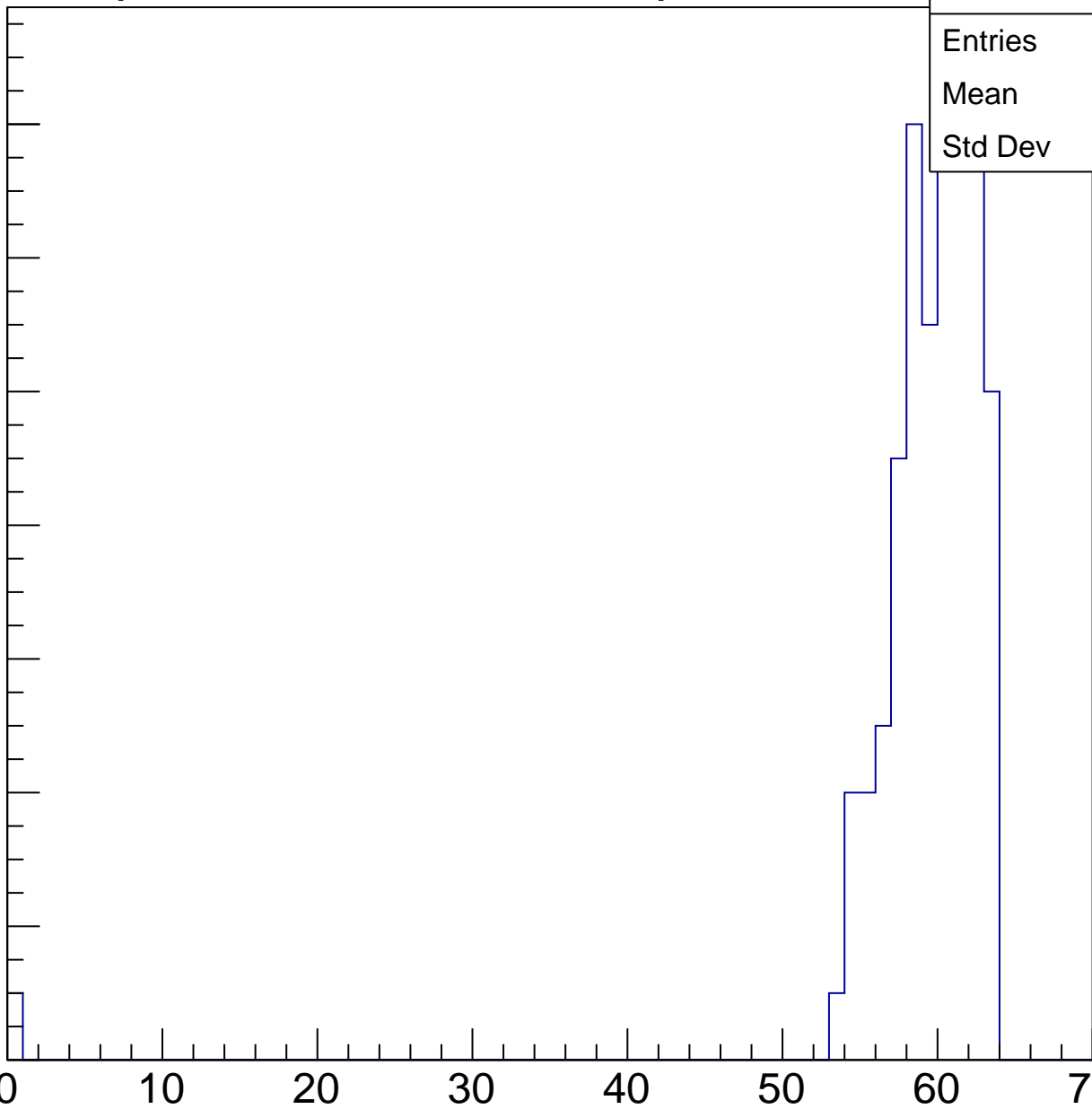
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 102 |
| Mean | 58.79 |
| Std Dev | 6.368 |

ampl

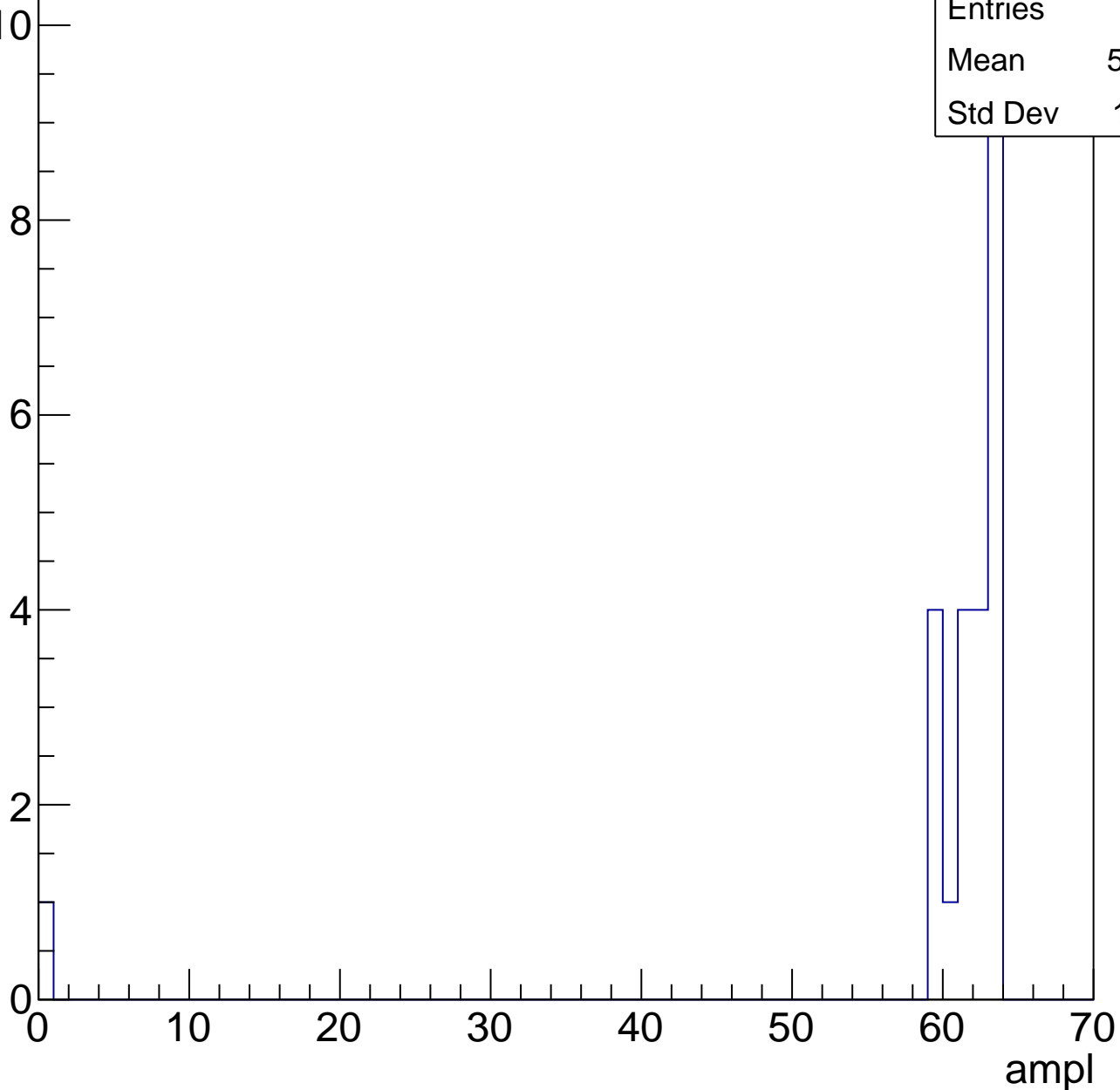


B1L001S, U26-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 59.08 |
| Std Dev | 12.41 |



B1L001S, U26-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



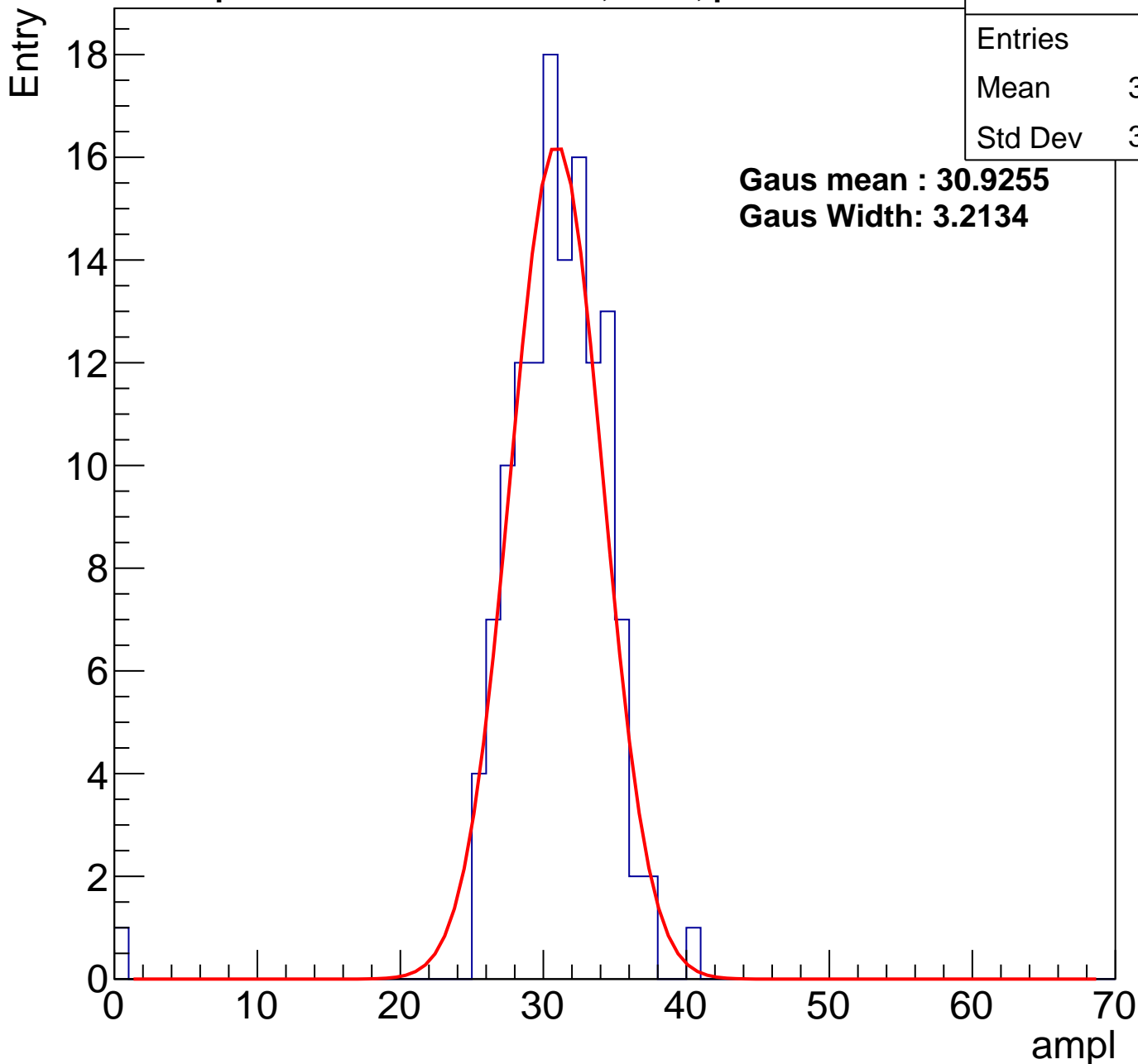
B1L001S, U26-ch41, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 30.47 |
| Std Dev | 3.978 |

Gaus mean : 30.9255

Gaus Width: 3.2134



B1L001S, U26-ch41, adc1

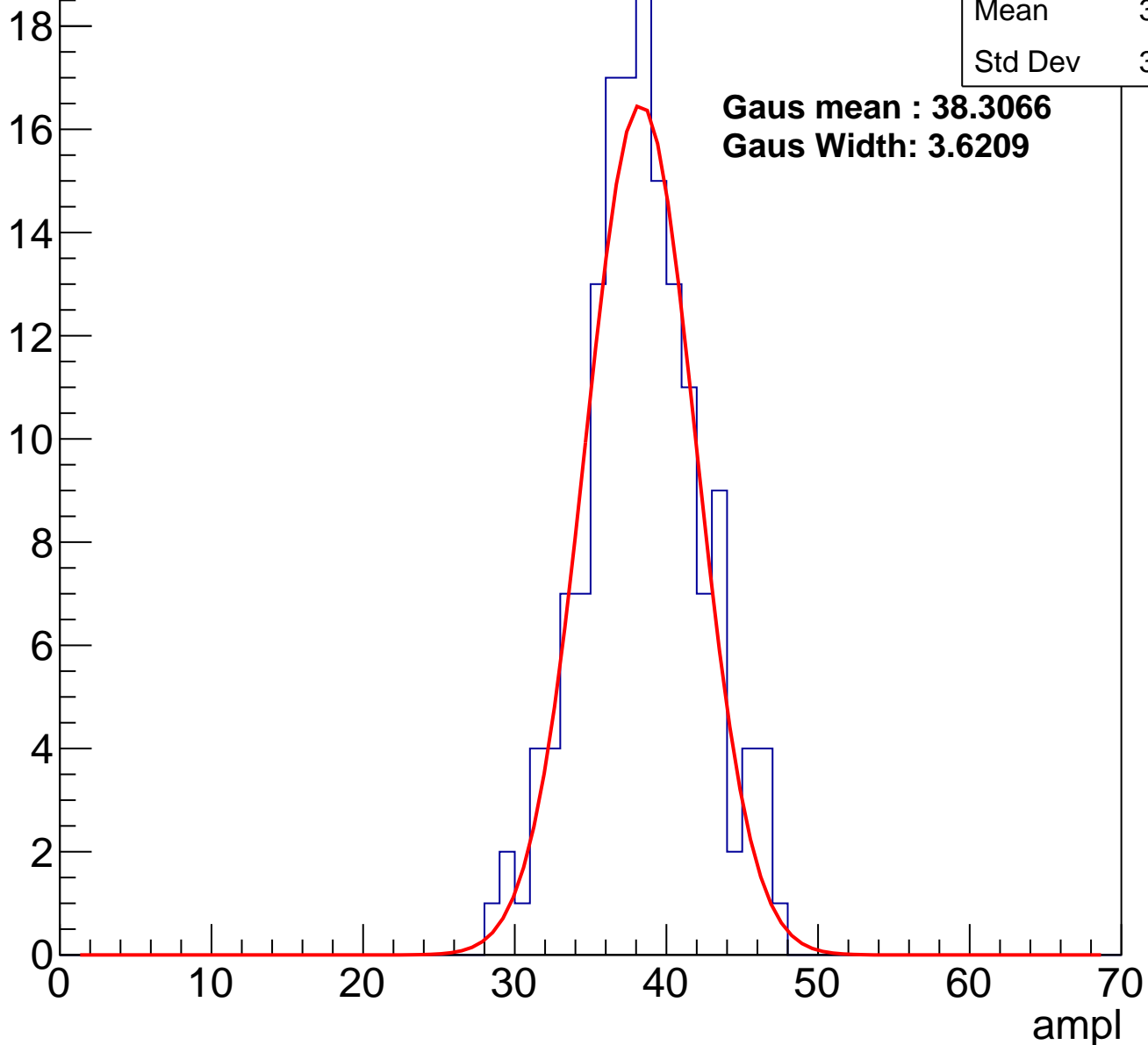
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 37.92 |
| Std Dev | 3.784 |

Gaus mean : 38.3066

Gaus Width: 3.6209

Entry



B1L001S, U26-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 104 |
| Mean | 44.07 |
| Std Dev | 3.126 |

Gaus mean : 44.5964

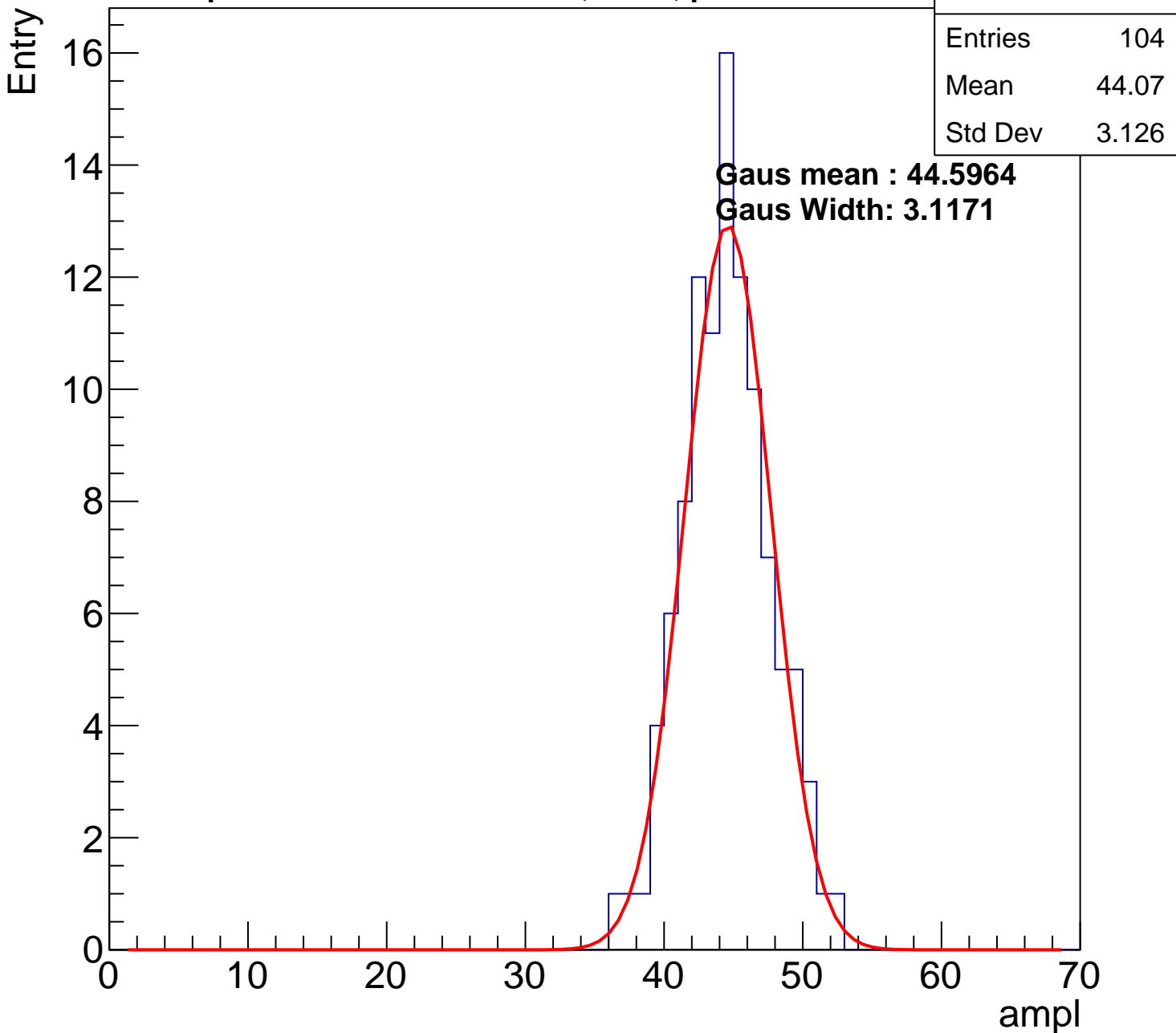
Gaus Width: 3.1171

Entry

16
14
12
10
8
6
4
2
0

ampl

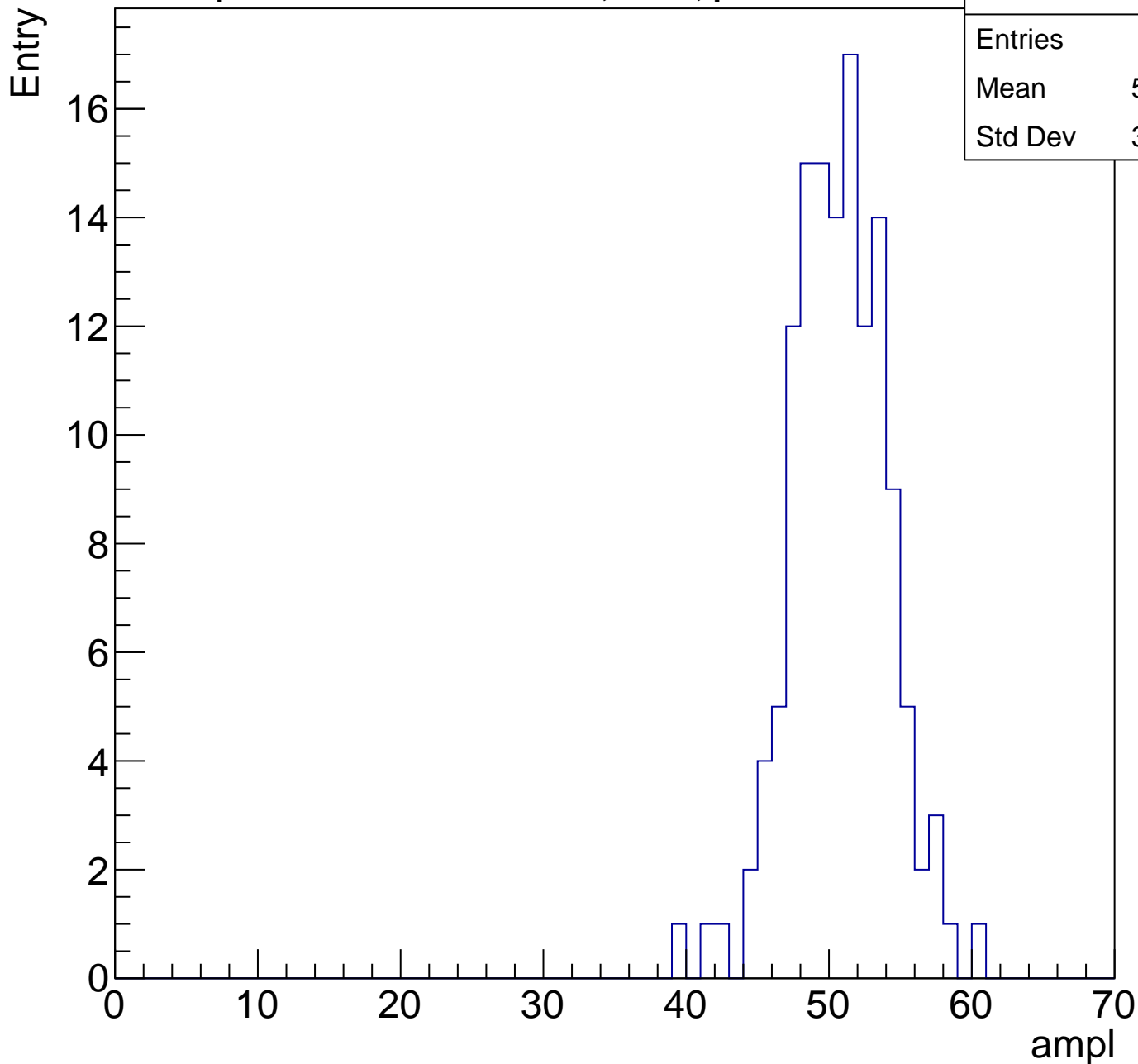
0 10 20 30 40 50 60 70



B1L001S, U26-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 50.25 |
| Std Dev | 3.385 |



B1L001S, U26-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

137

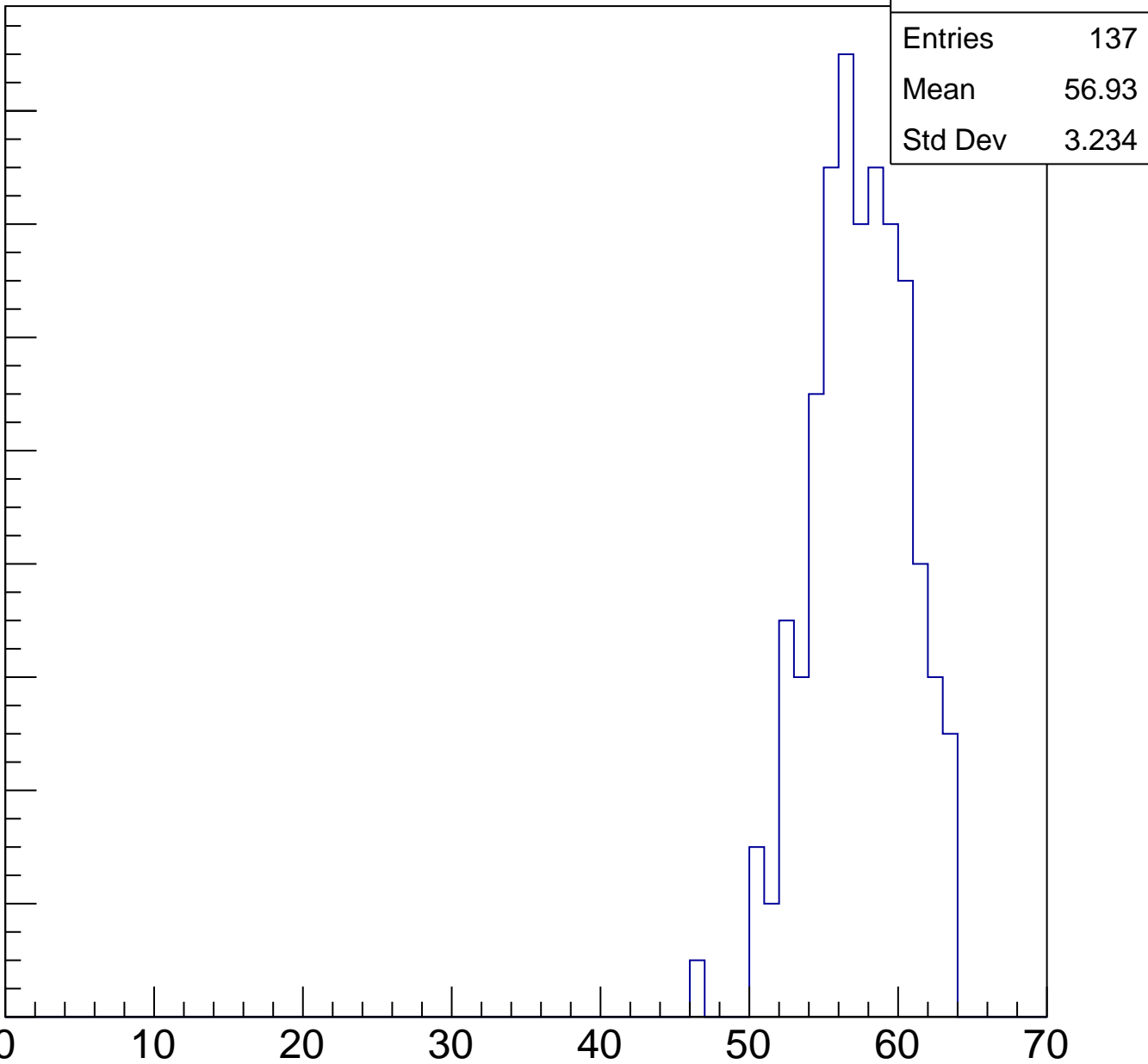
Mean

56.93

Std Dev

3.234

ampl



B1L001S, U26-ch41, adc5

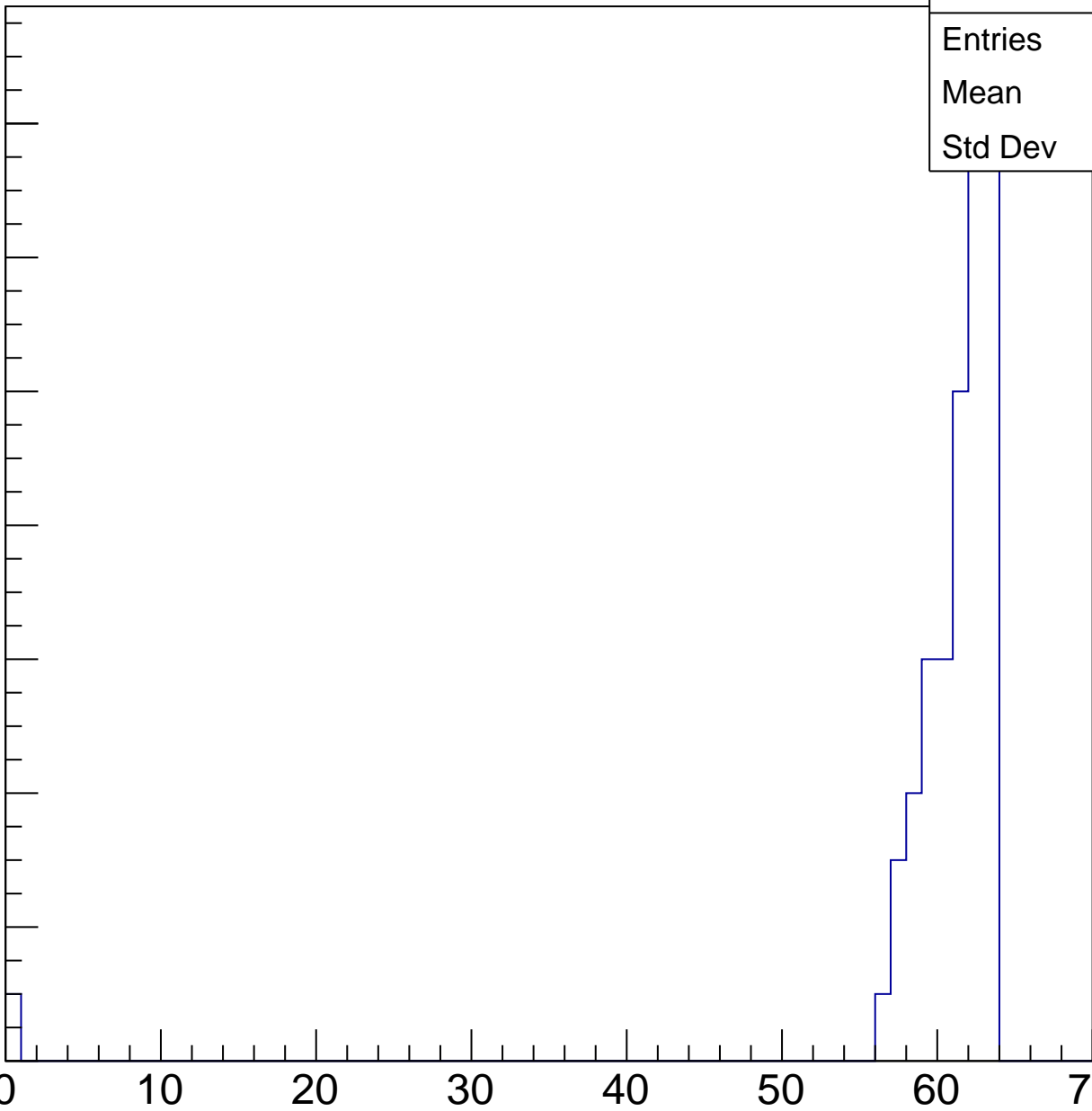
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 60 |
| Mean | 59.93 |
| Std Dev | 8.027 |

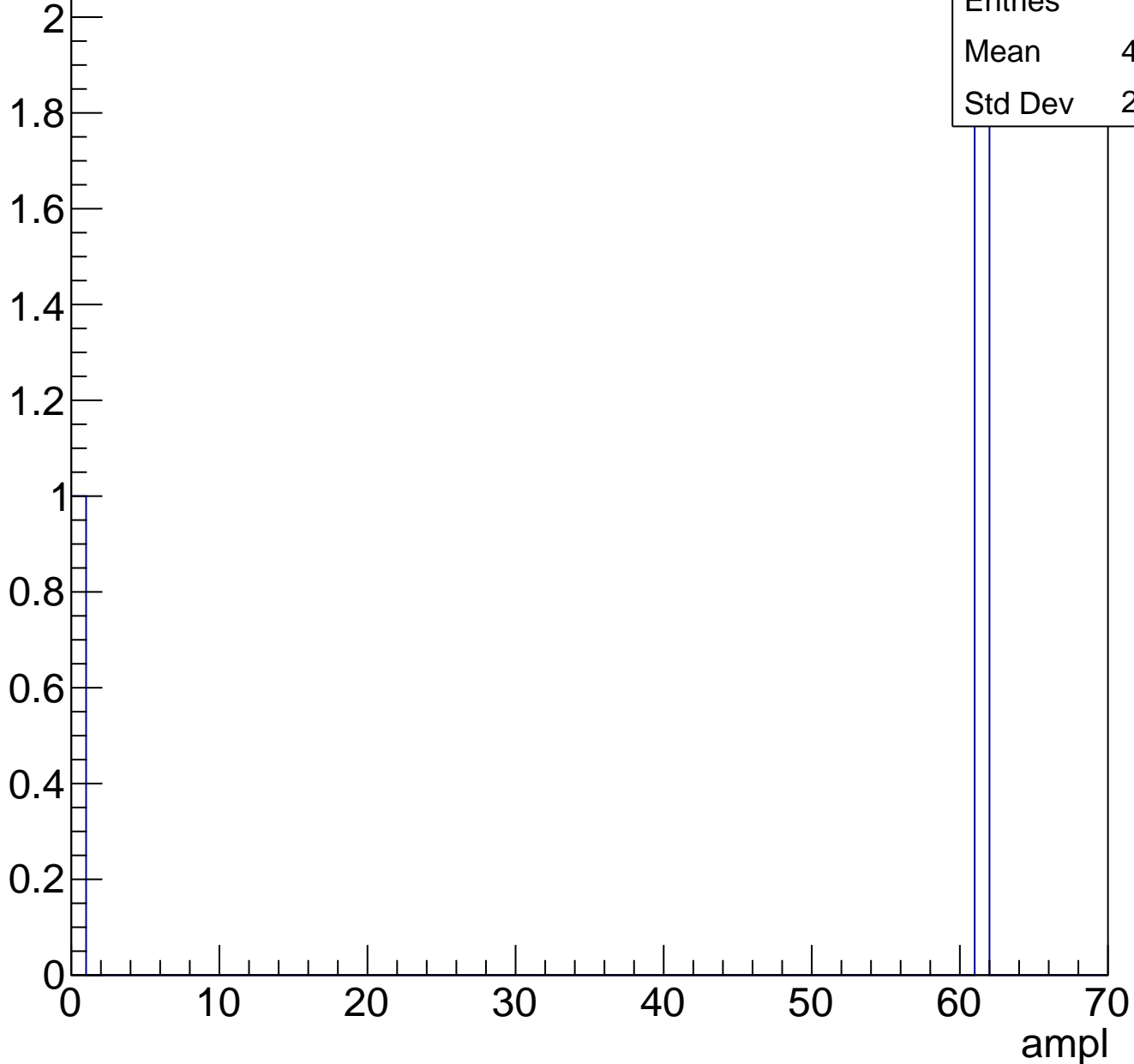
ampl



B1L001S, U26-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch41, adc7

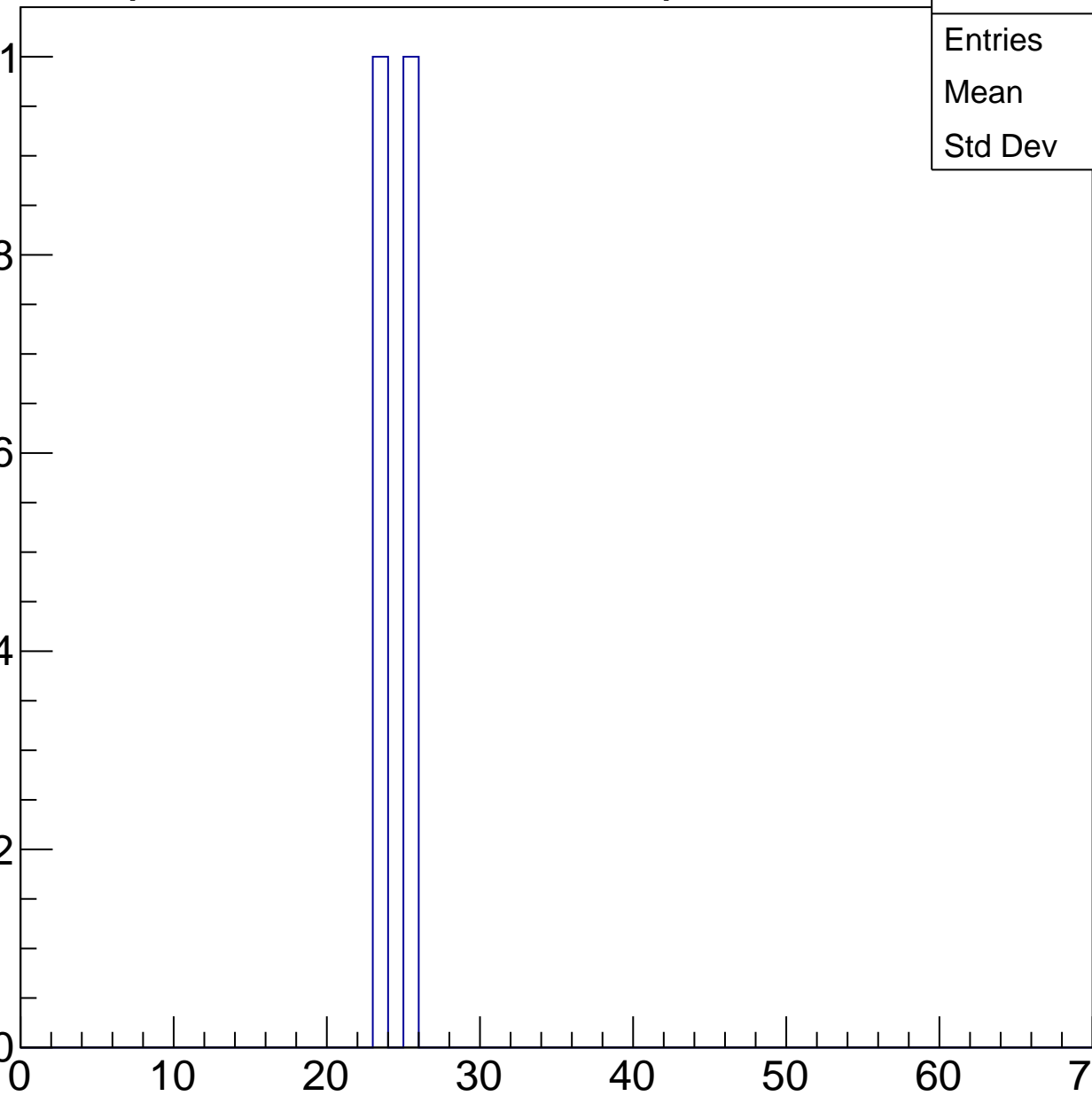
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 24 |
| Std Dev | 1 |

ampl



B1L001S, U26-ch42, adc0

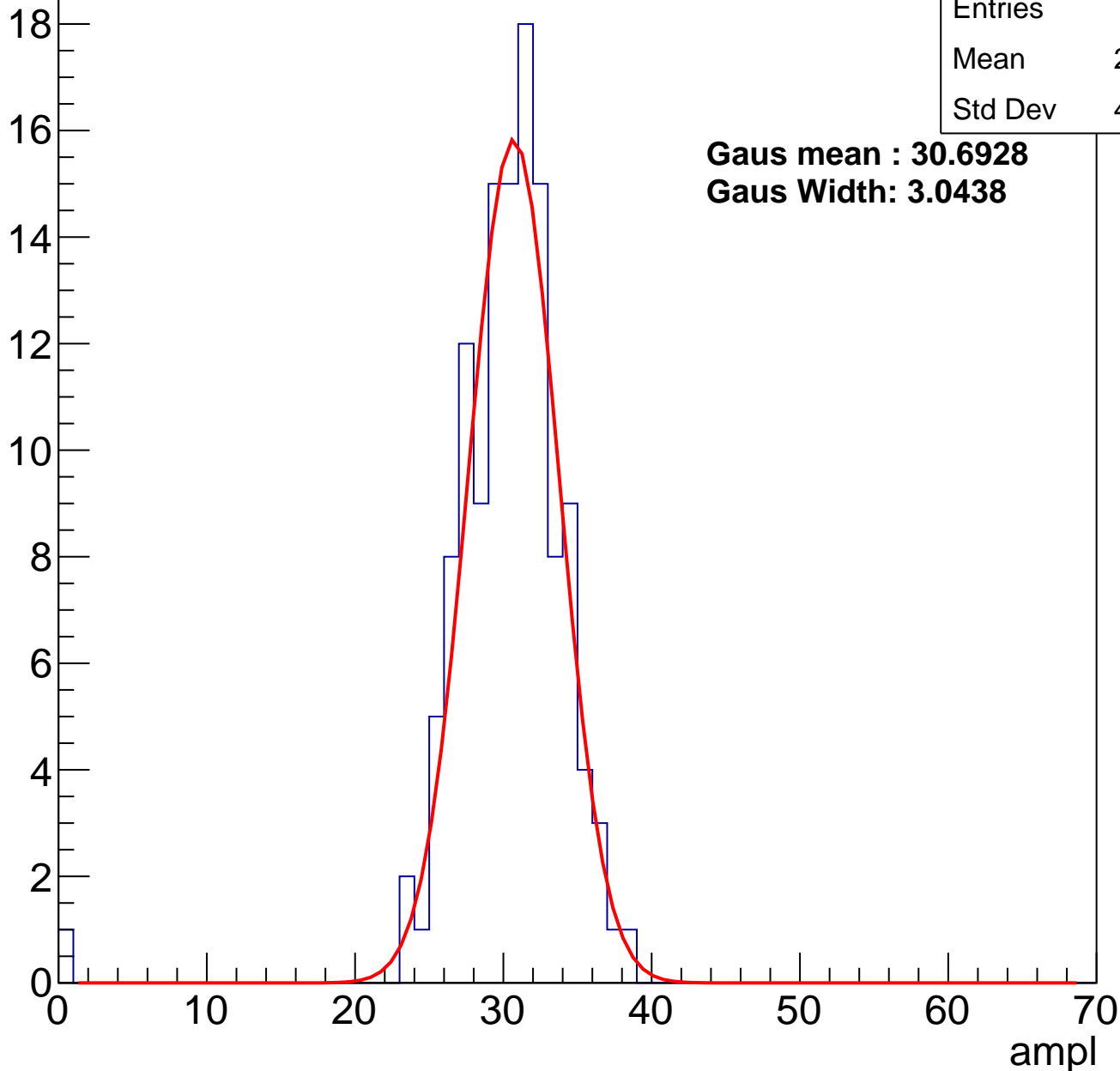
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 29.88 |
| Std Dev | 4.027 |

Gaus mean : 30.6928

Gaus Width: 3.0438

Entry



B1L001S, U26-ch42, adc1

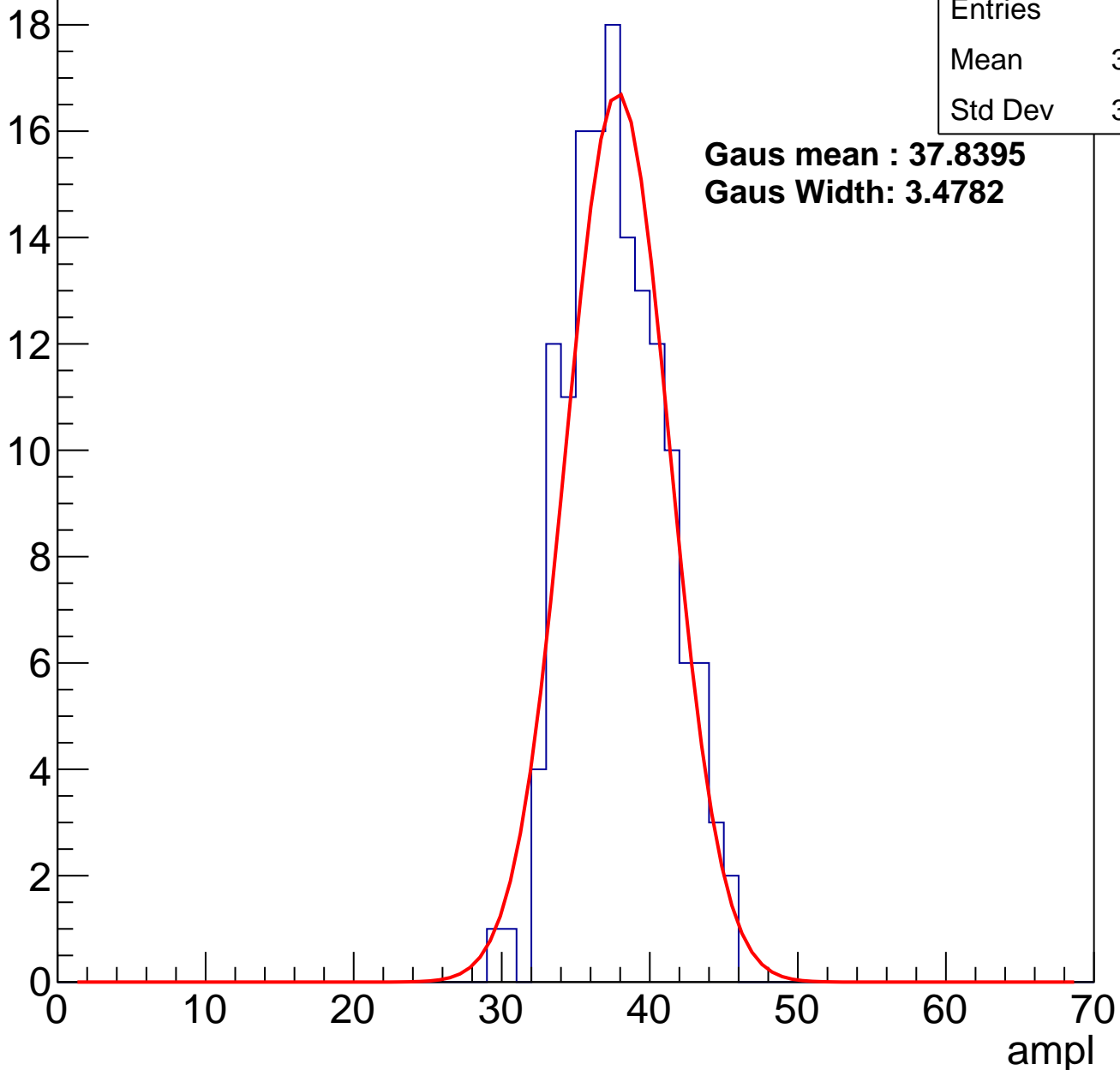
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 37.38 |
| Std Dev | 3.257 |

Gaus mean : 37.8395

Gaus Width: 3.4782

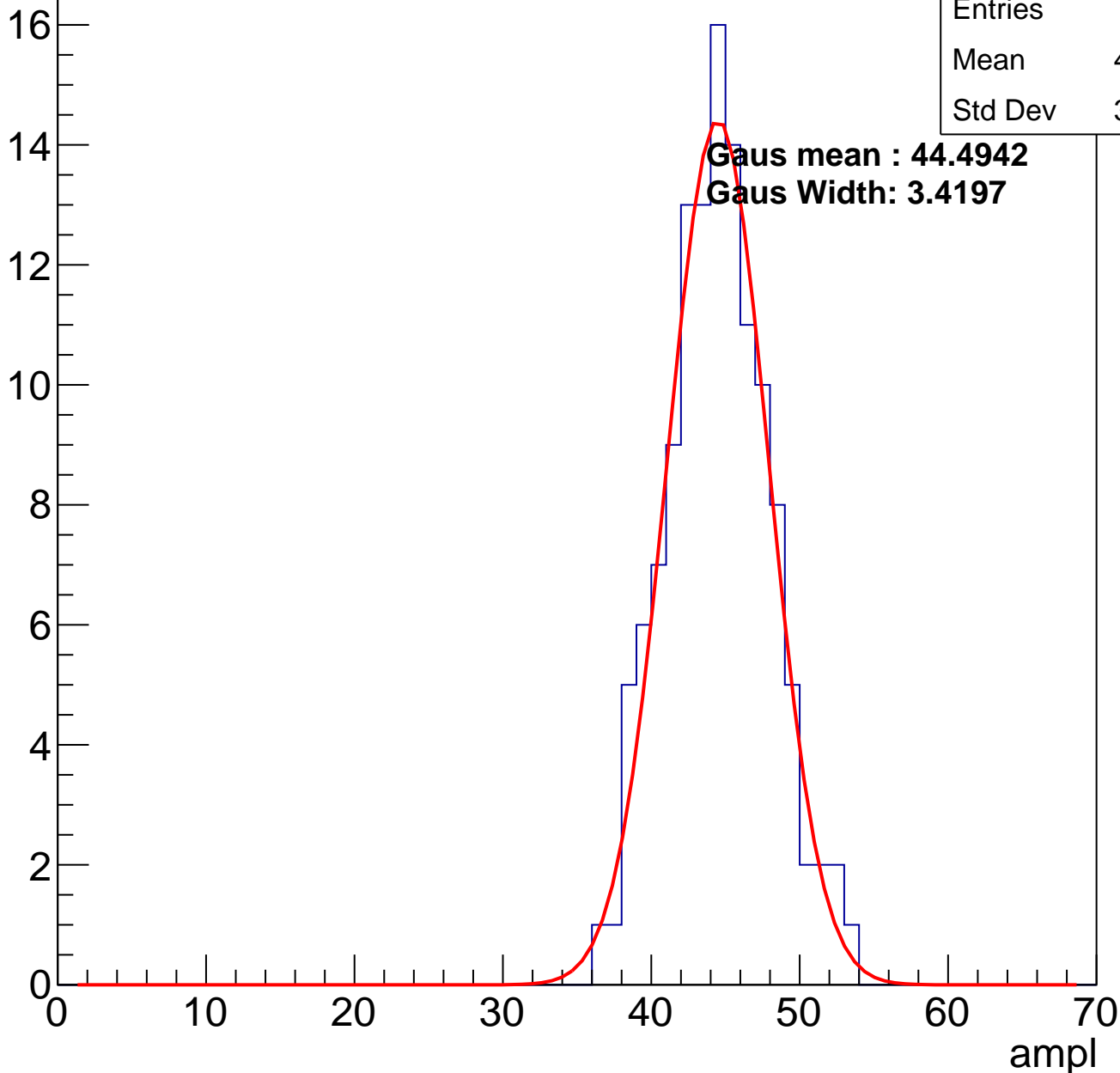
Entry



B1L001S, U26-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

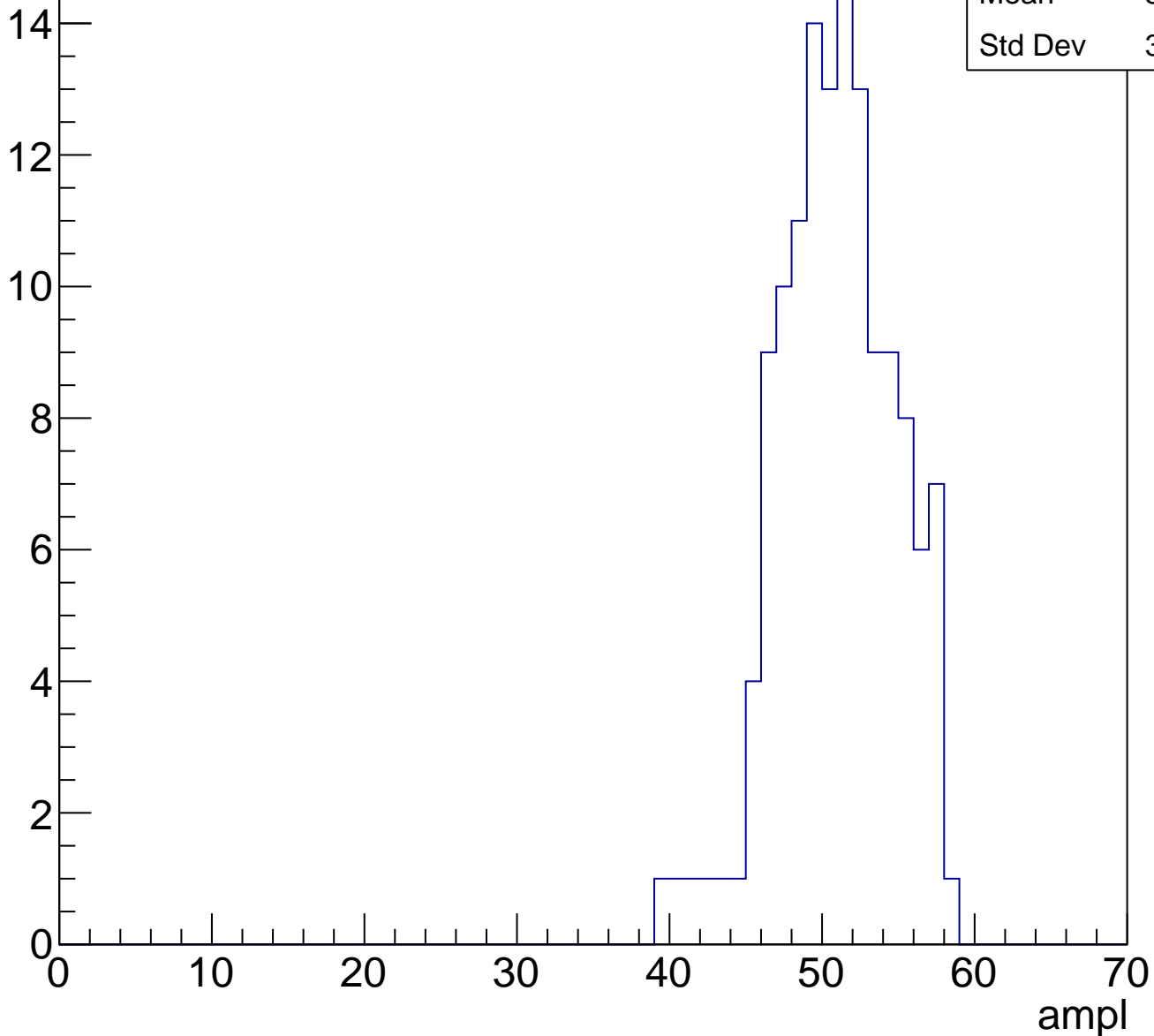


B1L001S, U26-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 50.47 |
| Std Dev | 3.779 |

Entry



B1L001S, U26-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

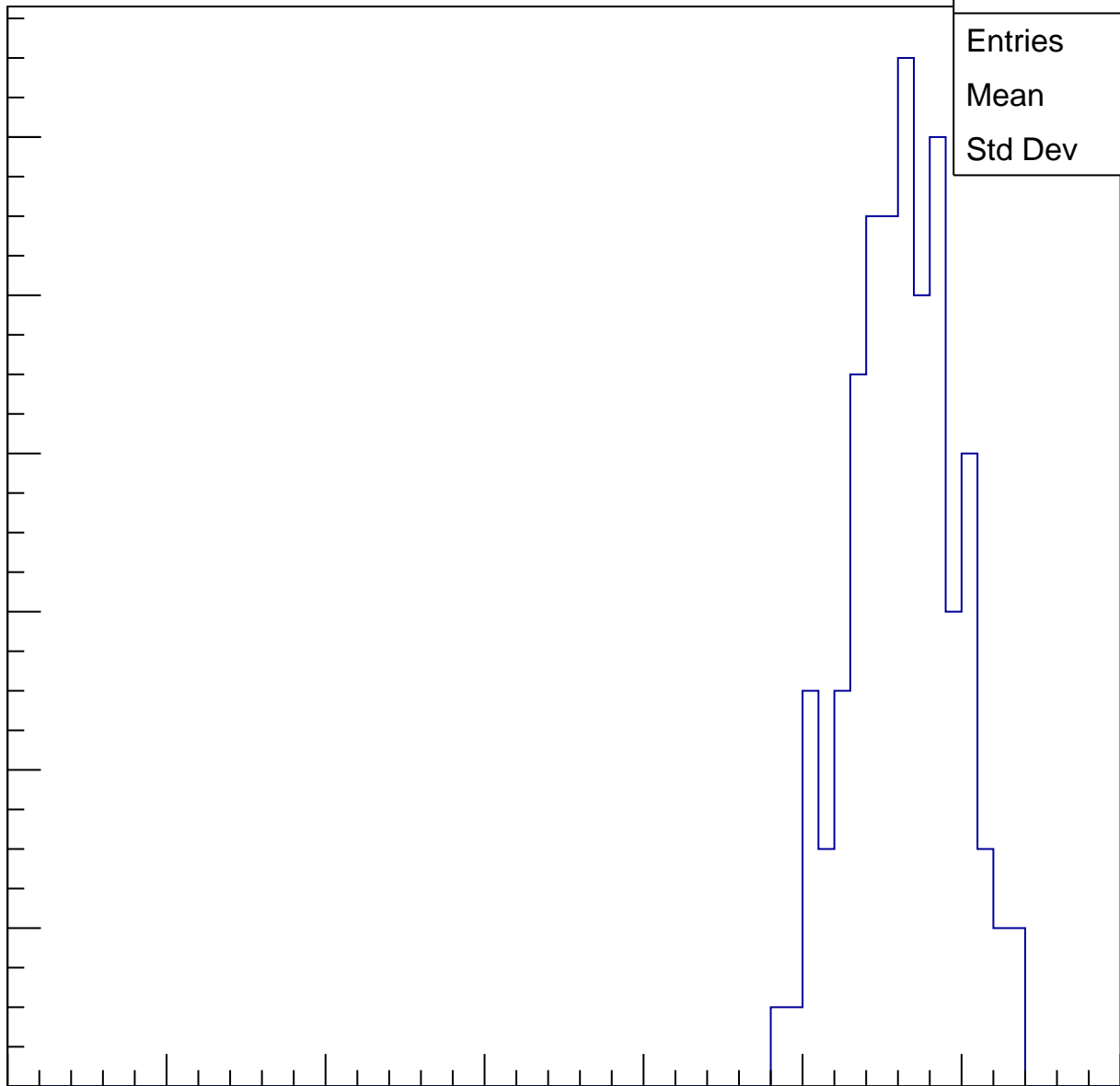
2

0

| | |
|---------|-------|
| Entries | 102 |
| Mean | 55.85 |
| Std Dev | 3.234 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch42, adc5

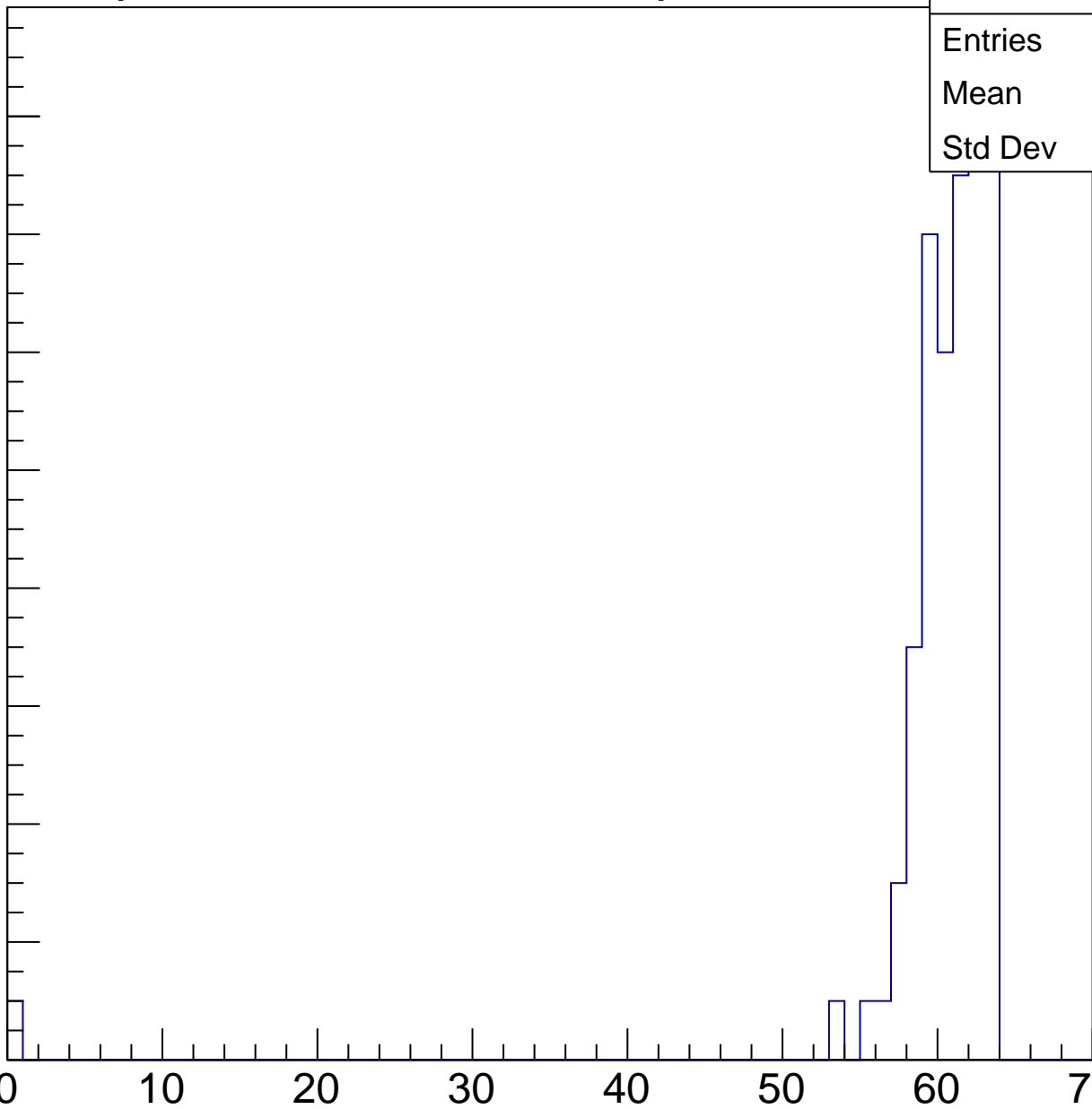
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 88 |
| Mean | 59.83 |
| Std Dev | 6.732 |

ampl



B1L001S, U26-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 4 |
| Mean | 61.75 |
| Std Dev | 0.8292 |

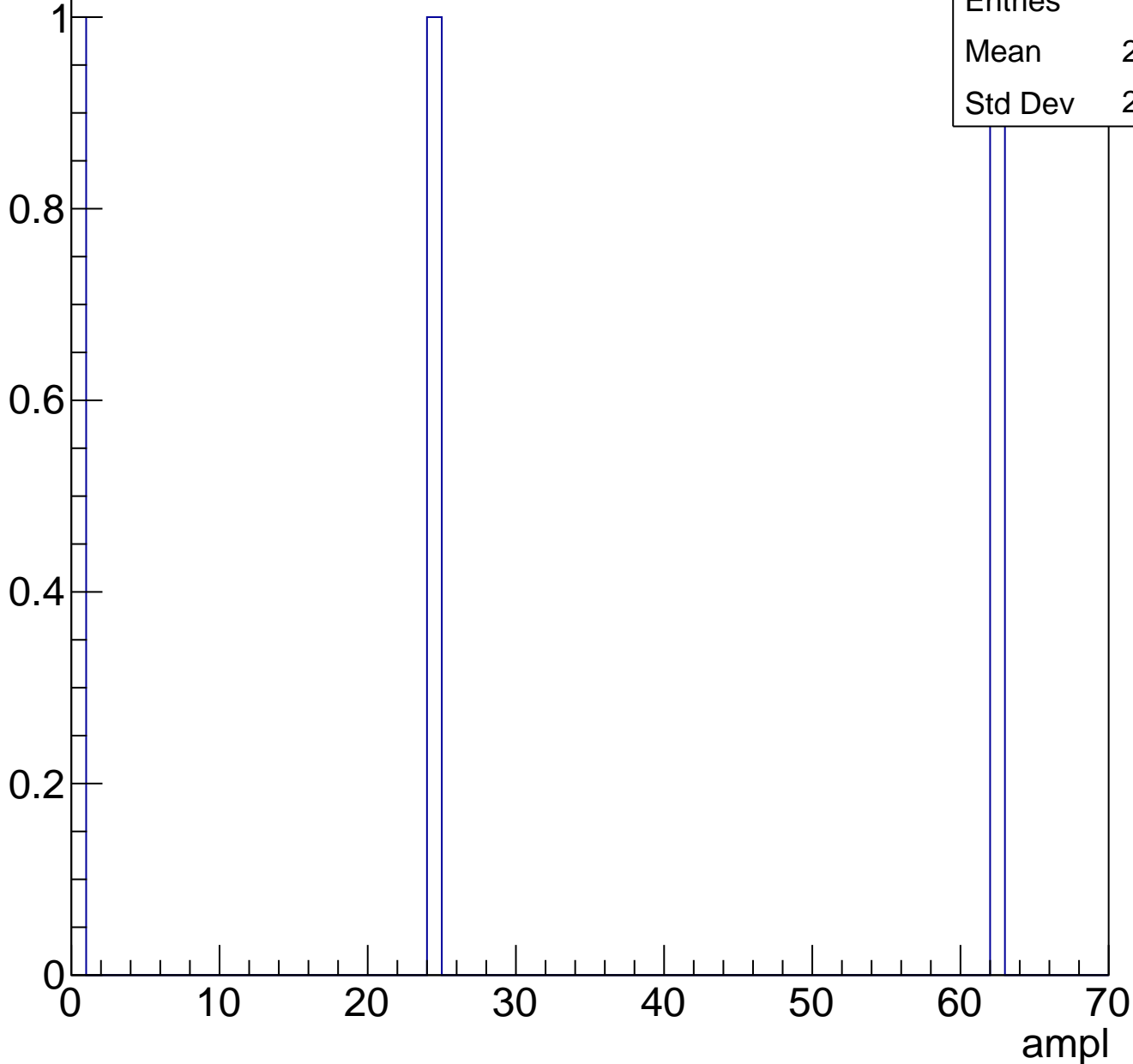
ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch43, adc0

calib_packv5_042523_0143.root, FC#2, port C2

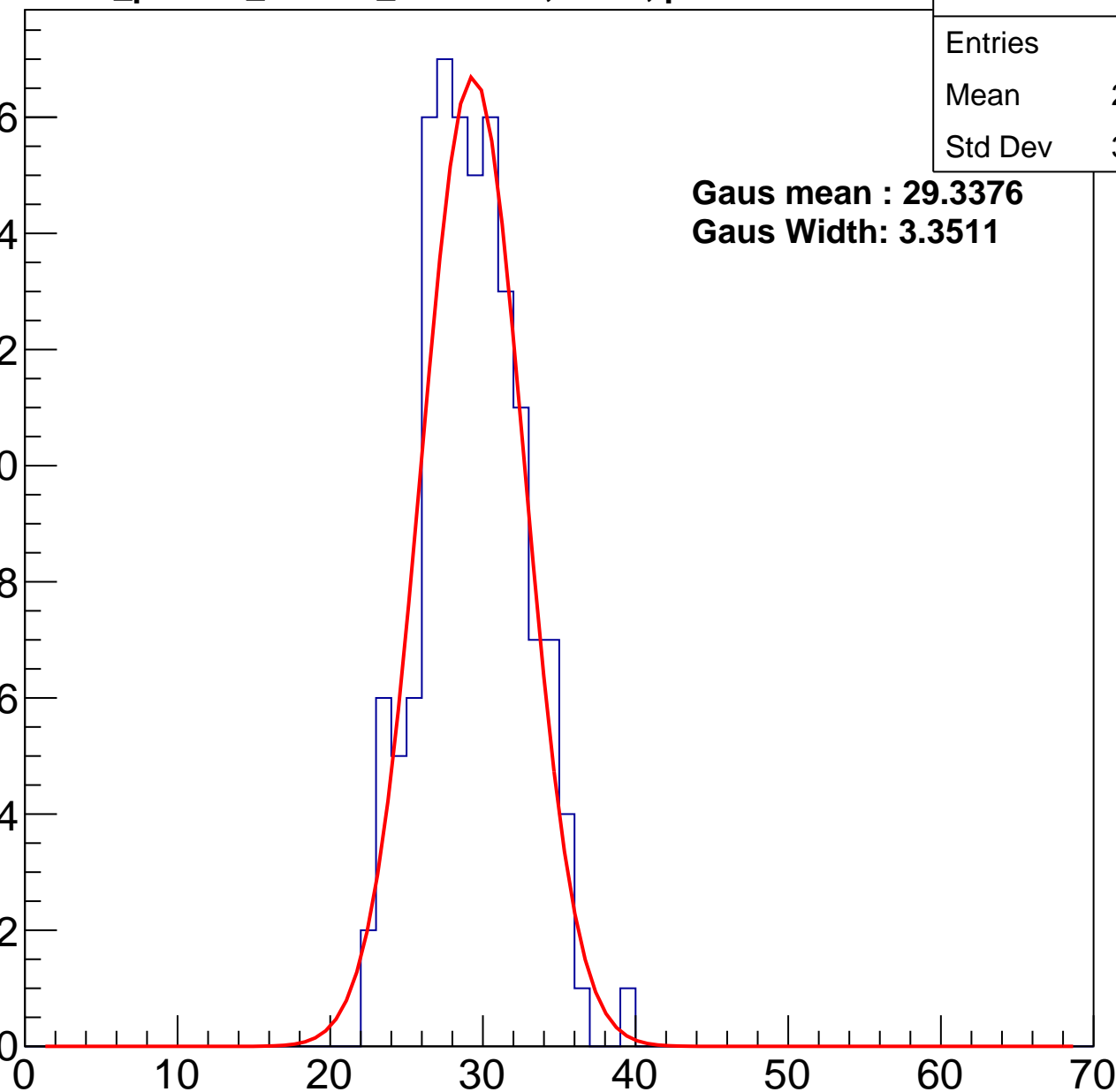
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 143 |
| Mean | 28.87 |
| Std Dev | 3.247 |

Gaus mean : 29.3376

Gaus Width: 3.3511



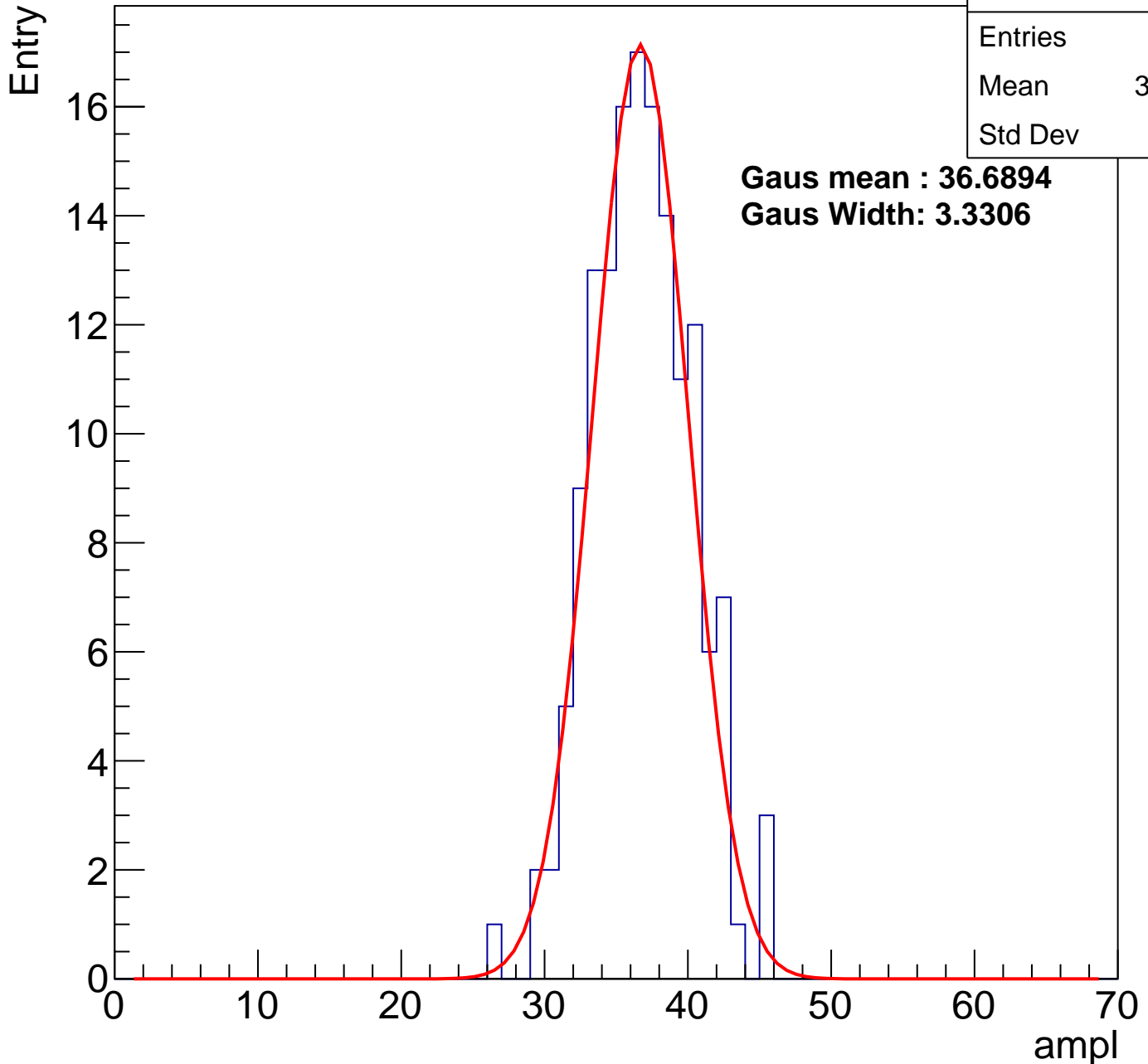
ampl

B1L001S, U26-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 36.36 |
| Std Dev | 3.45 |

Gaus mean : 36.6894
Gaus Width: 3.3306



B1L001S, U26-ch43, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

123

Mean

42.87

Std Dev

3.149

Gaus mean : 43.2074

Gaus Width: 3.2717

0

10

20

30

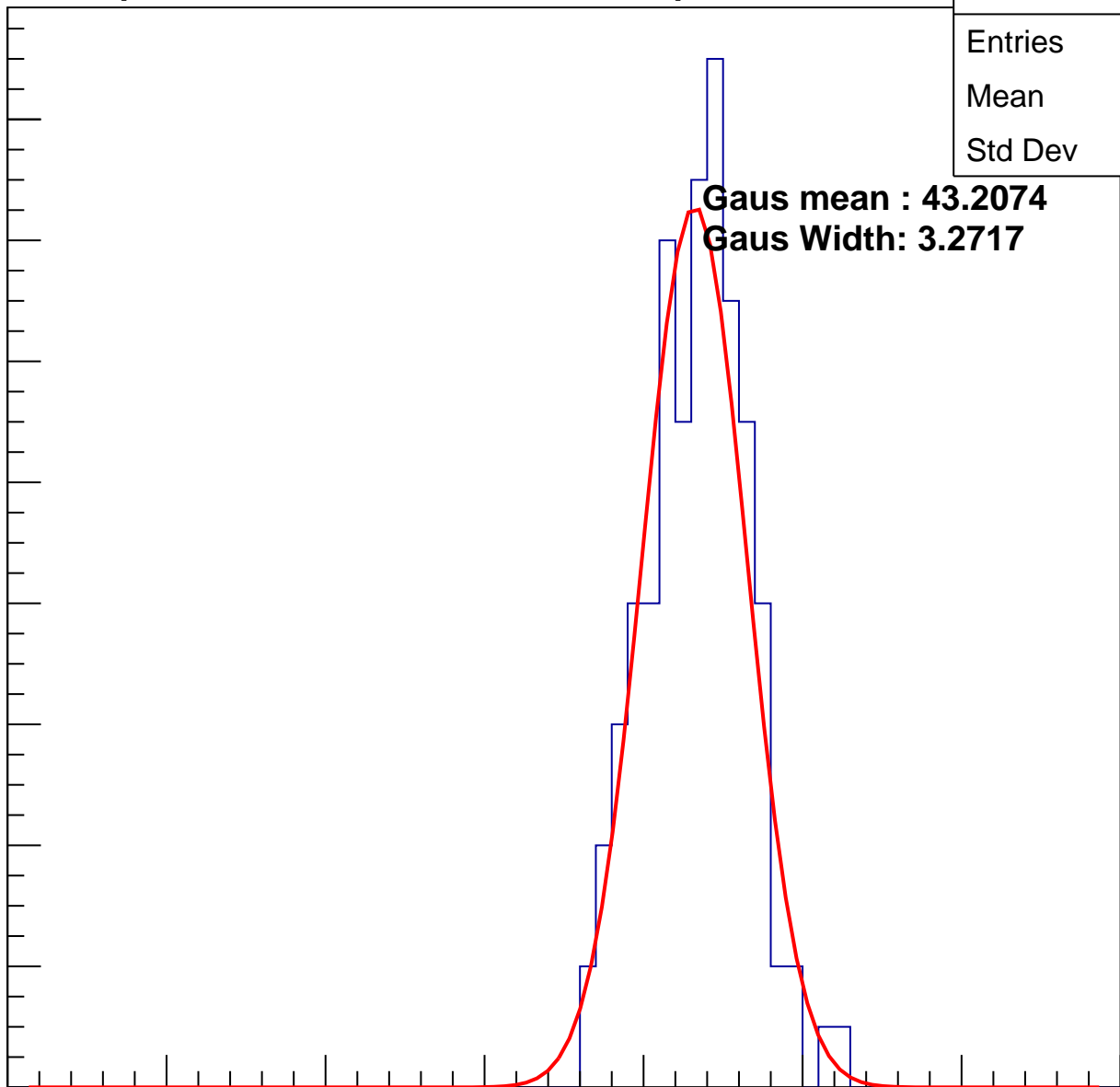
40

50

60

70

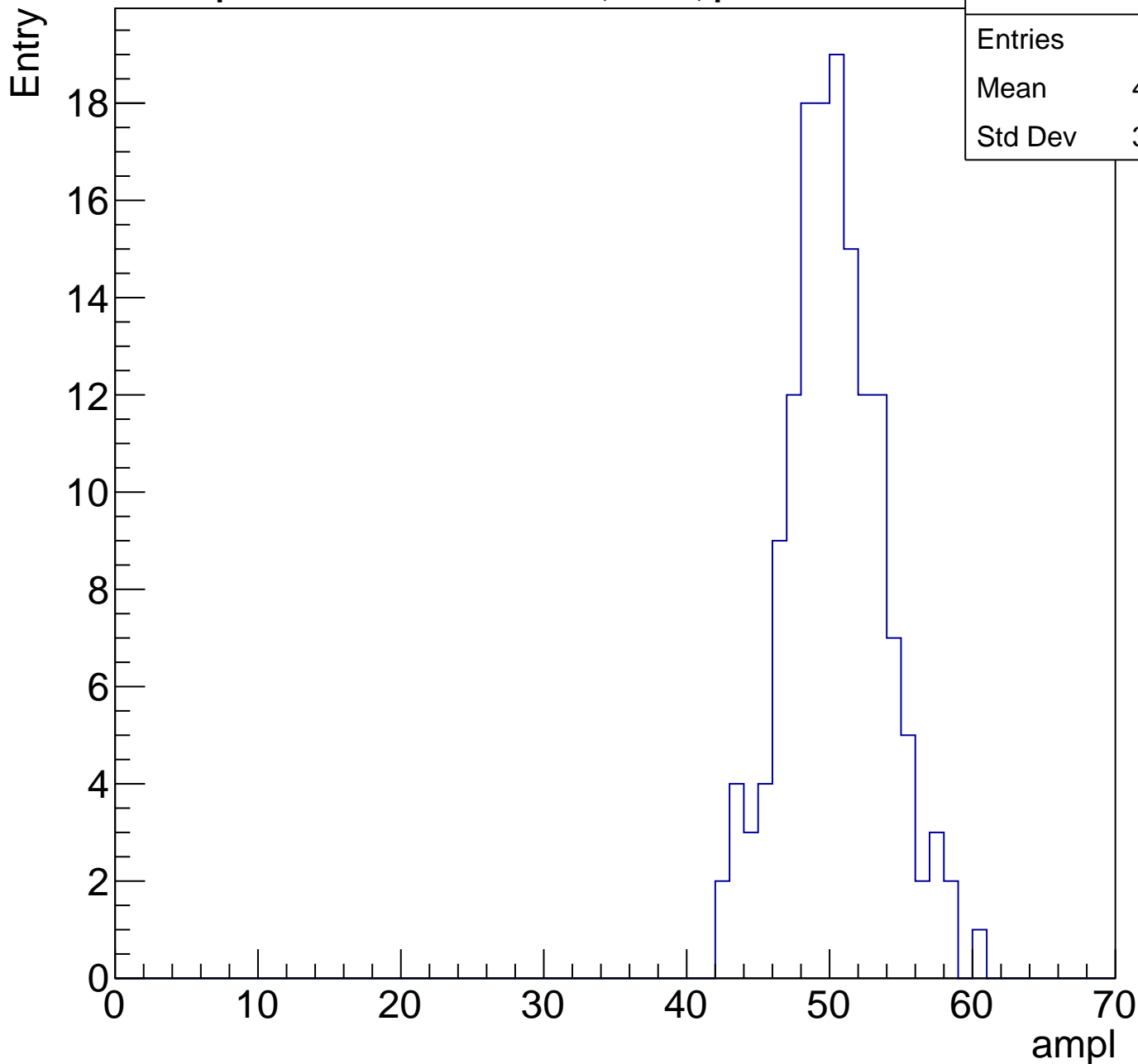
ampl



B1L001S, U26-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 49.86 |
| Std Dev | 3.415 |



B1L001S, U26-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

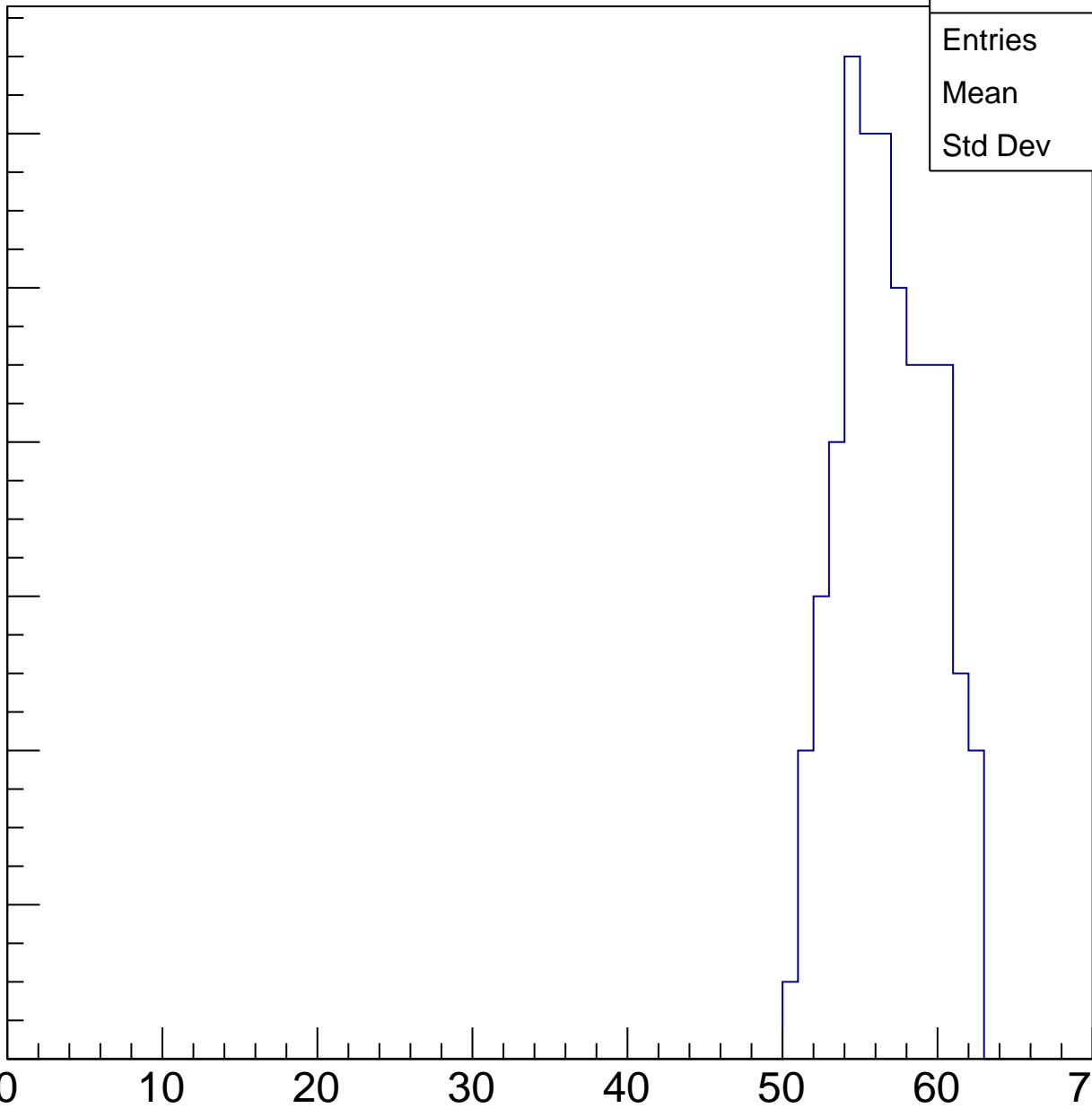
40

50

60

ampl

| | |
|---------|-------|
| Entries | 102 |
| Mean | 56.27 |
| Std Dev | 2.971 |



B1L001S, U26-ch43, adc5

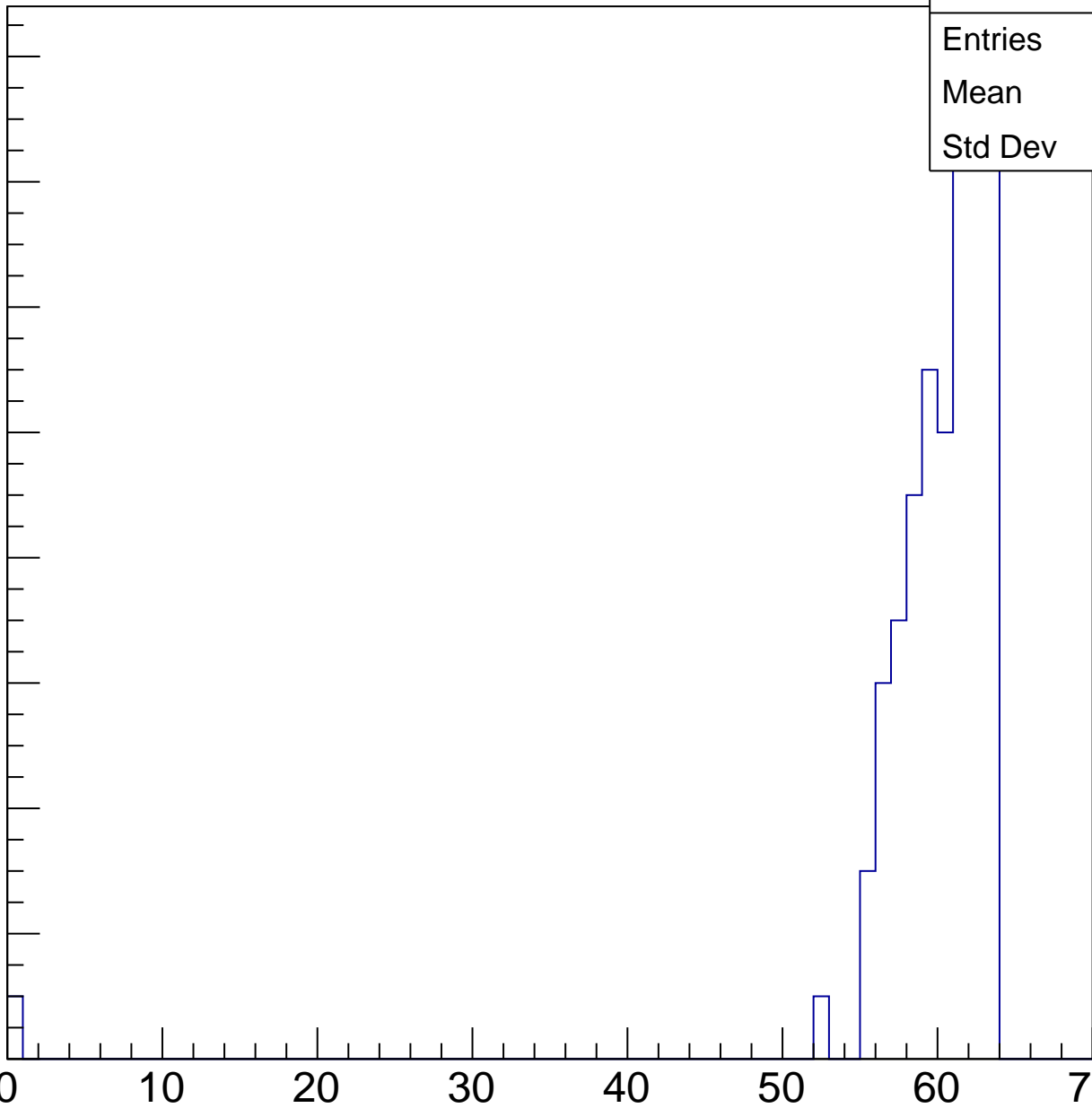
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 94 |
| Mean | 59.32 |
| Std Dev | 6.621 |

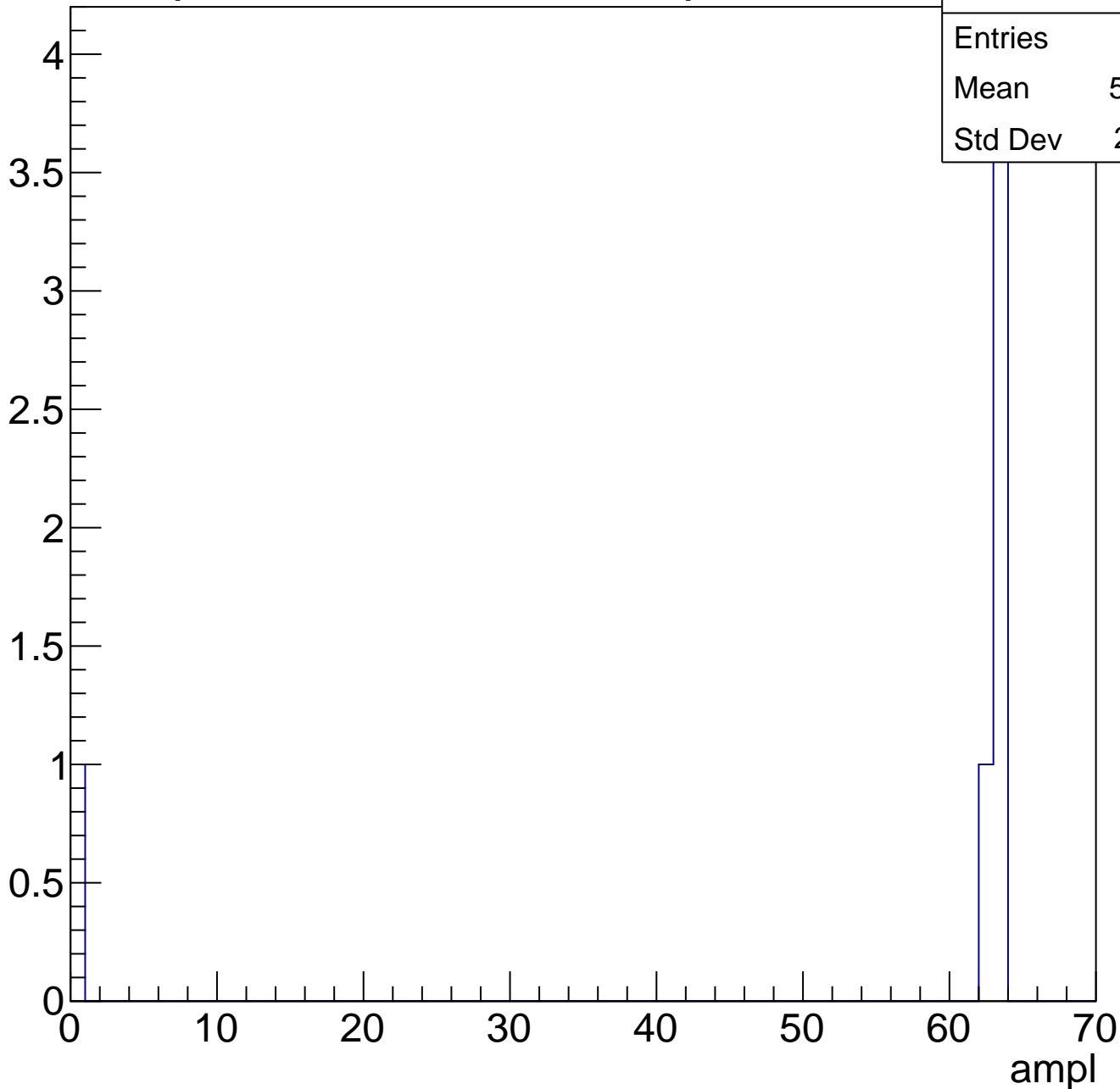
ampl



B1L001S, U26-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch44, adc0

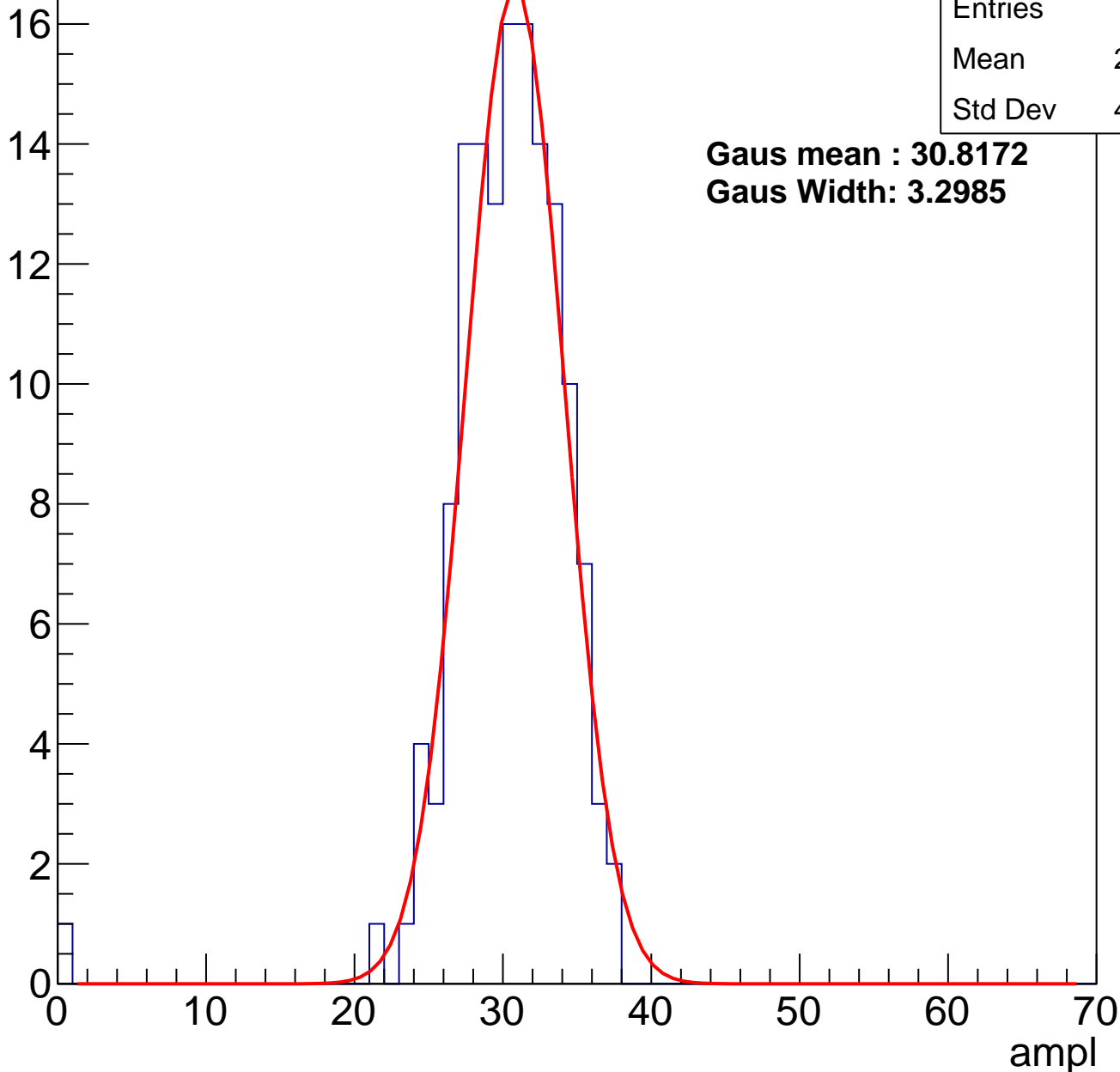
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 29.93 |
| Std Dev | 4.054 |

Gaus mean : 30.8172

Gaus Width: 3.2985

Entry



B1L001S, U26-ch44, adc1

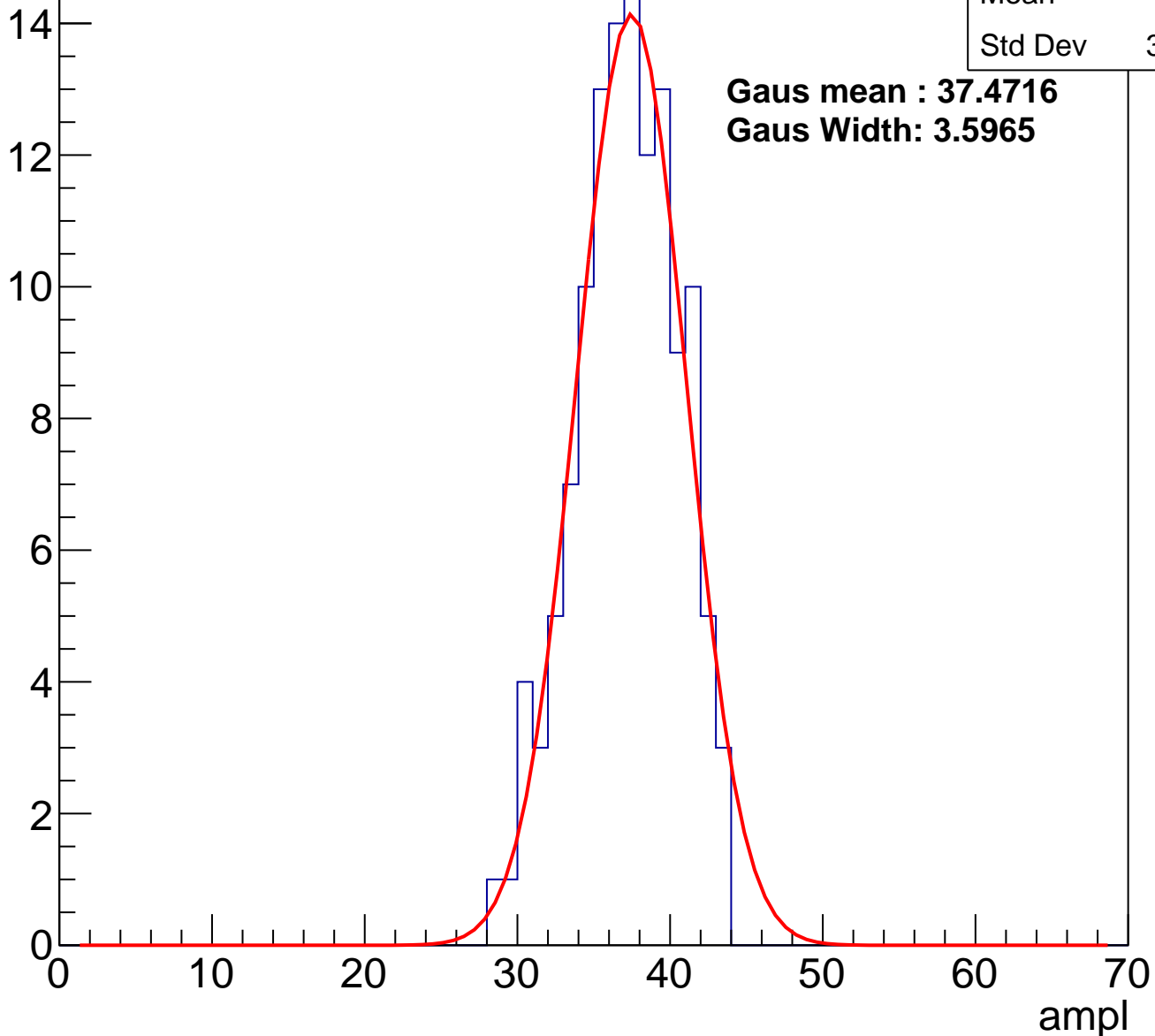
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 36.7 |
| Std Dev | 3.309 |

Gaus mean : 37.4716

Gaus Width: 3.5965

Entry



B1L001S, U26-ch44, adc2

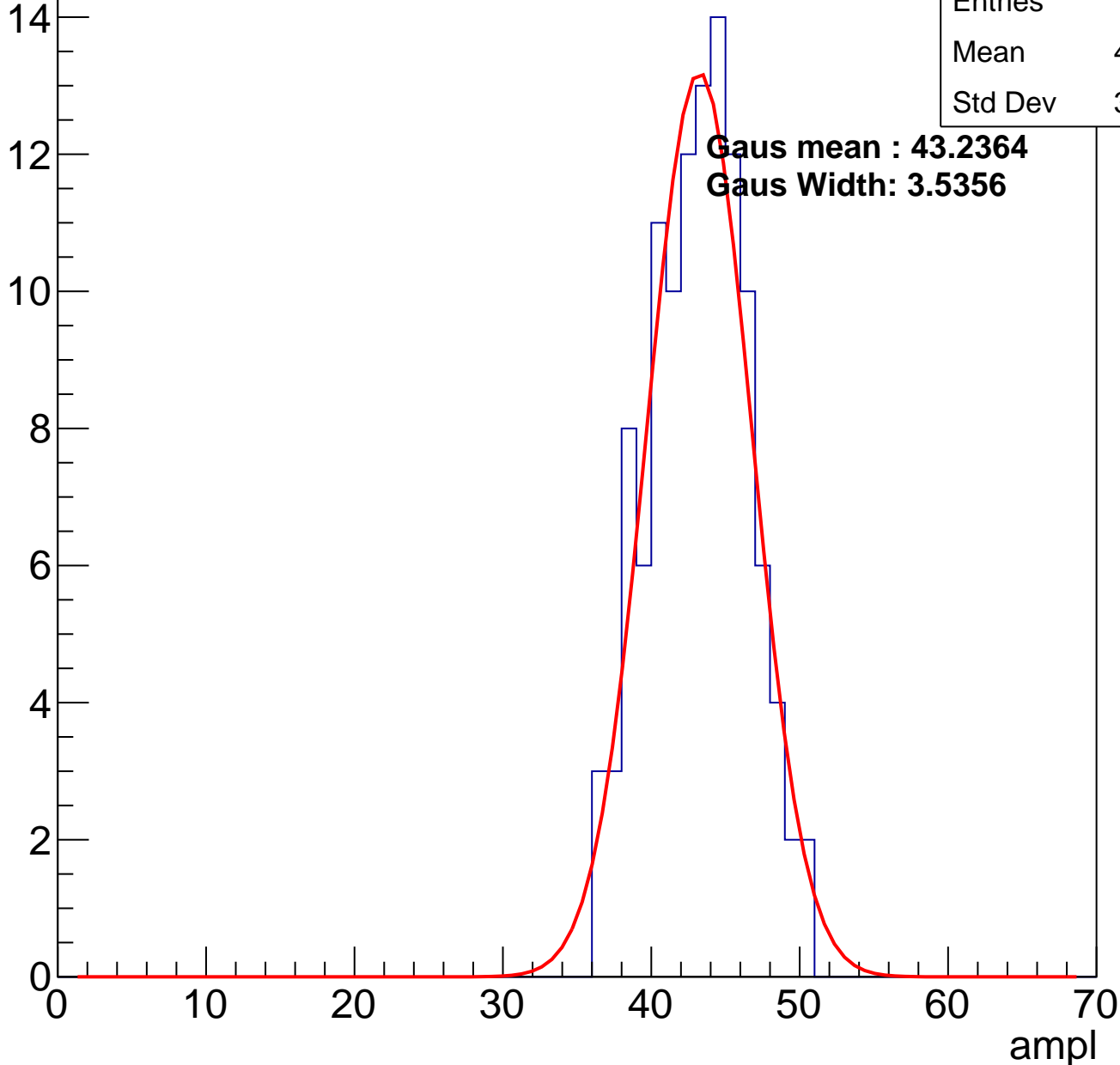
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 42.74 |
| Std Dev | 3.233 |

Gaus mean : 43.2364

Gaus Width: 3.5356

Entry



B1L001S, U26-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

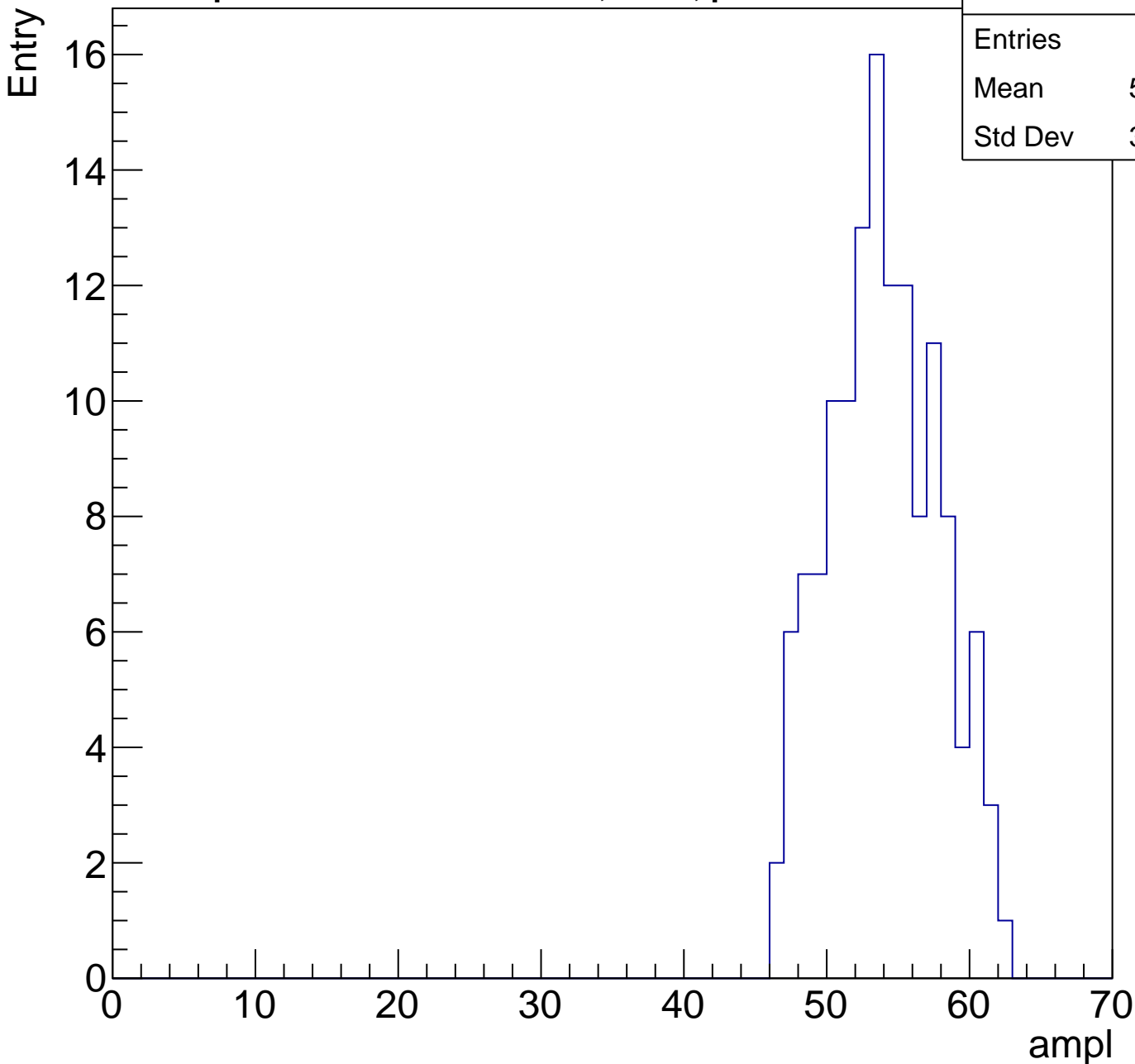
ampl

| | |
|---------|-------|
| Entries | 85 |
| Mean | 47.89 |
| Std Dev | 3.286 |

B1L001S, U26-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 53.49 |
| Std Dev | 3.767 |



B1L001S, U26-ch44, adc5

calib_packv5_042523_0143.root, FC#2, port C2

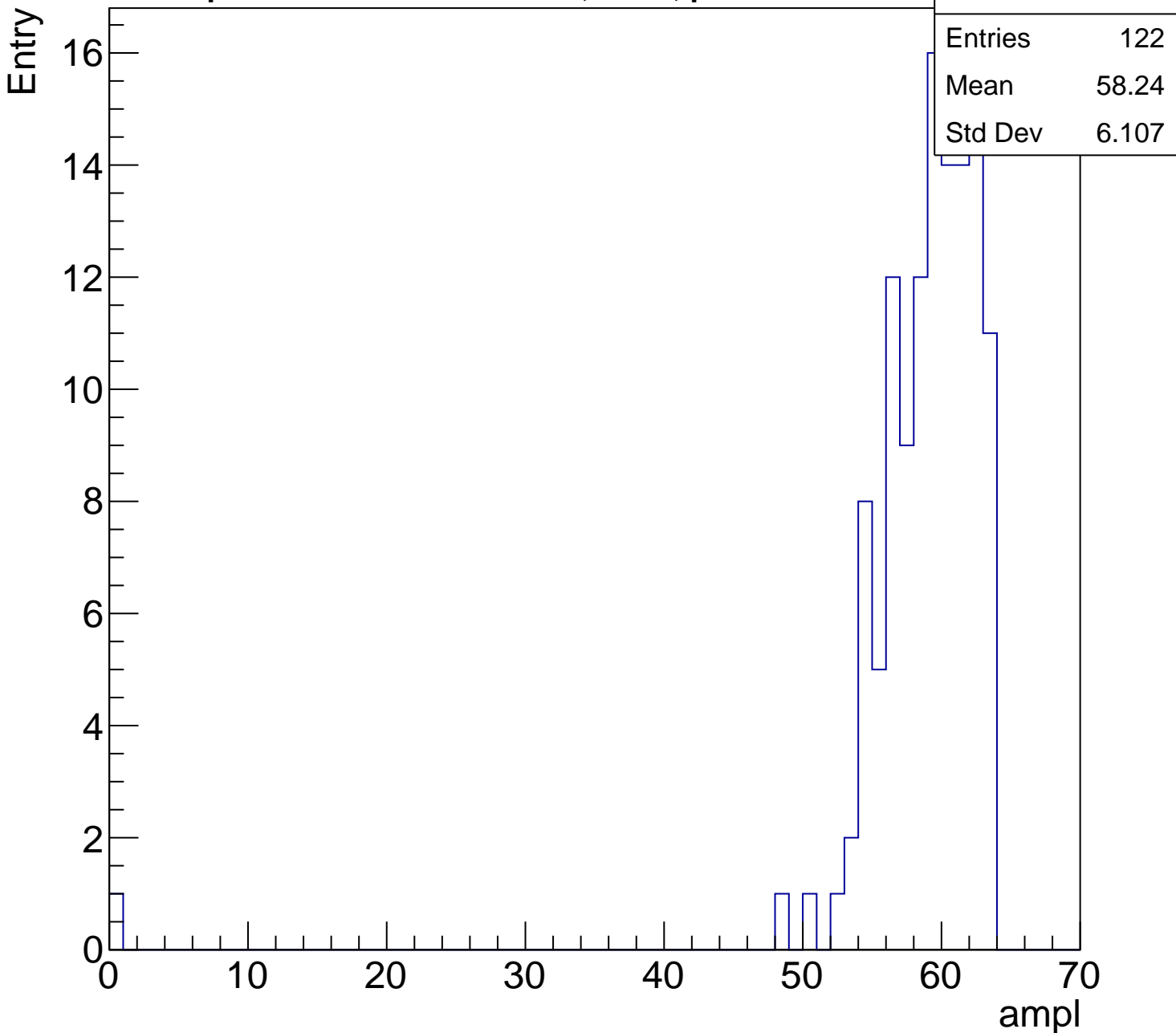
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 122 |
| Mean | 58.24 |
| Std Dev | 6.107 |

ampl

0 10 20 30 40 50 60 70

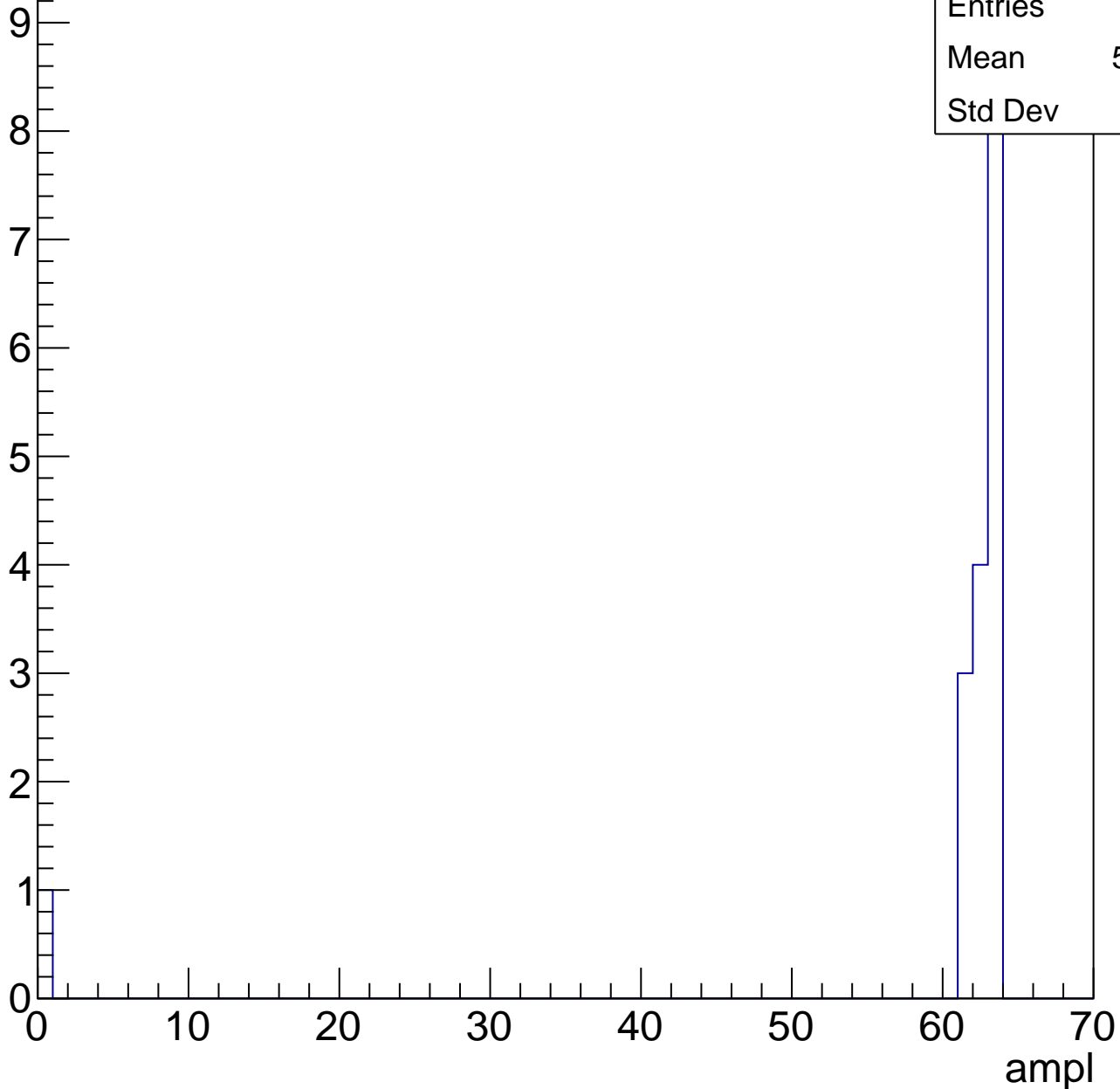


B1L001S, U26-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 58.71 |
| Std Dev | 14.7 |



B1L001S, U26-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch45, adc0

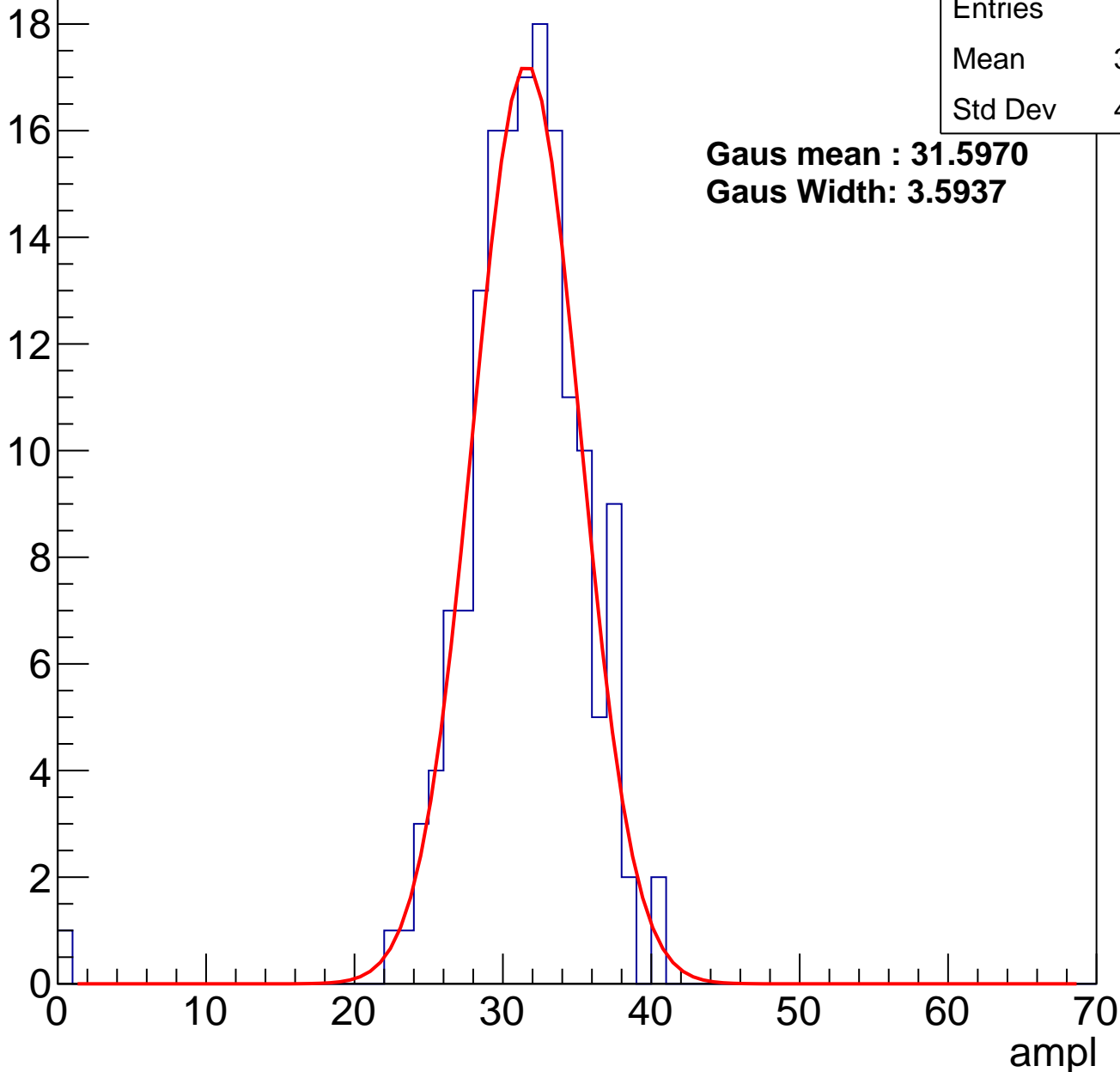
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 159 |
| Mean | 30.94 |
| Std Dev | 4.293 |

Gaus mean : 31.5970

Gaus Width: 3.5937

Entry



B1L001S, U26-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 37.97 |
| Std Dev | 3.093 |

Gaus mean : 38.6378

Gaus Width: 2.8368

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

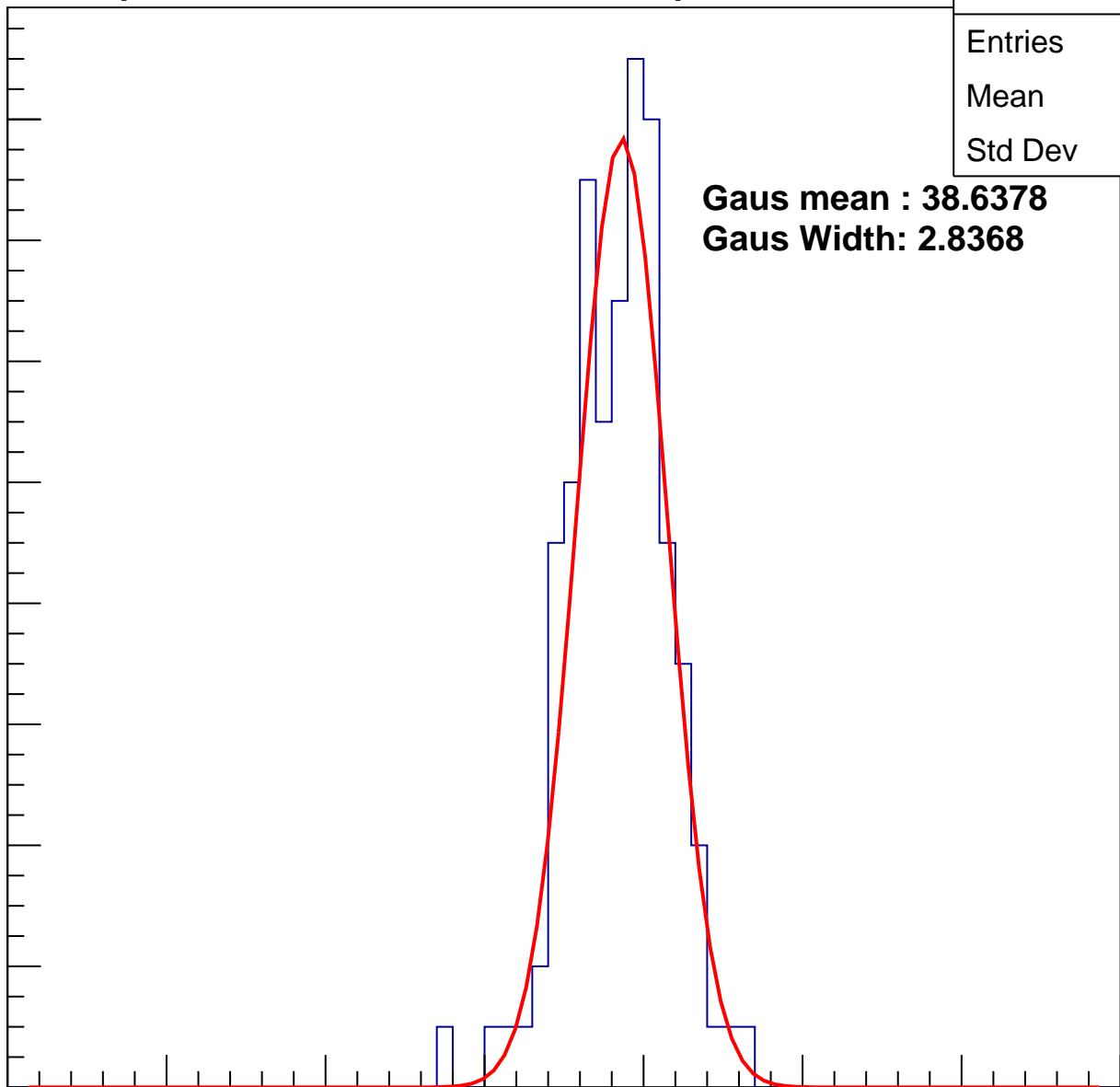
40

50

60

70

ampl



B1L001S, U26-ch45, adc2

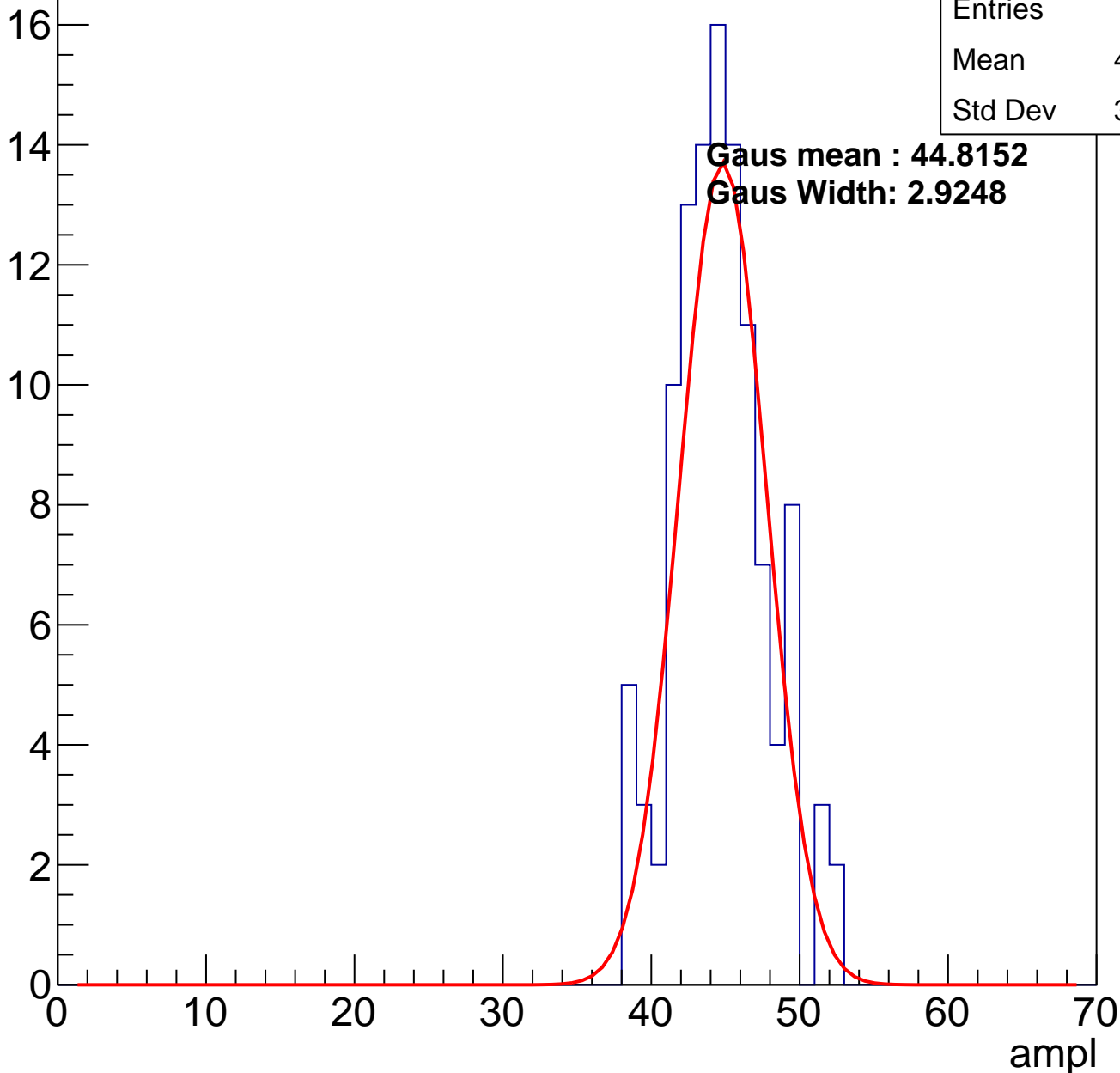
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 44.24 |
| Std Dev | 3.129 |

Gaus mean : 44.8152

Gaus Width: 2.9248

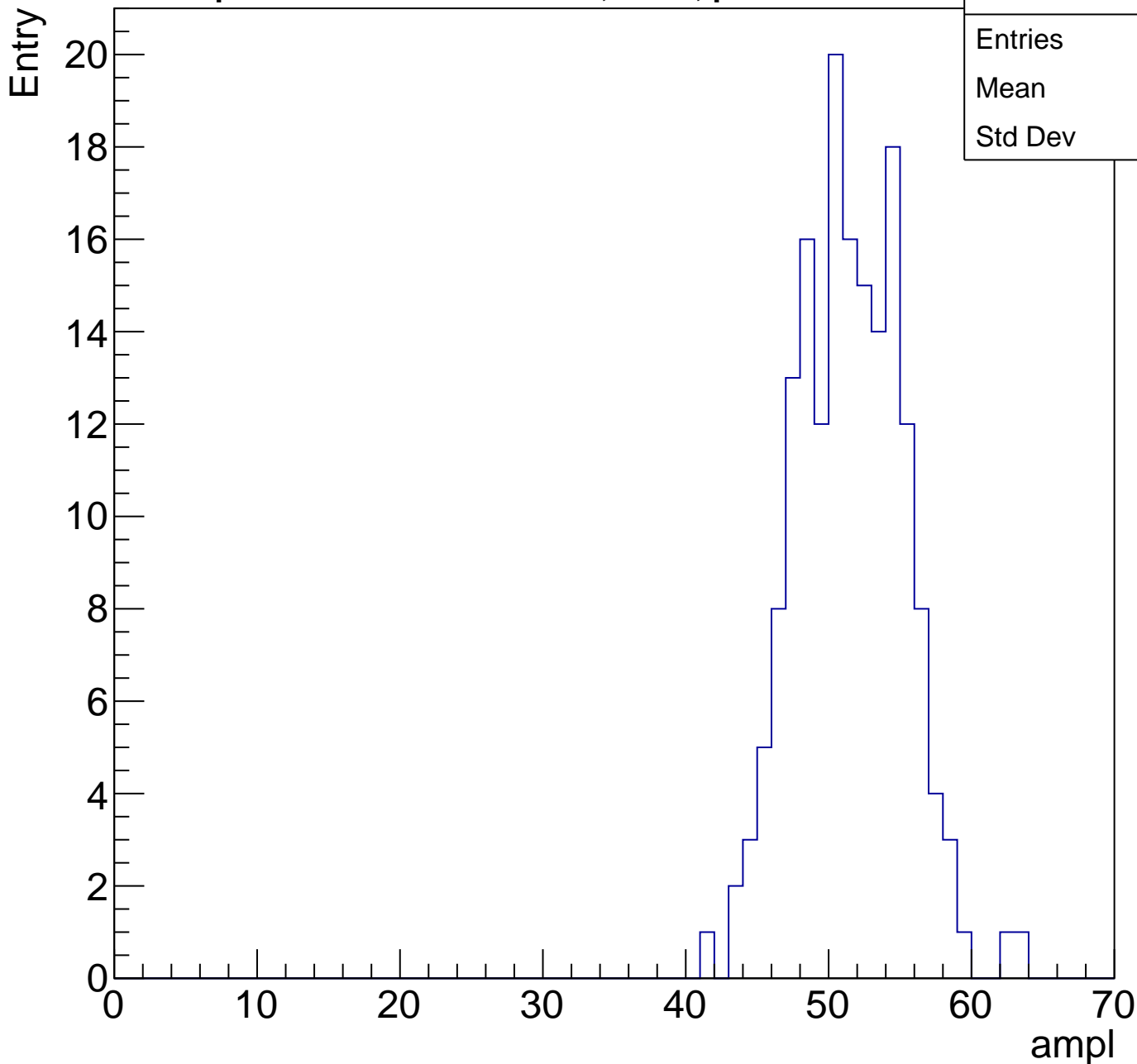
Entry



B1L001S, U26-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|------|
| Entries | 173 |
| Mean | 51 |
| Std Dev | 3.74 |



B1L001S, U26-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

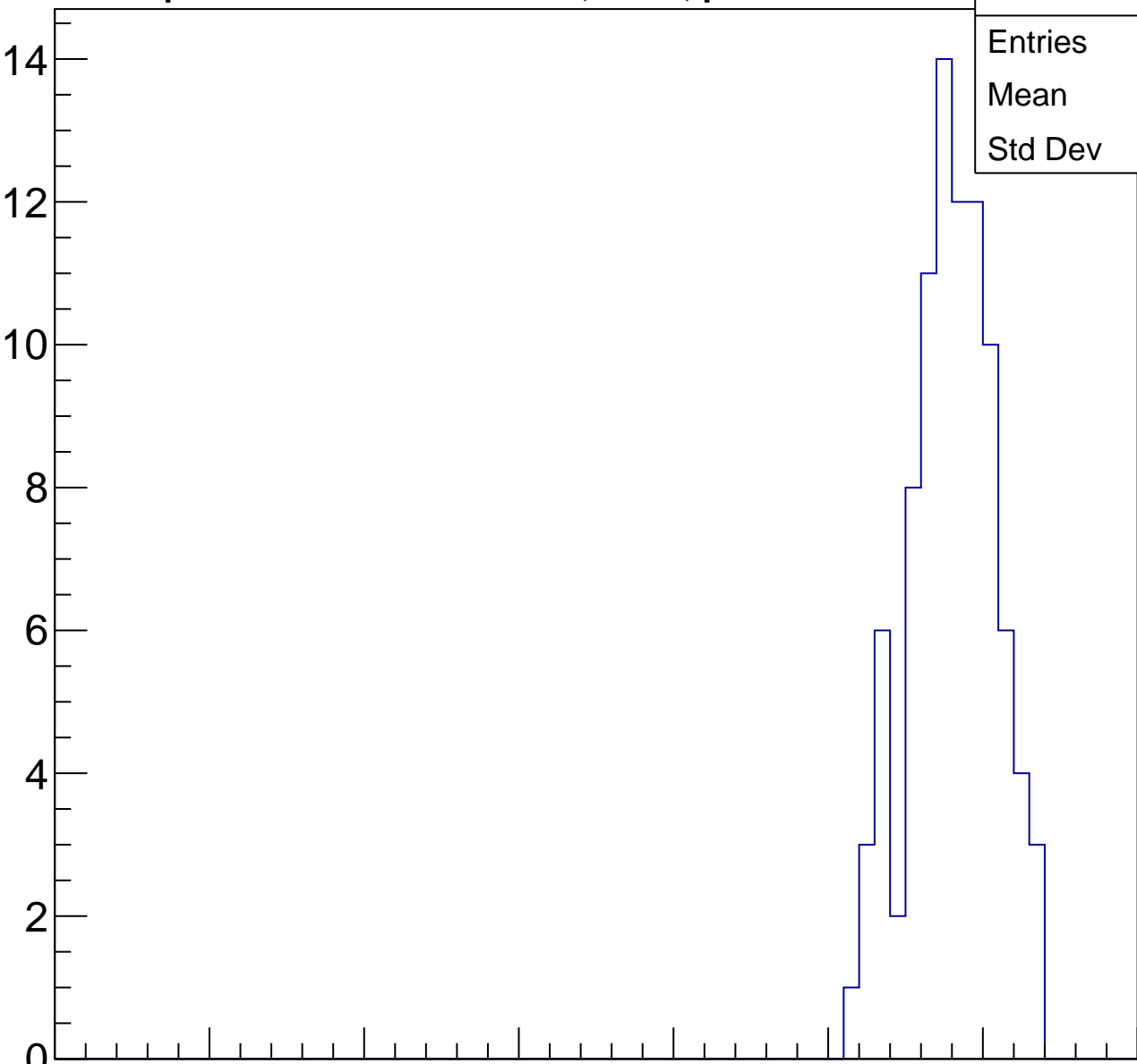
| | |
|---------|-------|
| Entries | 92 |
| Mean | 57.54 |
| Std Dev | 2.748 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch45, adc5

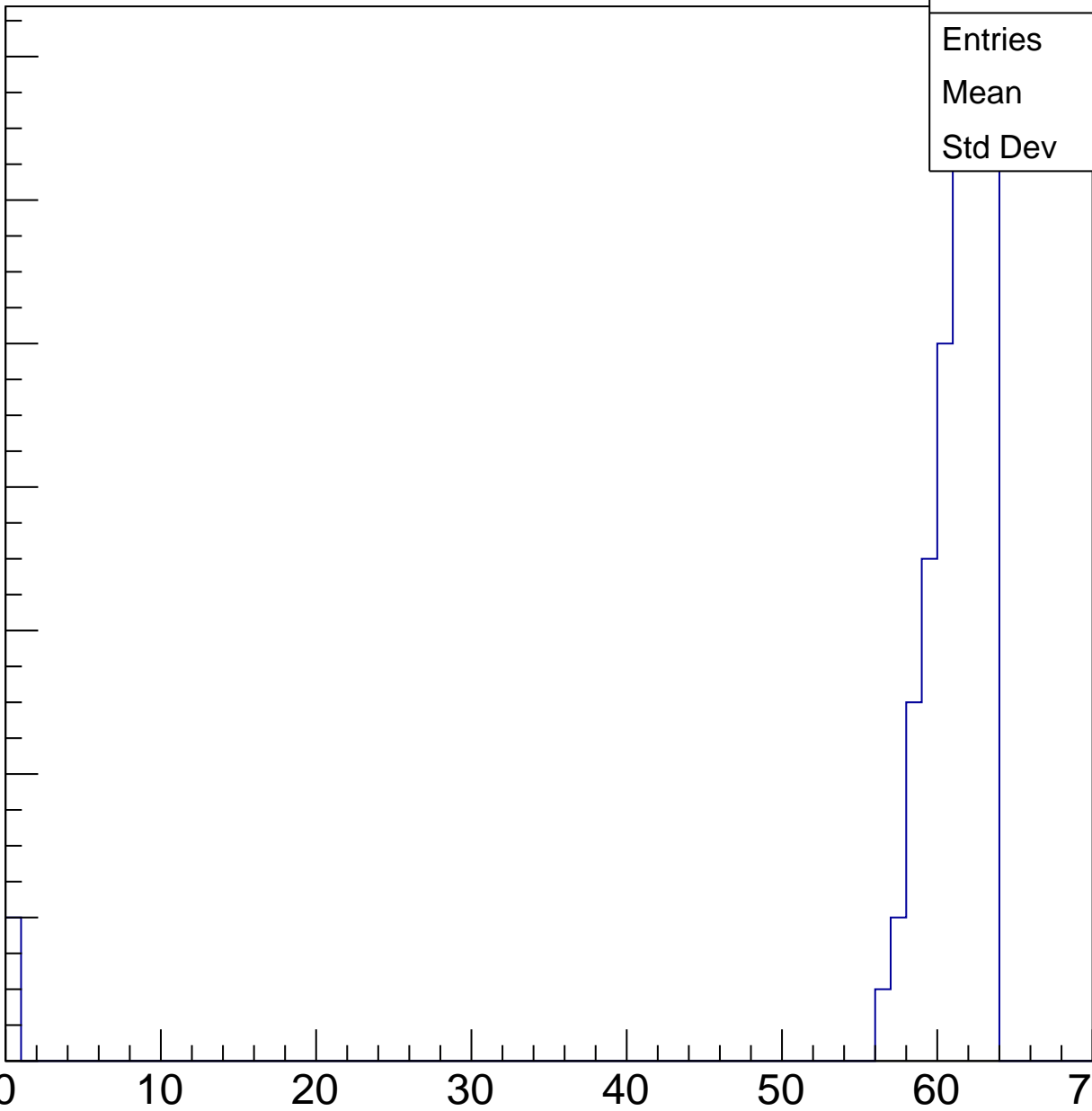
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 67 |
| Mean | 58.99 |
| Std Dev | 10.49 |

ampl



B1L001S, U26-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch46, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 156 |
| Mean | 29.37 |
| Std Dev | 3.623 |

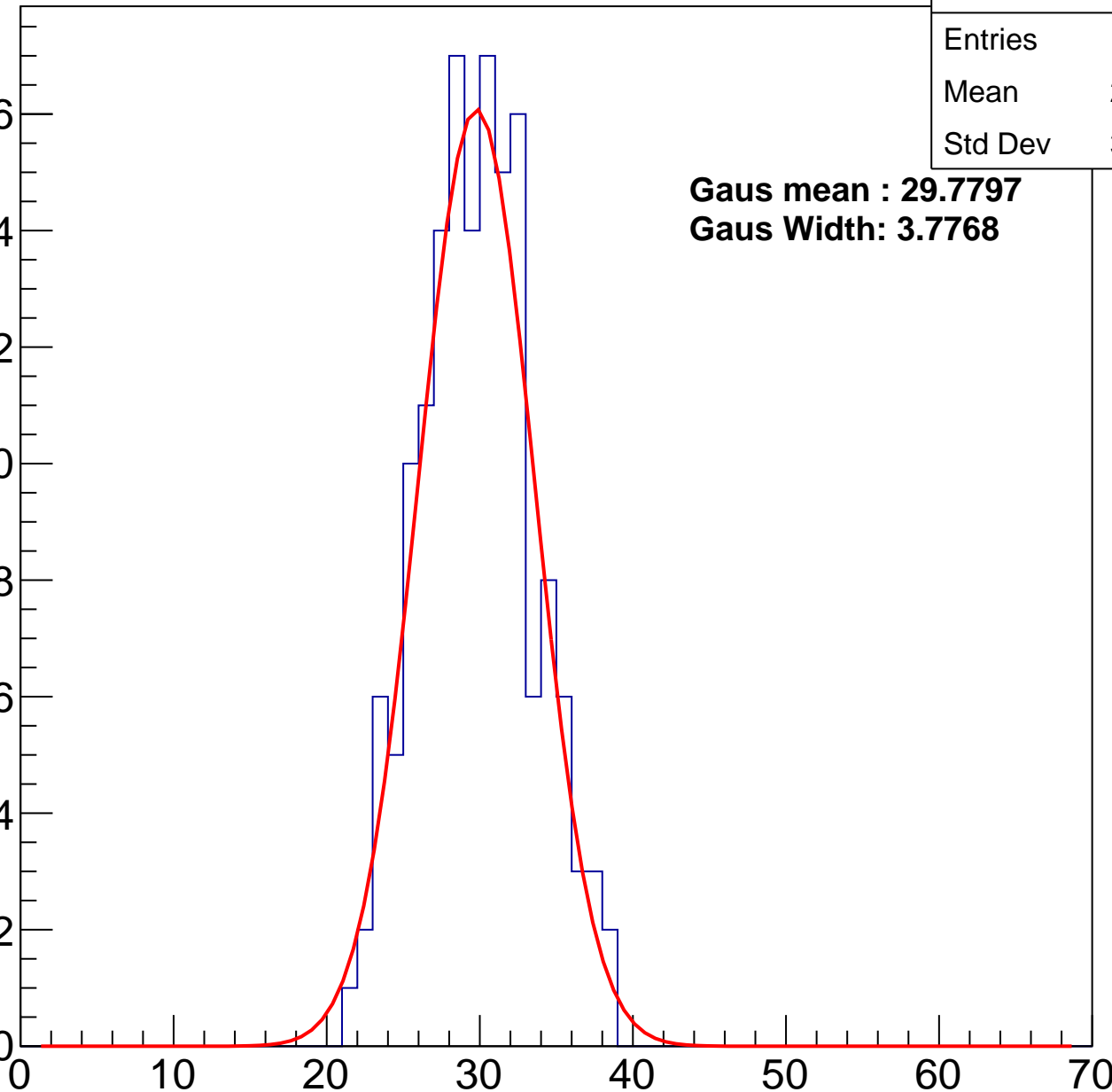
Gaus mean : 29.7797

Gaus Width: 3.7768

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch46, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 36.05 |
| Std Dev | 3.207 |

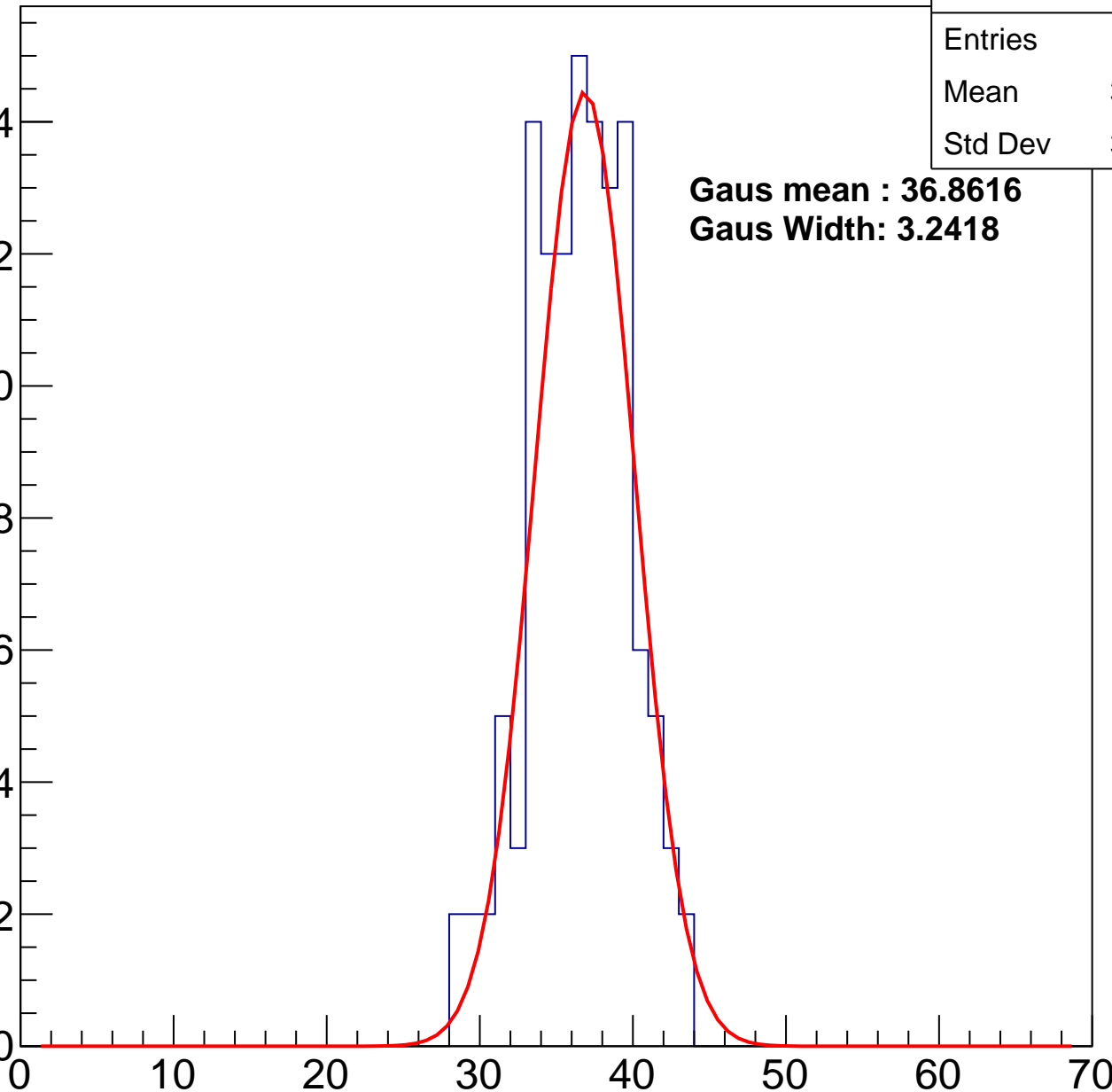
Gaus mean : 36.8616

Gaus Width: 3.2418

Entry

14
12
10
8
6
4
2
0

ampl



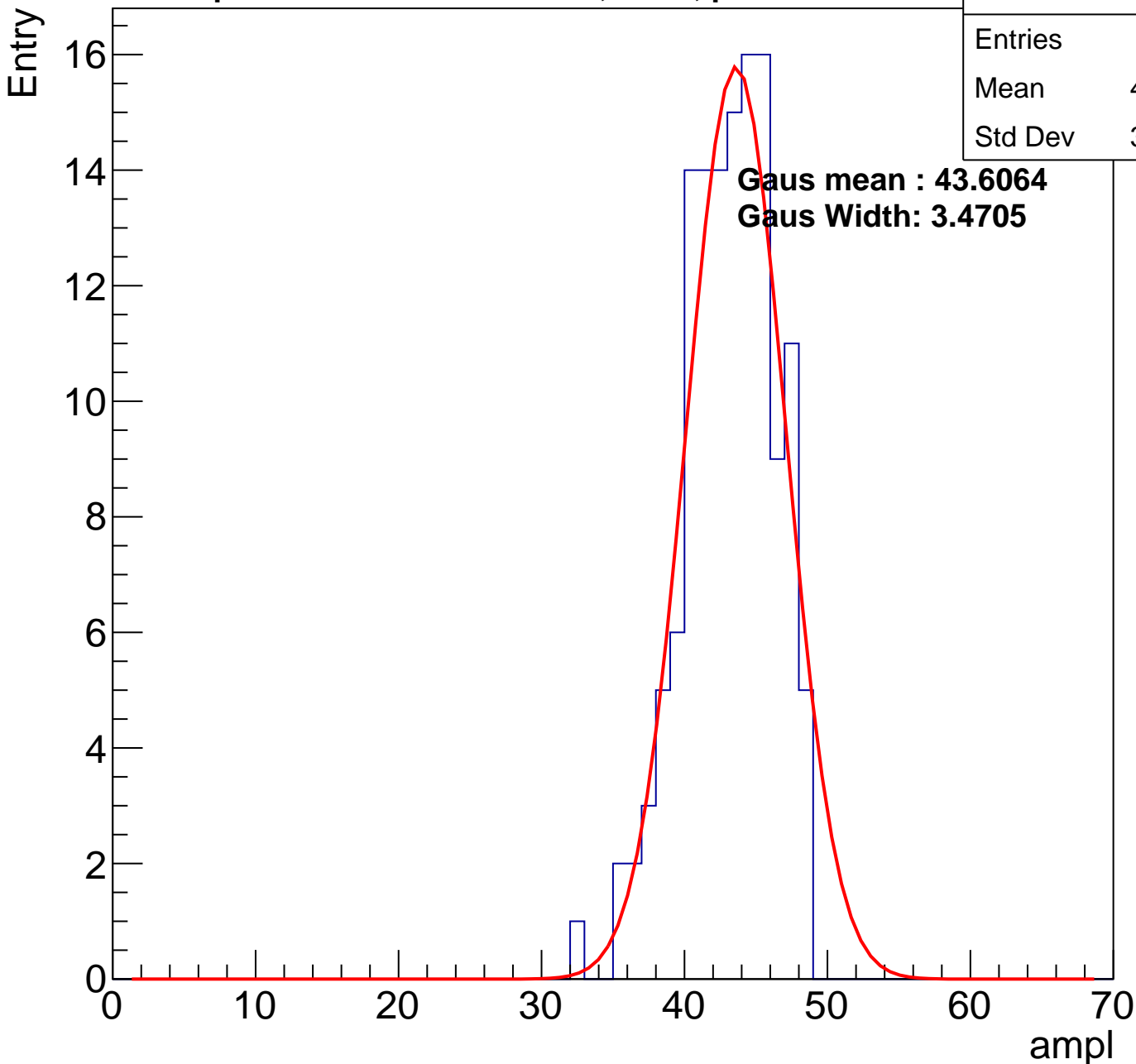
B1L001S, U26-ch46, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 42.64 |
| Std Dev | 3.146 |

Gaus mean : 43.6064

Gaus Width: 3.4705



B1L001S, U26-ch46, adc3

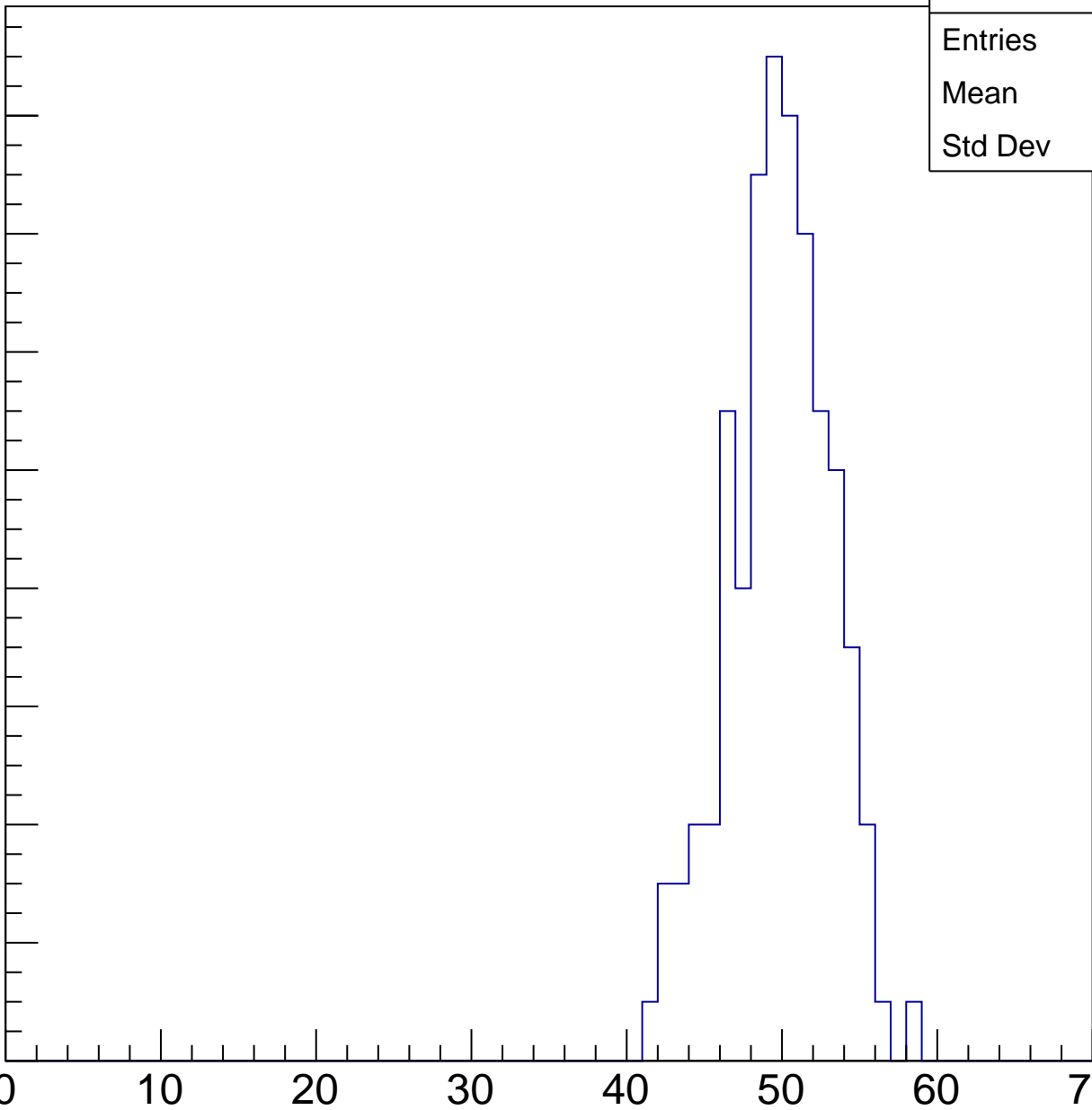
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 130 |
| Mean | 49.35 |
| Std Dev | 3.274 |

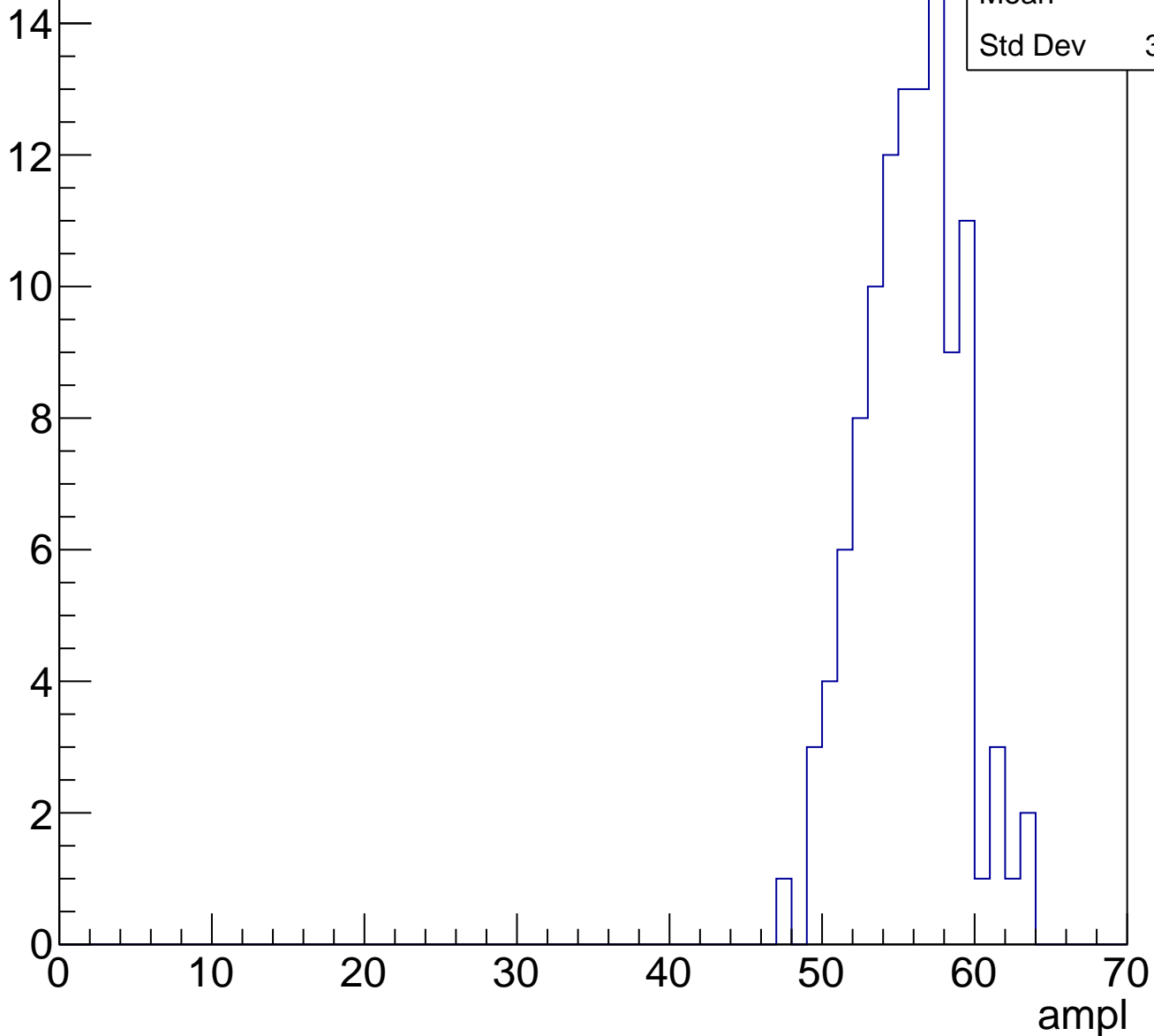
ampl



B1L001S, U26-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 112 |
| Mean | 55.3 |
| Std Dev | 3.165 |

B1L001S, U26-ch46, adc5

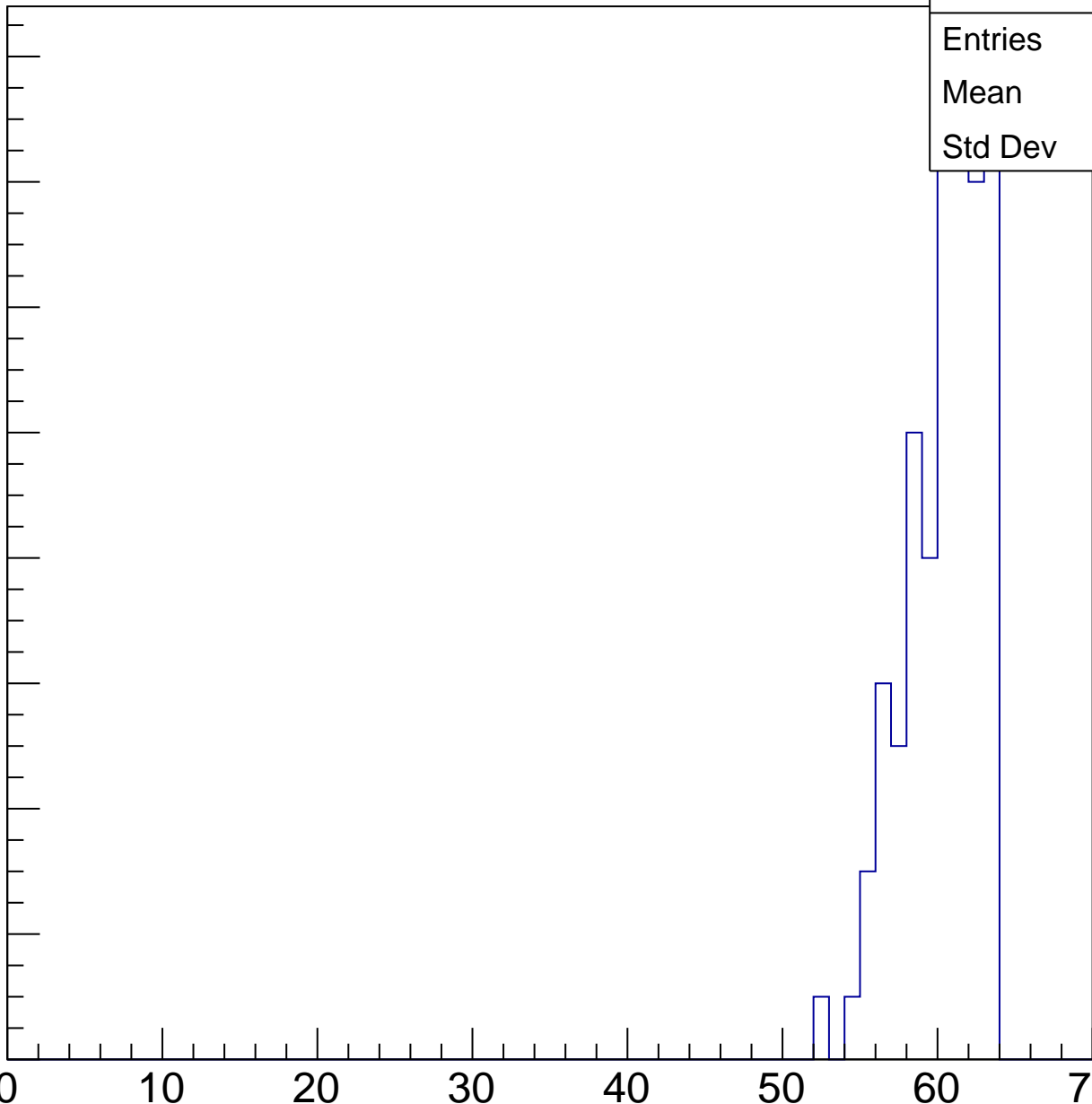
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 94 |
| Mean | 59.91 |
| Std Dev | 2.461 |

ampl

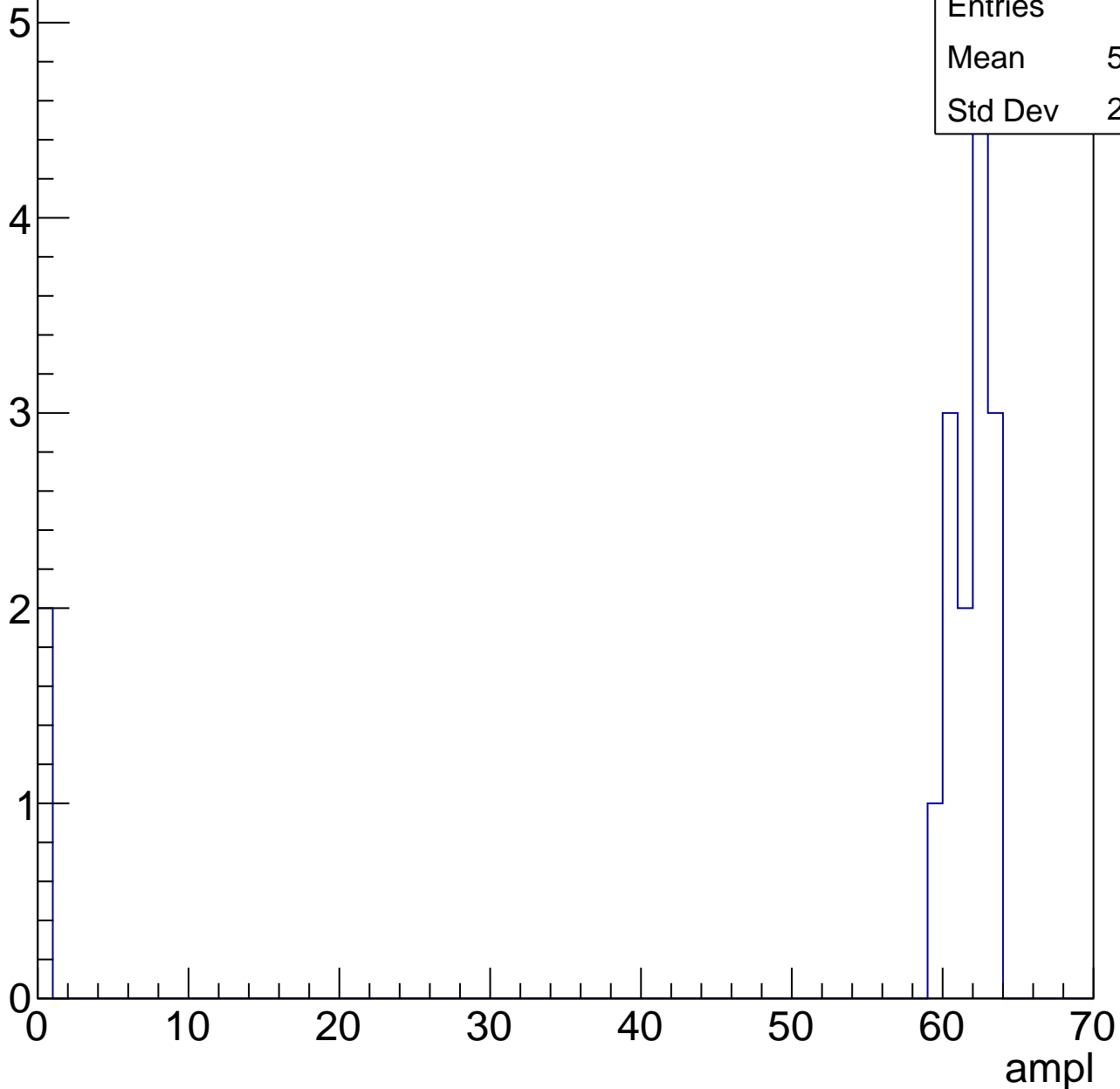


B1L001S, U26-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

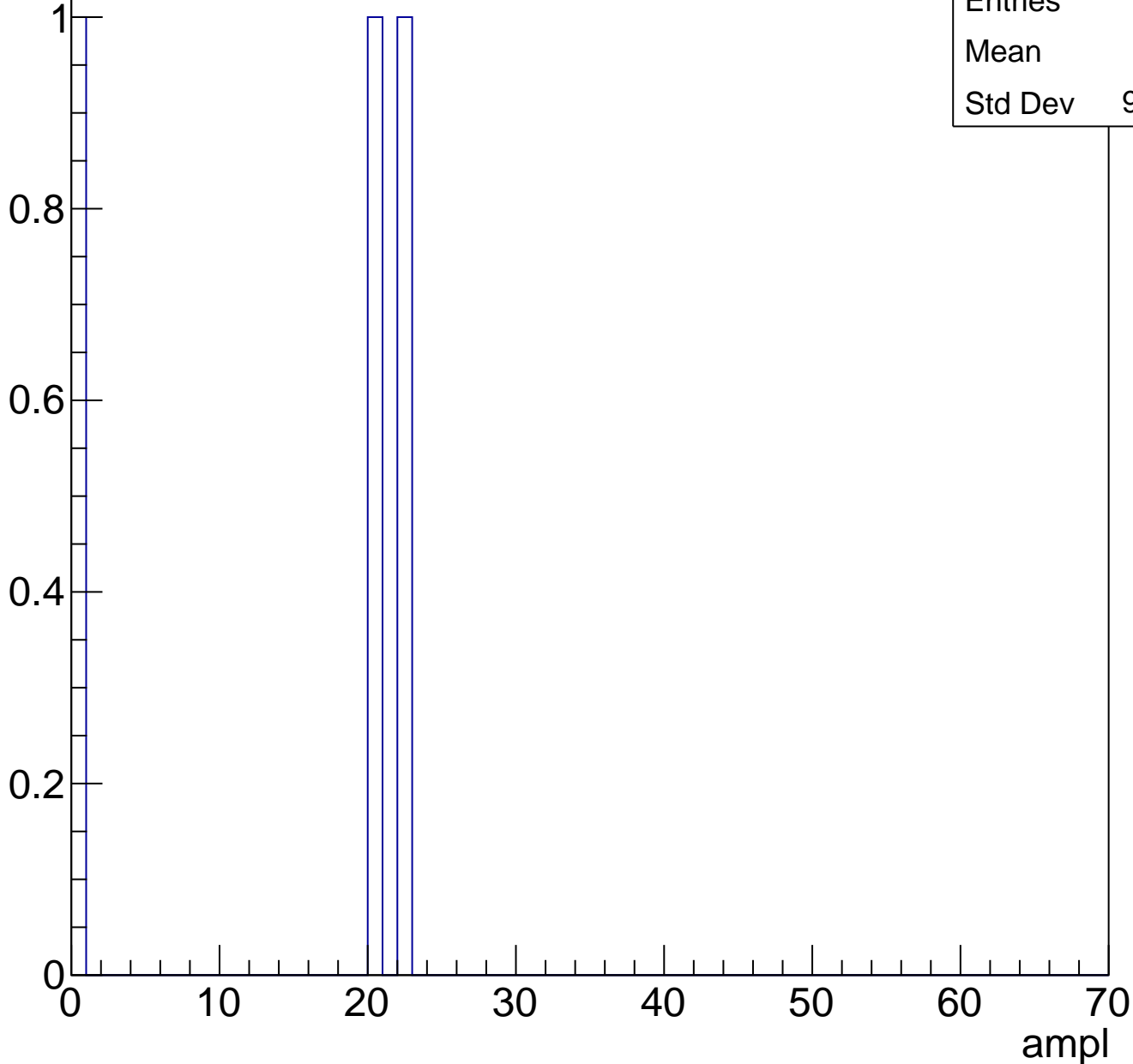
| | |
|---------|-------|
| Entries | 16 |
| Mean | 53.75 |
| Std Dev | 20.35 |



B1L001S, U26-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 30.9 |
| Std Dev | 3.506 |

Gaus mean : 31.3314

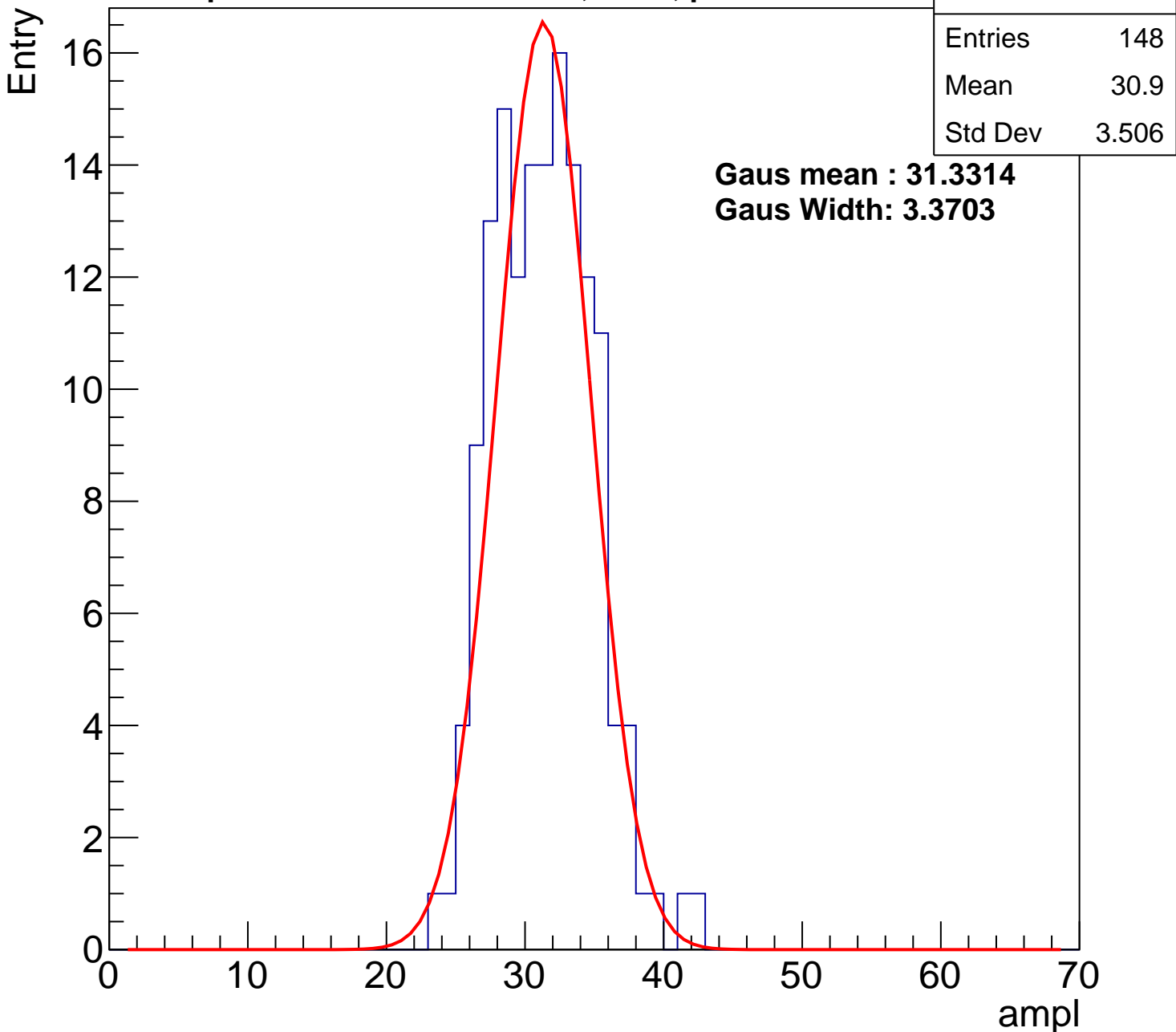
Gaus Width: 3.3703

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



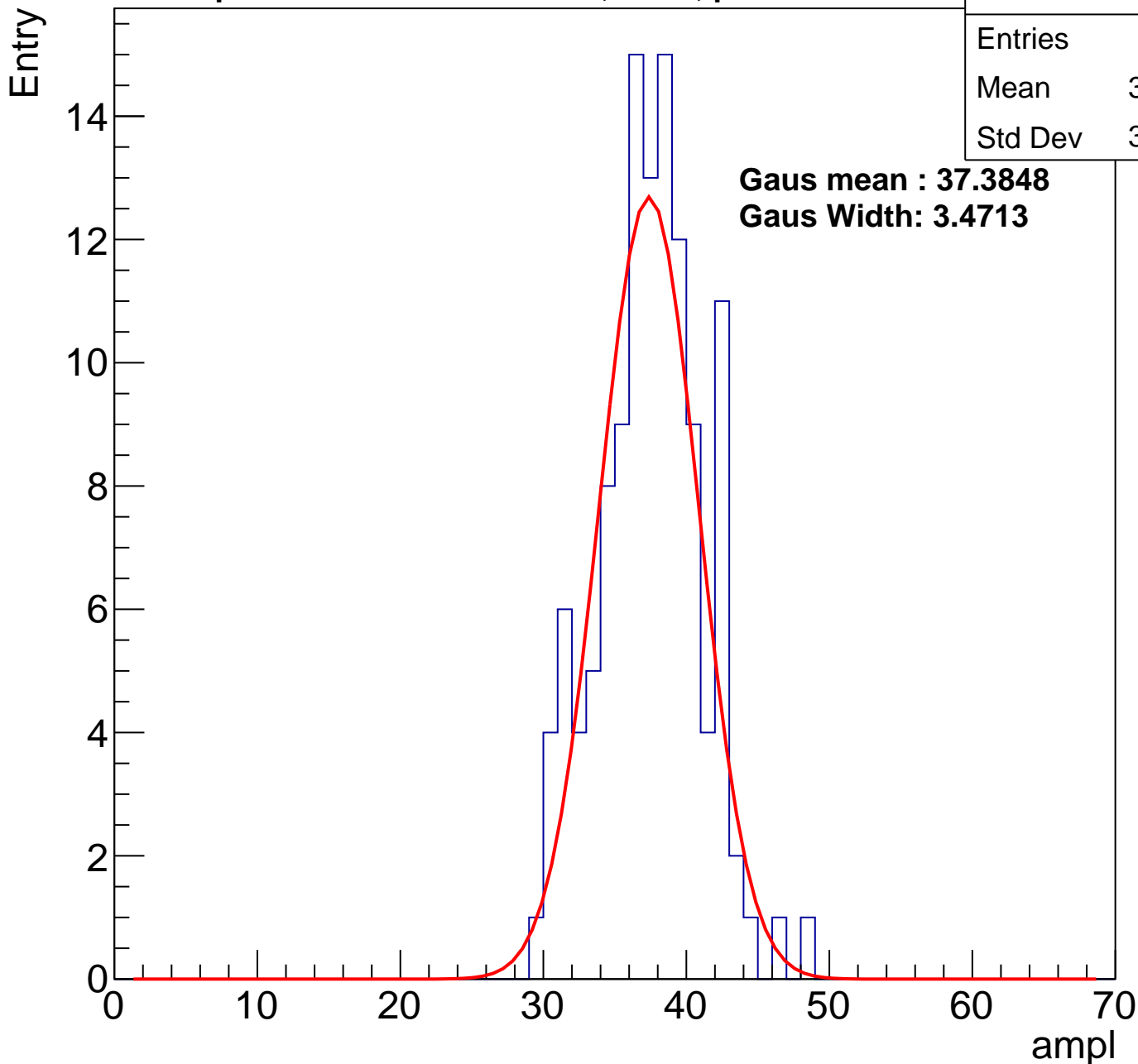
B1L001S, U26-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 37.06 |
| Std Dev | 3.619 |

Gaus mean : 37.3848

Gaus Width: 3.4713



B1L001S, U26-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 43.87 |
| Std Dev | 3.691 |

Gaus mean : 44.3574

Gaus Width: 3.6461

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

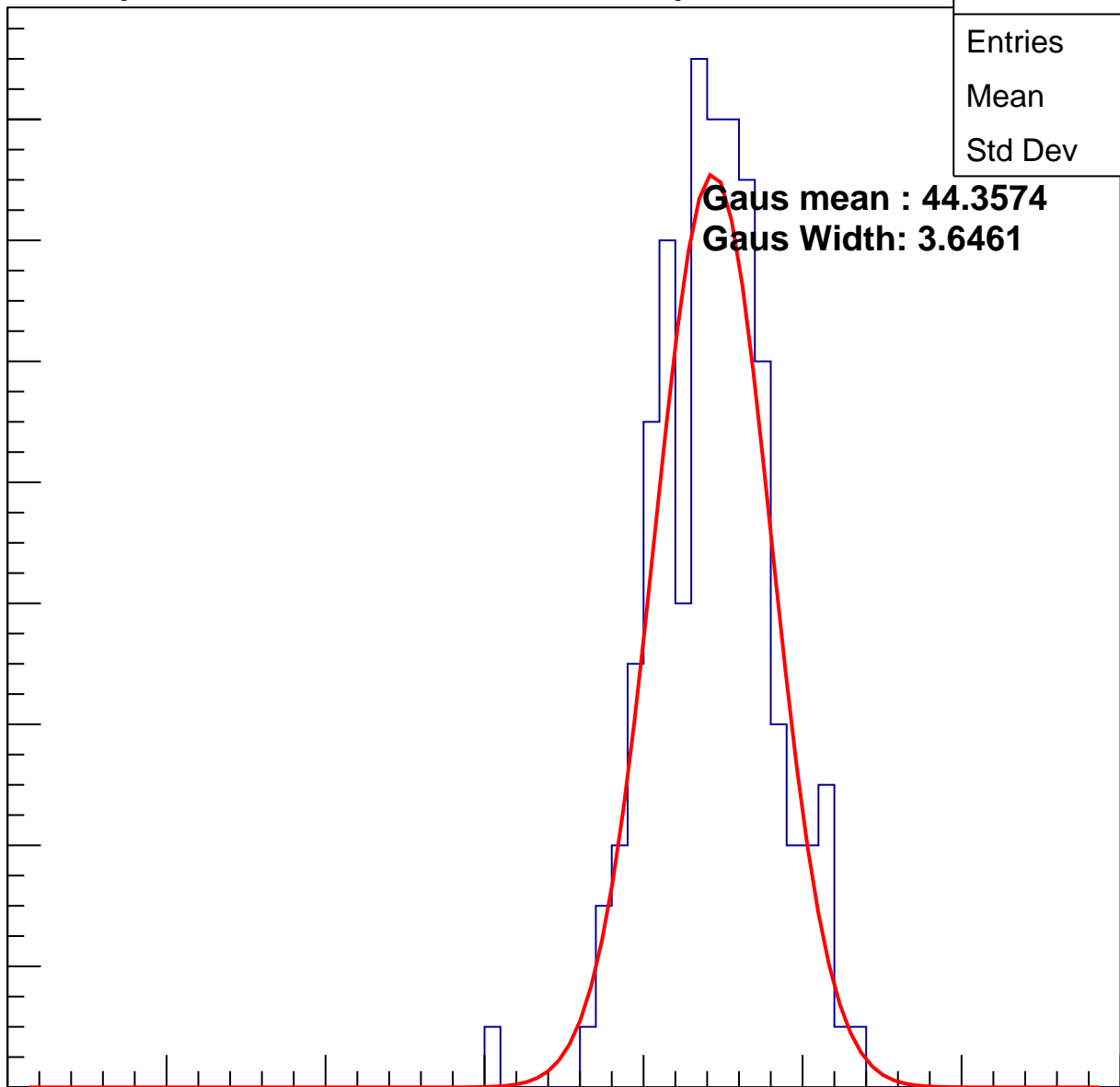
40

50

60

70

ampl



B1L001S, U26-ch47, adc3

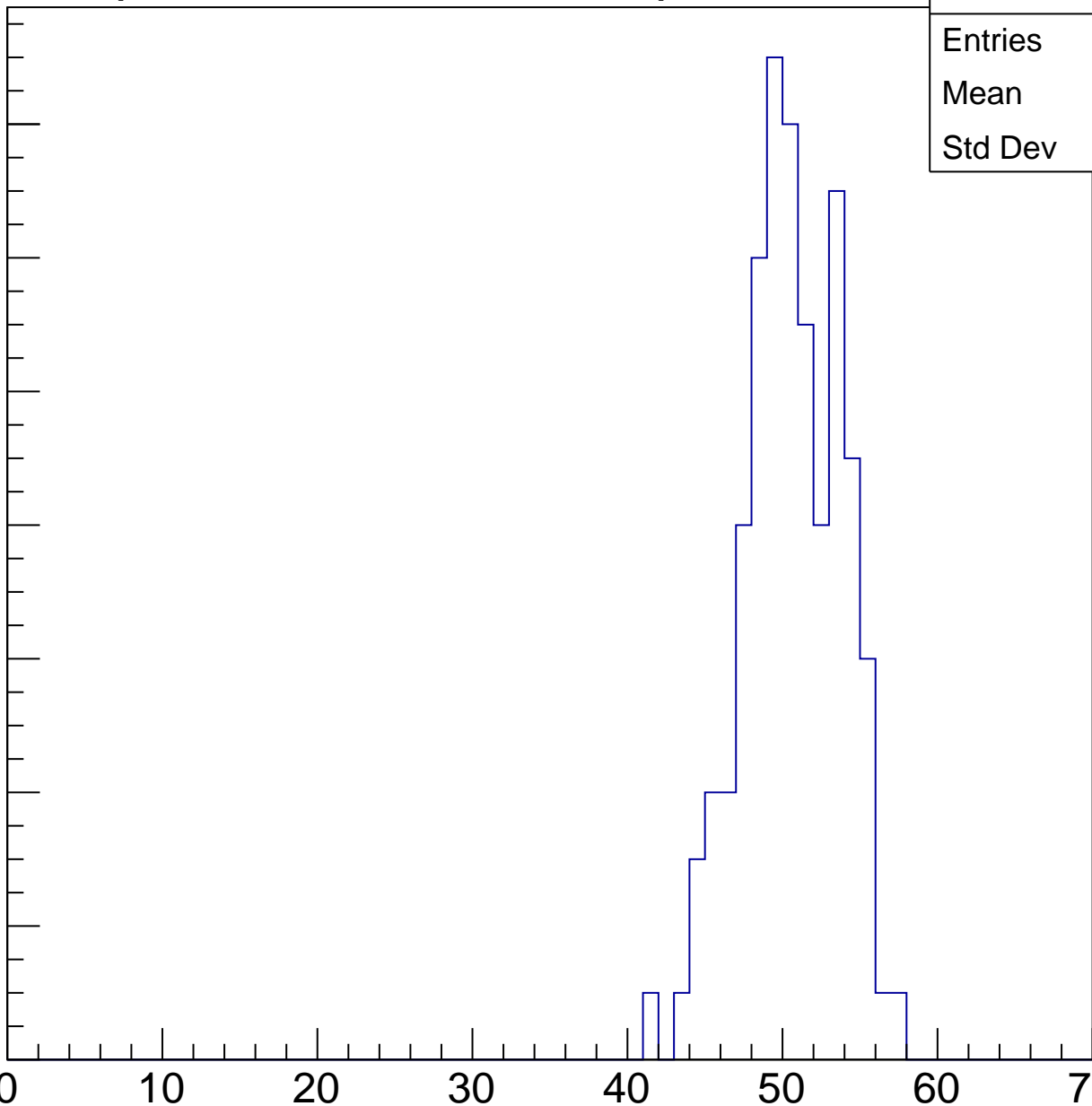
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 111 |
| Mean | 50.11 |
| Std Dev | 3.112 |

ampl



B1L001S, U26-ch47, adc4

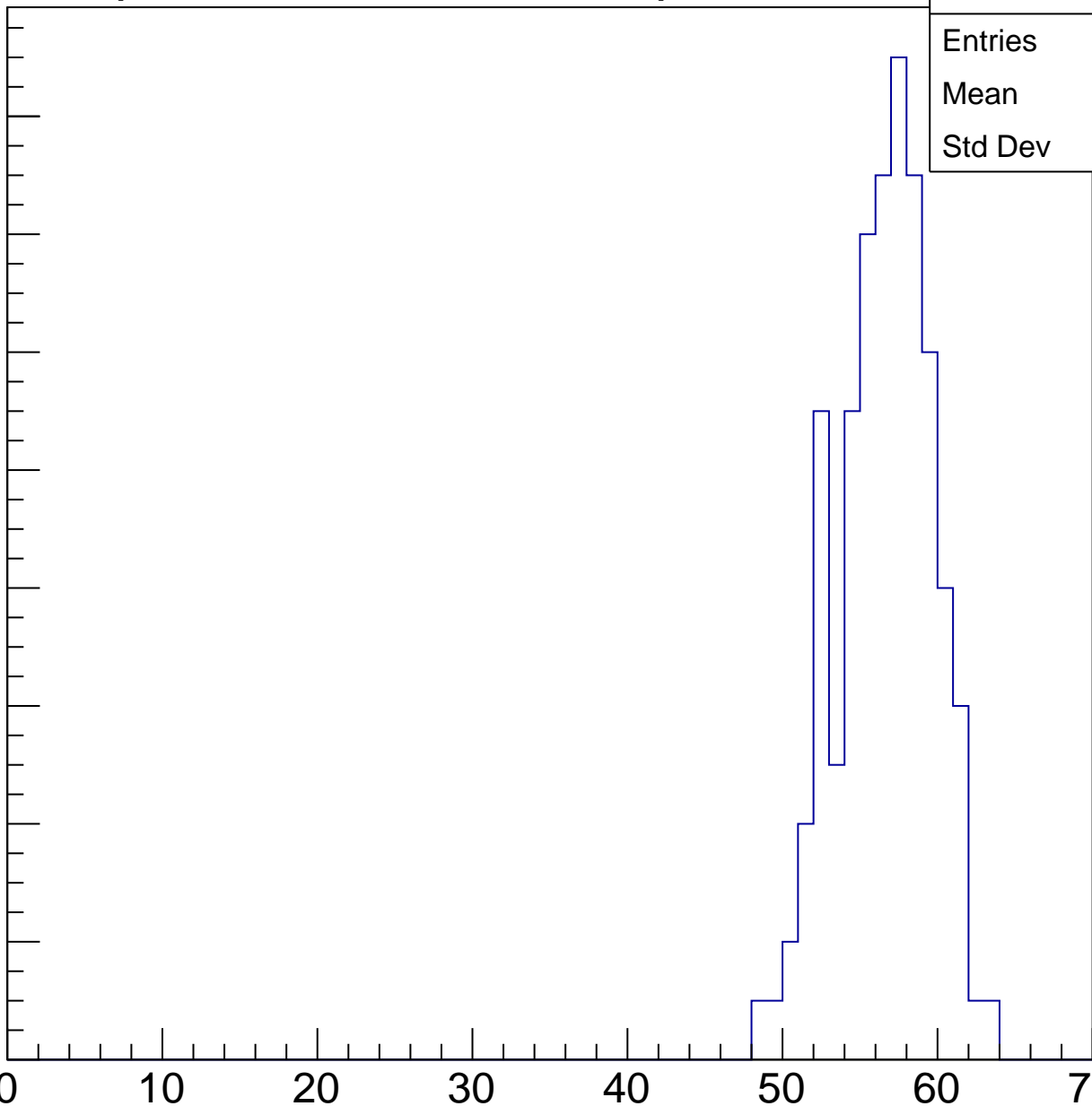
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 124 |
| Mean | 56.13 |
| Std Dev | 2.992 |

ampl



B1L001S, U26-ch47, adc5

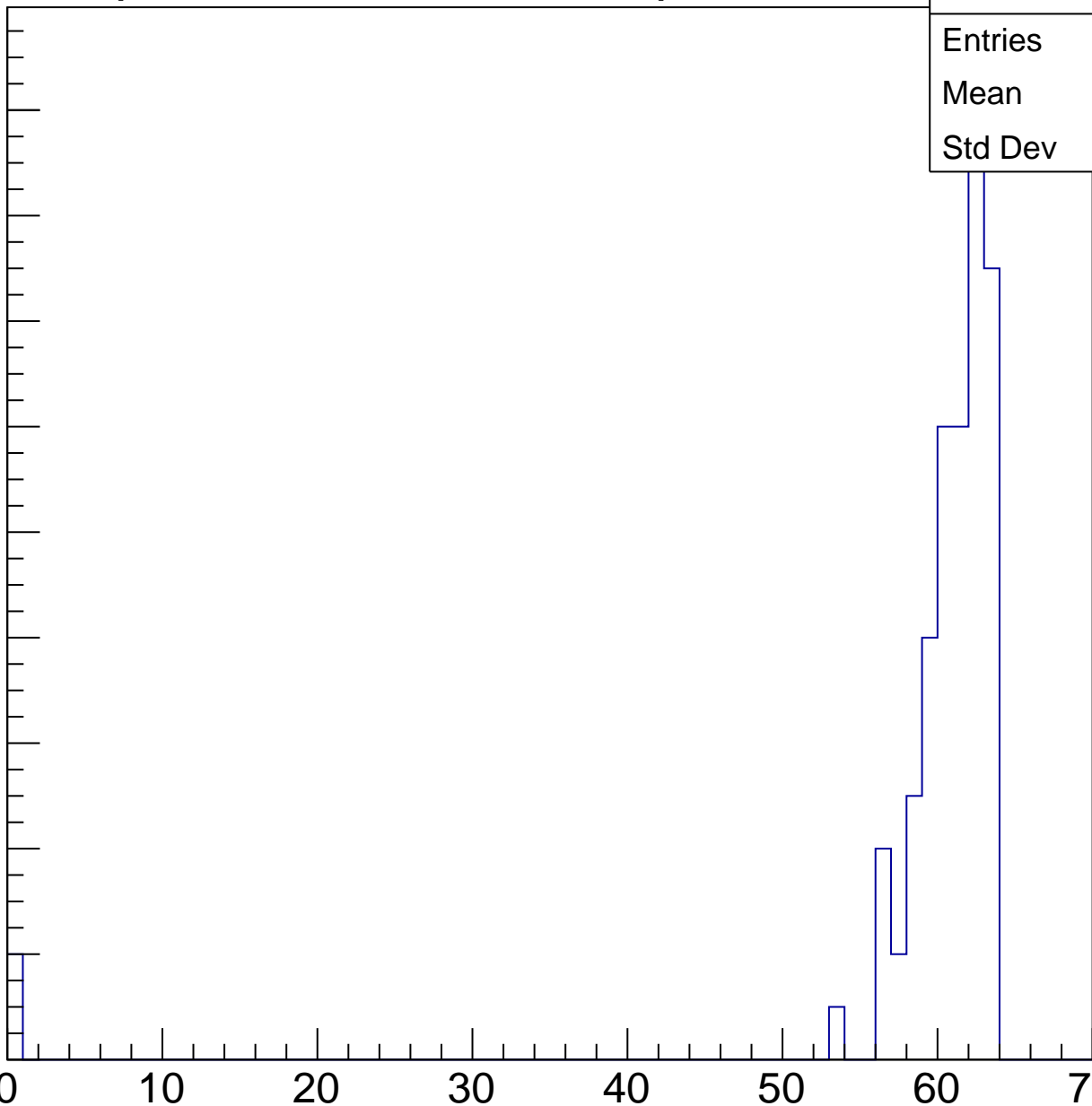
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 80 |
| Mean | 59.1 |
| Std Dev | 9.694 |

ampl



B1L001S, U26-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

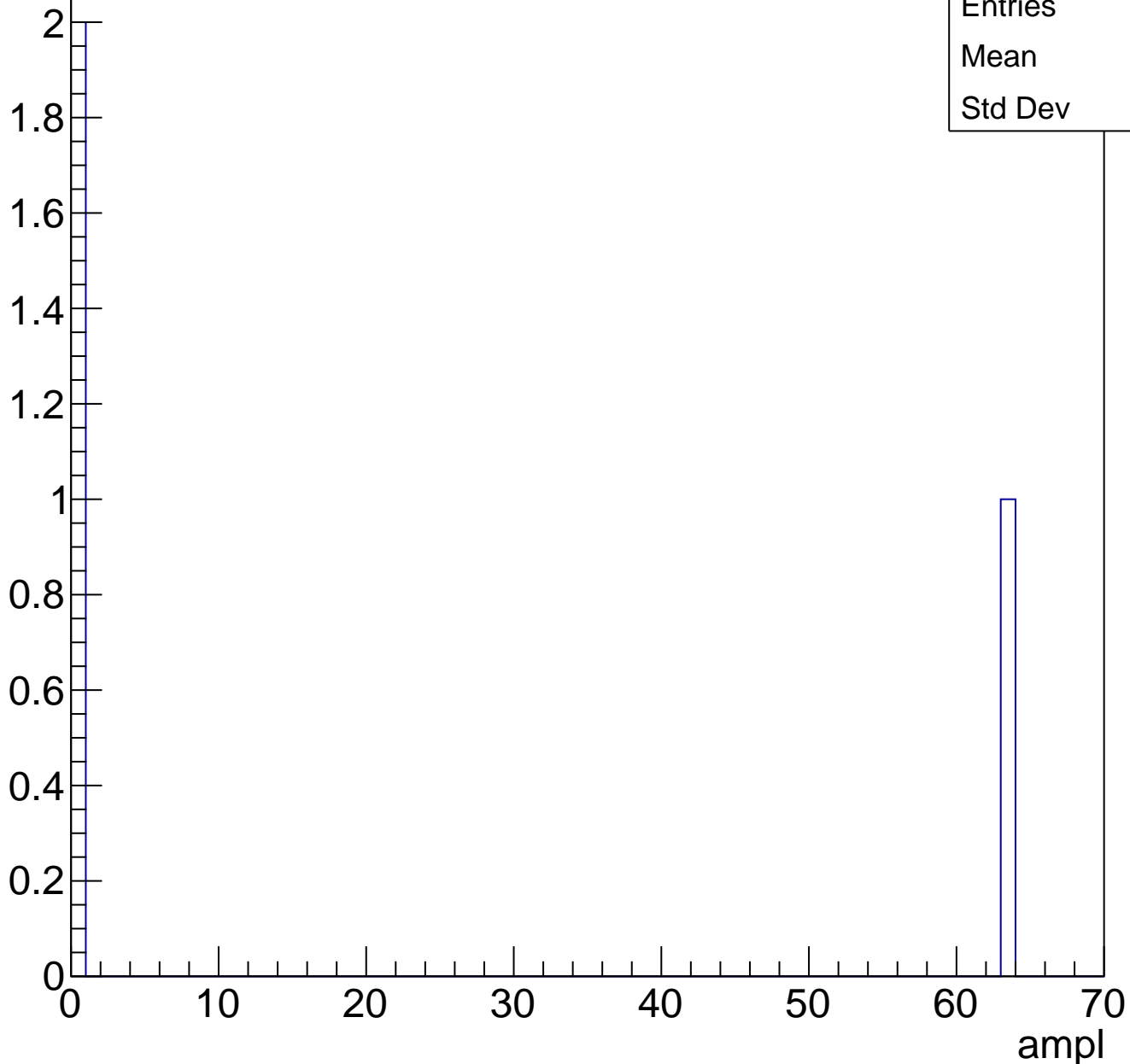
Entry



B1L001S, U26-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

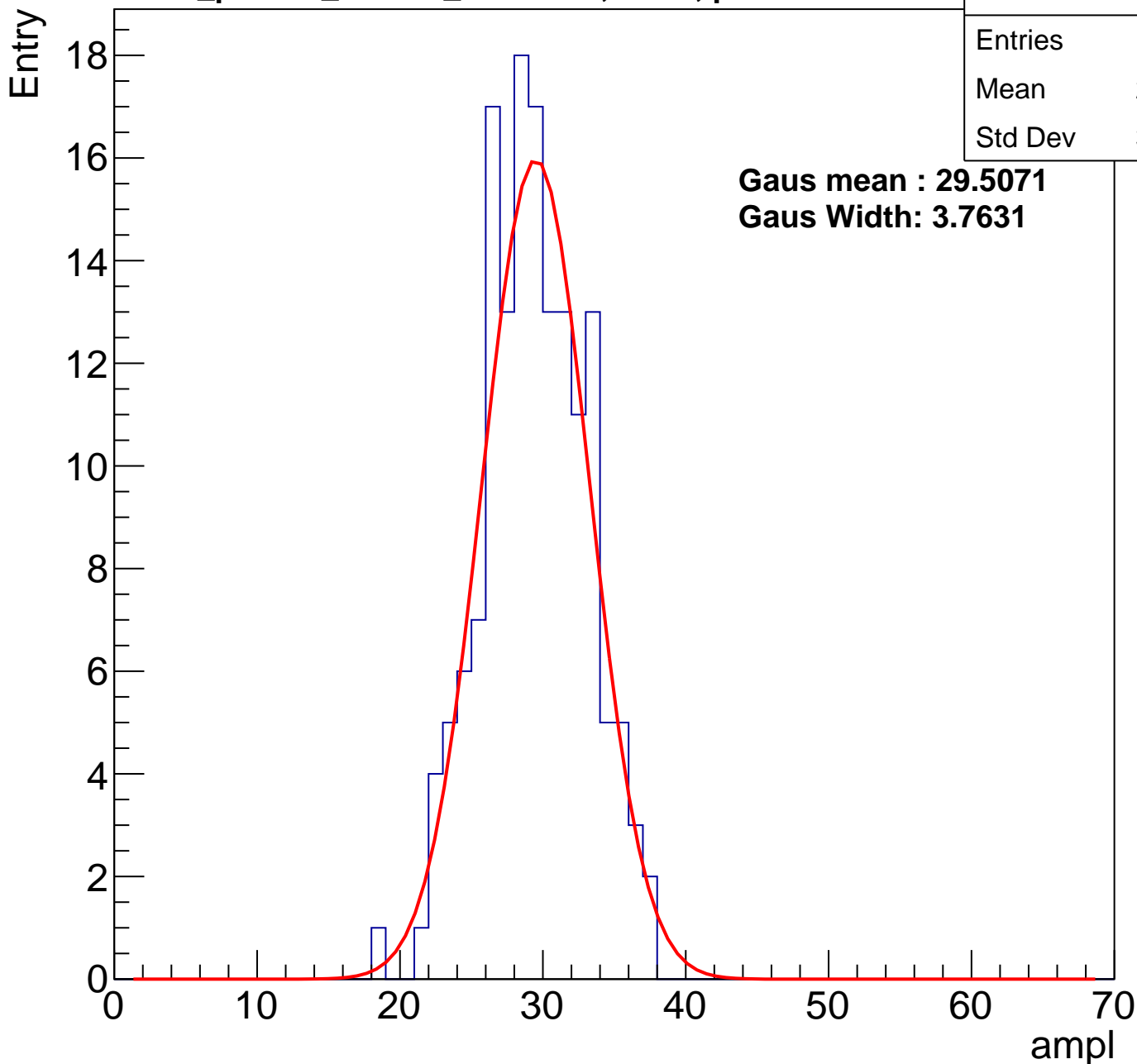
B1L001S, U26-ch48, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 154 |
| Mean | 28.91 |
| Std Dev | 3.601 |

Gaus mean : 29.5071

Gaus Width: 3.7631



B1L001S, U26-ch48, adc1

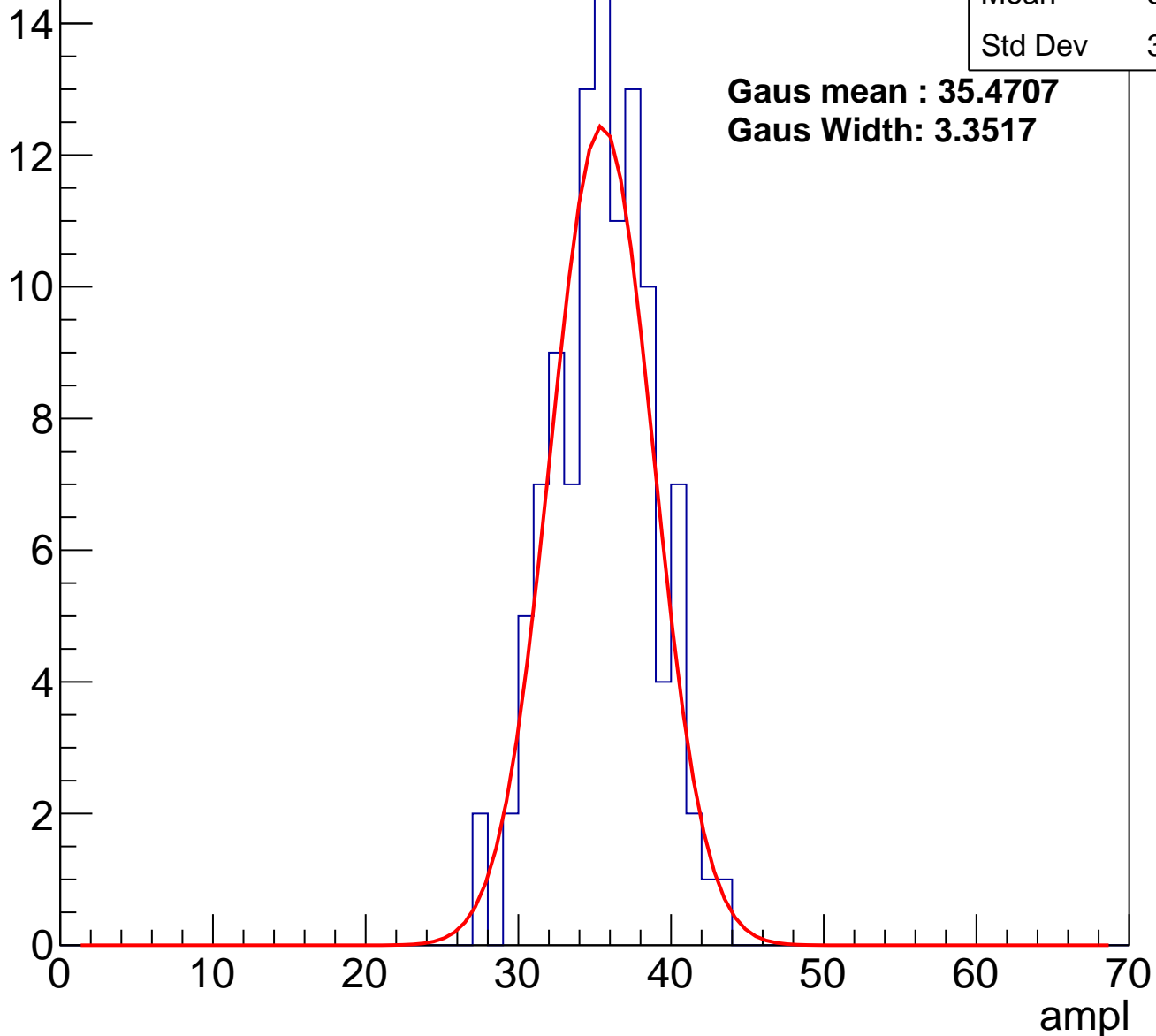
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 109 |
| Mean | 35.09 |
| Std Dev | 3.227 |

Gaus mean : 35.4707

Gaus Width: 3.3517

Entry



B1L001S, U26-ch48, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

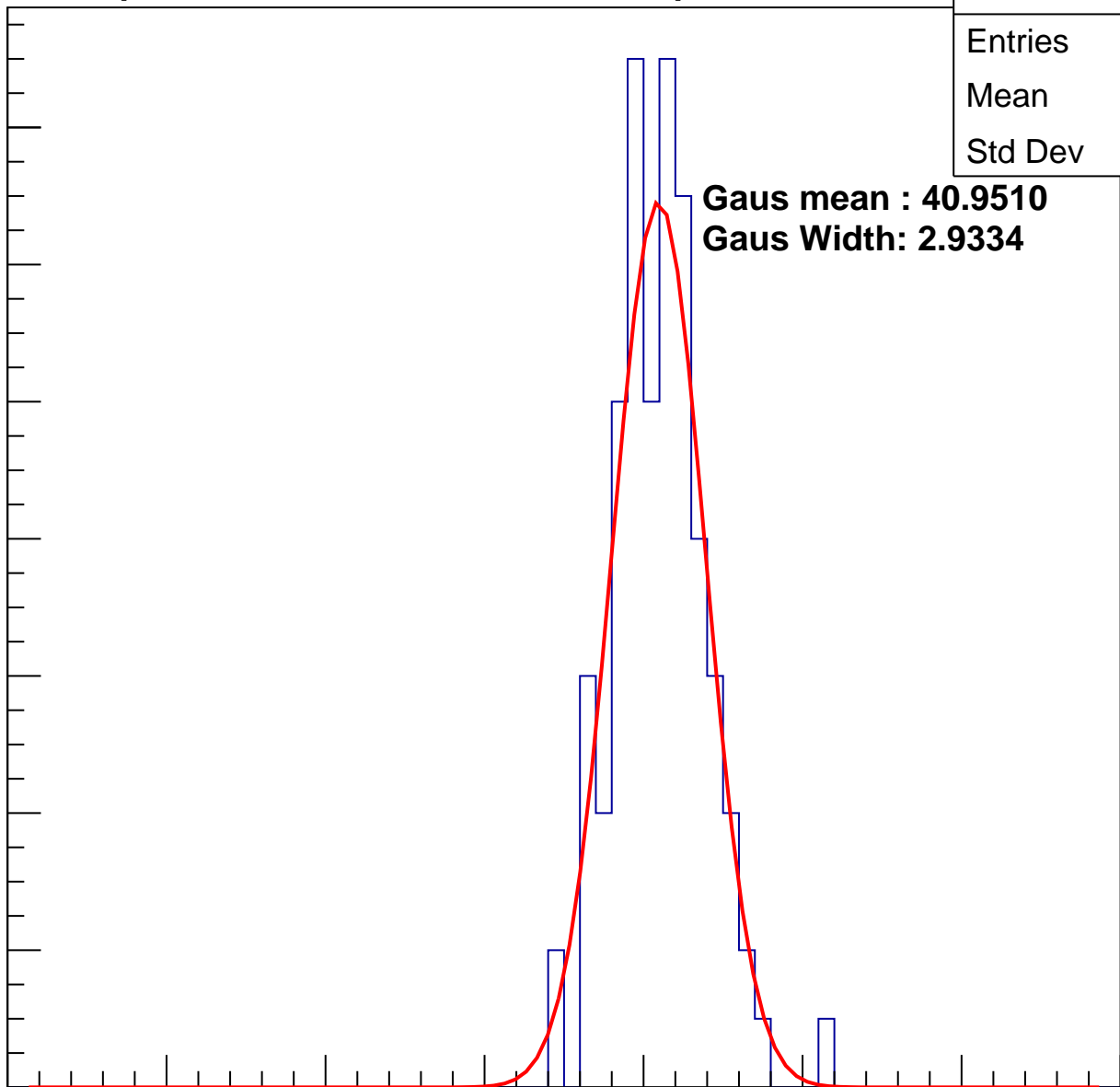
70

ampl

| | |
|---------|-------|
| Entries | 97 |
| Mean | 40.58 |
| Std Dev | 2.878 |

Gaus mean : 40.9510

Gaus Width: 2.9334

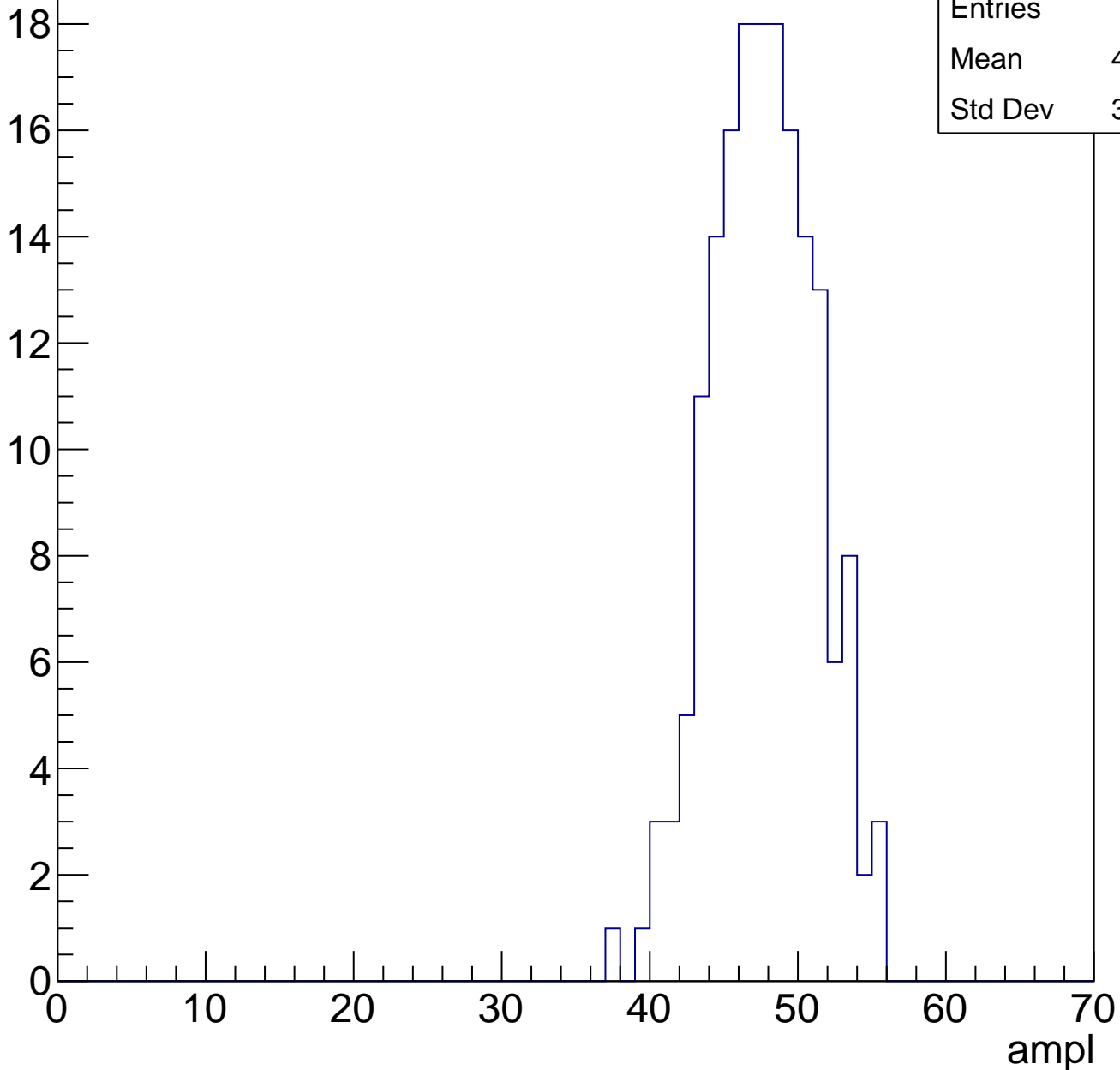


B1L001S, U26-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 170 |
| Mean | 47.25 |
| Std Dev | 3.482 |

Entry



B1L001S, U26-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 54.72 |
| Std Dev | 3.38 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

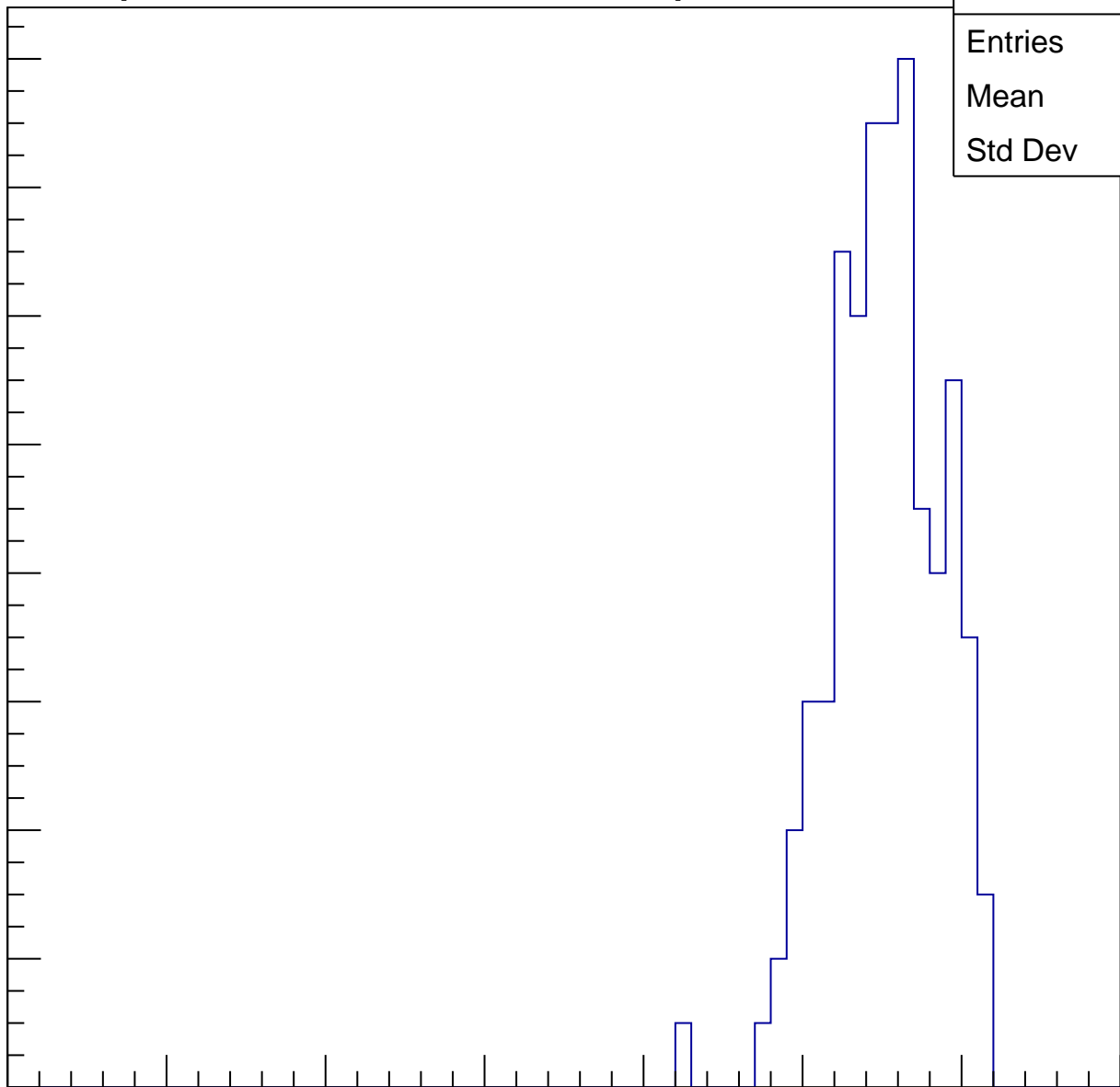
40

50

60

70

ampl



B1L001S, U26-ch48, adc5

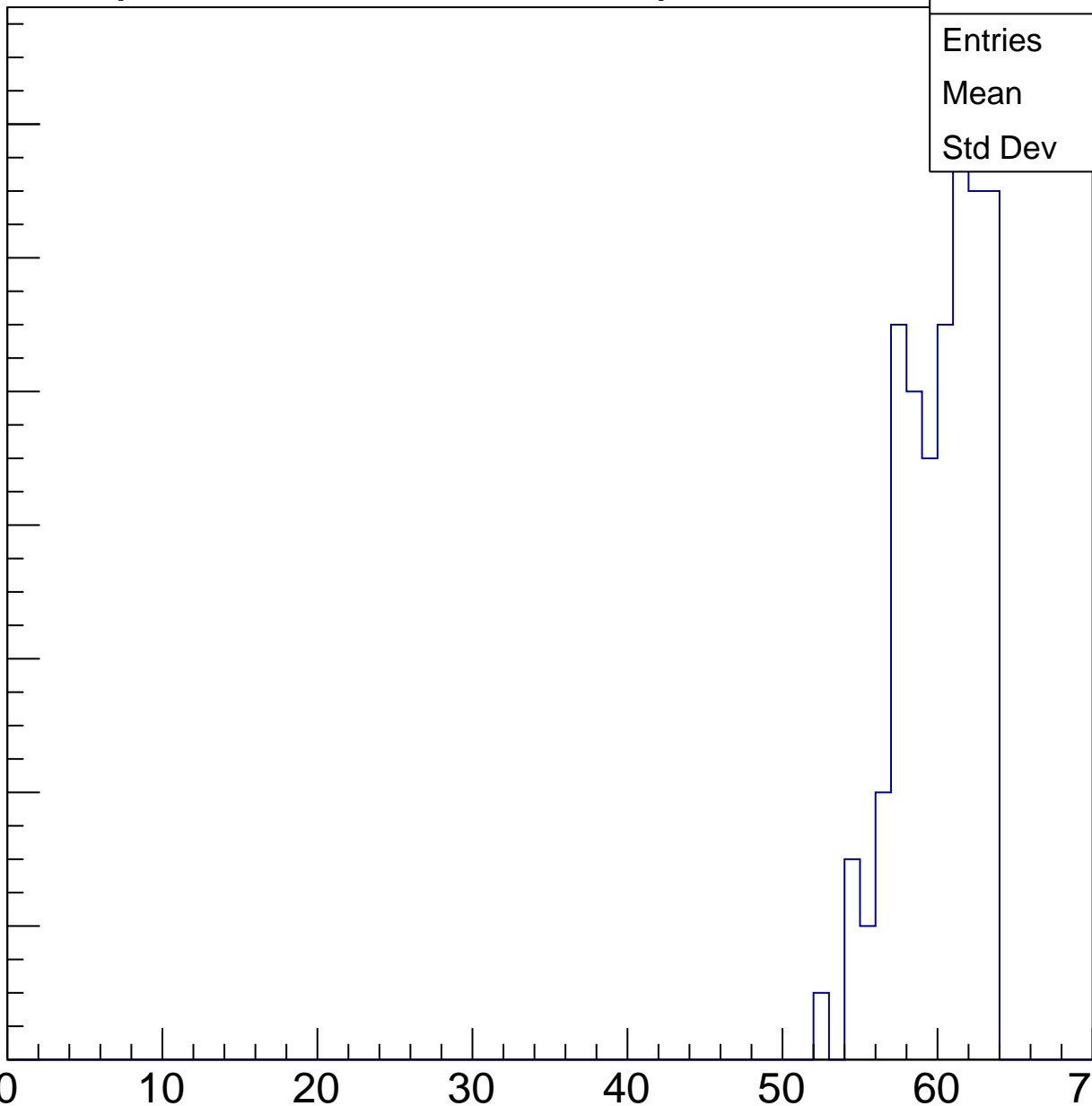
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 92 |
| Mean | 59.63 |
| Std Dev | 2.565 |

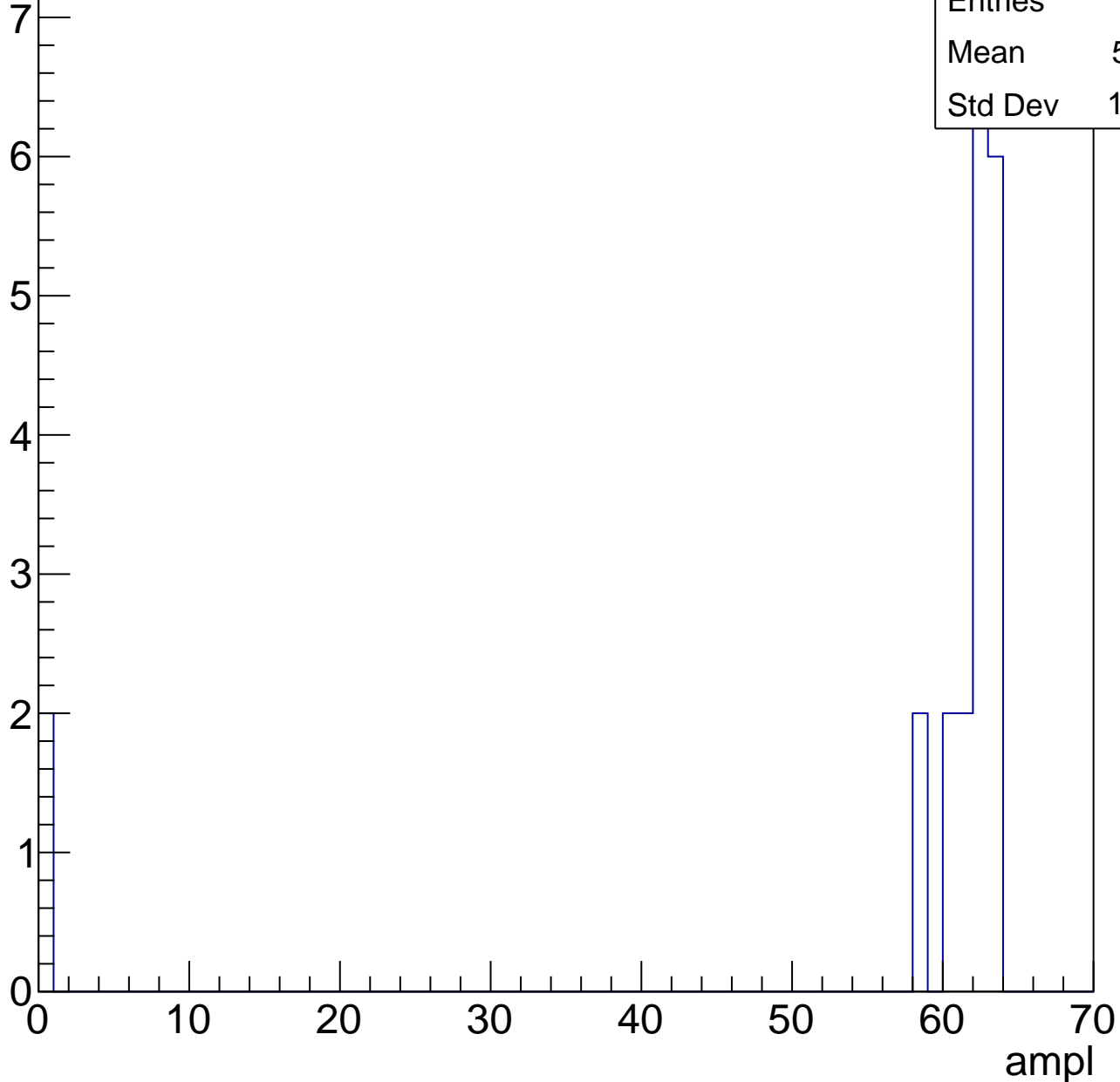
ampl



B1L001S, U26-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B1L001S, U26-ch49, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 29.09 |
| Std Dev | 3.105 |

Gaus mean : 29.6280

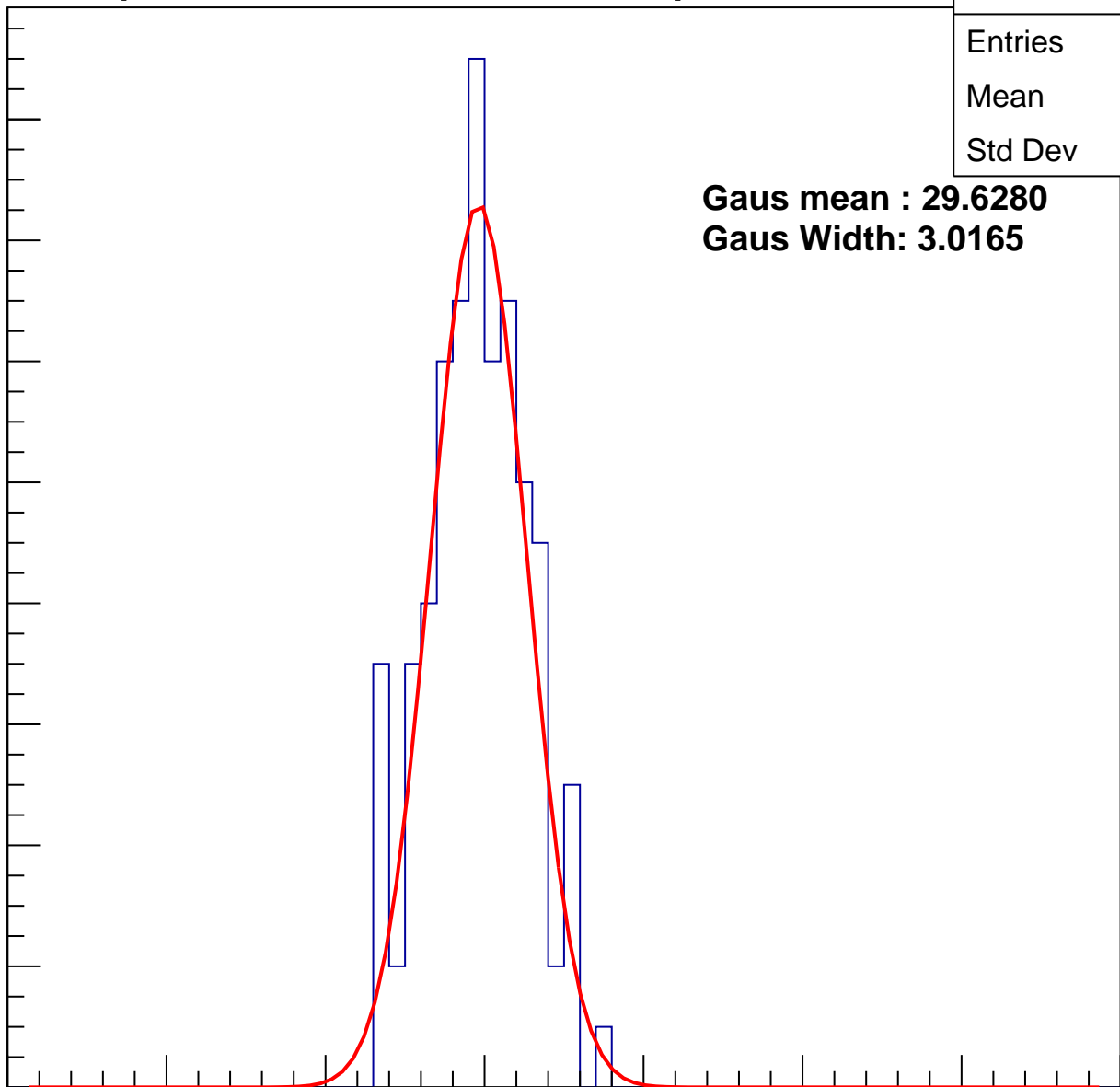
Gaus Width: 3.0165

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch49, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 35.2 |
| Std Dev | 3.291 |

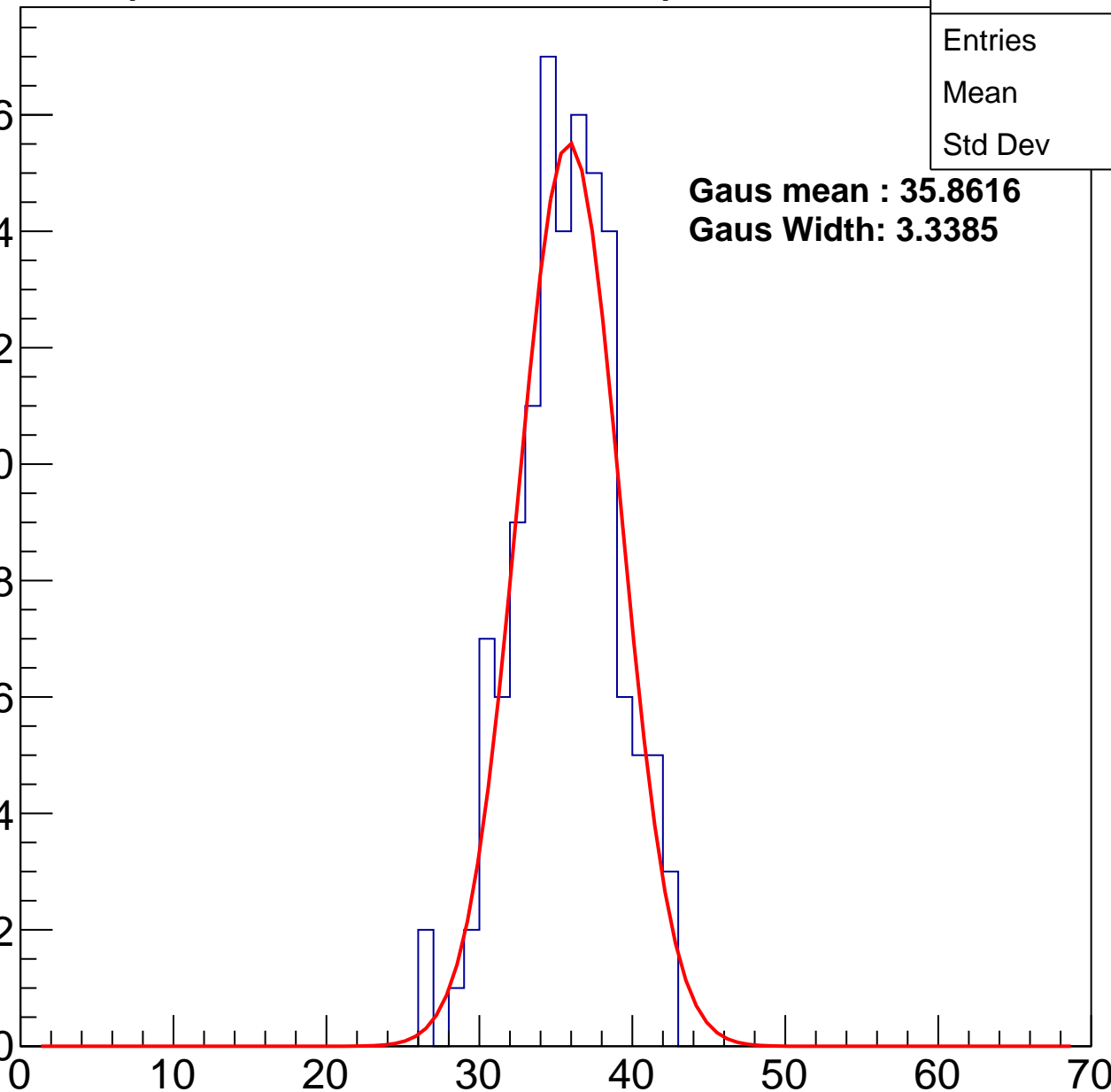
Gaus mean : 35.8616

Gaus Width: 3.3385

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch49, adc2

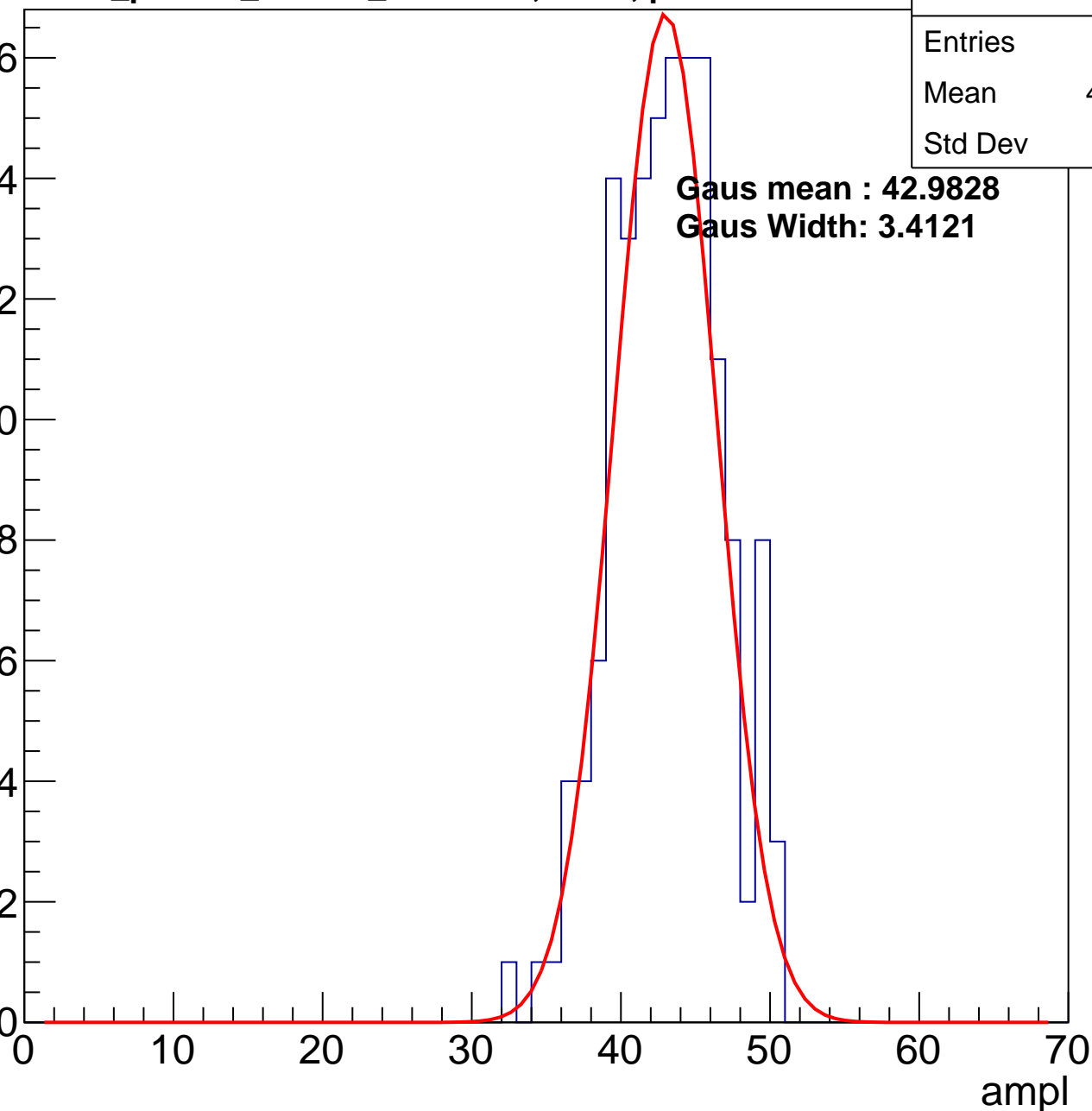
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 153 |
| Mean | 42.63 |
| Std Dev | 3.56 |

Gaus mean : 42.9828
Gaus Width: 3.4121

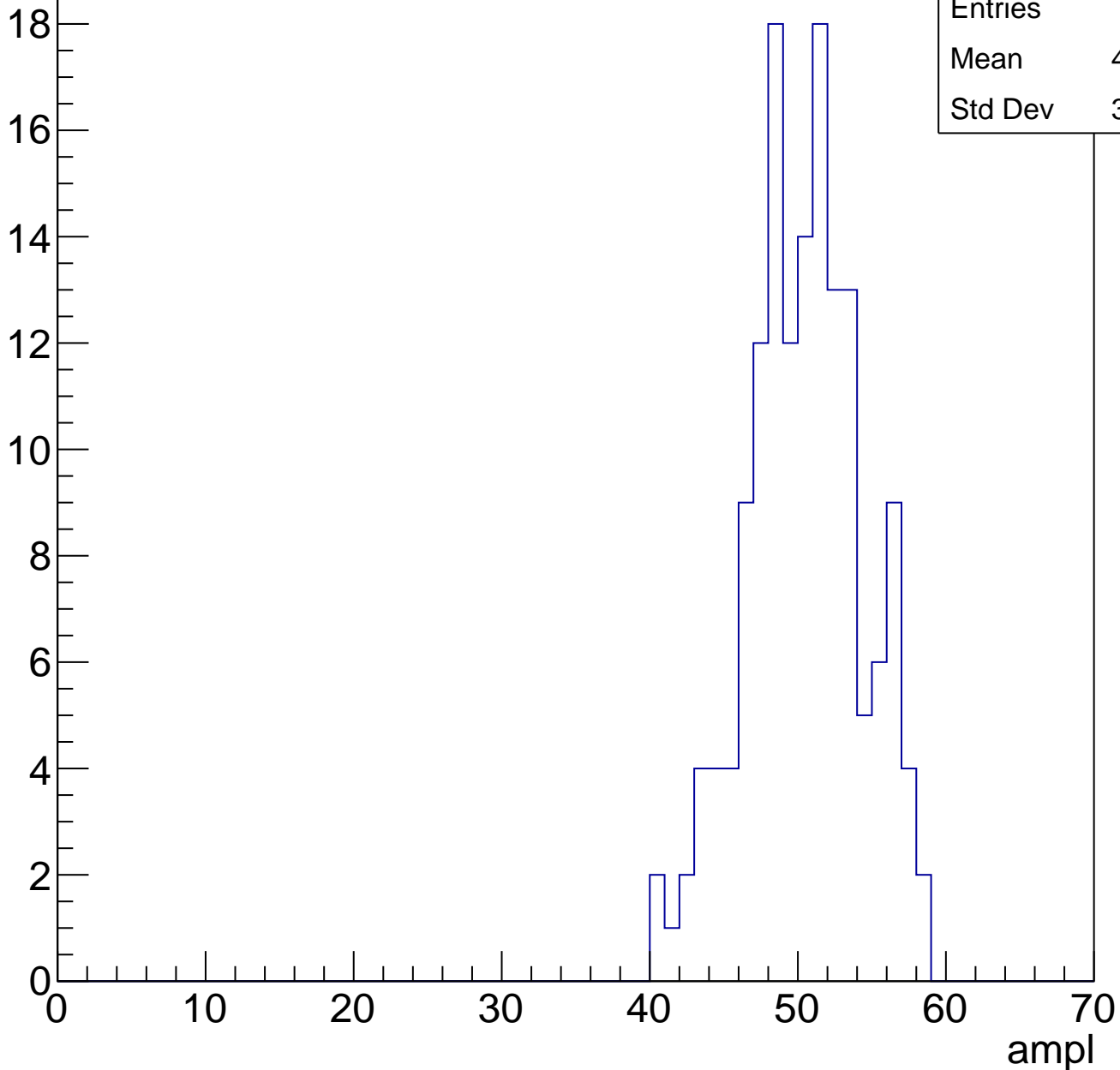


B1L001S, U26-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 49.96 |
| Std Dev | 3.847 |

Entry



B1L001S, U26-ch49, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 100 |
| Mean | 56.29 |
| Std Dev | 3.099 |

Entry

12

10

8

6

4

2

0

0

10

20

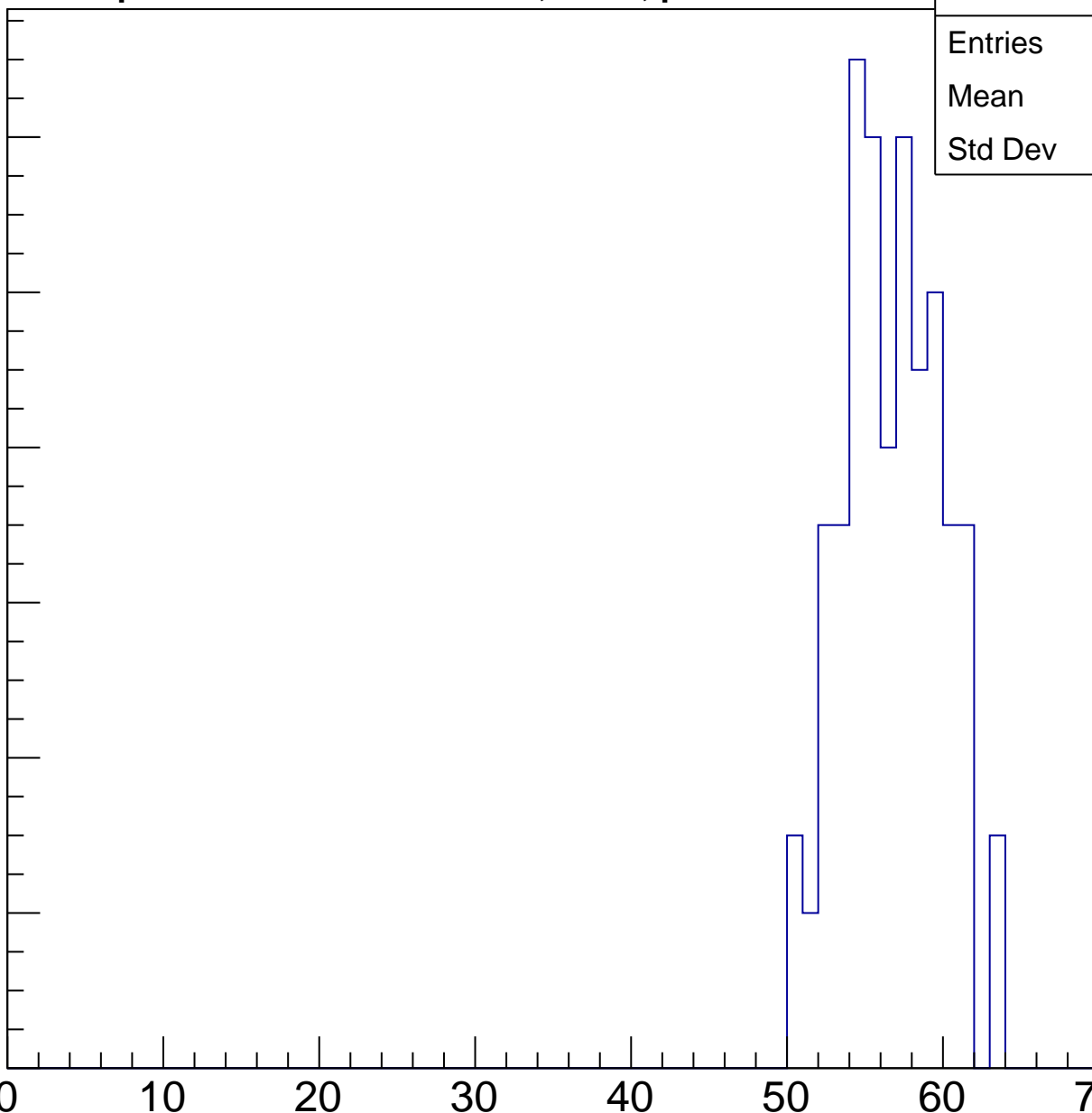
30

40

50

60

ampl



B1L001S, U26-ch49, adc5

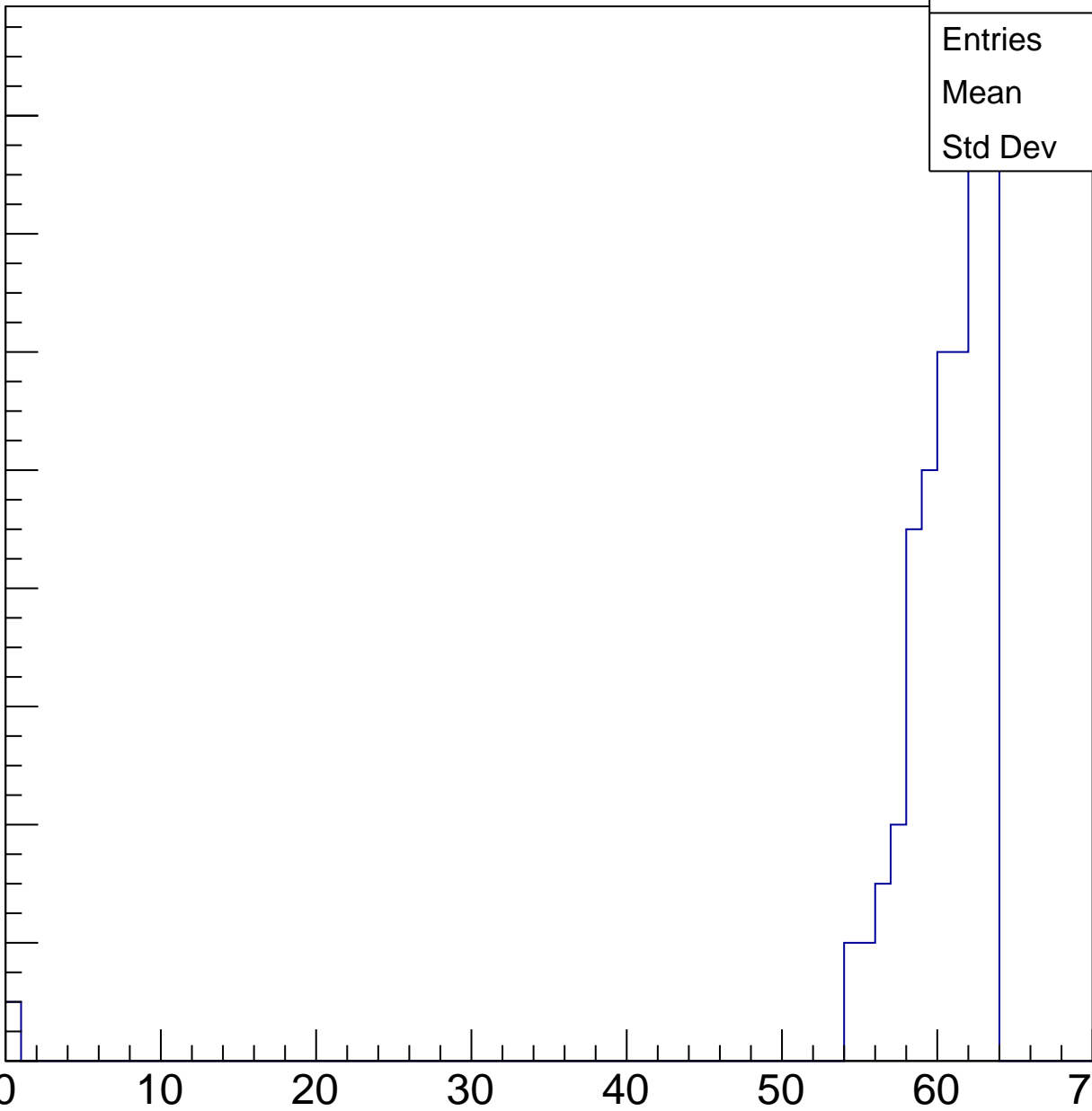
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 59.58 |
| Std Dev | 6.762 |

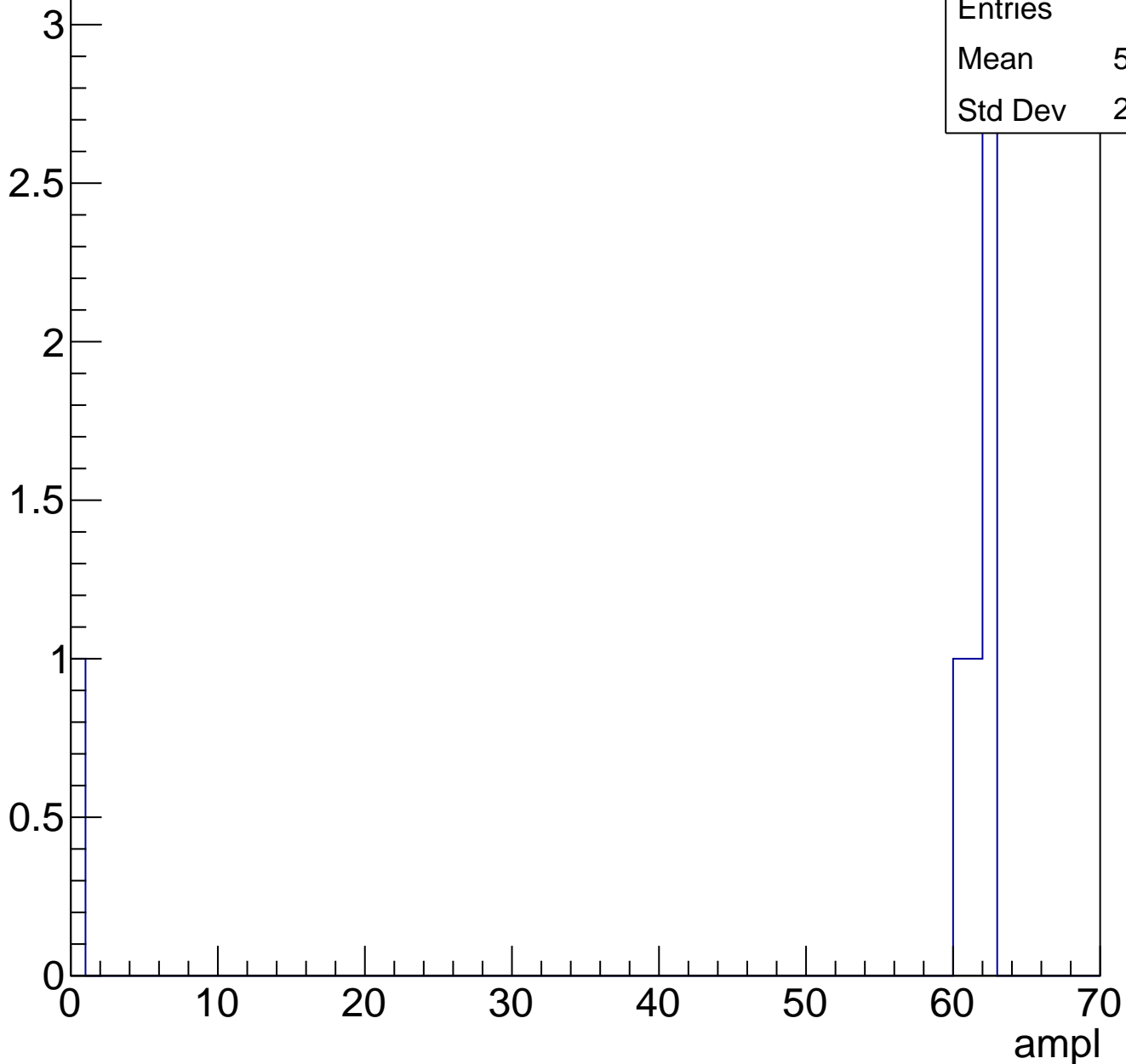
ampl



B1L001S, U26-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

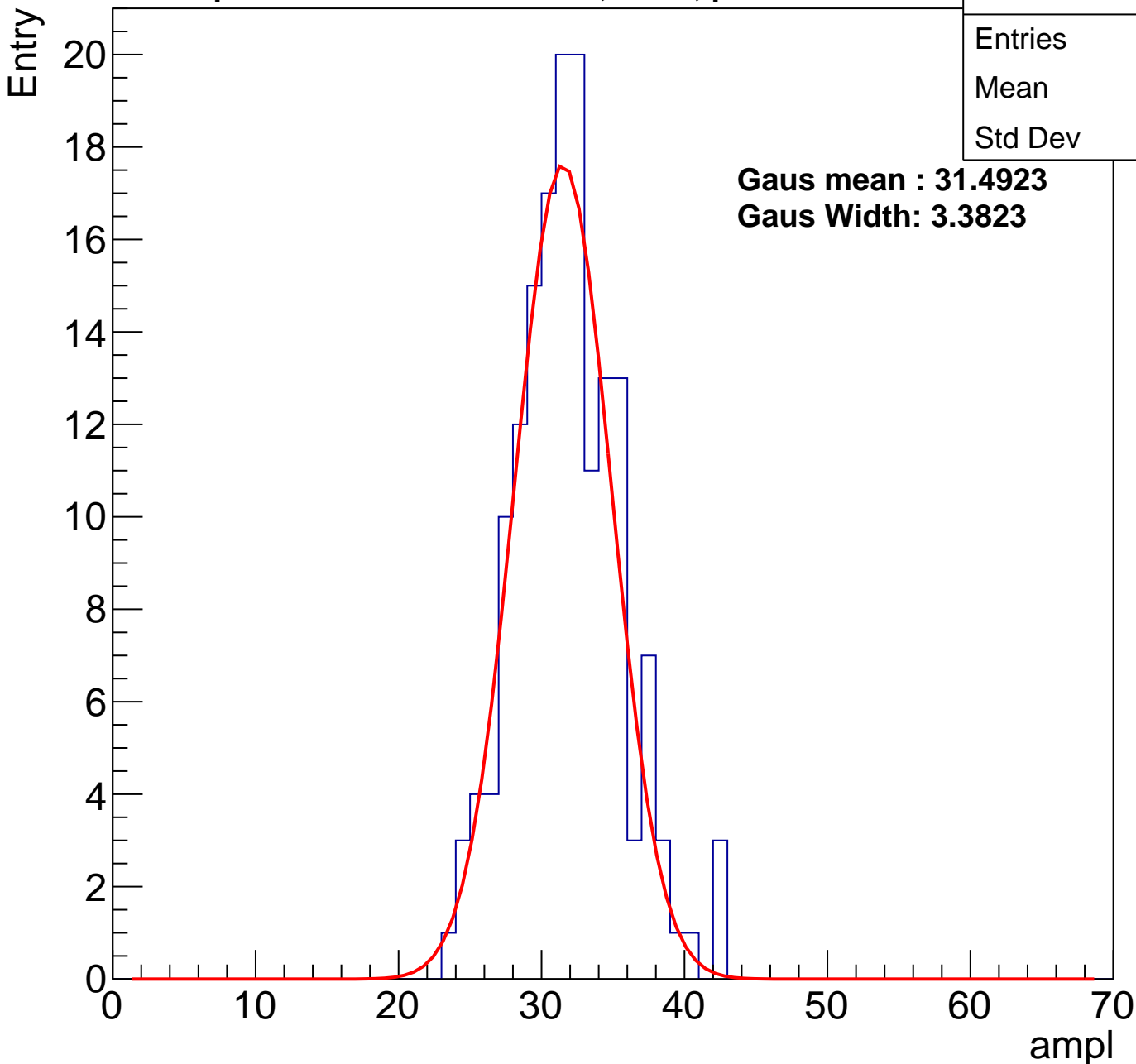
B1L001S, U26-ch50, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|------|
| Entries | 161 |
| Mean | 31.4 |
| Std Dev | 3.65 |

Gaus mean : 31.4923

Gaus Width: 3.3823

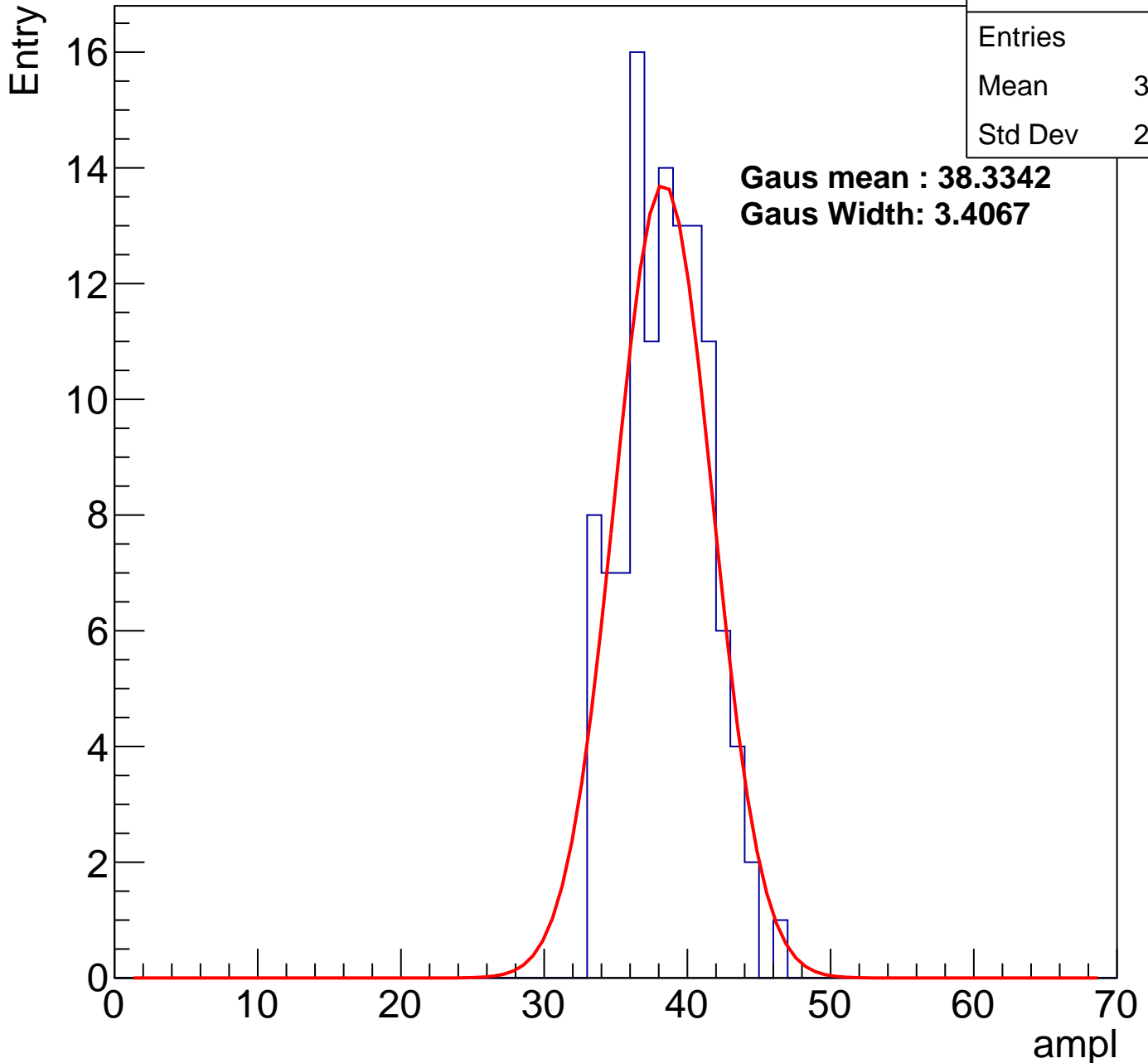


B1L001S, U26-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 113 |
| Mean | 38.04 |
| Std Dev | 2.893 |

Gaus mean : 38.3342
Gaus Width: 3.4067



B1L001S, U26-ch50, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

127

Mean

44.31

Std Dev

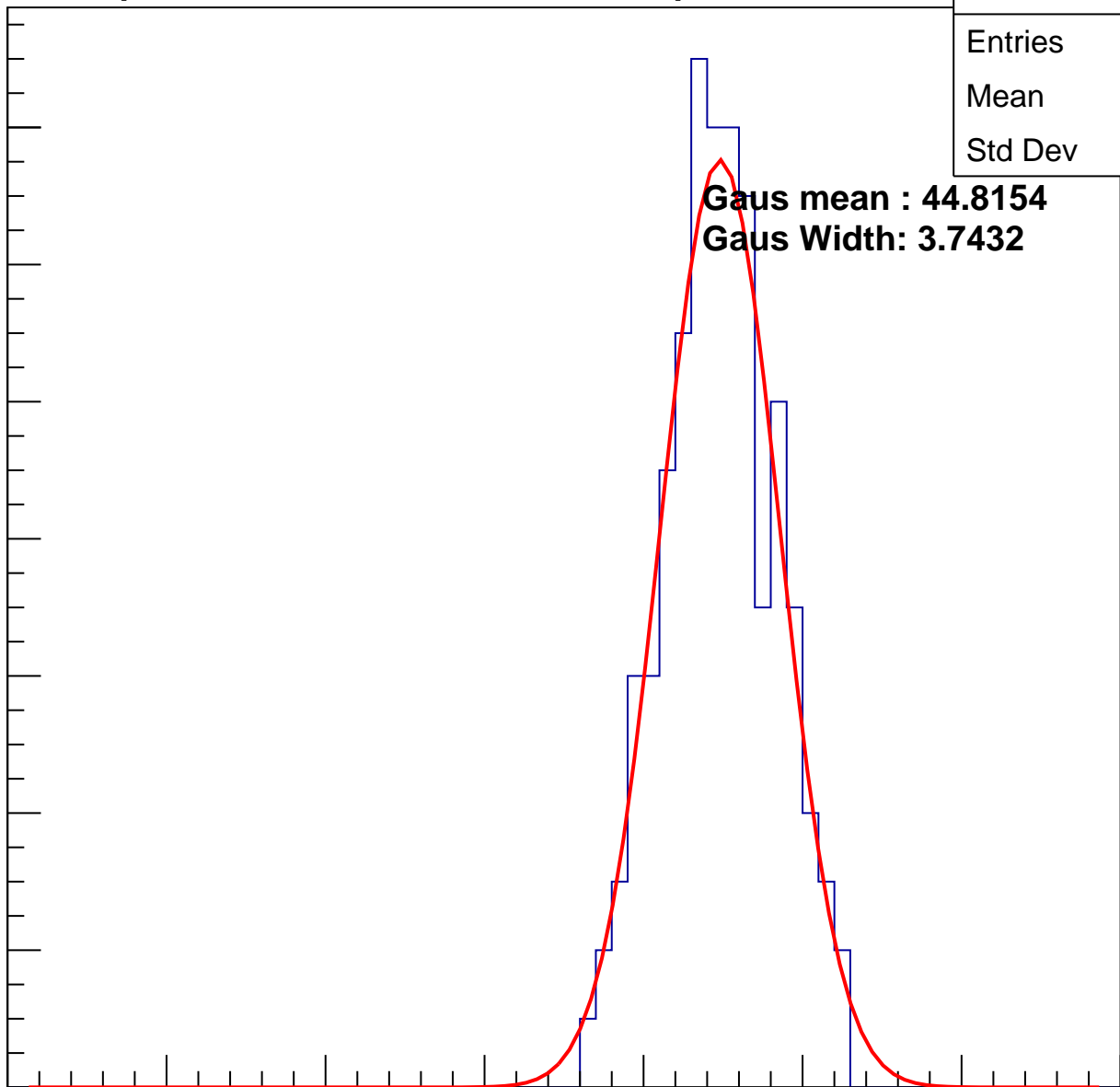
3.468

Gaus mean : 44.8154

Gaus Width: 3.7432

0 10 20 30 40 50 60 70

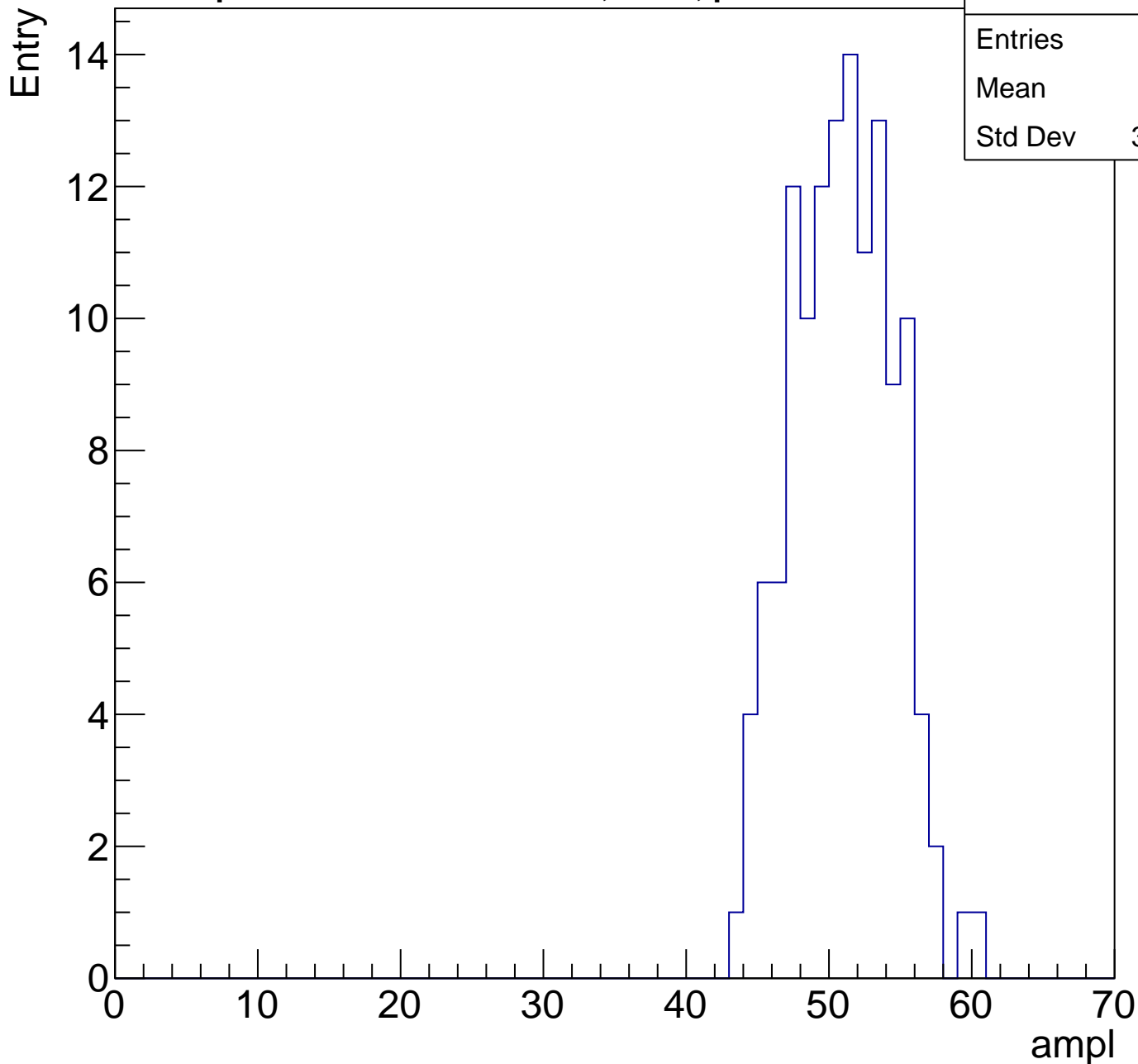
ampl



B1L001S, U26-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

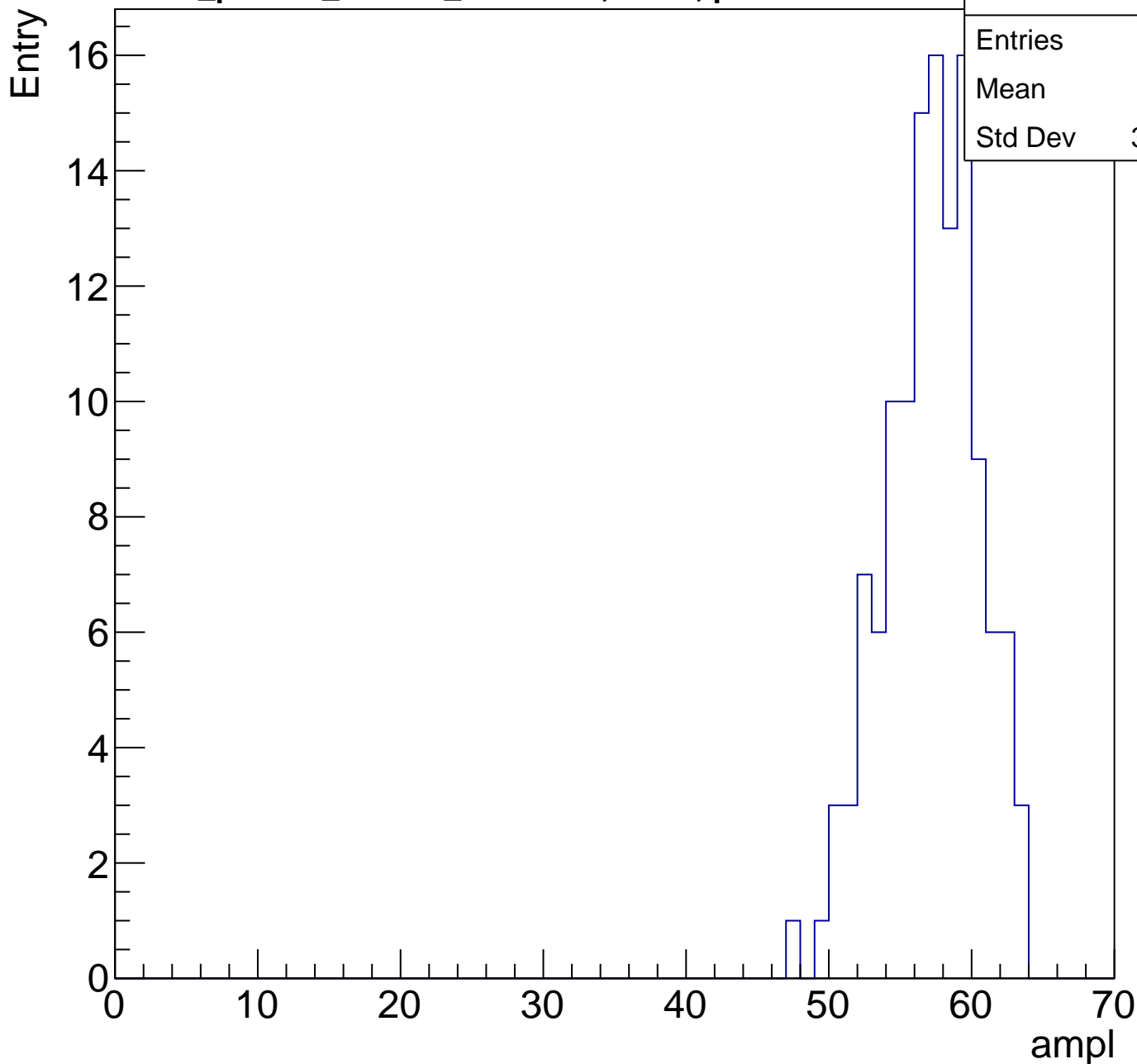
| | |
|---------|-------|
| Entries | 129 |
| Mean | 50.5 |
| Std Dev | 3.476 |



B1L001S, U26-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 56.7 |
| Std Dev | 3.269 |



B1L001S, U26-ch50, adc5

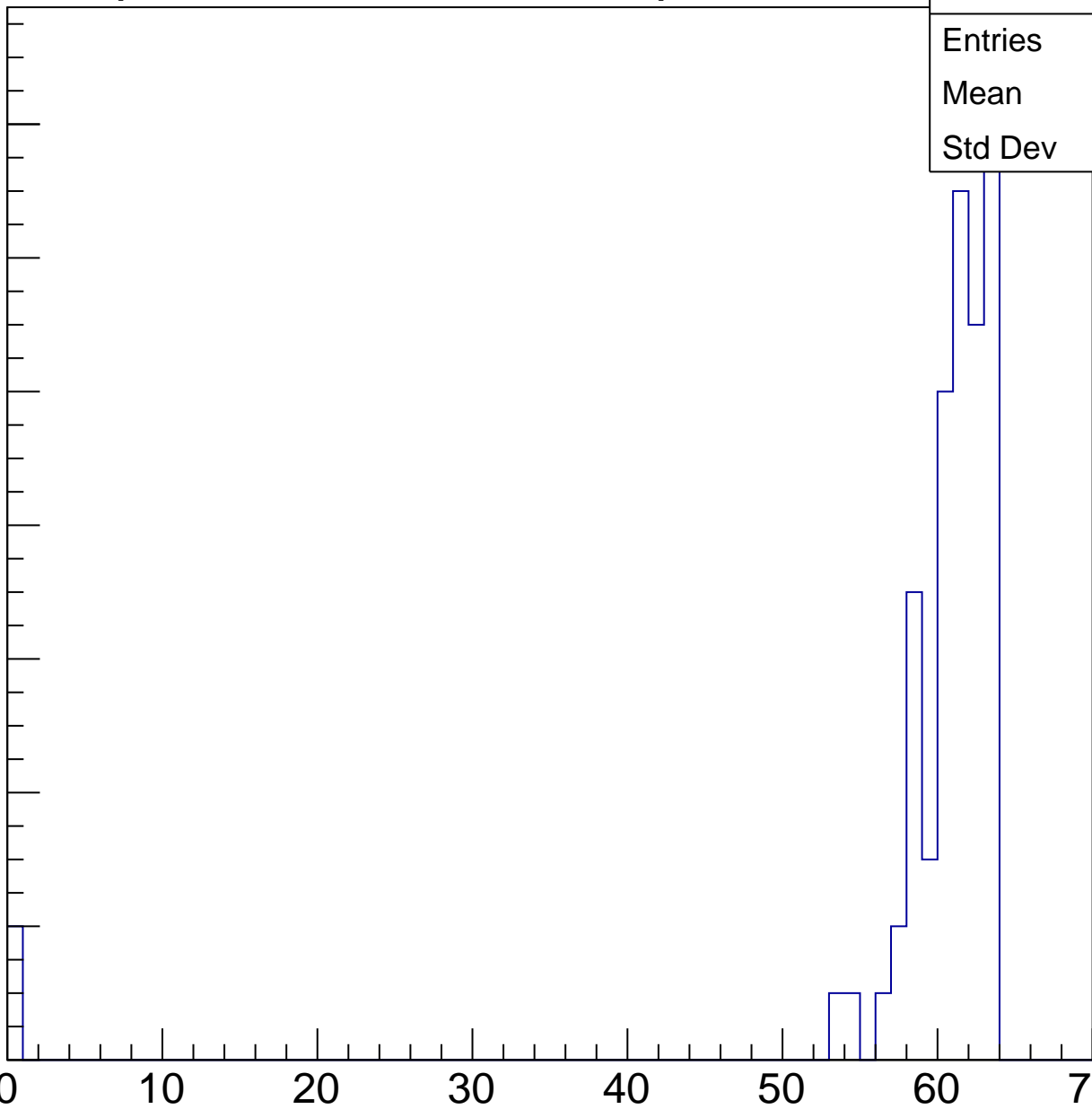
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 66 |
| Mean | 58.79 |
| Std Dev | 10.62 |

ampl



B1L001S, U26-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch51, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 30.43 |
| Std Dev | 3.165 |

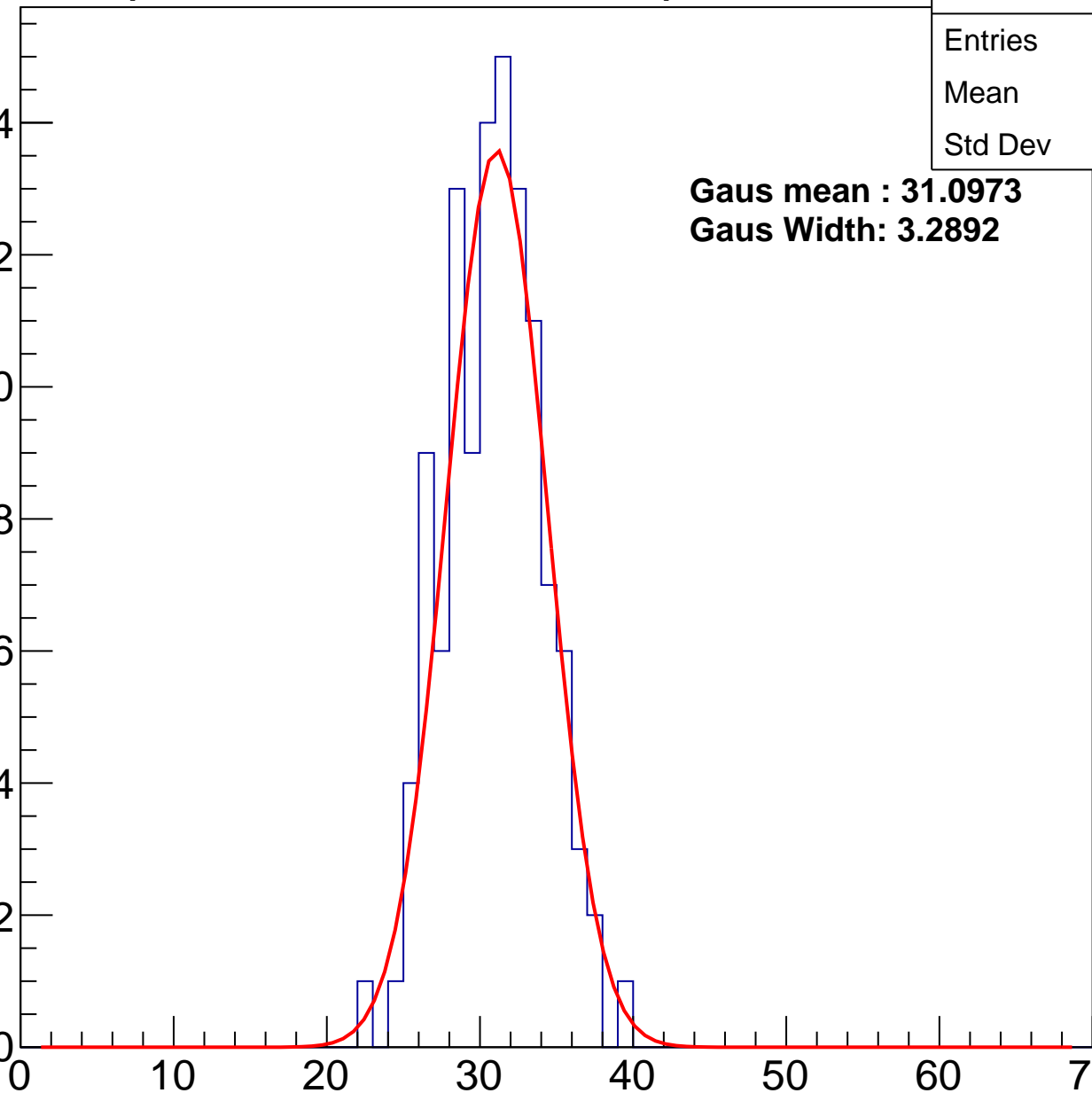
Gaus mean : 31.0973

Gaus Width: 3.2892

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch51, adc1

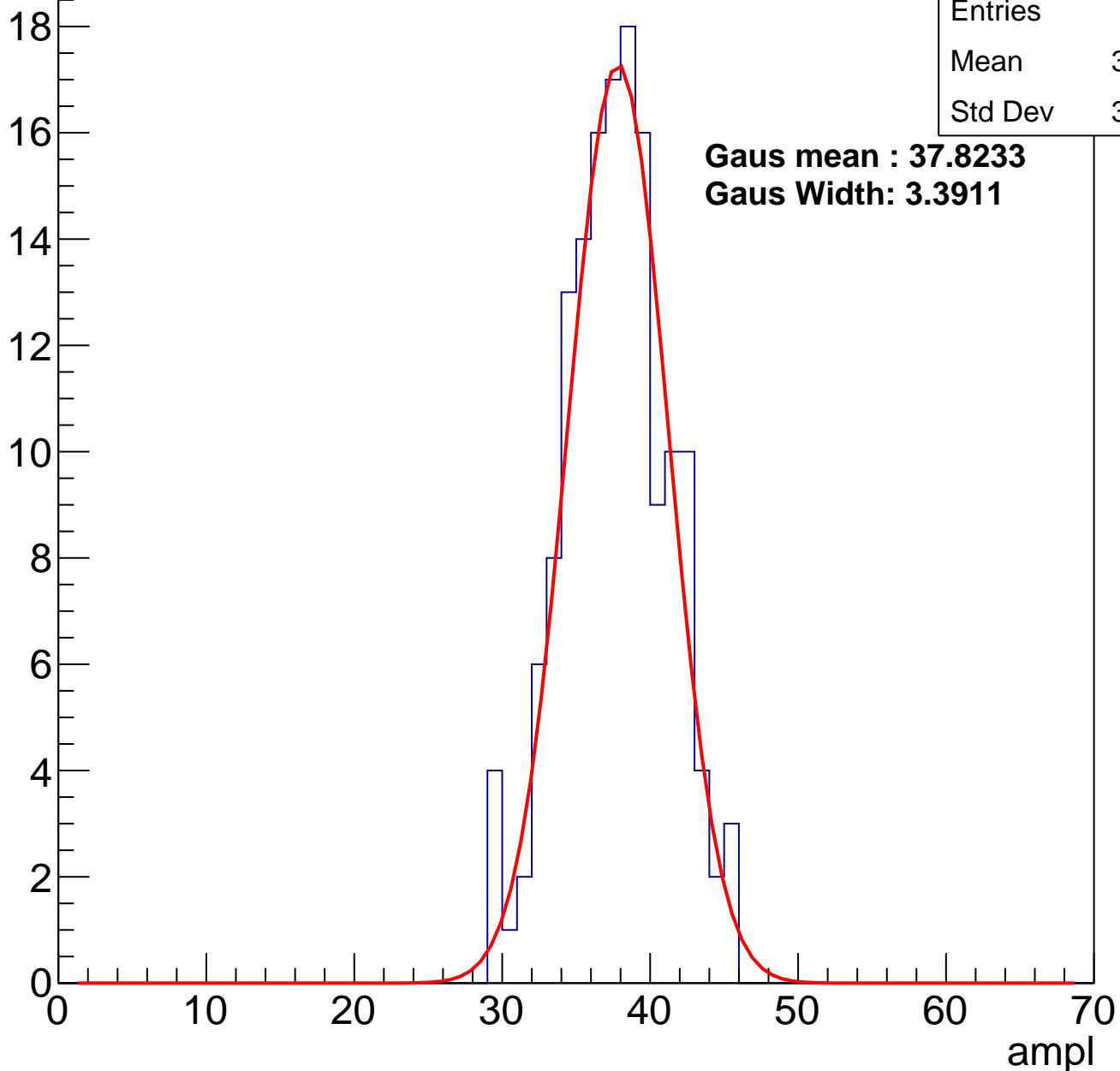
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 37.22 |
| Std Dev | 3.472 |

Gaus mean : 37.8233

Gaus Width: 3.3911

Entry



B1L001S, U26-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

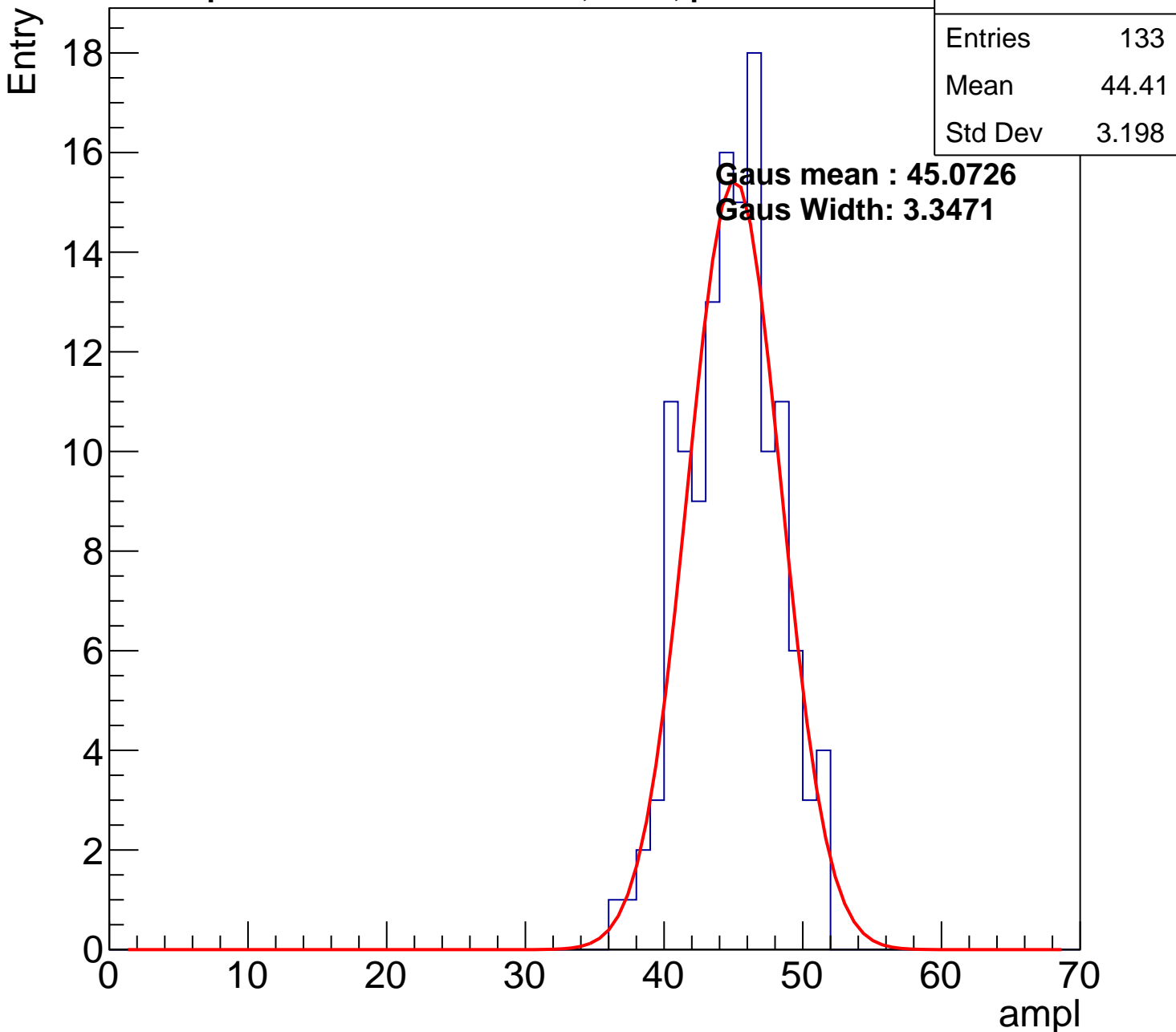
| | |
|---------|-------|
| Entries | 133 |
| Mean | 44.41 |
| Std Dev | 3.198 |

Gaus mean : 45.0726

Gaus Width: 3.3471

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

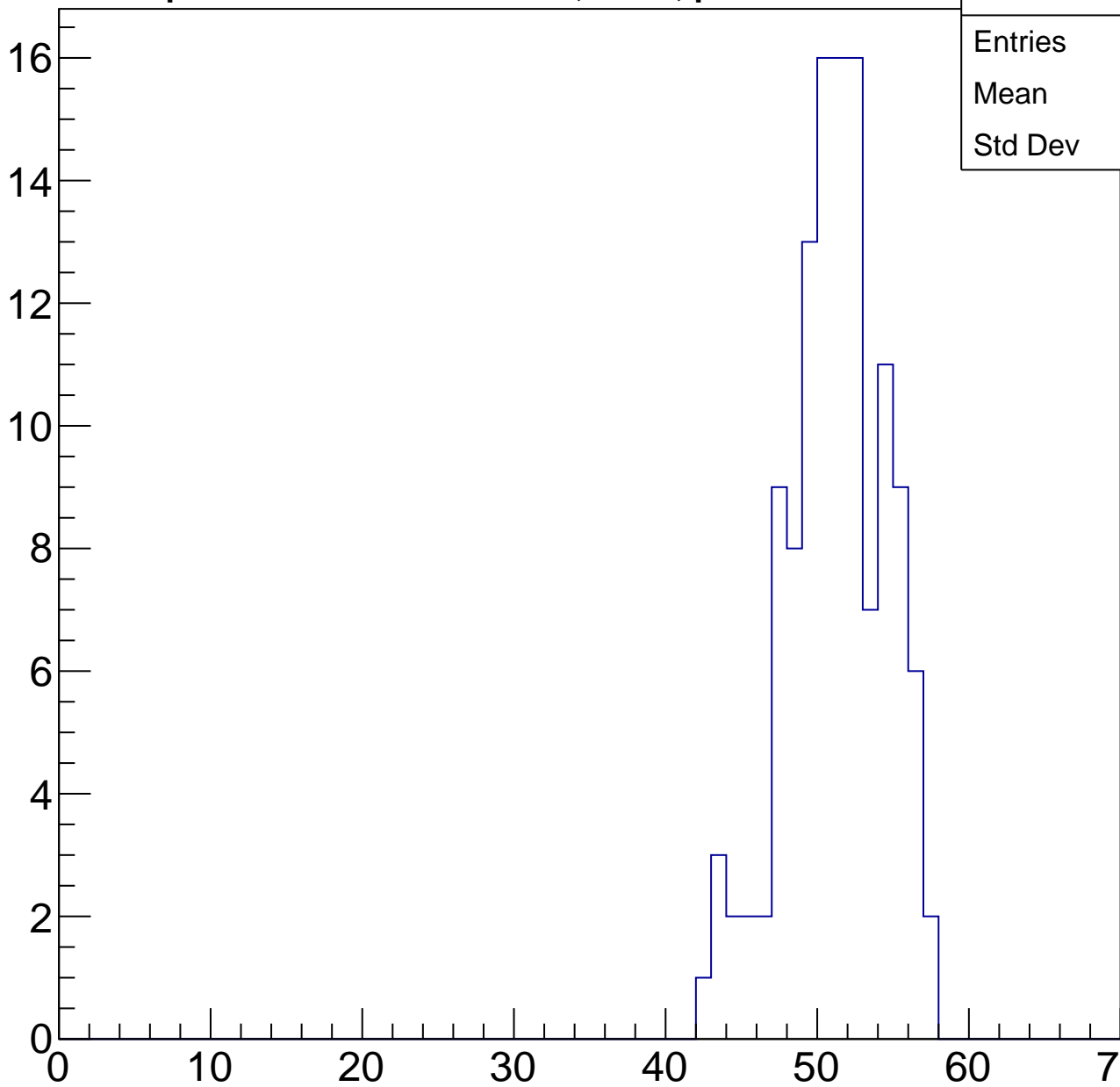
| | |
|---------|-------|
| Entries | 123 |
| Mean | 50.76 |
| Std Dev | 3.224 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

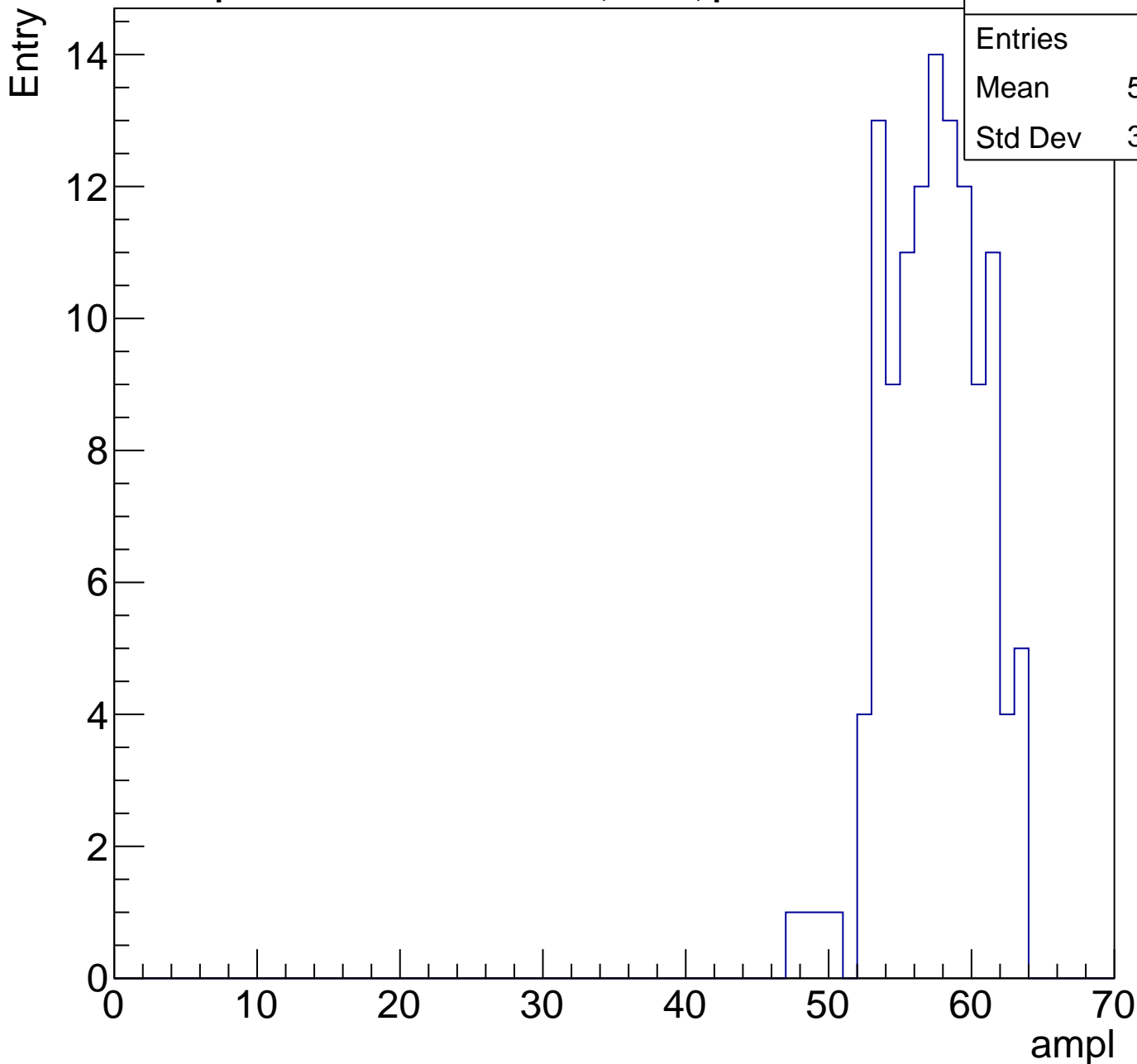
ampl



B1L001S, U26-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 56.93 |
| Std Dev | 3.323 |



B1L001S, U26-ch51, adc5

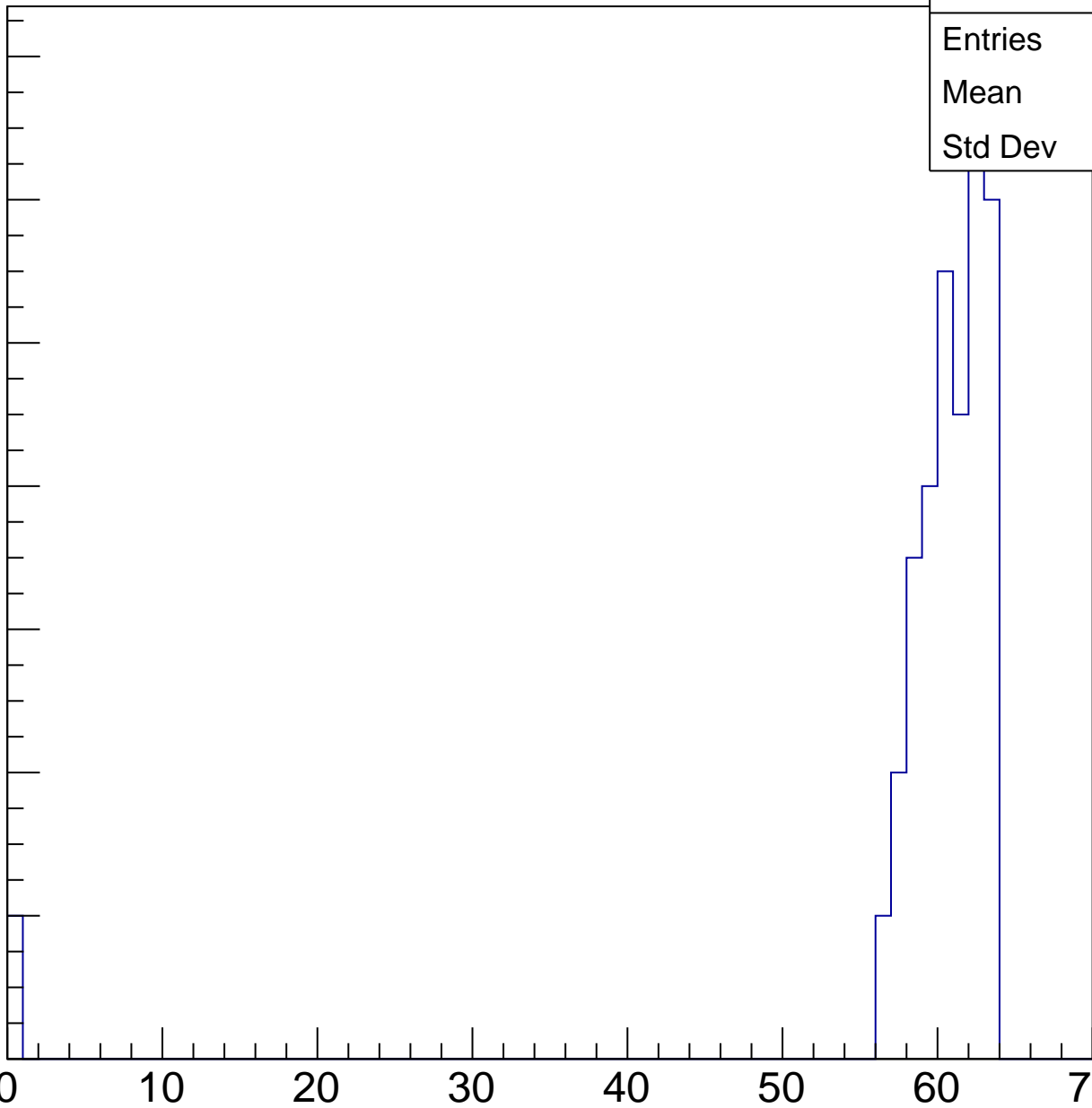
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 69 |
| Mean | 58.71 |
| Std Dev | 10.33 |

ampl



B1L001S, U26-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

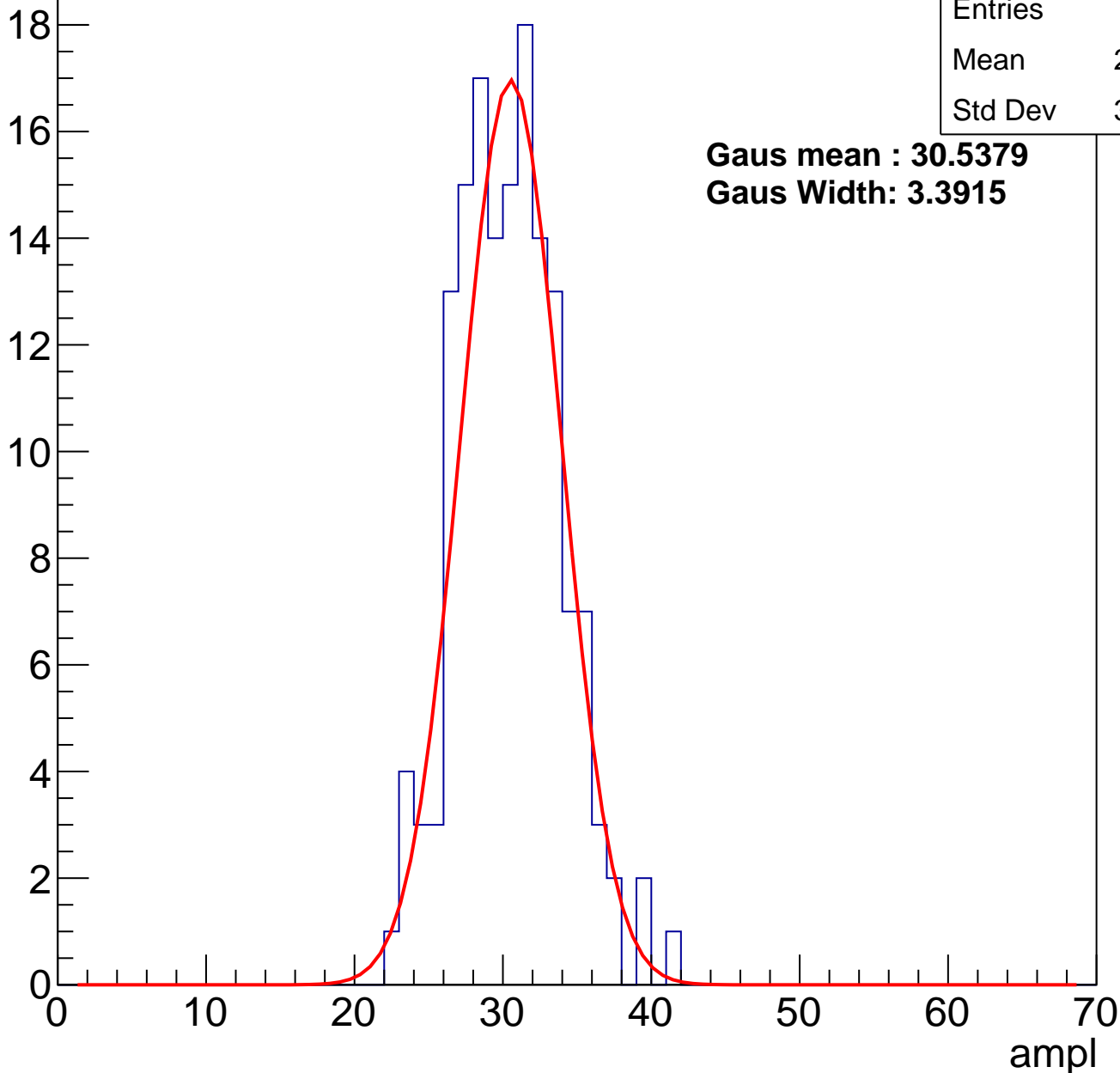


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch52, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 36.9 |
| Std Dev | 3.285 |

Gaus mean : 37.3981

Gaus Width: 3.3945

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

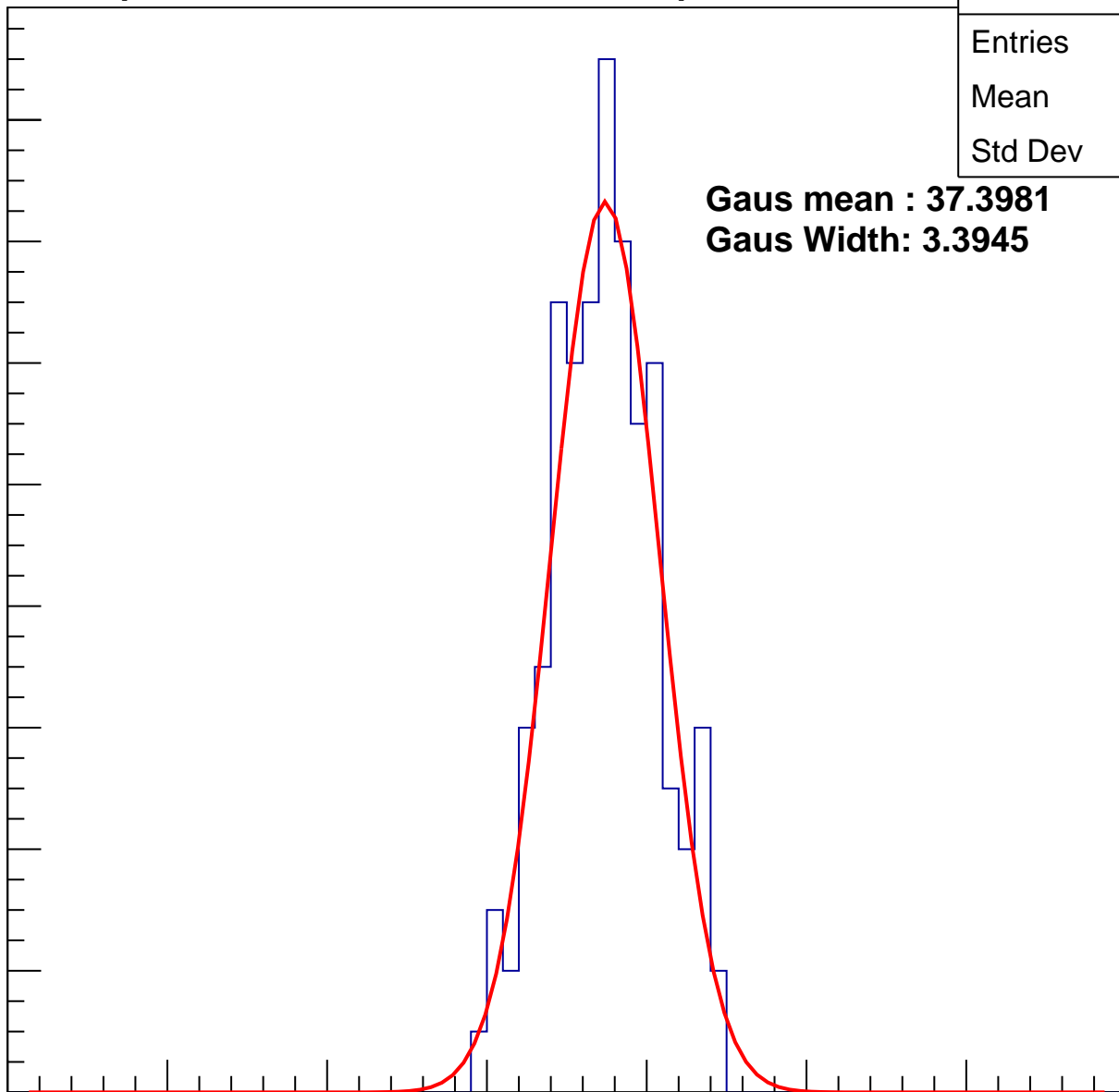
40

50

60

70

ampl



B1L001S, U26-ch52, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

110

Mean

42.75

Std Dev

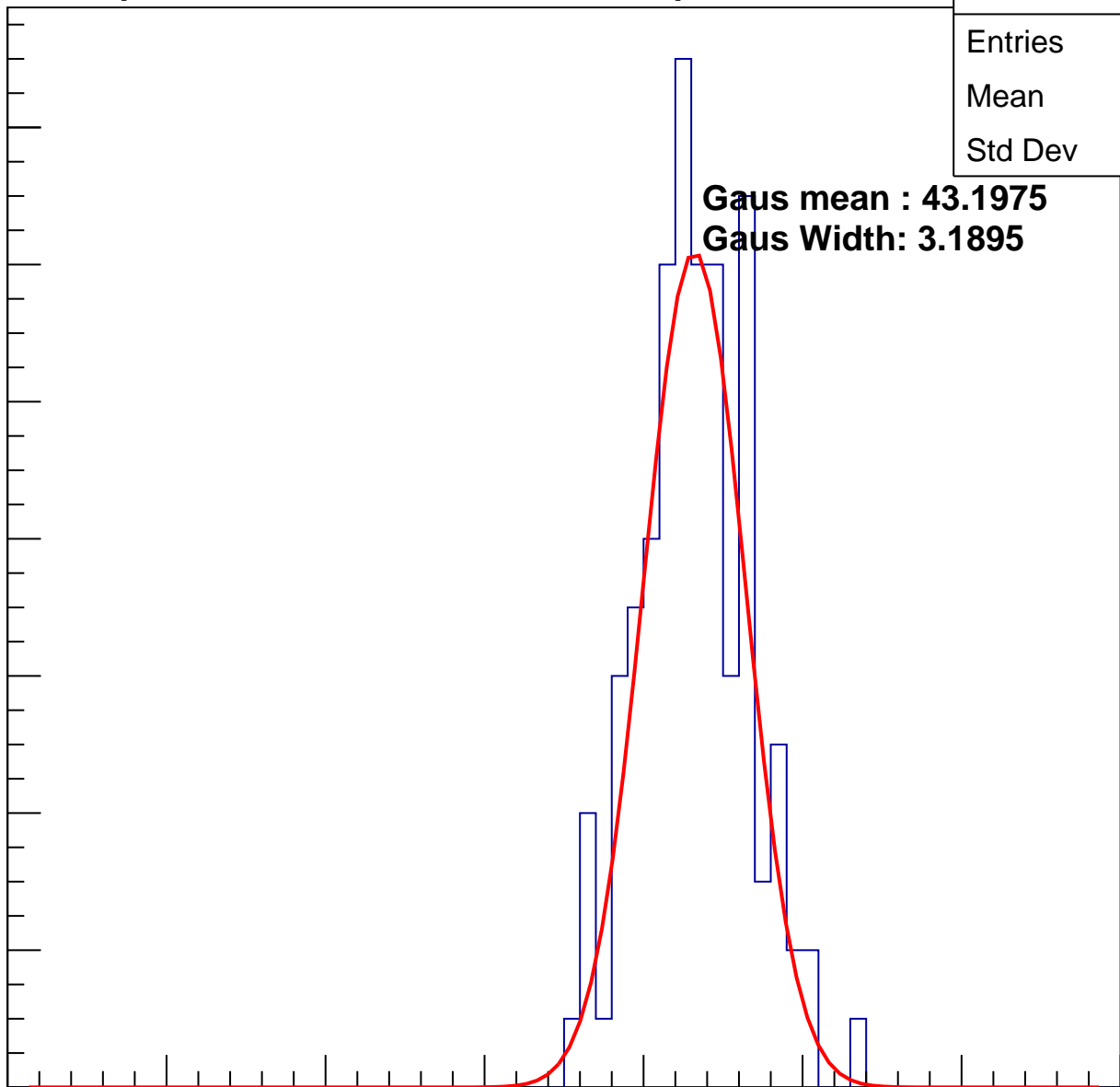
3.414

Gaus mean : 43.1975

Gaus Width: 3.1895

0 10 20 30 40 50 60 70

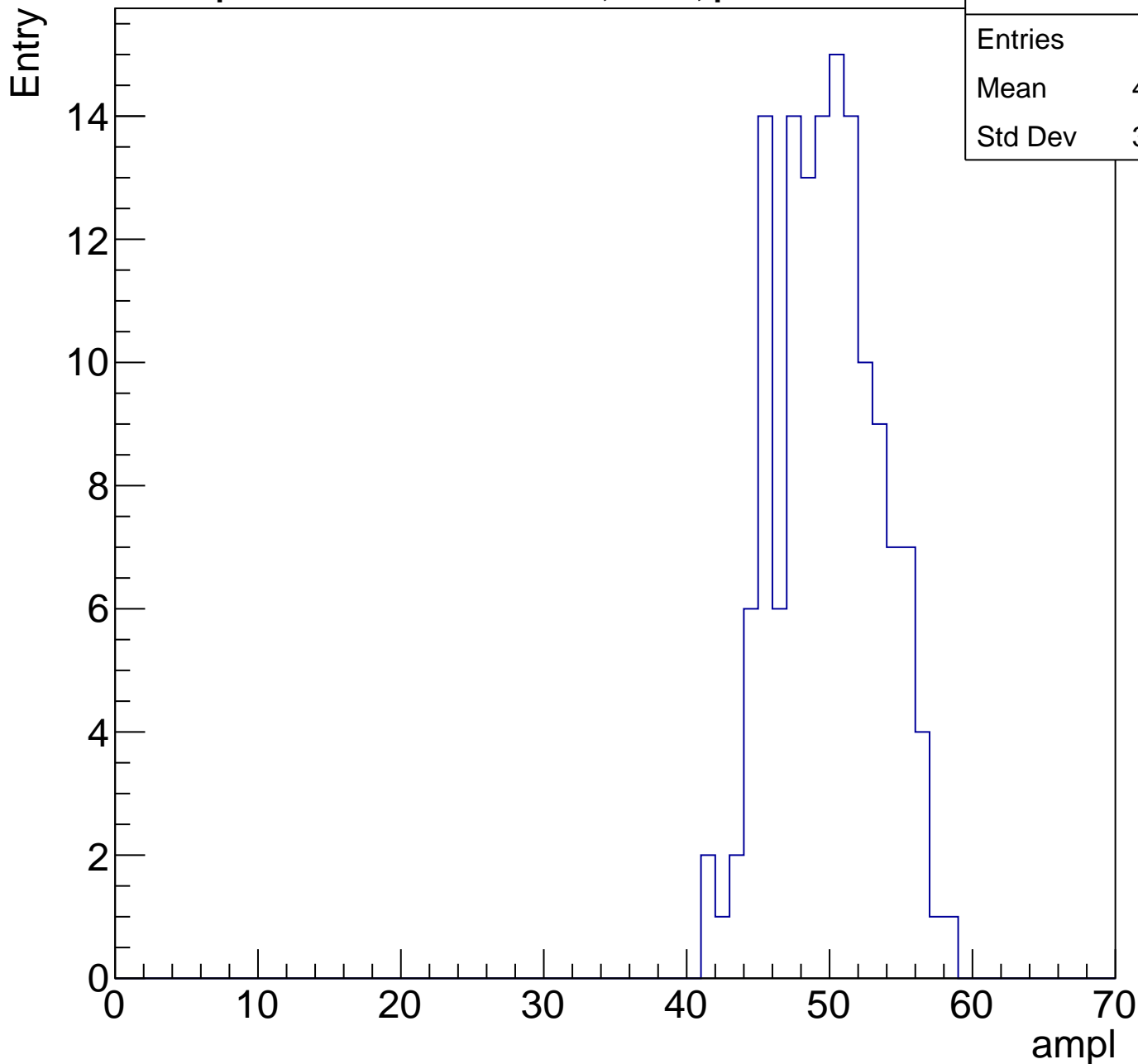
ampl



B1L001S, U26-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 49.36 |
| Std Dev | 3.586 |



B1L001S, U26-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

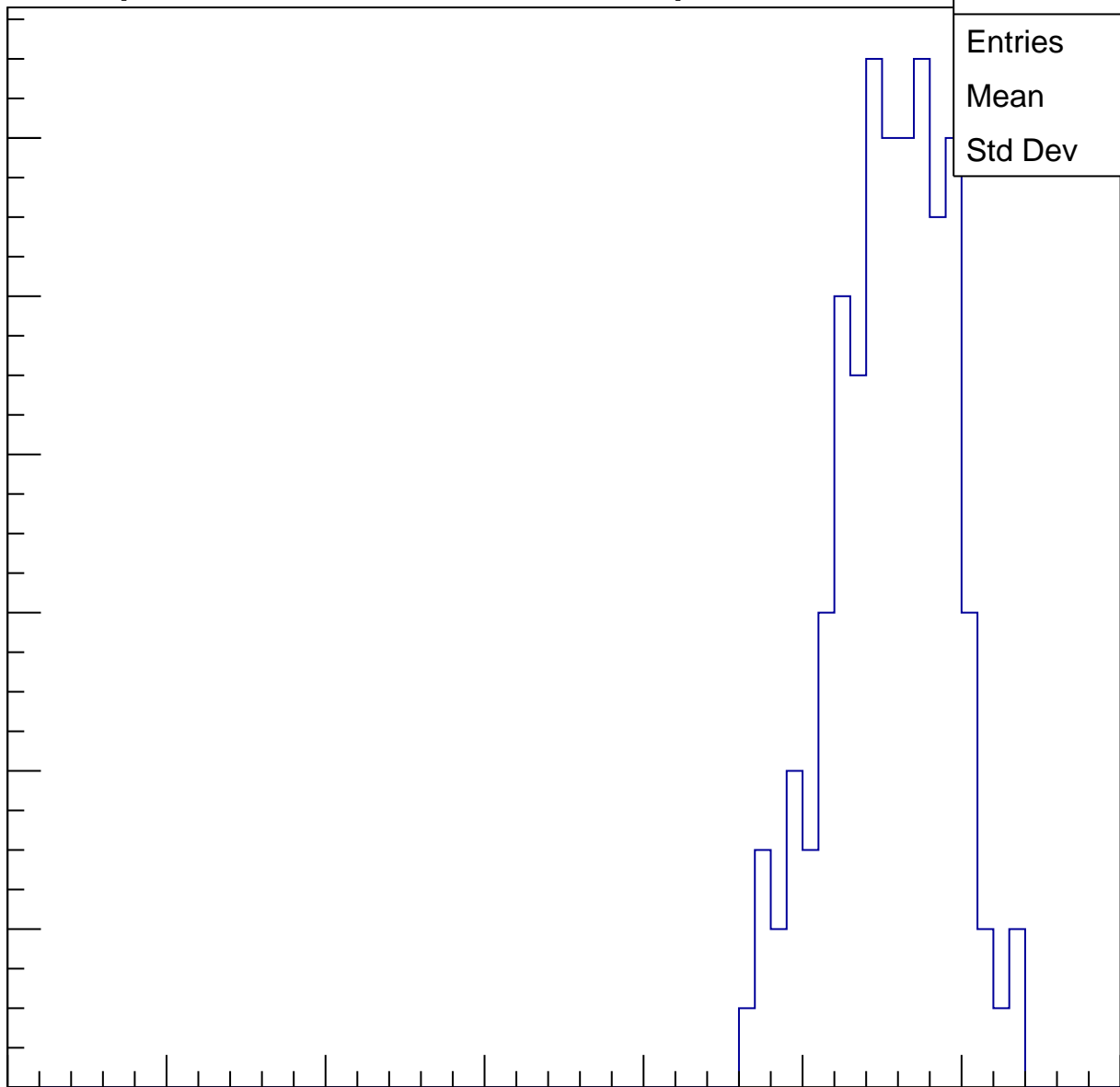
50

60

70

ampl

| | |
|---------|-------|
| Entries | 122 |
| Mean | 55.11 |
| Std Dev | 3.596 |



B1L001S, U26-ch52, adc5

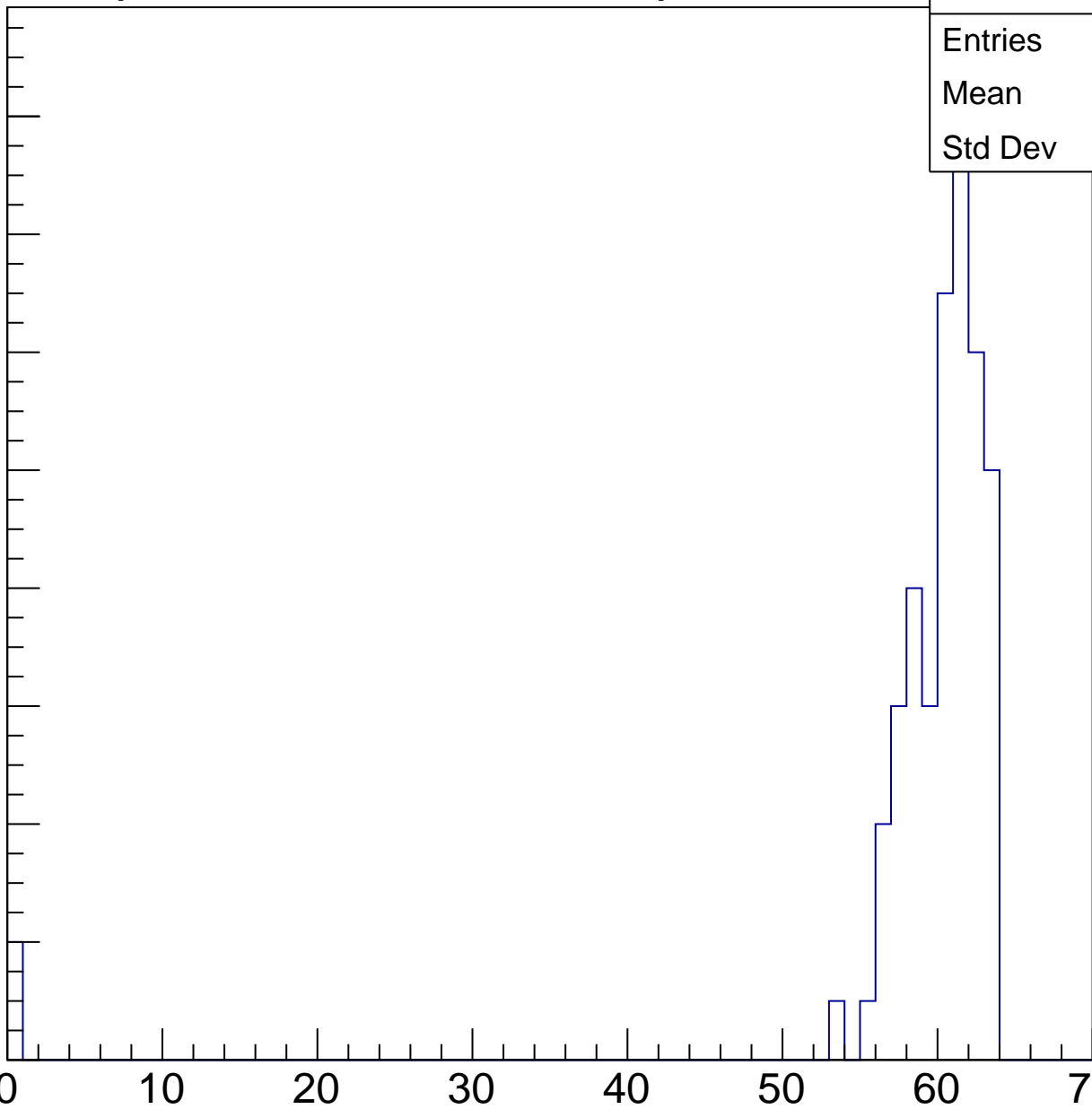
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 80 |
| Mean | 58.54 |
| Std Dev | 9.627 |

ampl

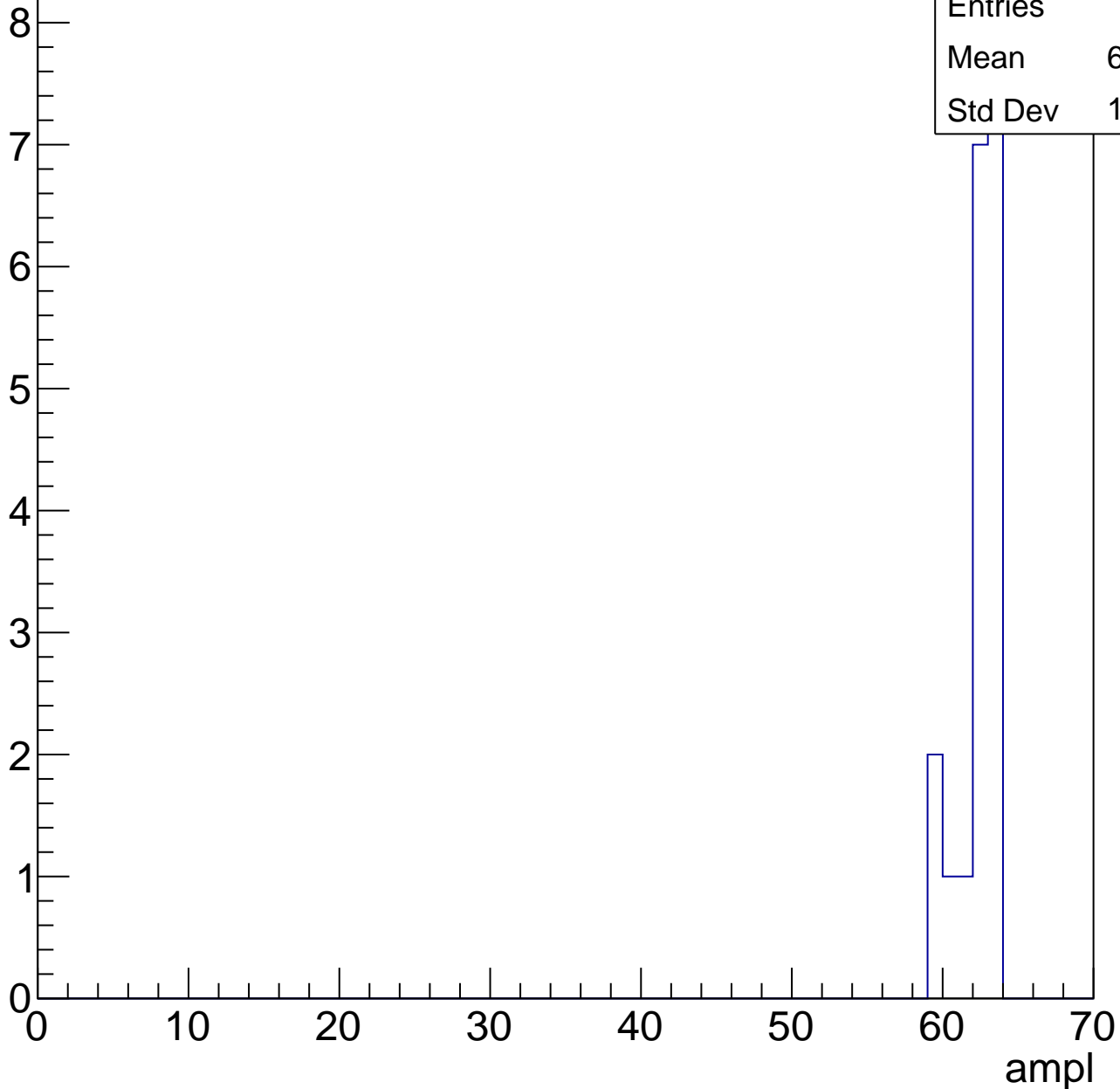


B1L001S, U26-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

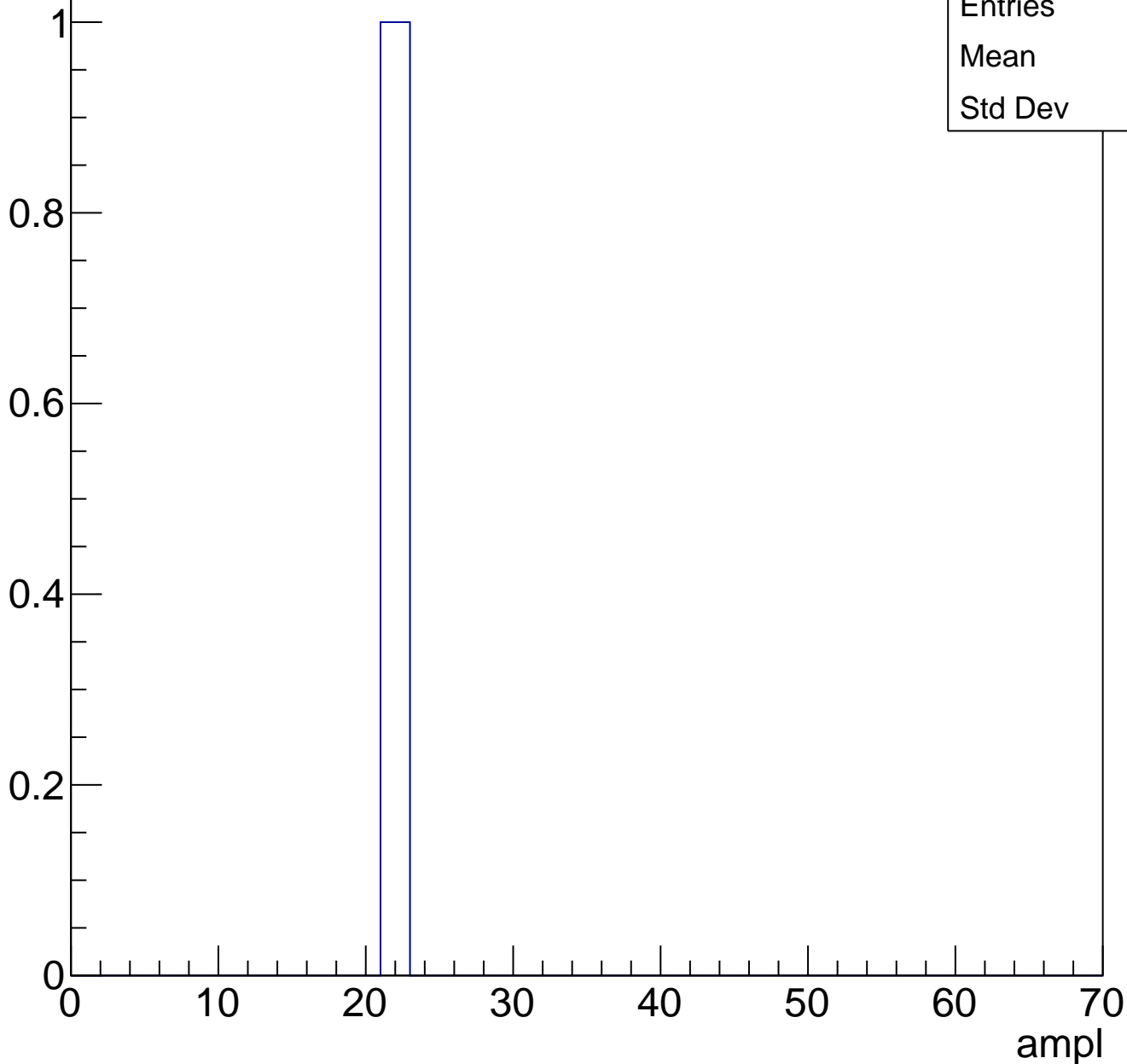
| | |
|---------|-------|
| Entries | 19 |
| Mean | 61.95 |
| Std Dev | 1.276 |



B1L001S, U26-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch53, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 30.76 |
| Std Dev | 4.377 |

Gaus mean : 31.2284

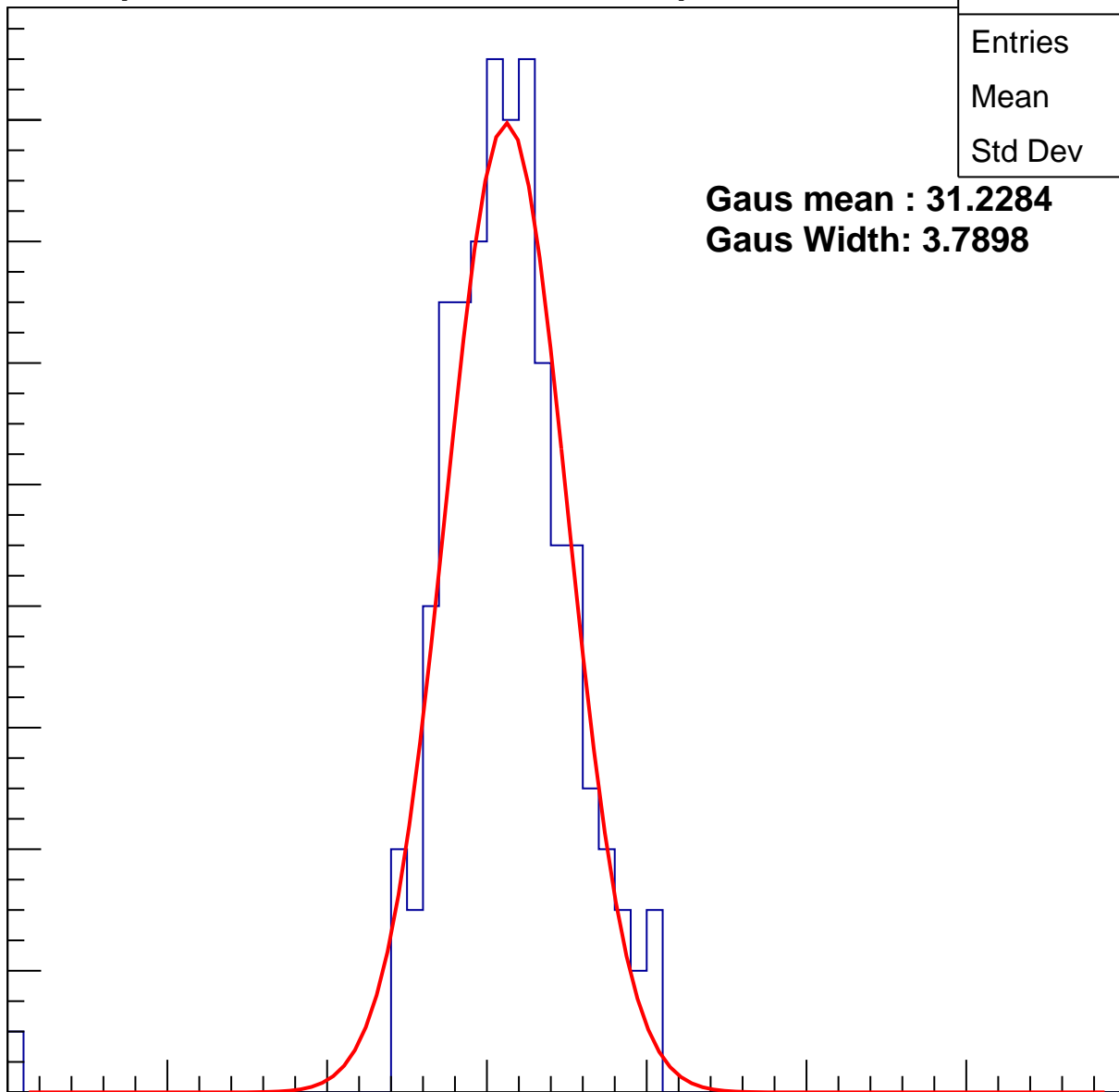
Gaus Width: 3.7898

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



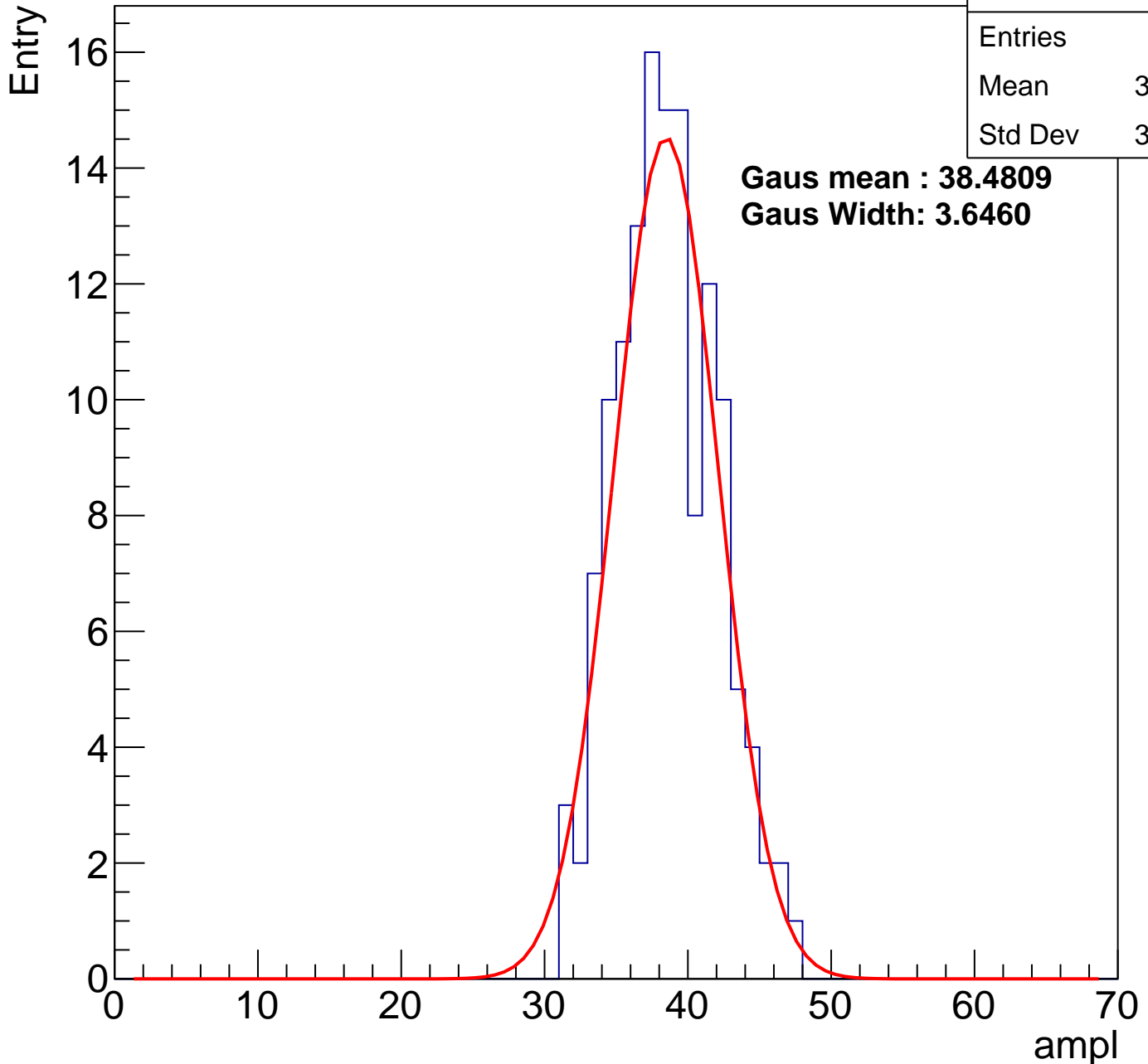
B1L001S, U26-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 38.09 |
| Std Dev | 3.442 |

Gaus mean : 38.4809

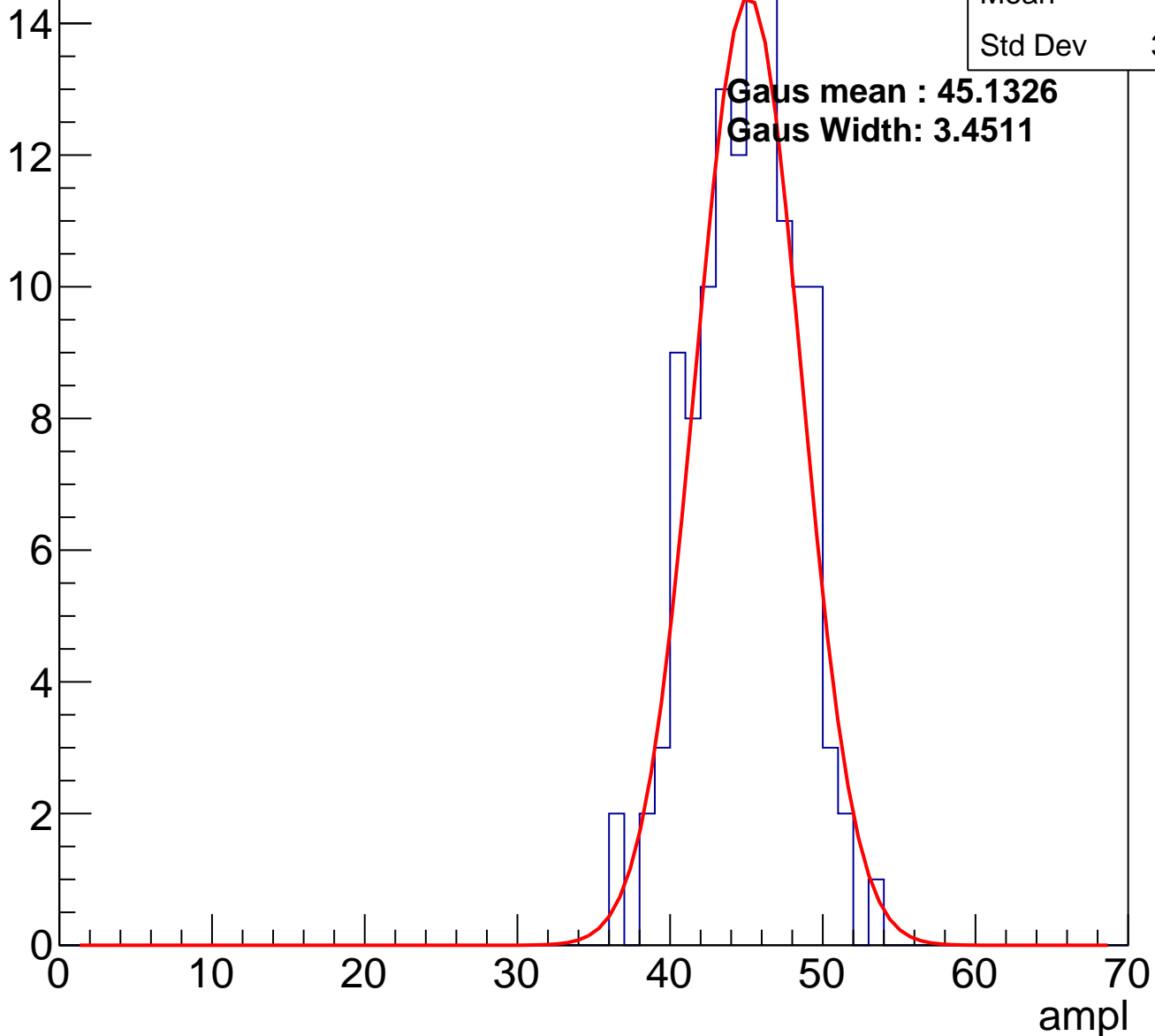
Gaus Width: 3.6460



B1L001S, U26-ch53, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



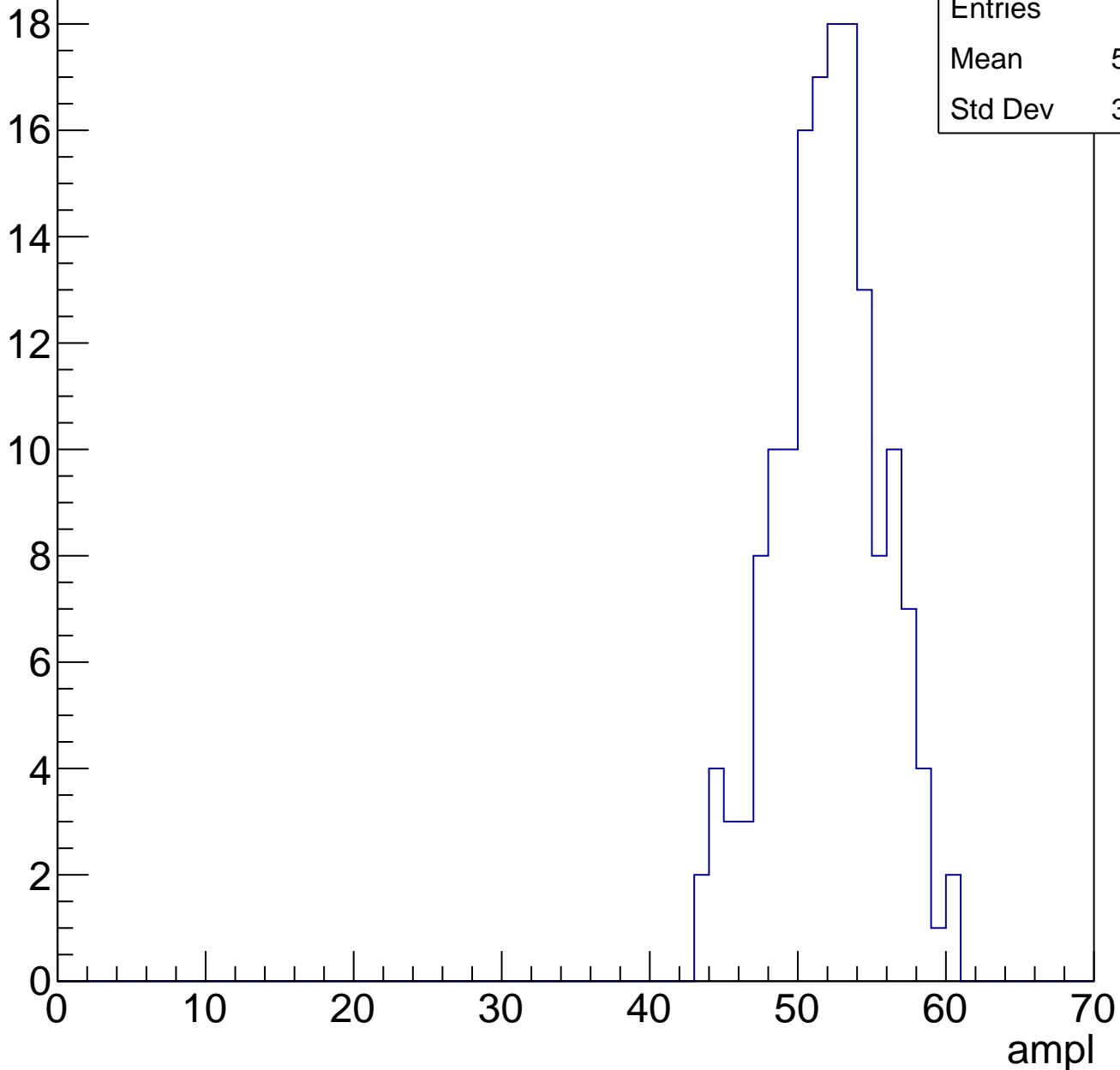
| | |
|---------|-------|
| Entries | 126 |
| Mean | 44.58 |
| Std Dev | 3.301 |

B1L001S, U26-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 154 |
| Mean | 51.62 |
| Std Dev | 3.586 |

Entry



B1L001S, U26-ch53, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

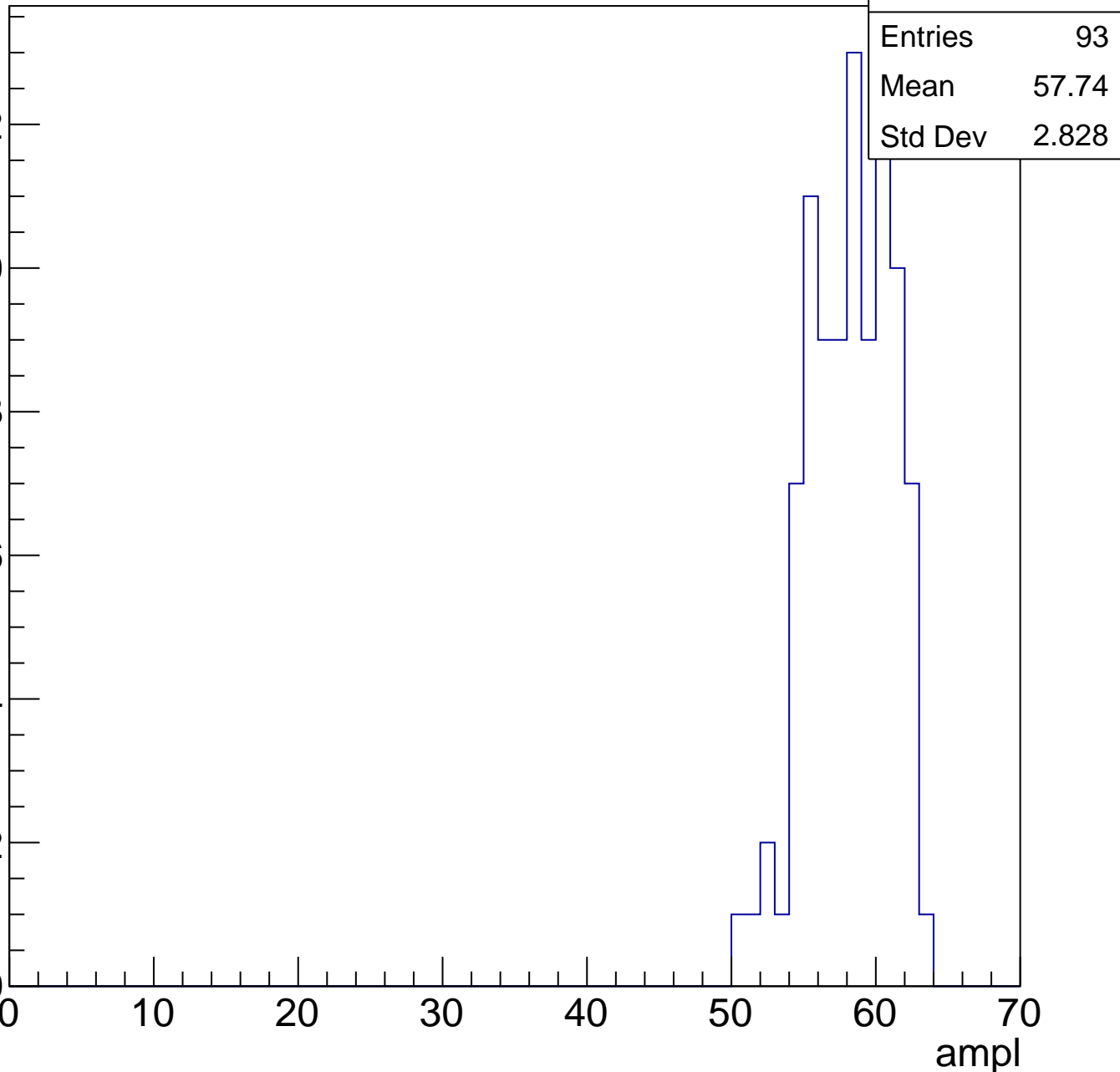
40

50

60

ampl

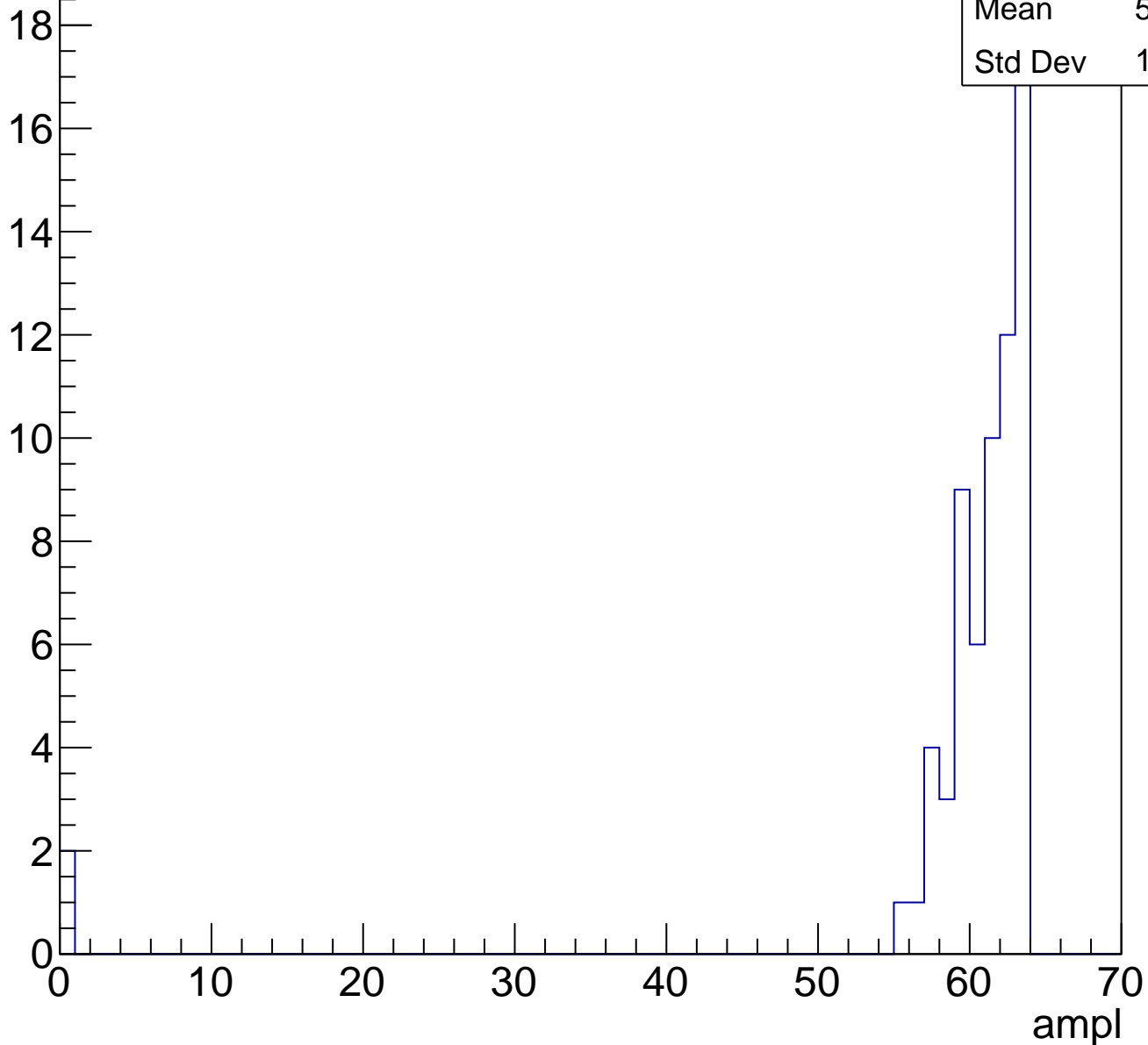
| | |
|---------|-------|
| Entries | 93 |
| Mean | 57.74 |
| Std Dev | 2.828 |



B1L001S, U26-ch53, adc5

calib_packv5_042523_0143.root, FC#2, port C2

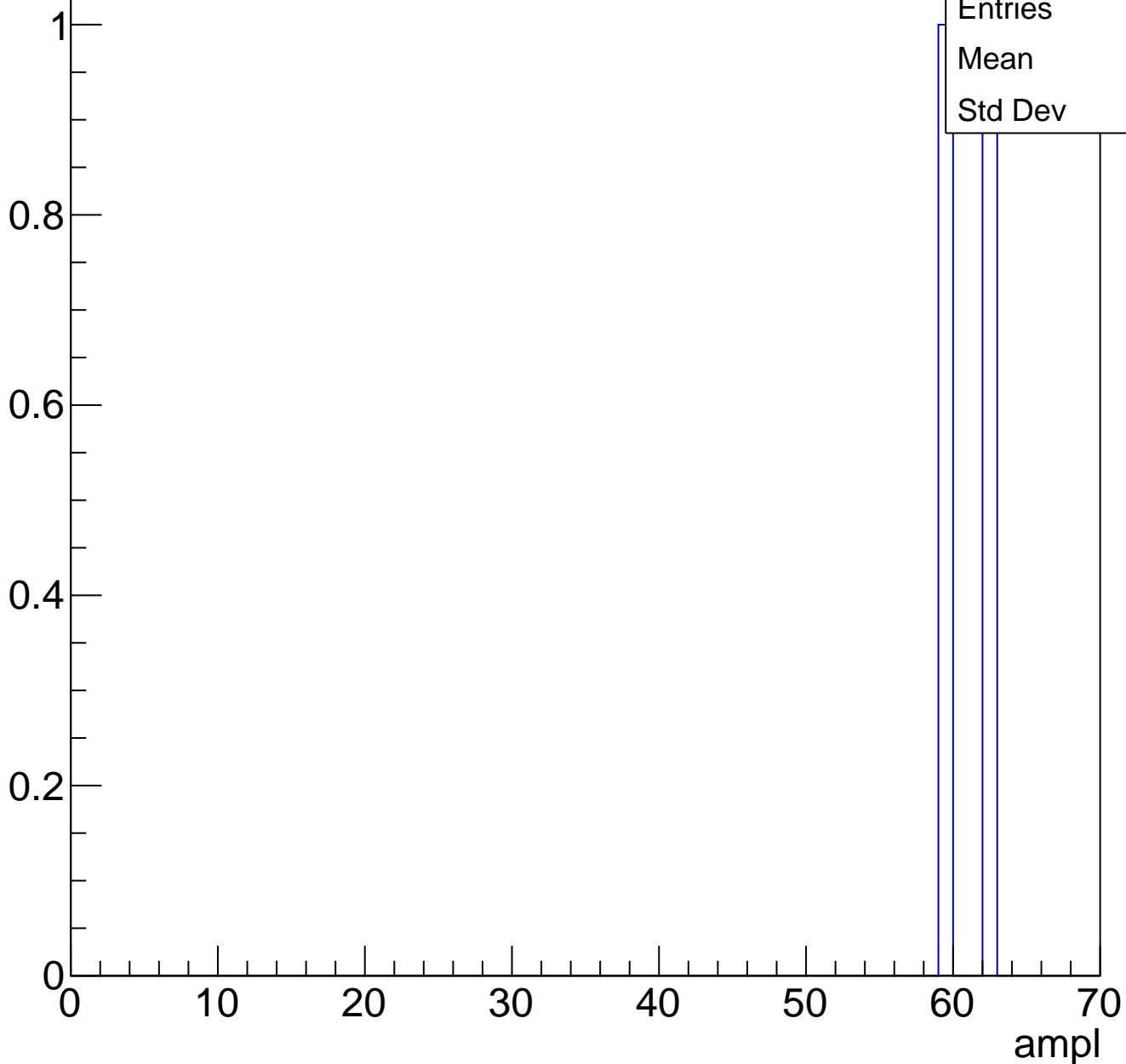
Entry



B1L001S, U26-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 22 |
| Std Dev | 0 |

ampl

B1L001S, U26-ch54, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 29.77 |
| Std Dev | 4.774 |

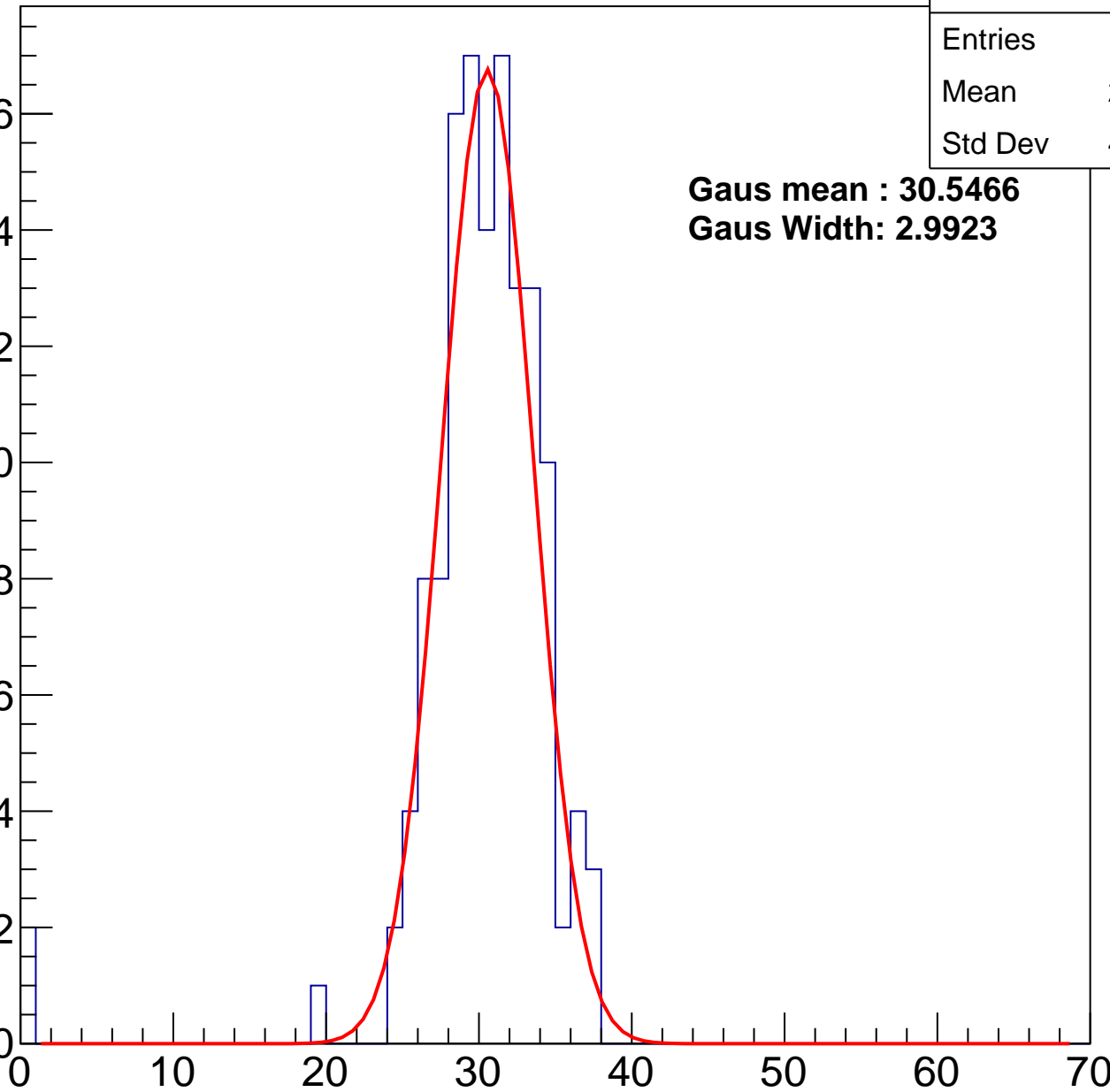
Gaus mean : 30.5466

Gaus Width: 2.9923

Entry

16
14
12
10
8
6
4
2
0

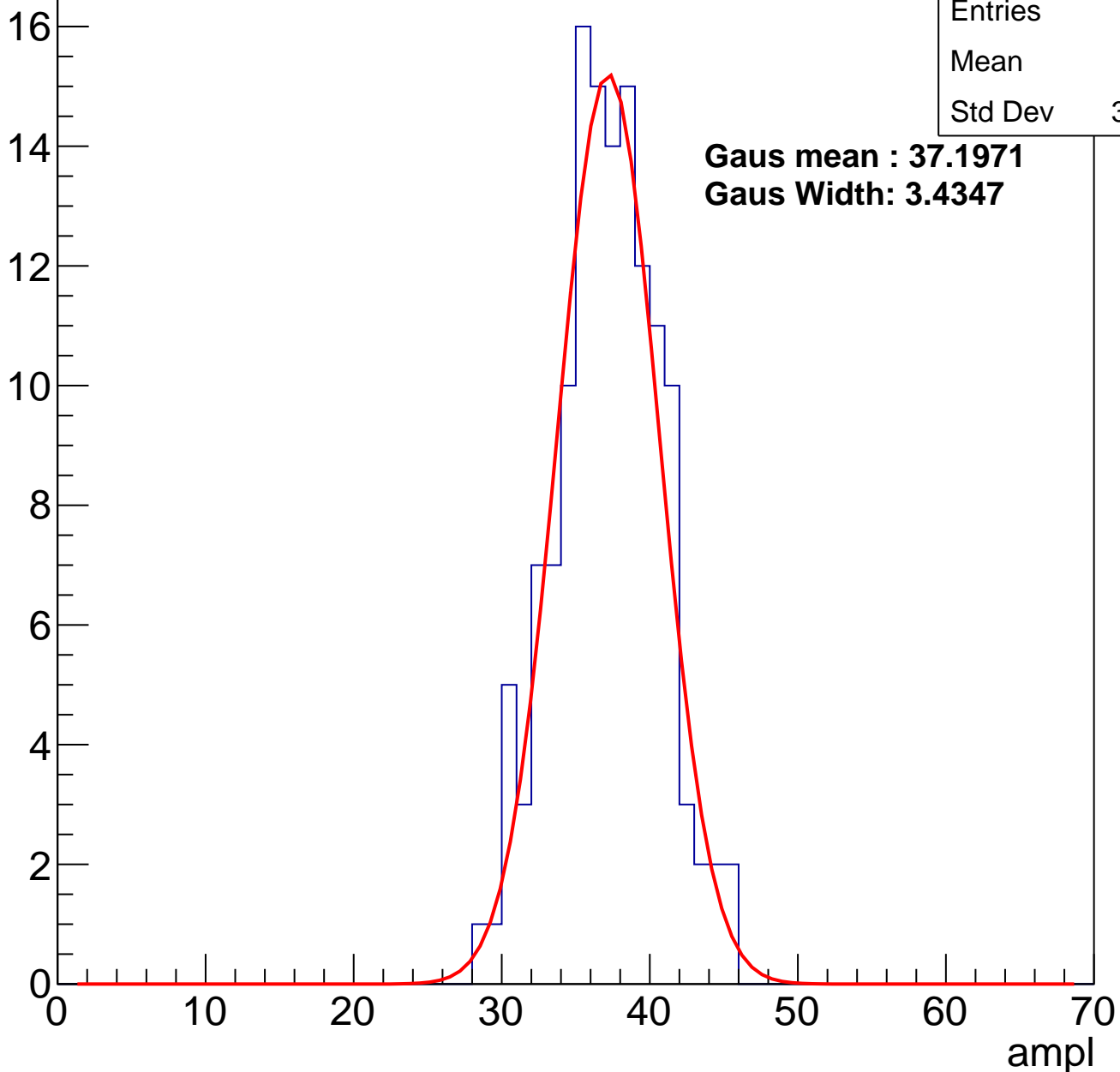
ampl



B1L001S, U26-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch54, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

133

Mean

43.25

Std Dev

3.368

Gaus mean : 43.9261

Gaus Width: 3.5580

0

10

20

30

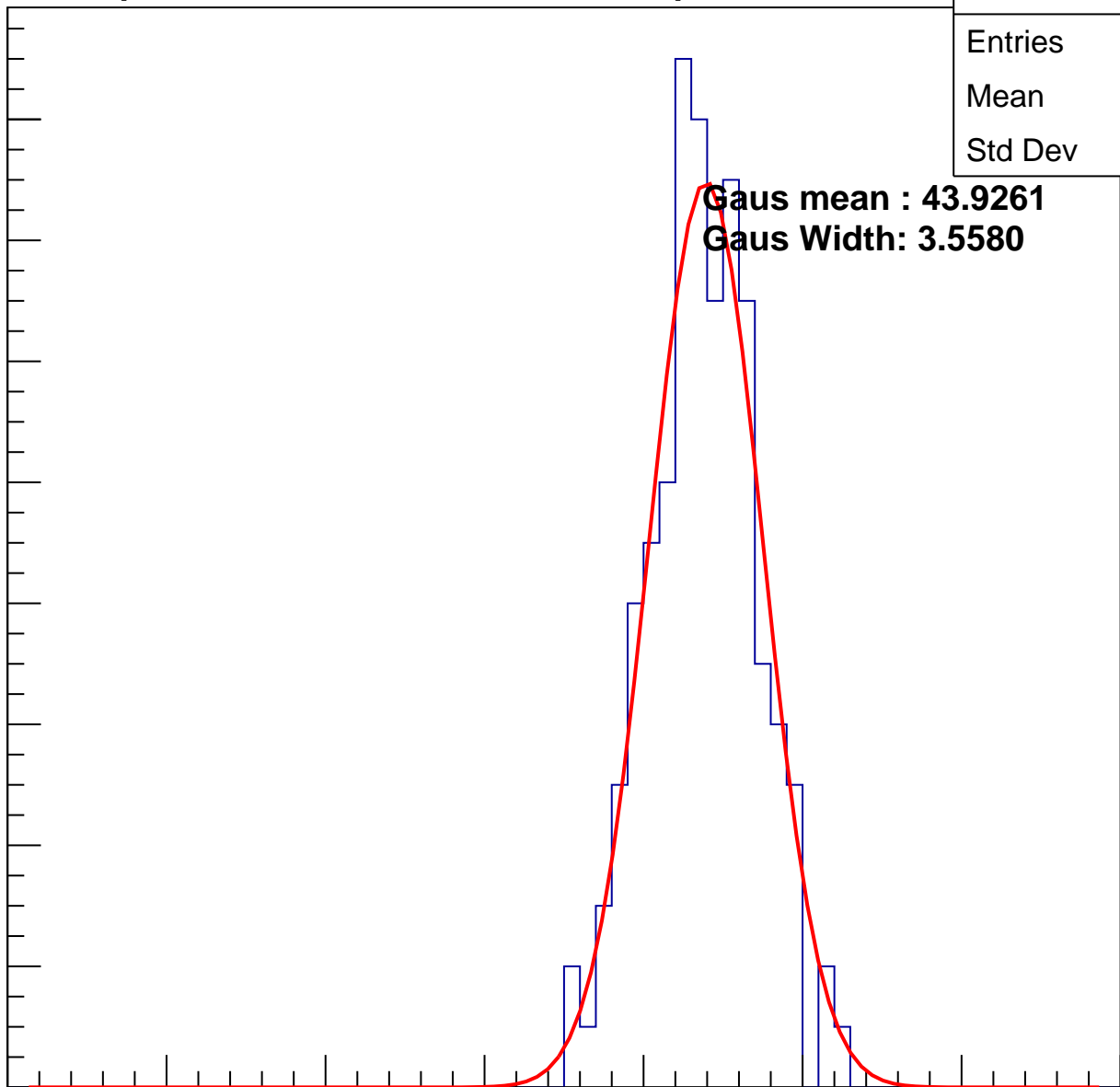
40

50

60

70

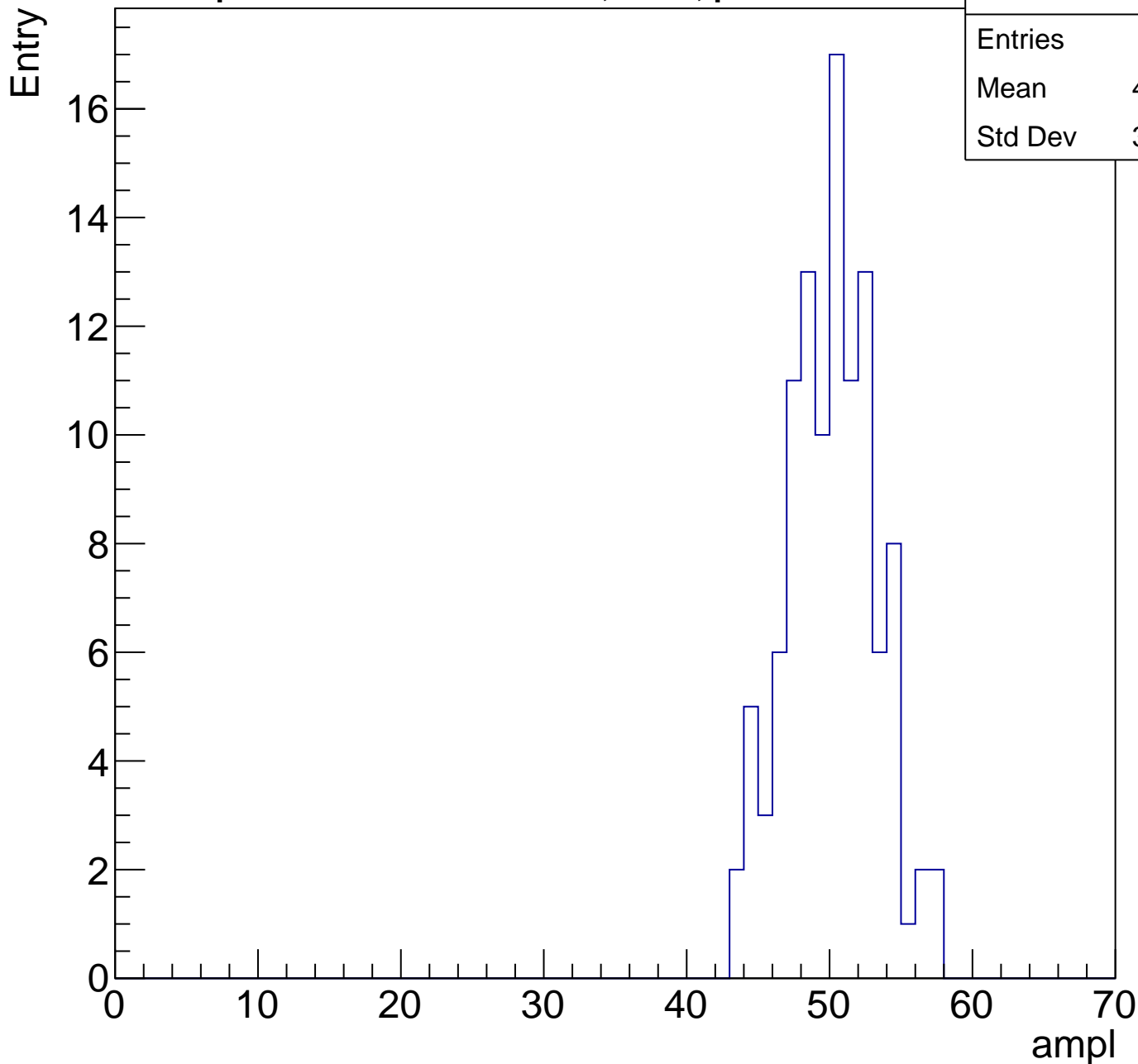
ampl



B1L001S, U26-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 110 |
| Mean | 49.69 |
| Std Dev | 3.074 |



B1L001S, U26-ch54, adc4

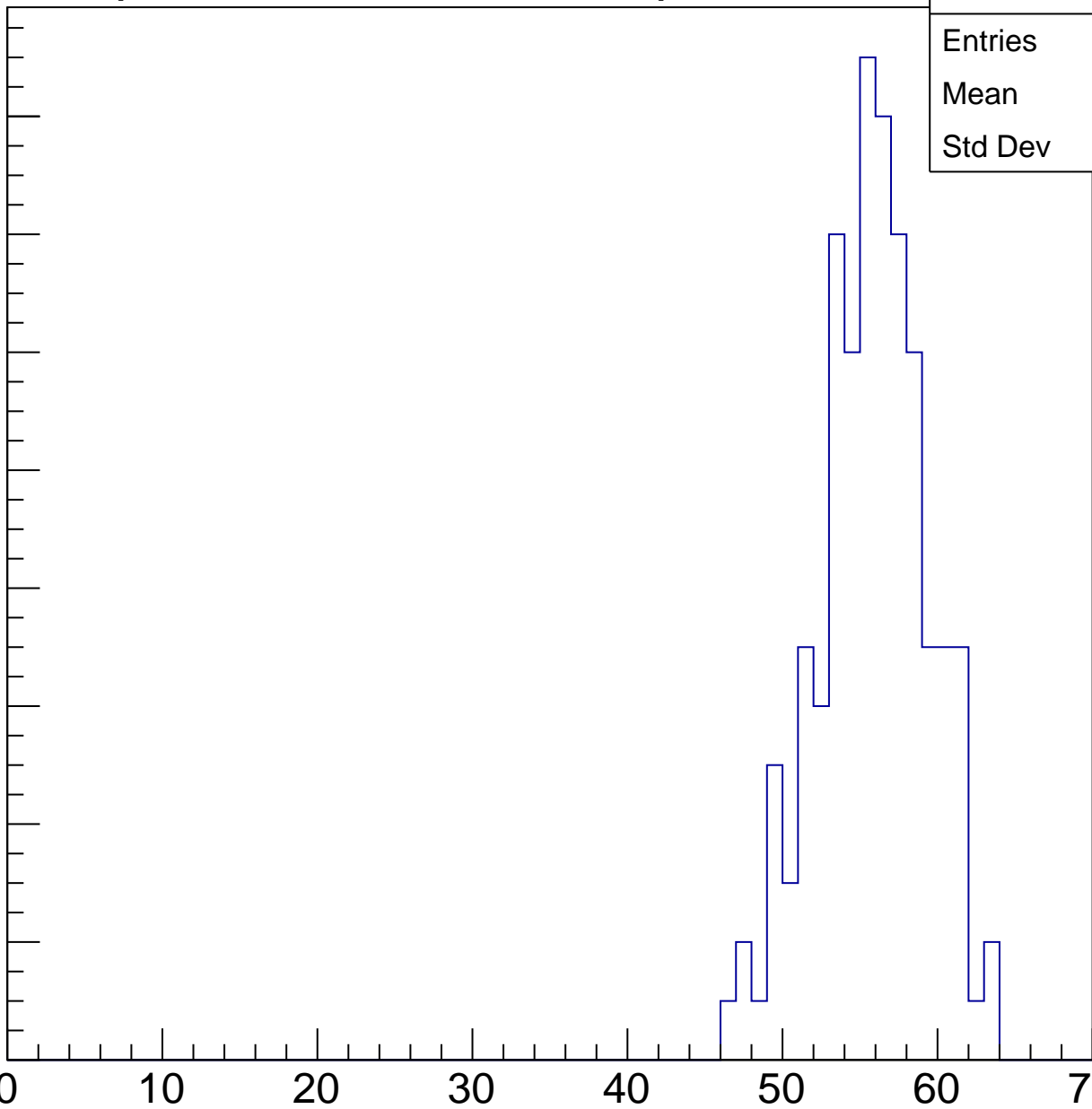
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 55.34 |
| Std Dev | 3.485 |

ampl



B1L001S, U26-ch54, adc5

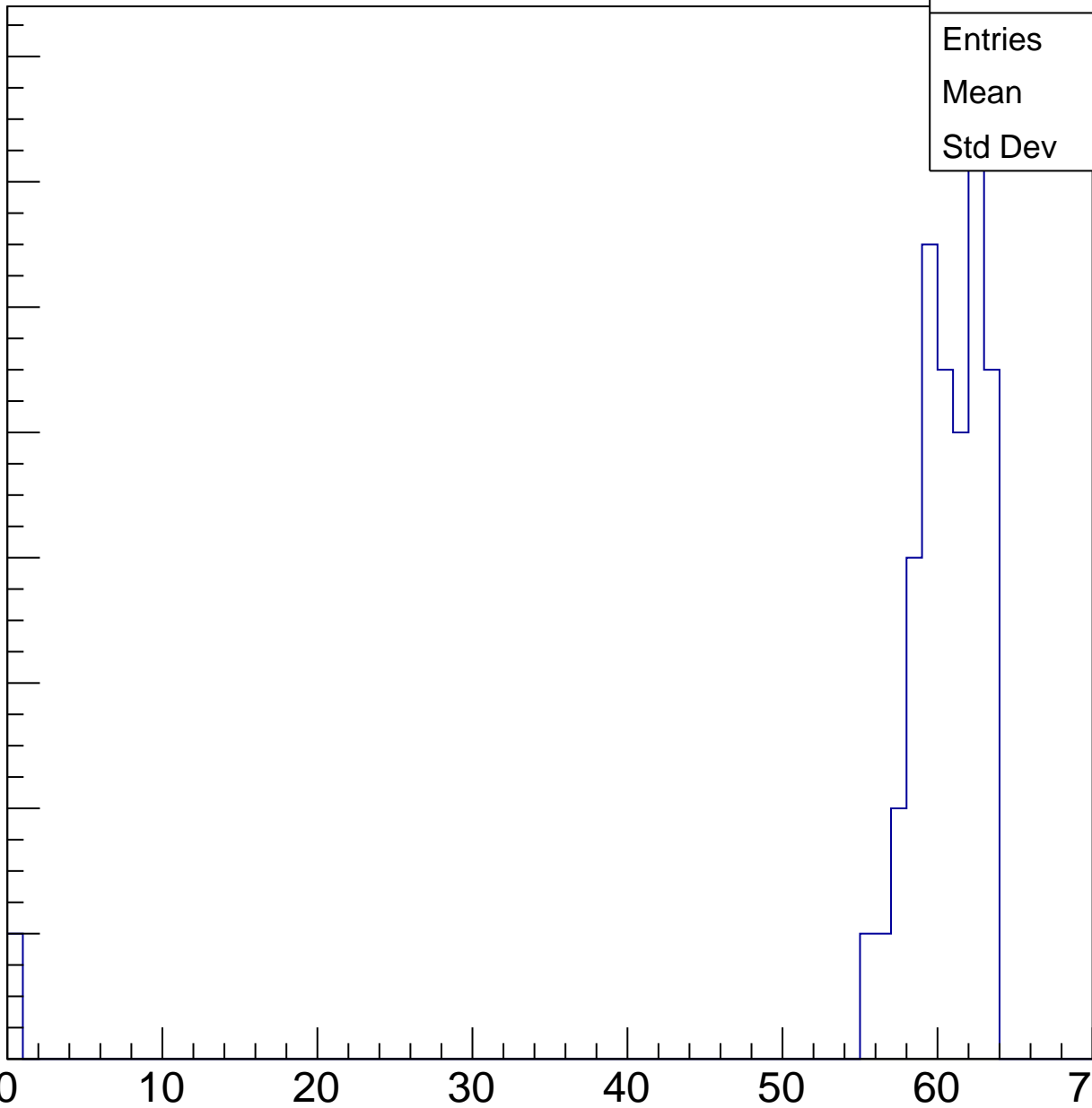
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 79 |
| Mean | 58.68 |
| Std Dev | 9.678 |

ampl

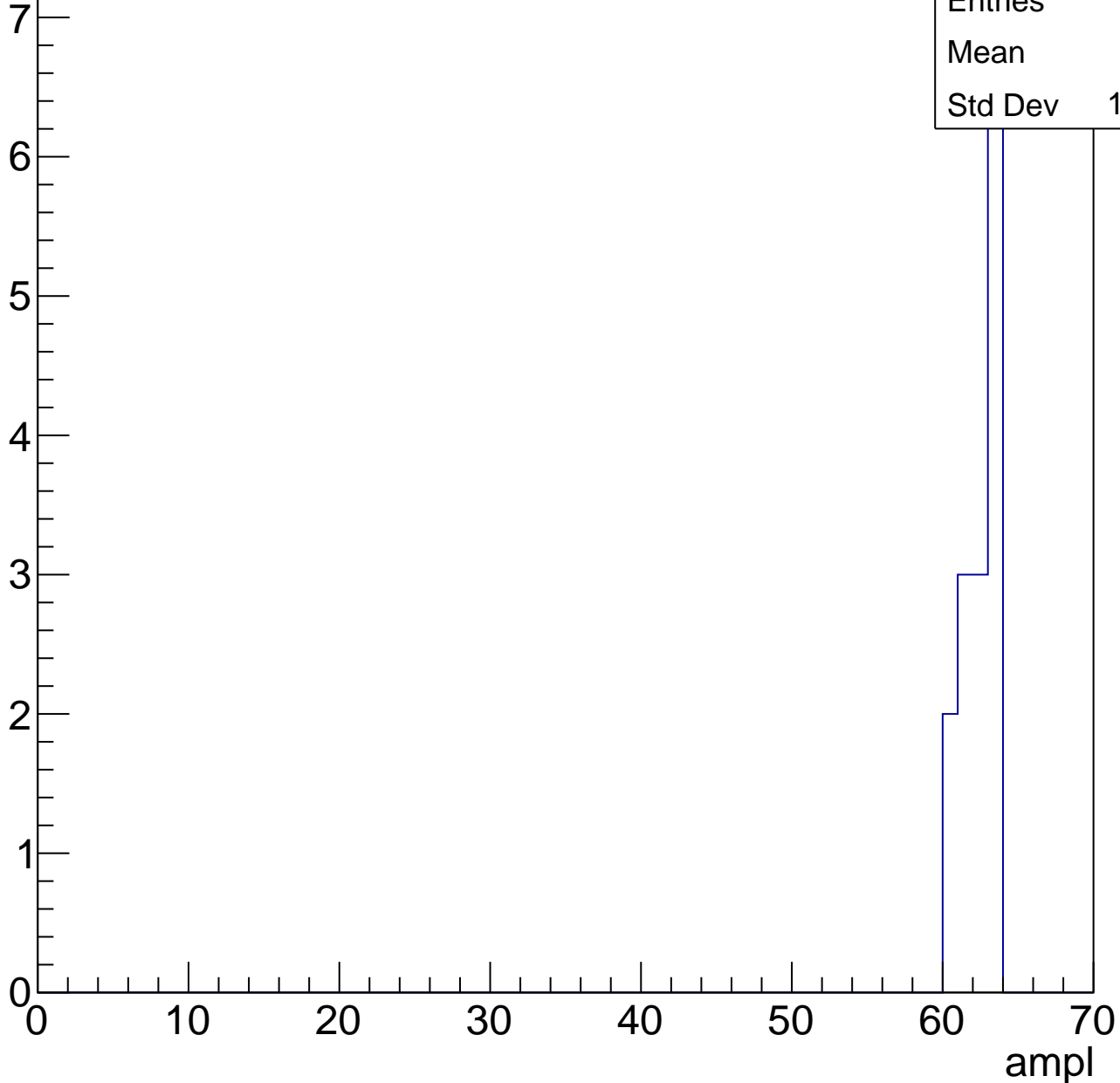


B1L001S, U26-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

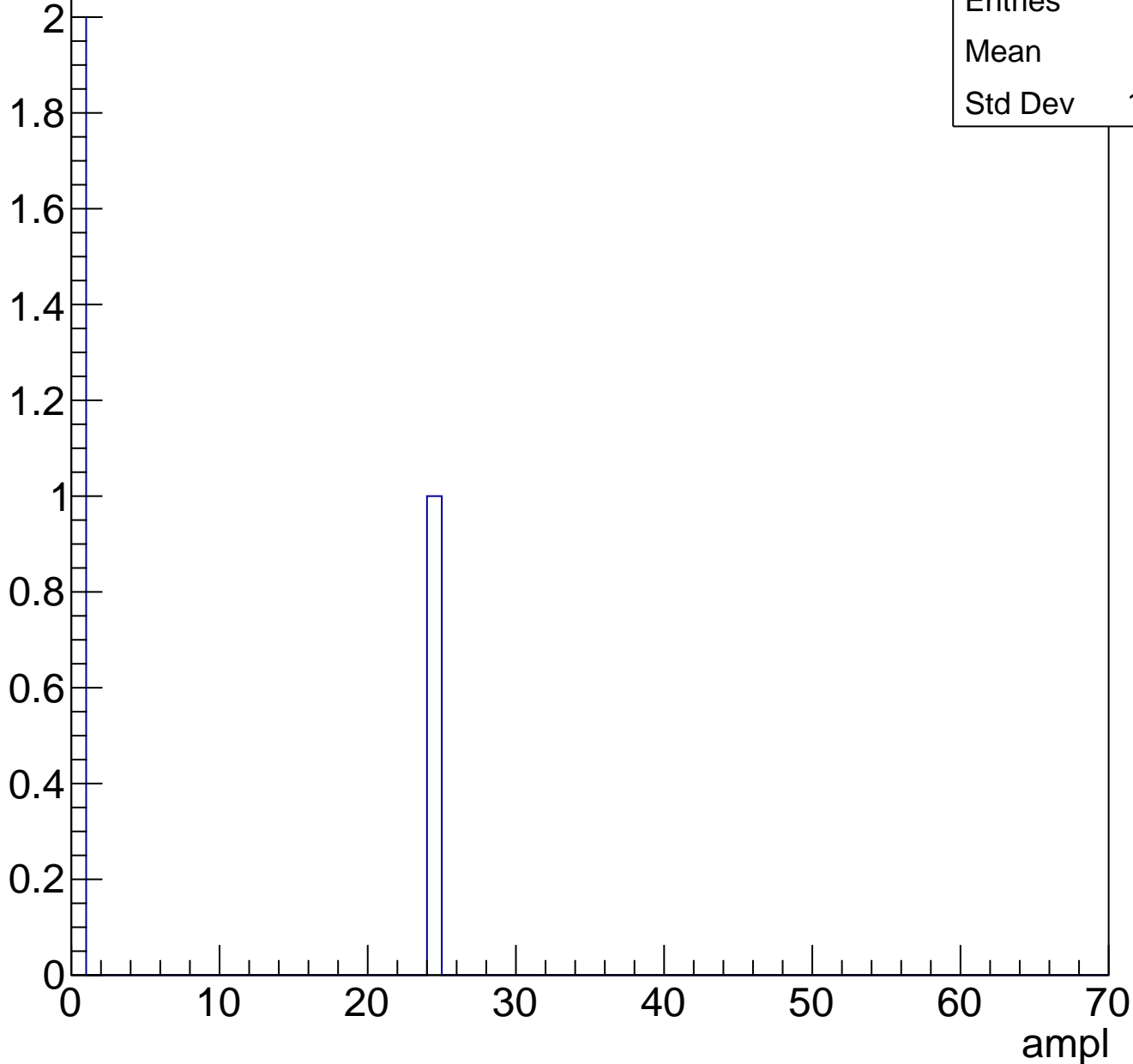
| | |
|---------|-------|
| Entries | 15 |
| Mean | 62 |
| Std Dev | 1.095 |



B1L001S, U26-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch55, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 30.93 |
| Std Dev | 3.57 |

Gaus mean : 31.5072

Gaus Width: 3.7040

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch55, adc1

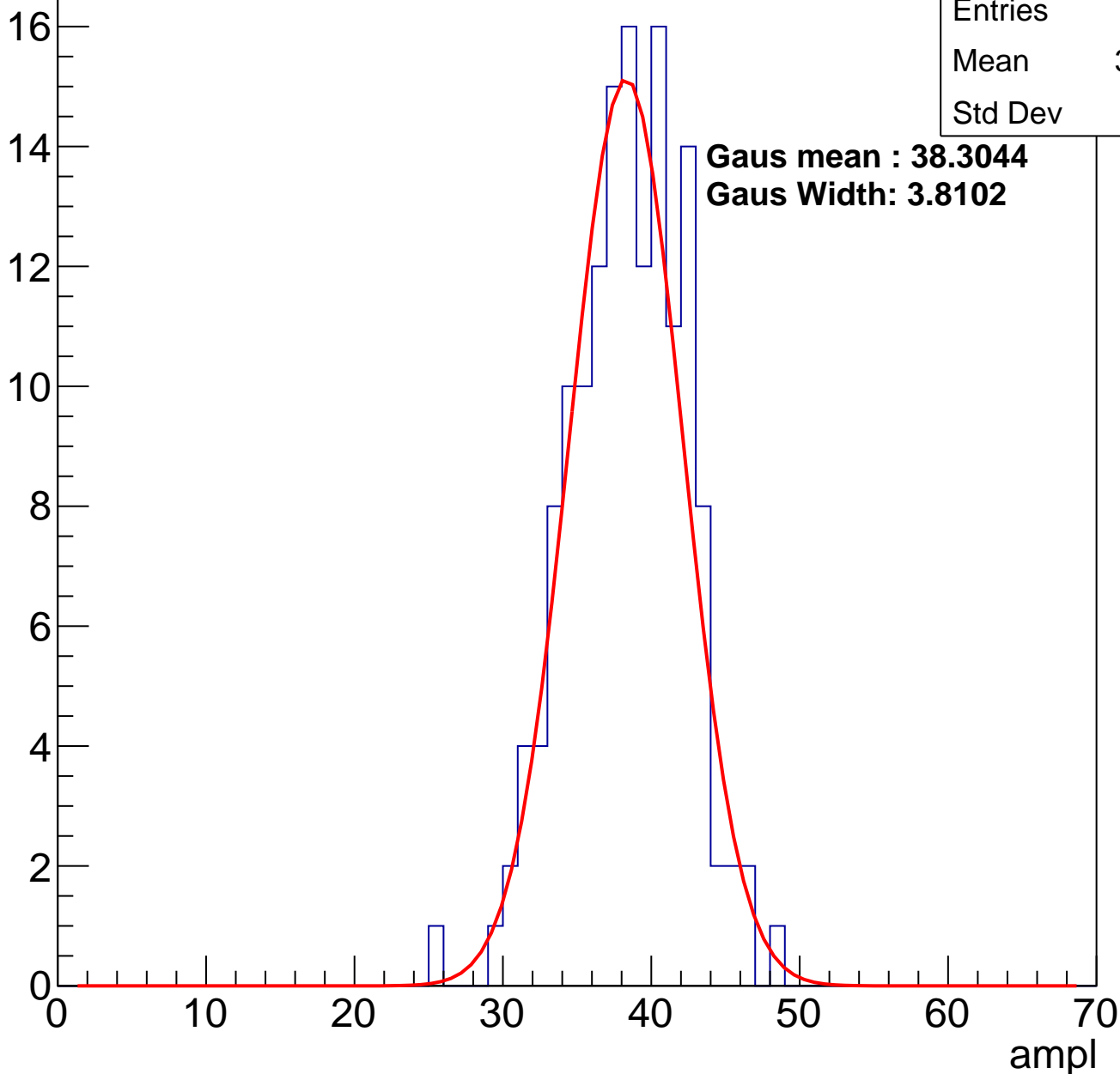
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 37.91 |
| Std Dev | 3.84 |

Gaus mean : 38.3044

Gaus Width: 3.8102

Entry



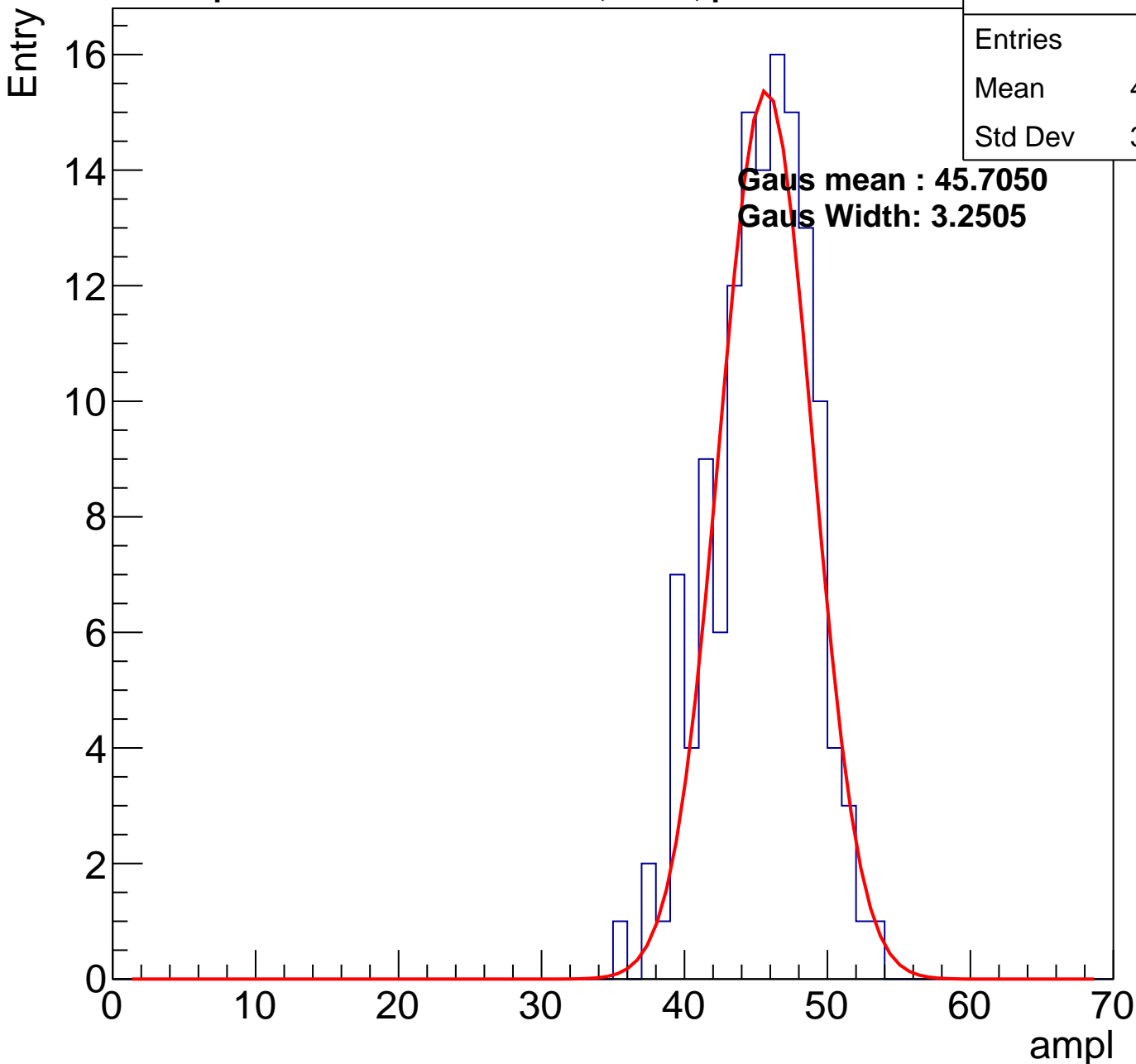
B1L001S, U26-ch55, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 44.93 |
| Std Dev | 3.416 |

Gaus mean : 45.7050

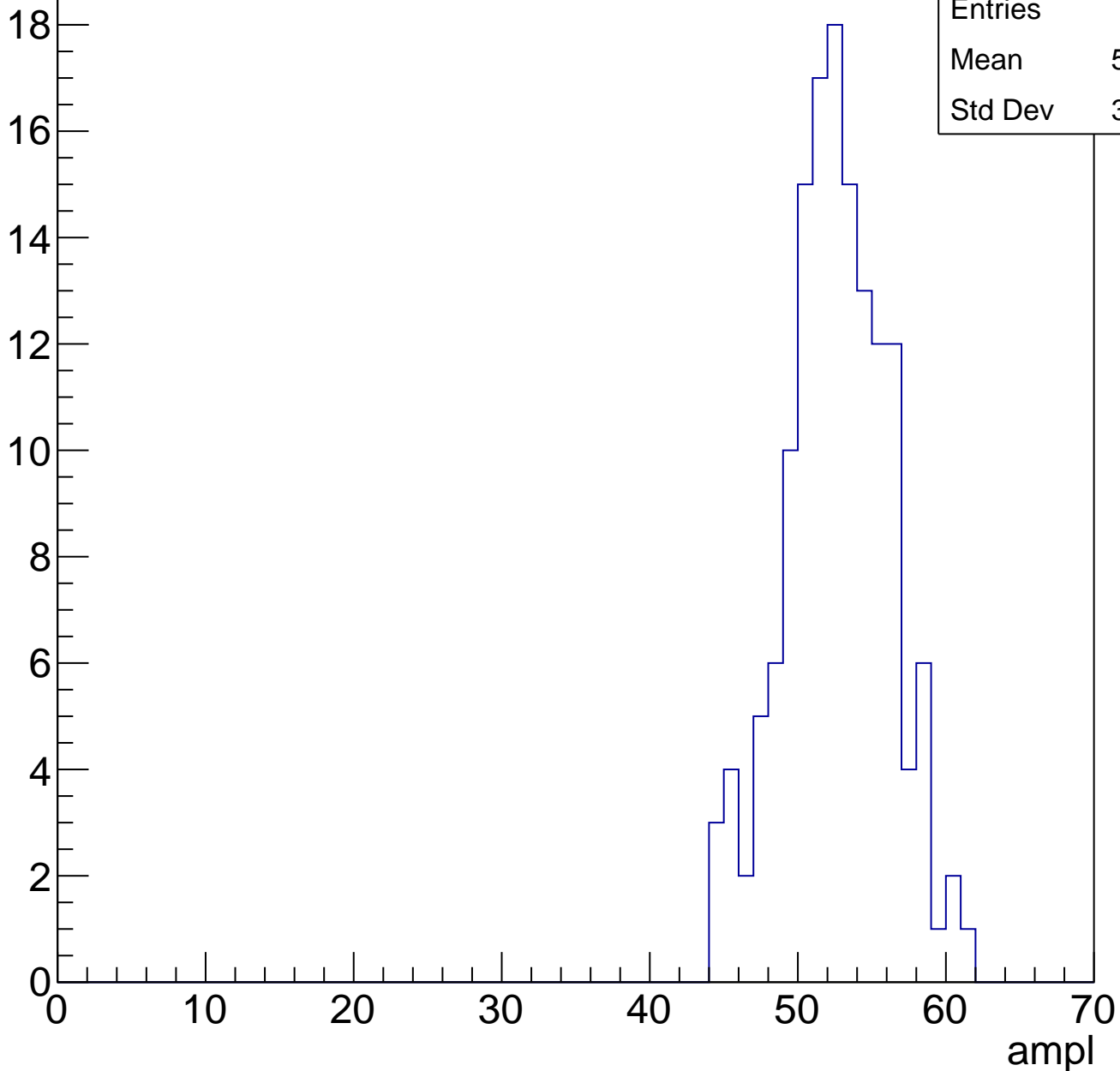
Gaus Width: 3.2505



B1L001S, U26-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 146 |
| Mean | 52.16 |
| Std Dev | 3.497 |

B1L001S, U26-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

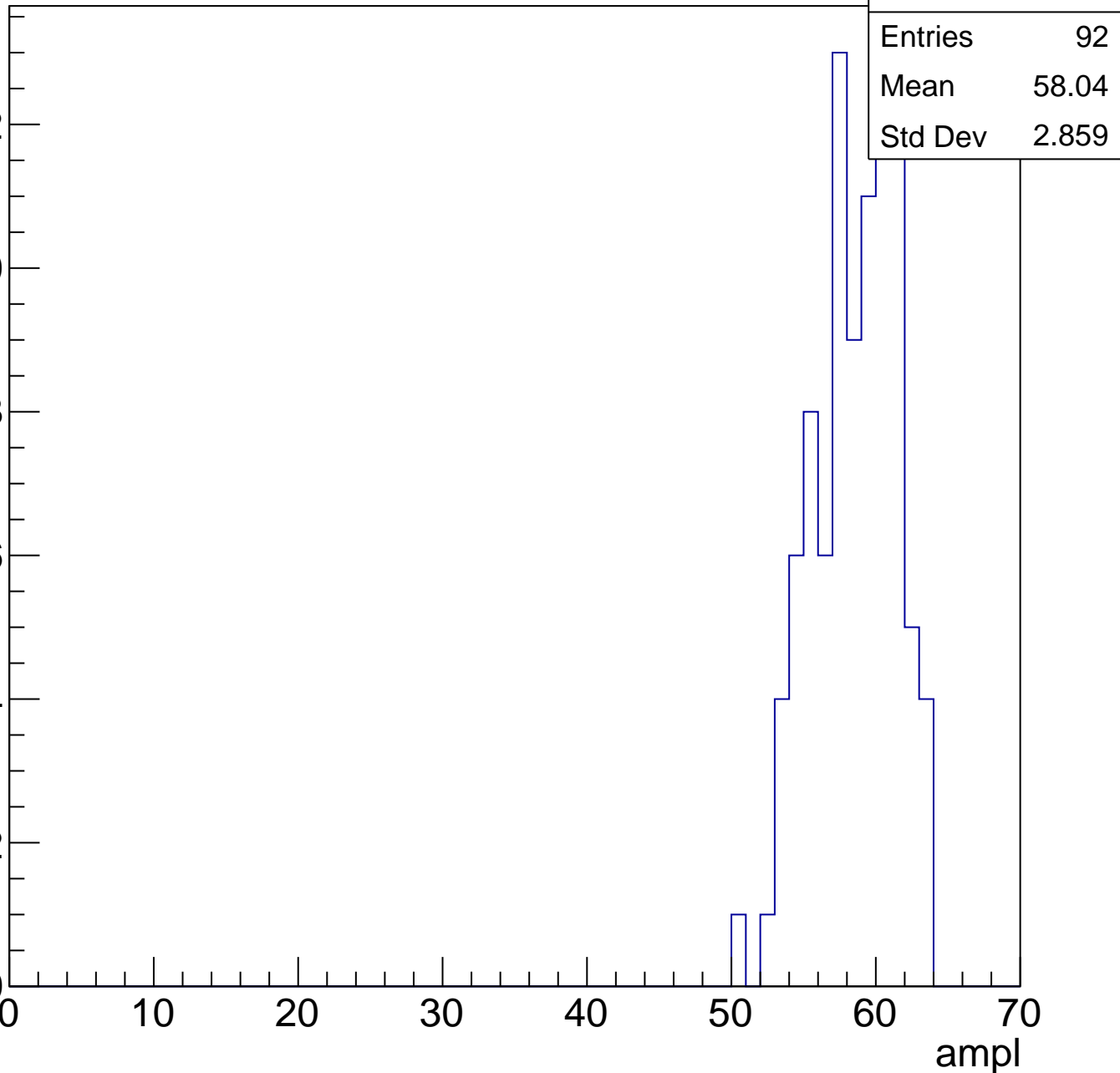
50

60

70

ampl

| | |
|---------|-------|
| Entries | 92 |
| Mean | 58.04 |
| Std Dev | 2.859 |



B1L001S, U26-ch55, adc5

calib_packv5_042523_0143.root, FC#2, port C2

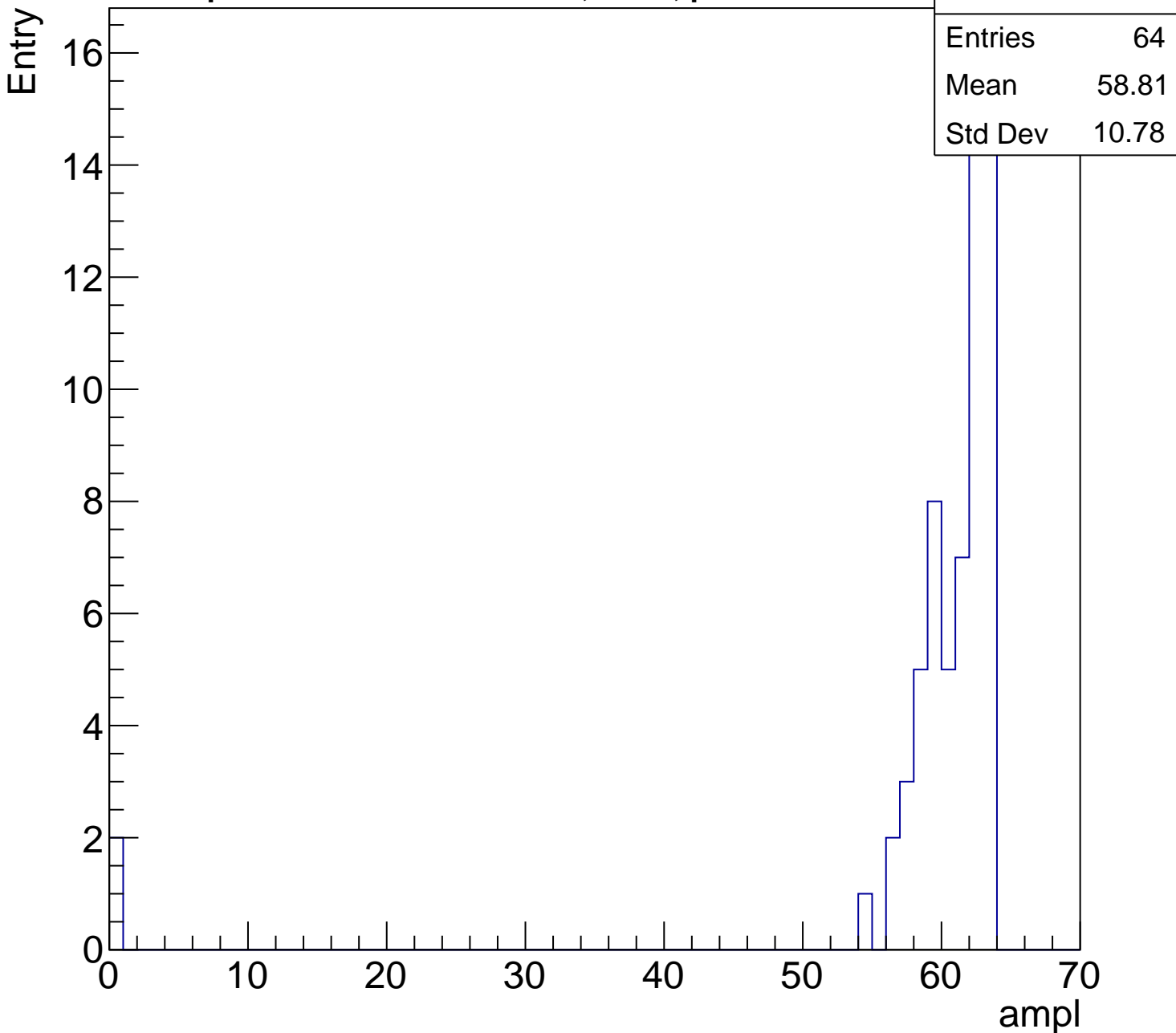
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 58.81 |
| Std Dev | 10.78 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch56, adc0

calib_packv5_042523_0143.root, FC#2, port C2

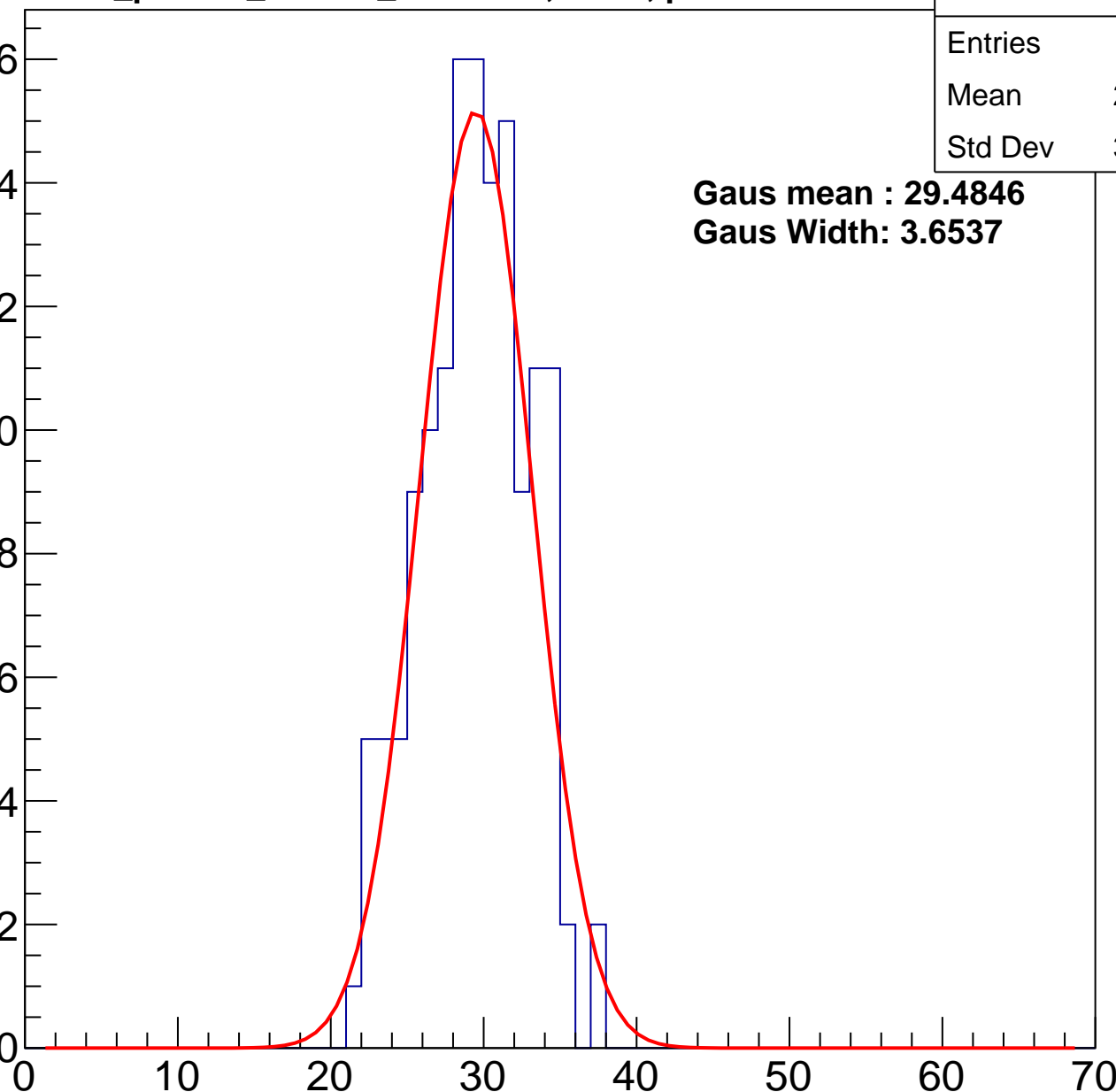
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 142 |
| Mean | 28.97 |
| Std Dev | 3.474 |

Gaus mean : 29.4846

Gaus Width: 3.6537



ampl

B1L001S, U26-ch56, adc1

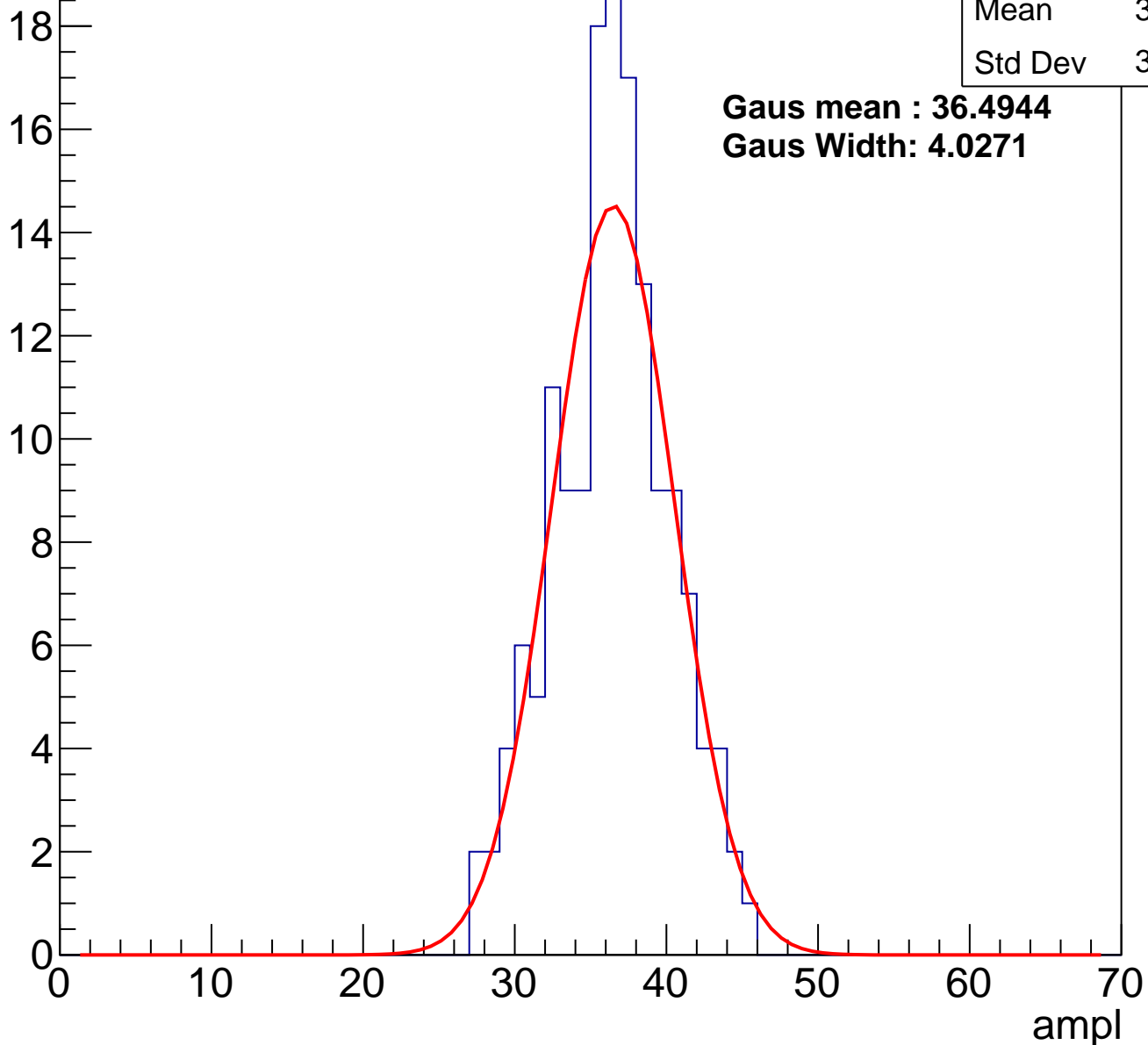
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 35.92 |
| Std Dev | 3.778 |

Gaus mean : 36.4944

Gaus Width: 4.0271

Entry



B1L001S, U26-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

96

Mean

42.22

Std Dev

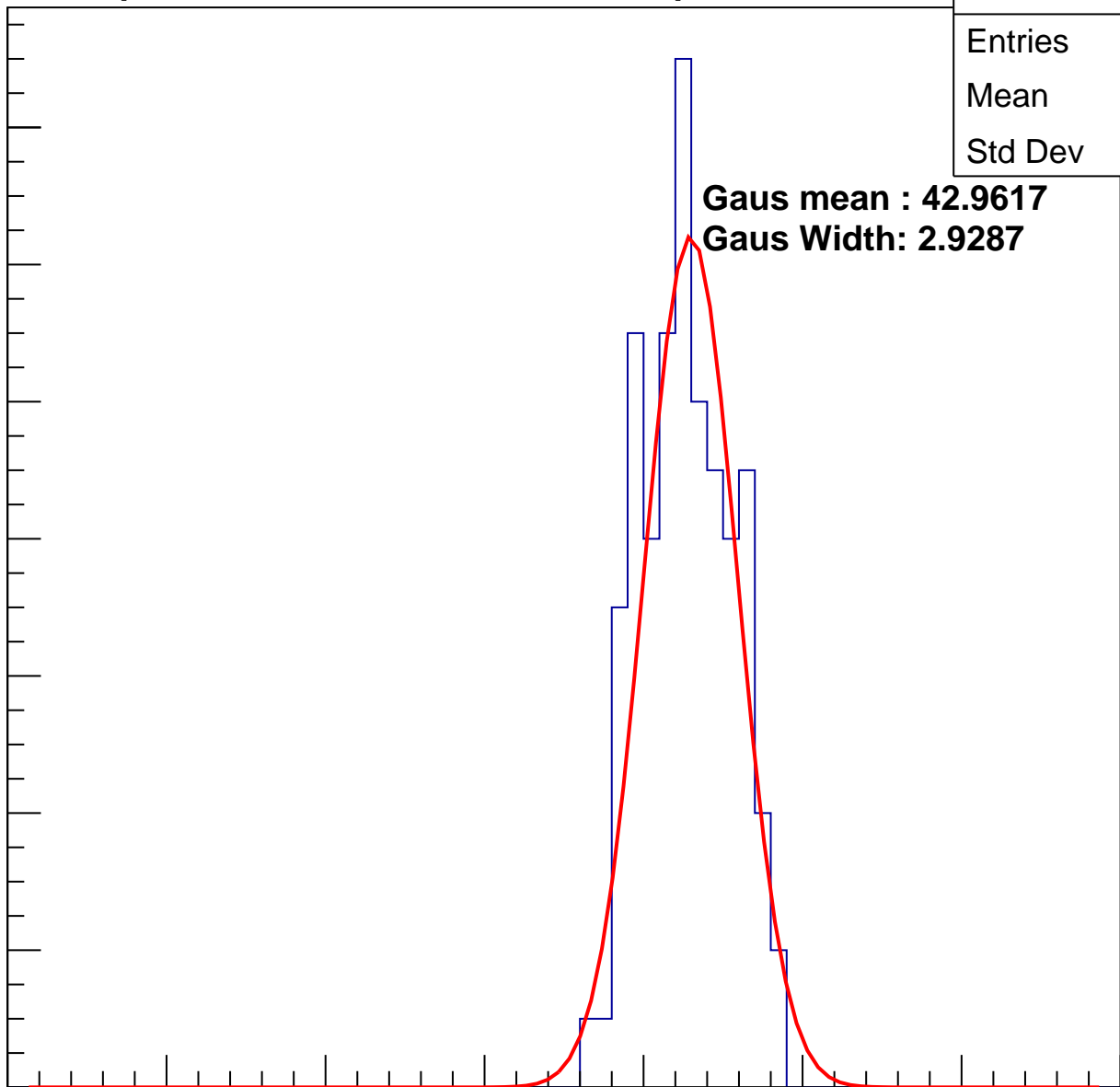
2.785

Gaus mean : 42.9617

Gaus Width: 2.9287

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

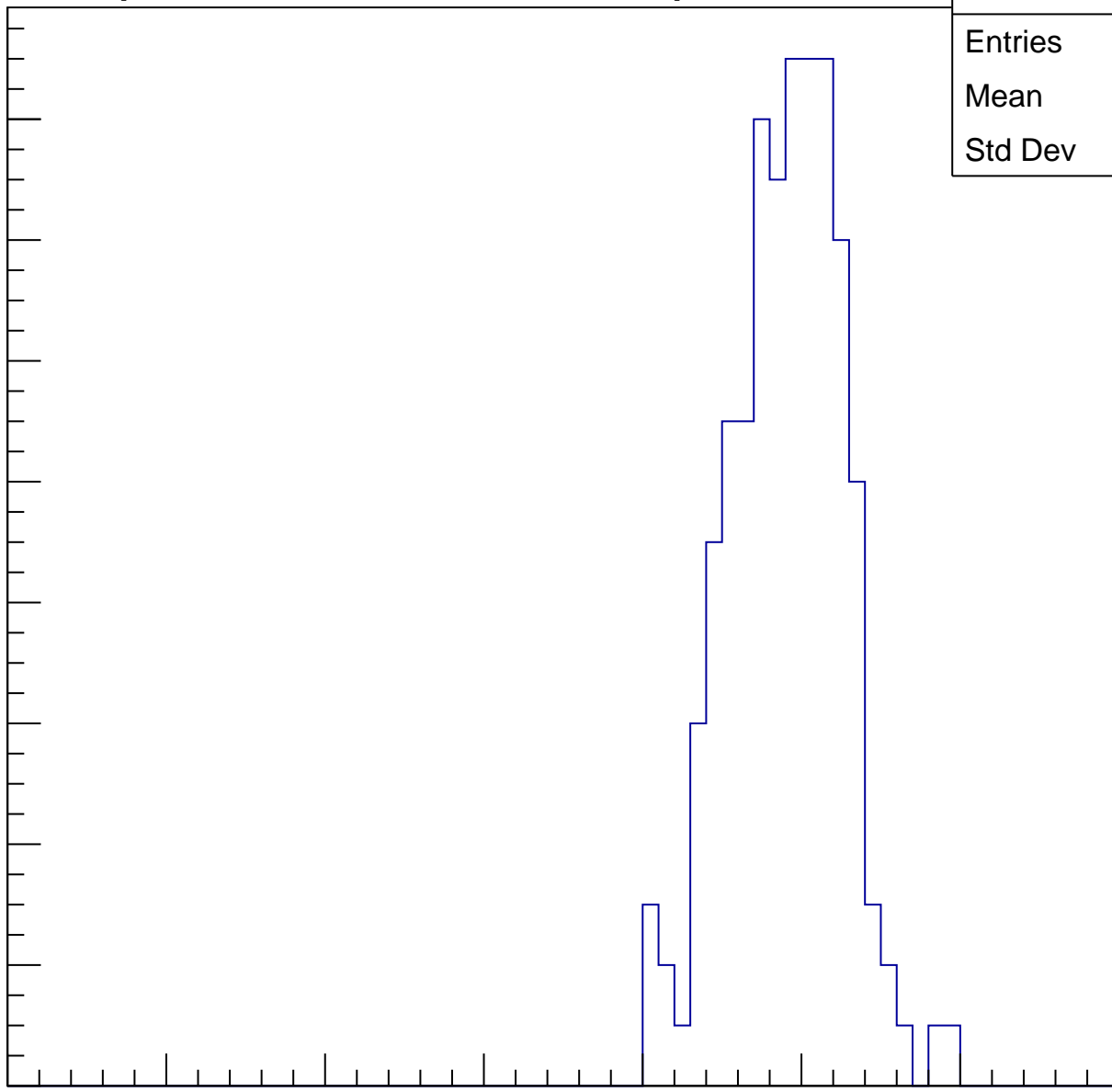
| | |
|---------|-------|
| Entries | 157 |
| Mean | 48.56 |
| Std Dev | 3.509 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

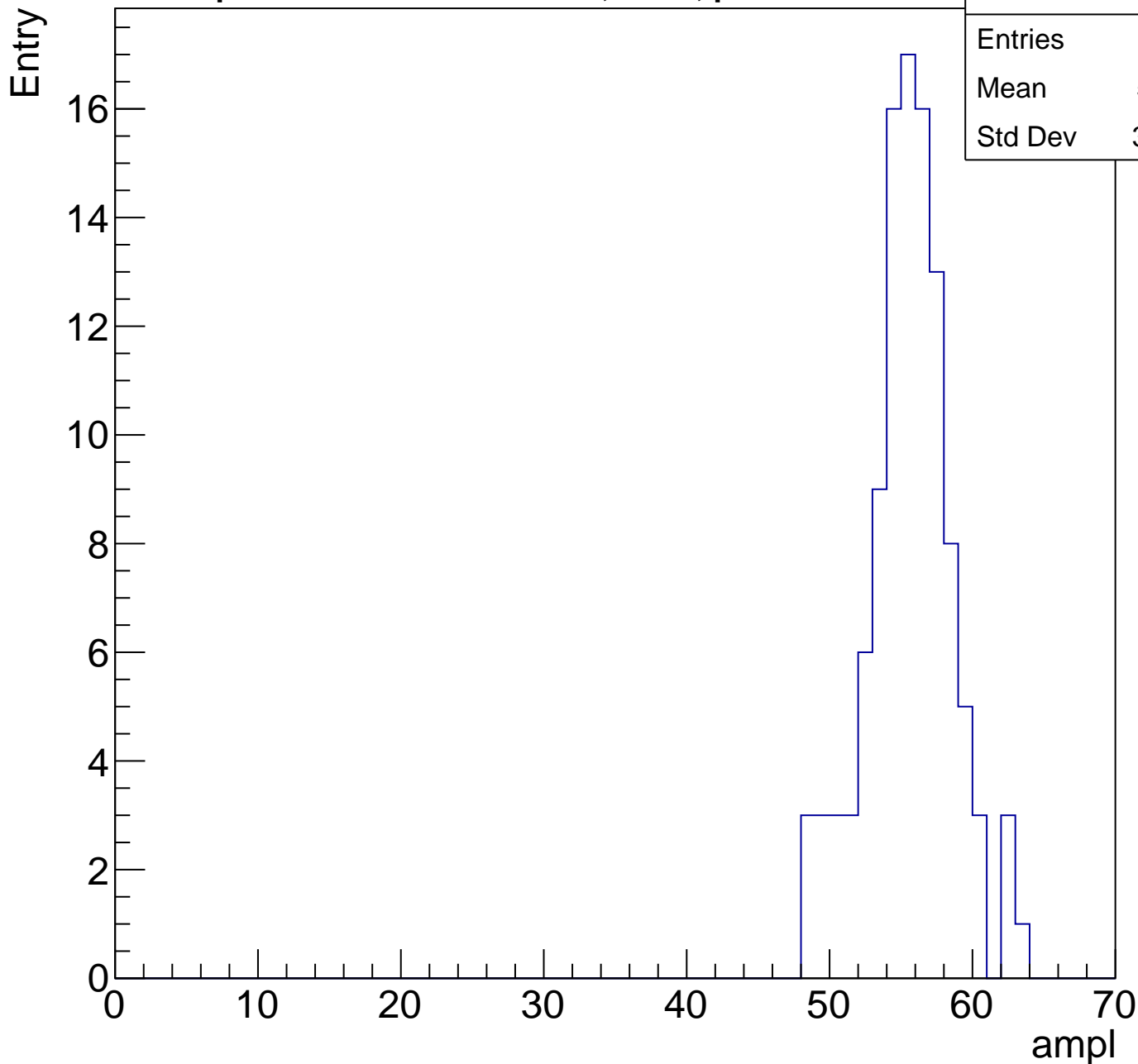
ampl



B1L001S, U26-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 109 |
| Mean | 55.11 |
| Std Dev | 3.012 |



B1L001S, U26-ch56, adc5

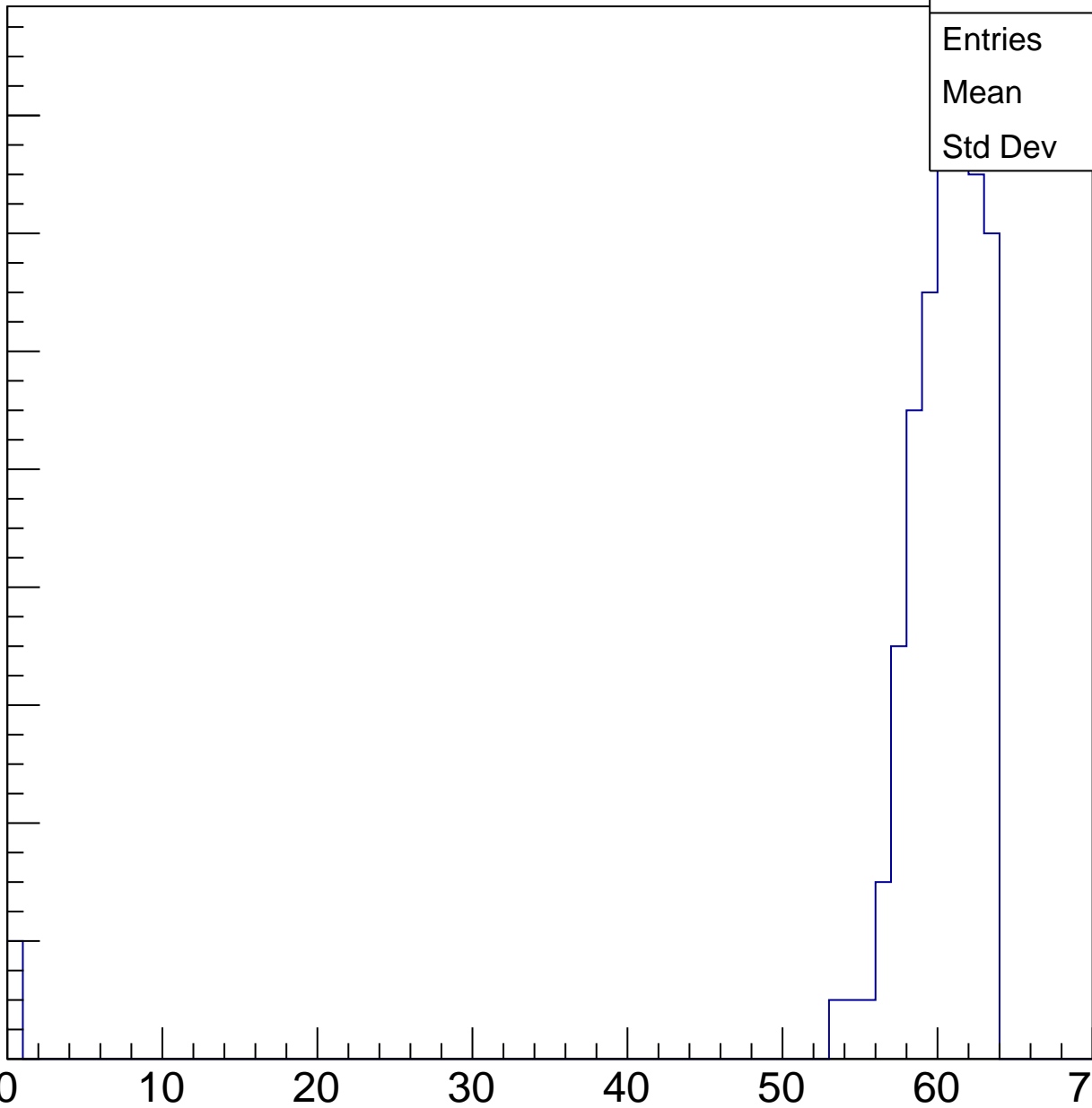
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 101 |
| Mean | 58.84 |
| Std Dev | 8.643 |

Entry

16
14
12
10
8
6
4
2
0

ampl

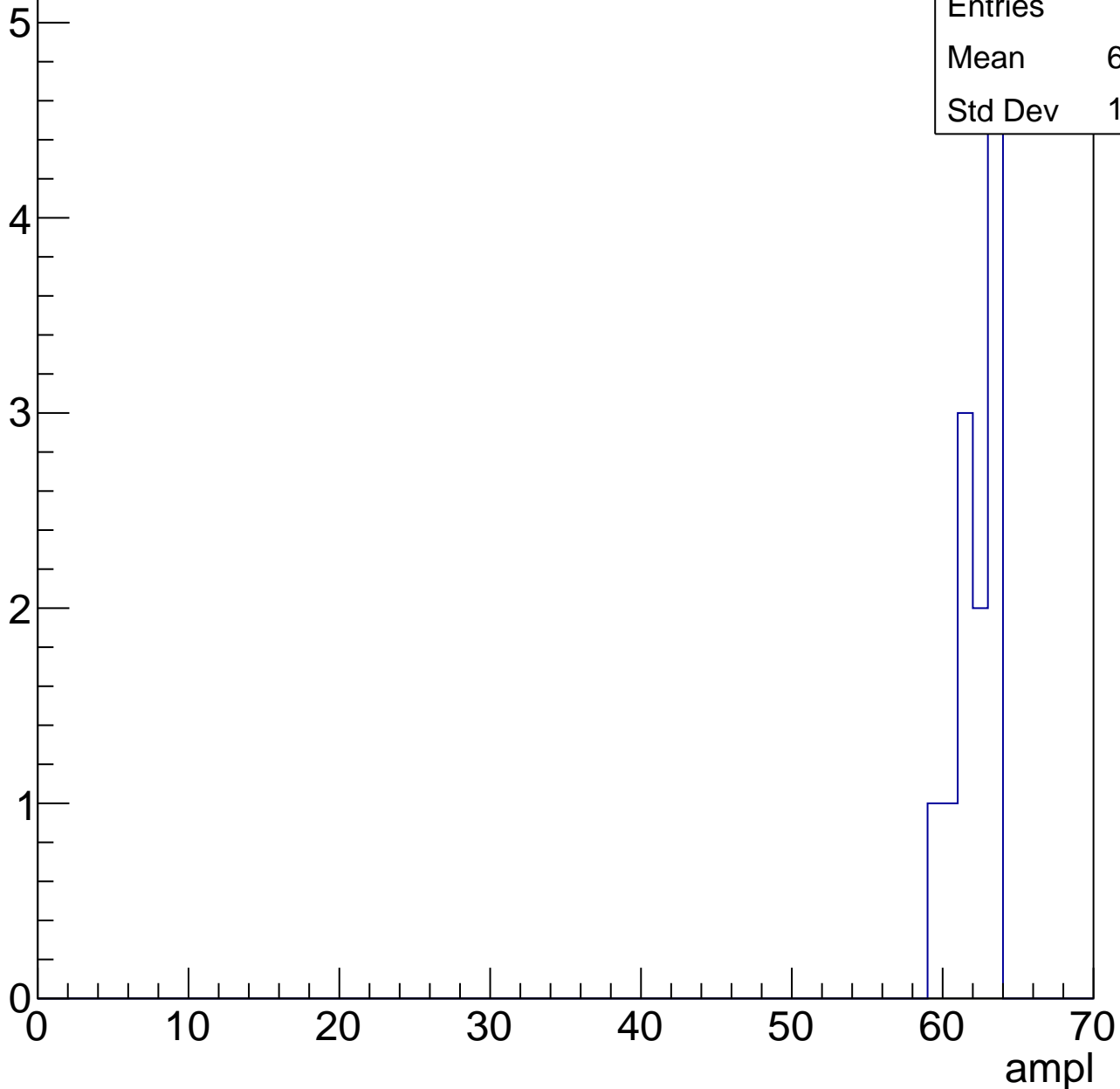


B1L001S, U26-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 61.75 |
| Std Dev | 1.299 |



B1L001S, U26-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch57, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 114 |
| Mean | 29.09 |
| Std Dev | 3.368 |

Gaus mean : 29.7230

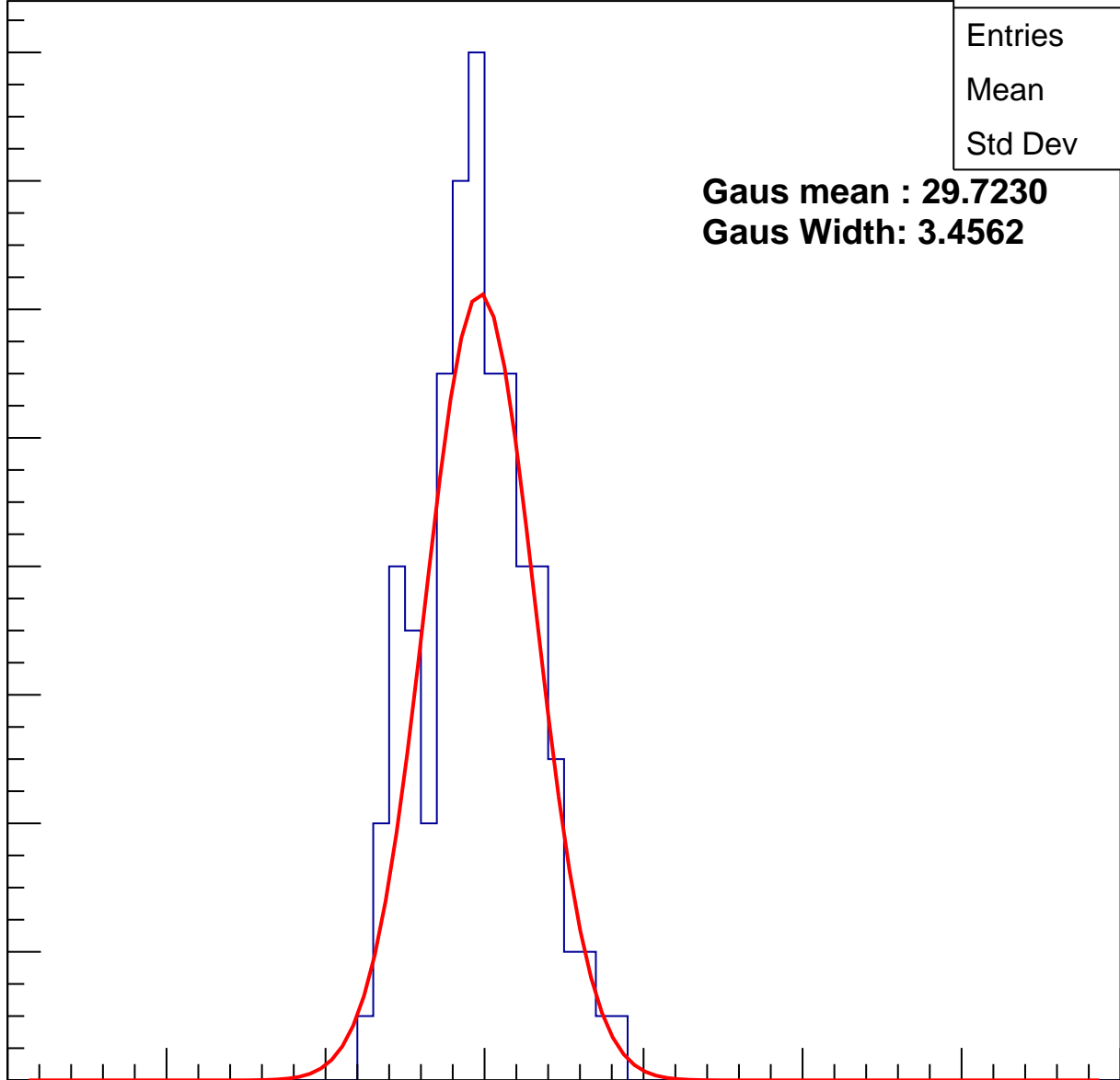
Gaus Width: 3.4562

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch57, adc1

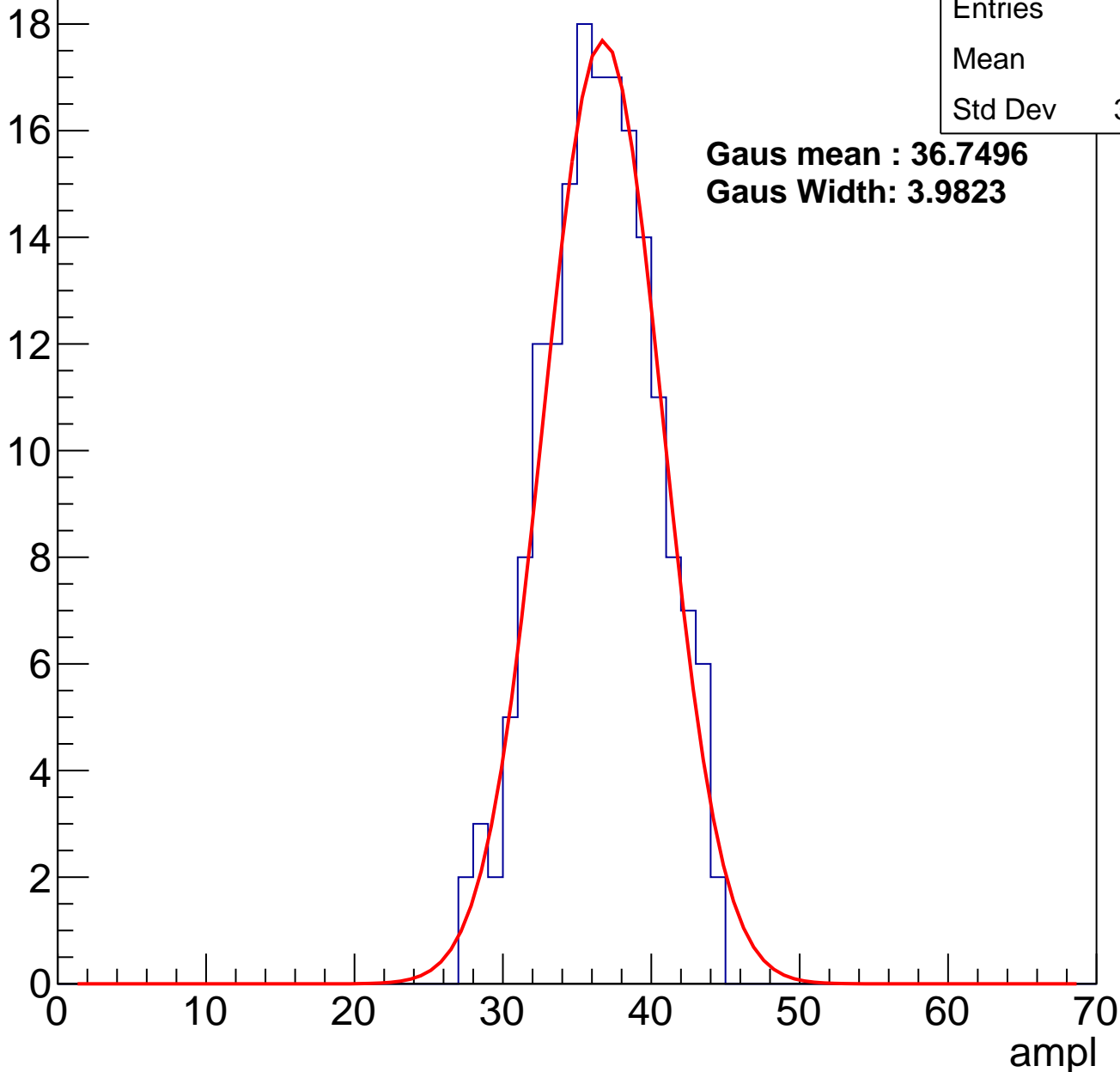
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 175 |
| Mean | 36.1 |
| Std Dev | 3.753 |

Gaus mean : 36.7496

Gaus Width: 3.9823

Entry



B1L001S, U26-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

113

Mean

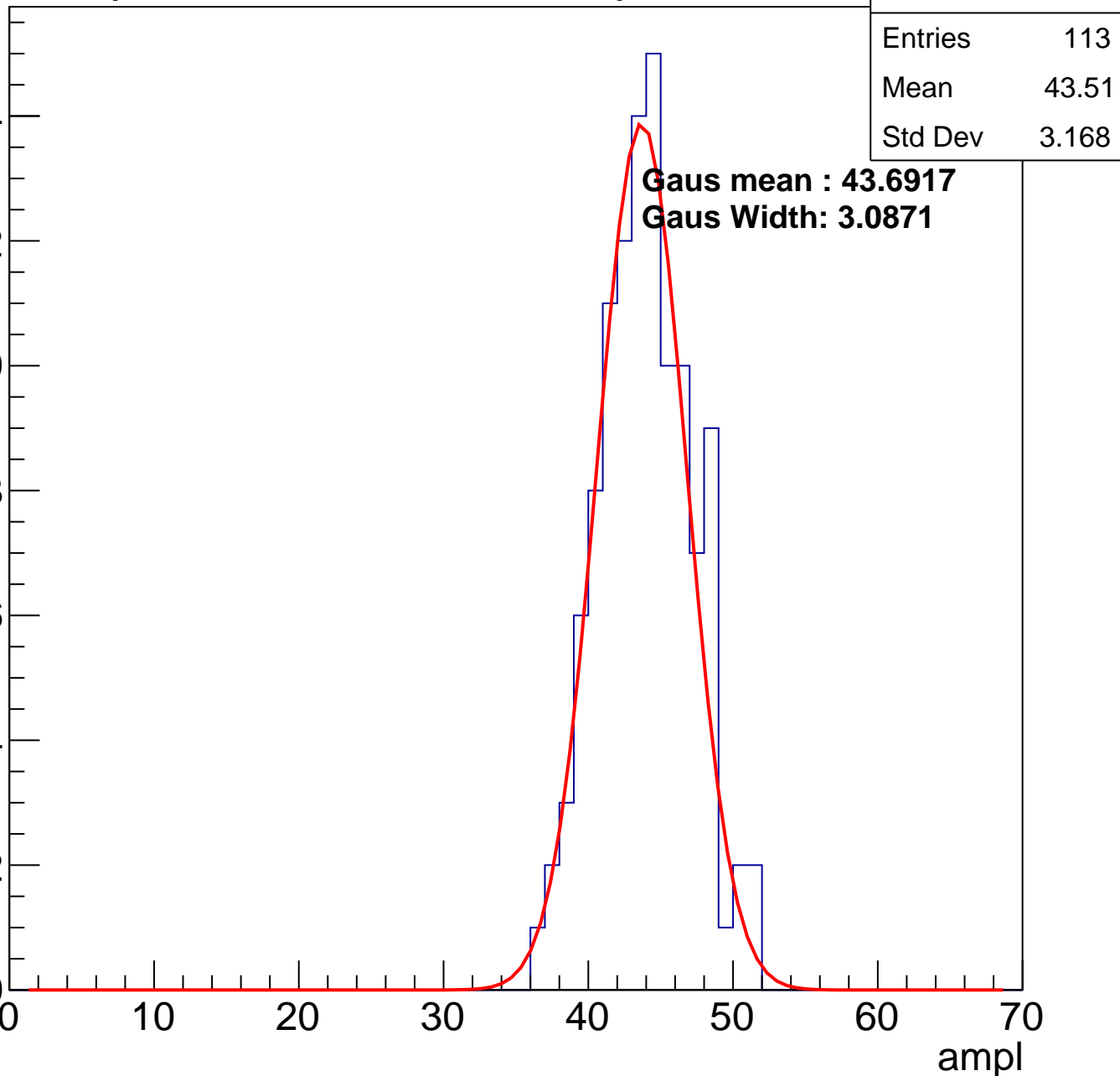
43.51

Std Dev

3.168

Gaus mean : 43.6917

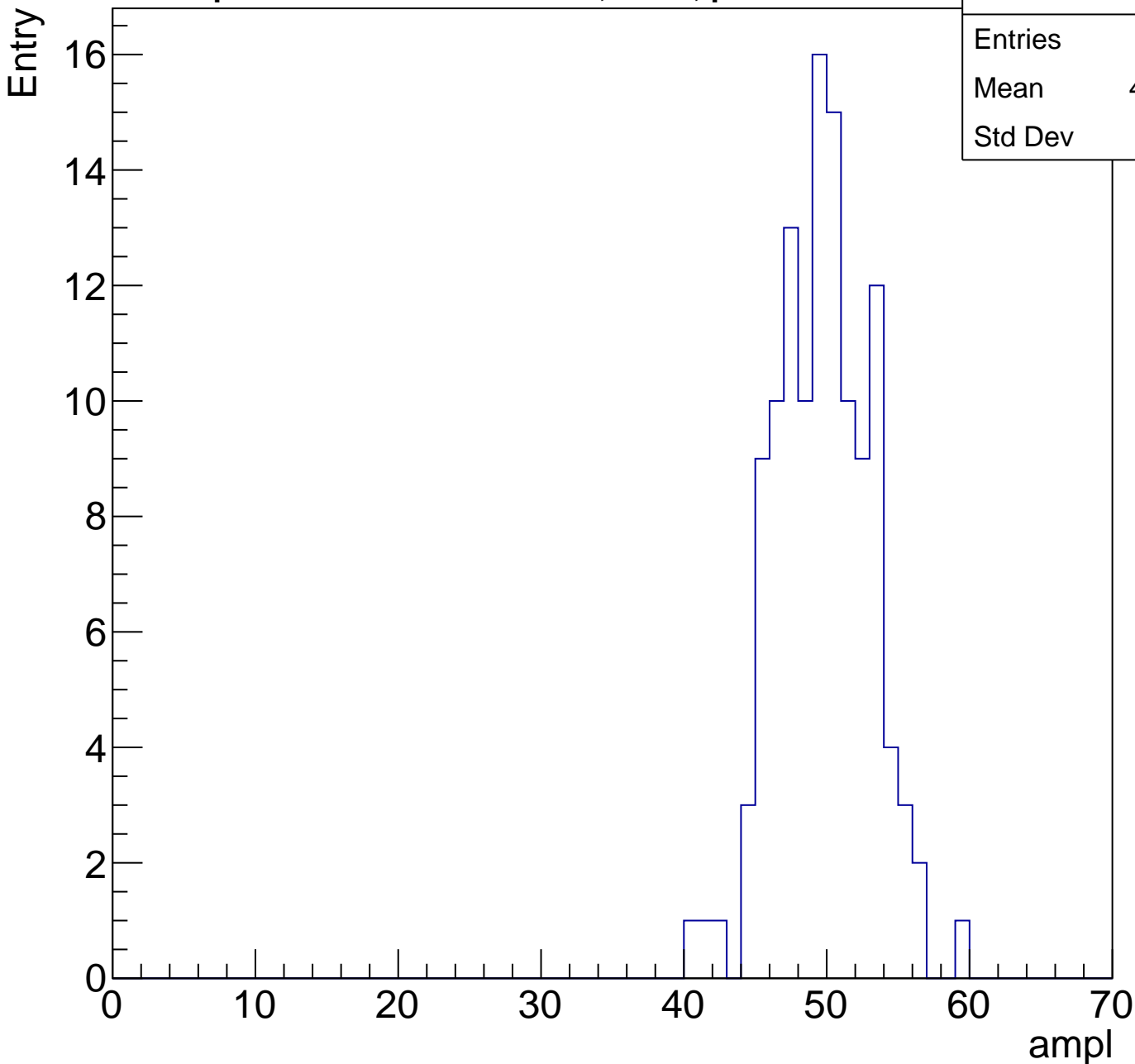
Gaus Width: 3.0871



B1L001S, U26-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 49.26 |
| Std Dev | 3.28 |



B1L001S, U26-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 55.65 |
| Std Dev | 3.471 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

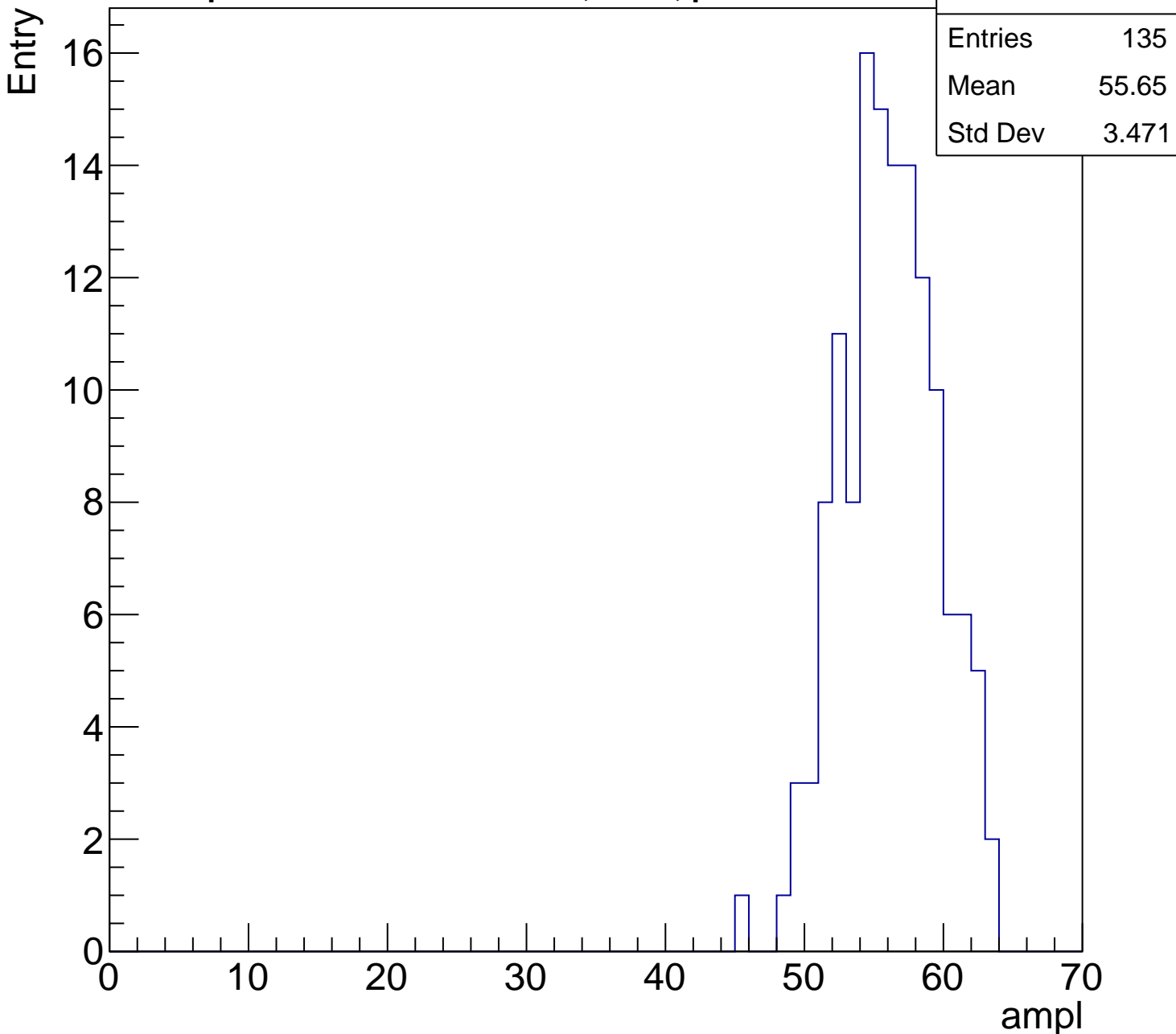
40

50

60

70

ampl



B1L001S, U26-ch57, adc5

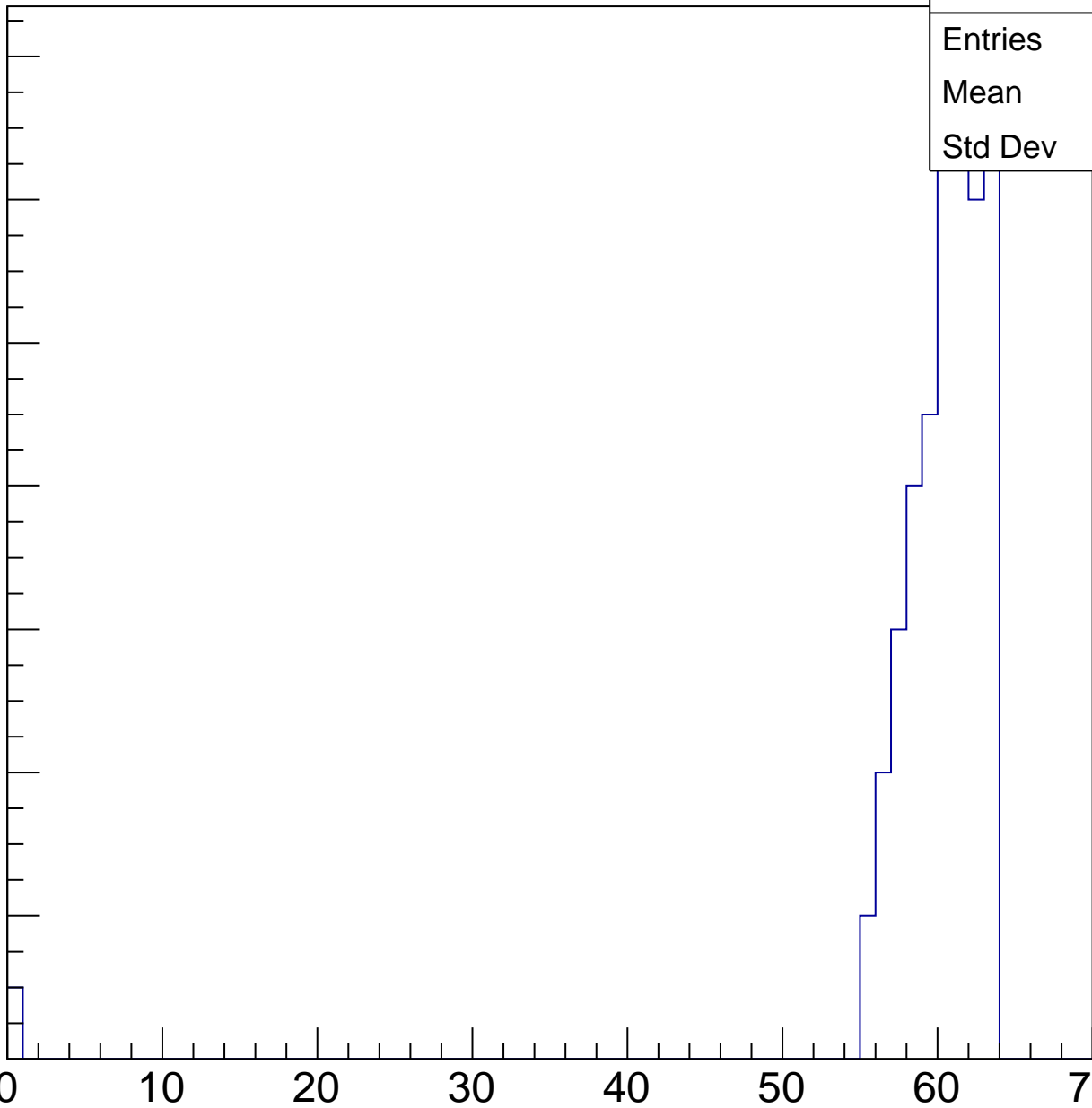
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 82 |
| Mean | 59.37 |
| Std Dev | 6.945 |

ampl

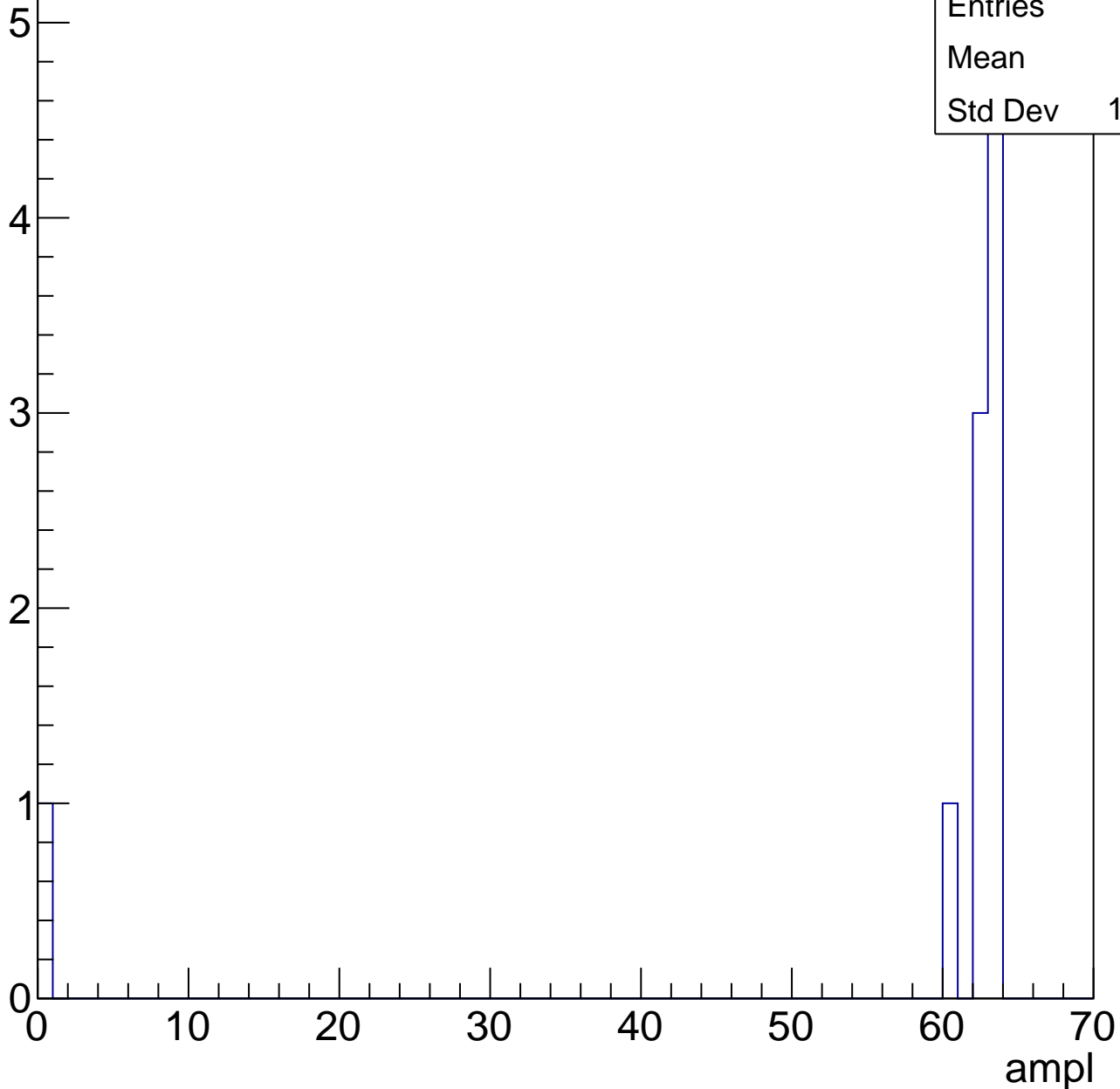


B1L001S, U26-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 56.1 |
| Std Dev | 18.72 |



B1L001S, U26-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



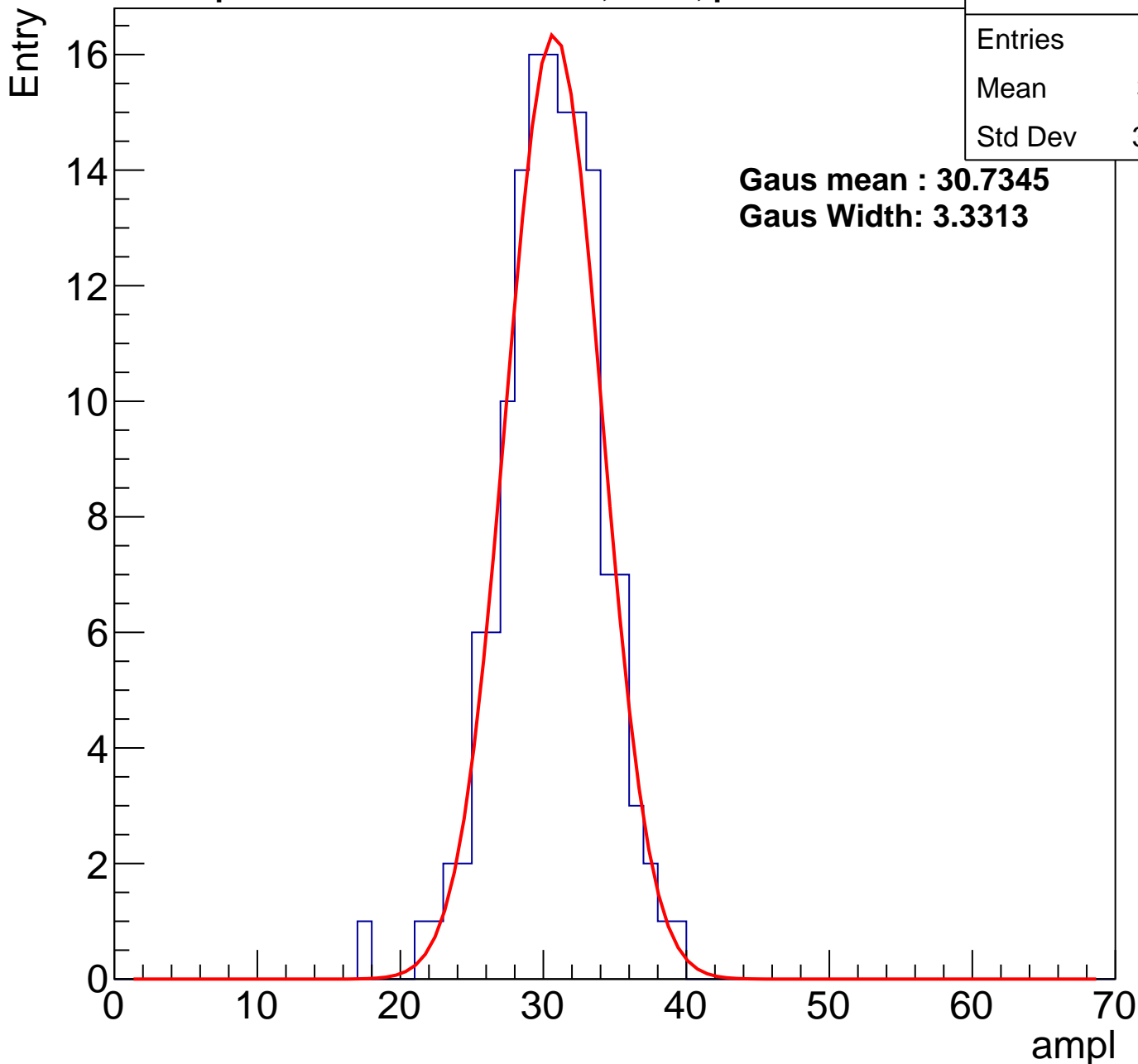
B1L001S, U26-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 30.11 |
| Std Dev | 3.515 |

Gaus mean : 30.7345

Gaus Width: 3.3313



B1L001S, U26-ch58, adc1

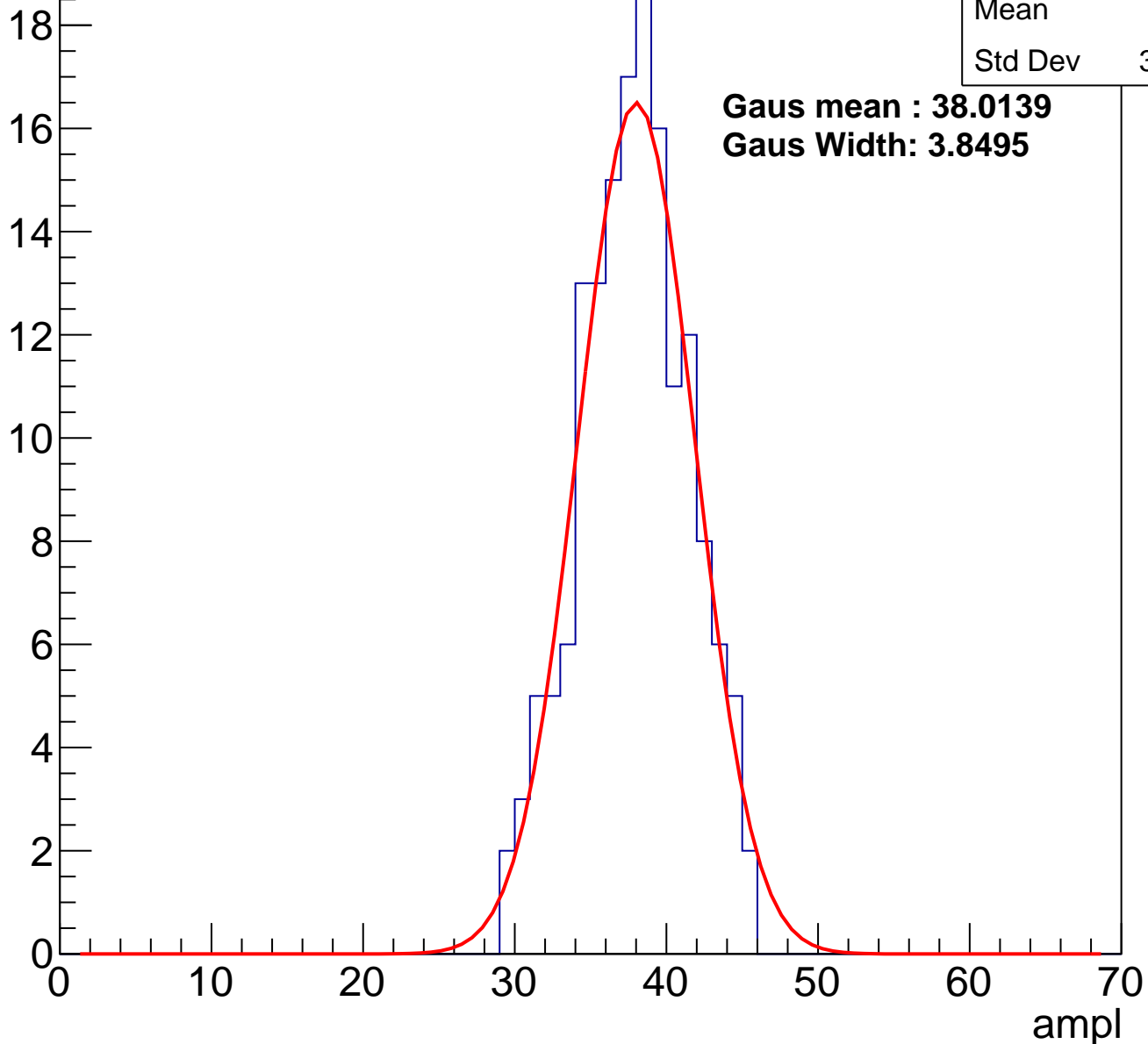
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 37.4 |
| Std Dev | 3.556 |

Gaus mean : 38.0139

Gaus Width: 3.8495

Entry



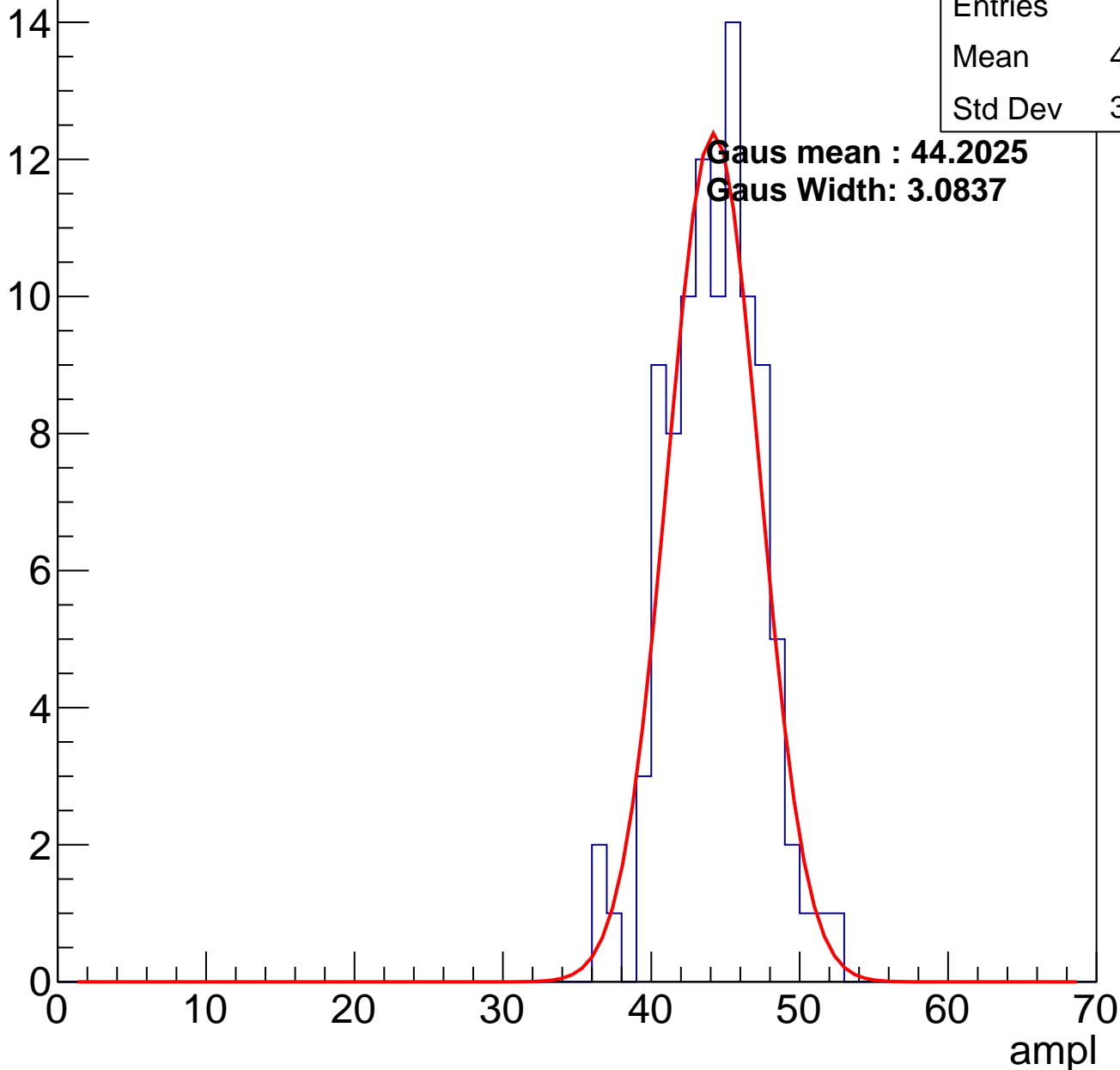
B1L001S, U26-ch58, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 98 |
| Mean | 43.82 |
| Std Dev | 3.082 |

Gaus mean : 44.2025
Gaus Width: 3.0837



B1L001S, U26-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 50.09 |
| Std Dev | 3.412 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

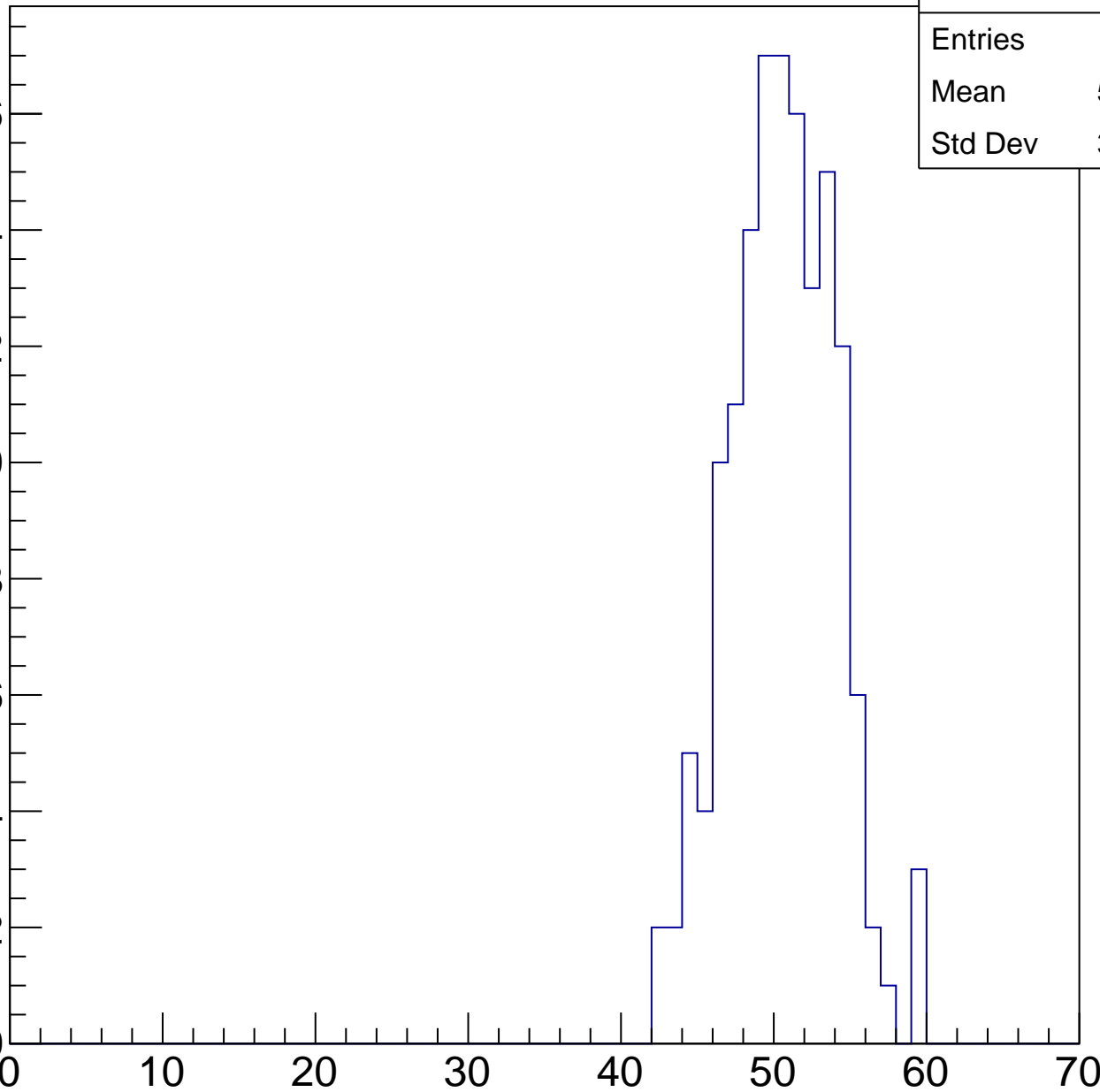
30

40

50

60

ampl



B1L001S, U26-ch58, adc4

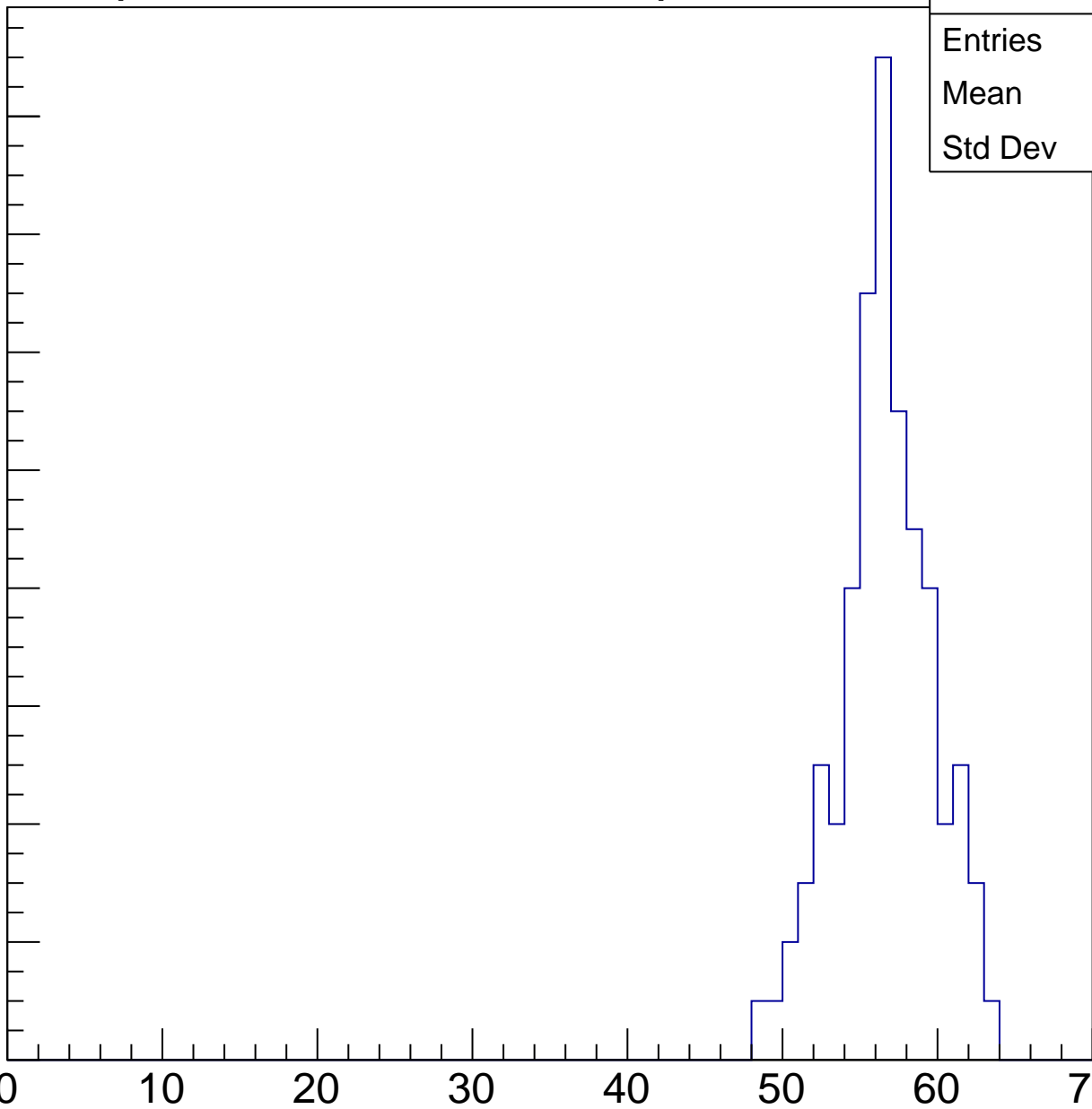
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 56.17 |
| Std Dev | 3.049 |

ampl



B1L001S, U26-ch58, adc5

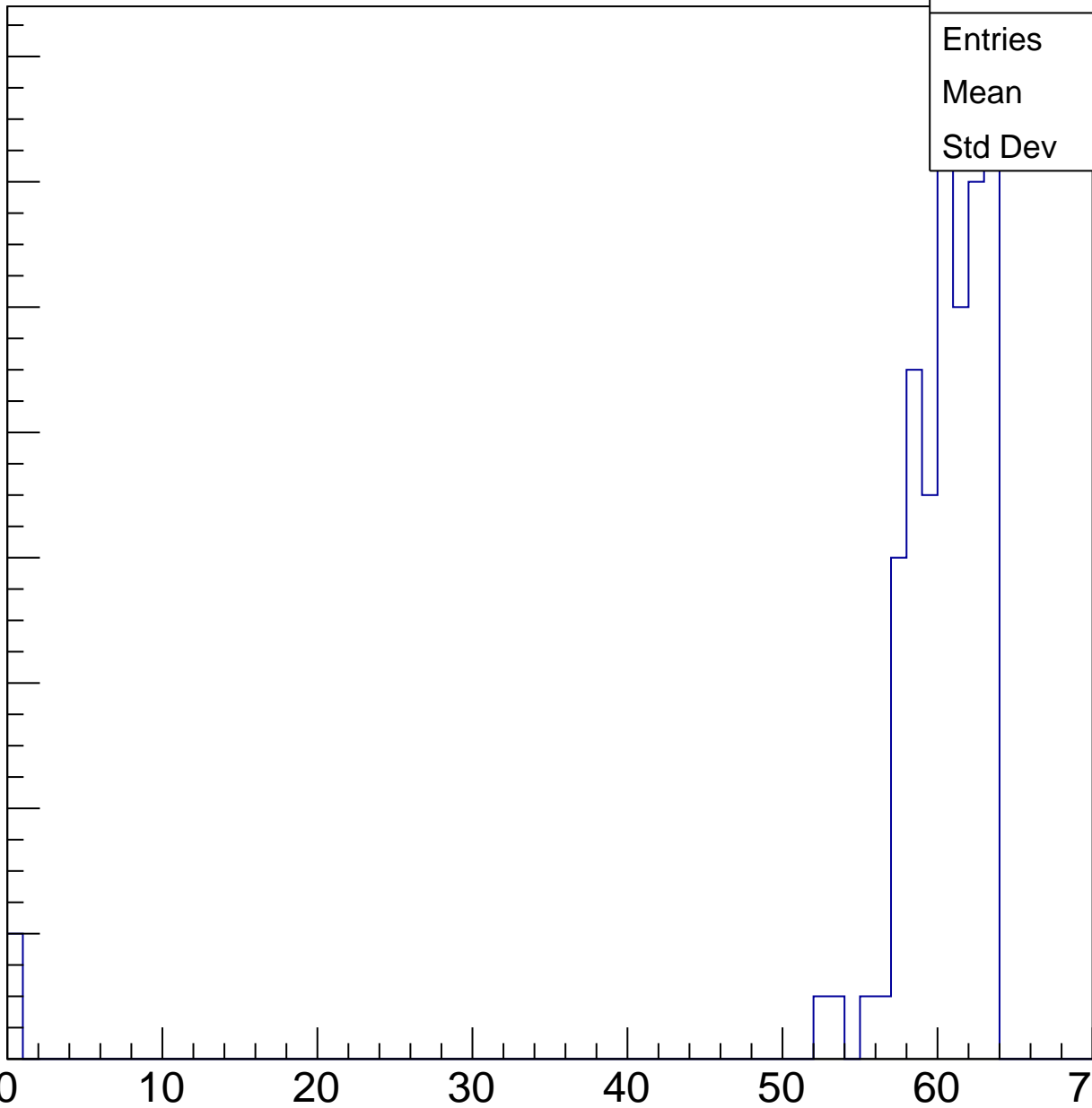
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 58.75 |
| Std Dev | 9.102 |

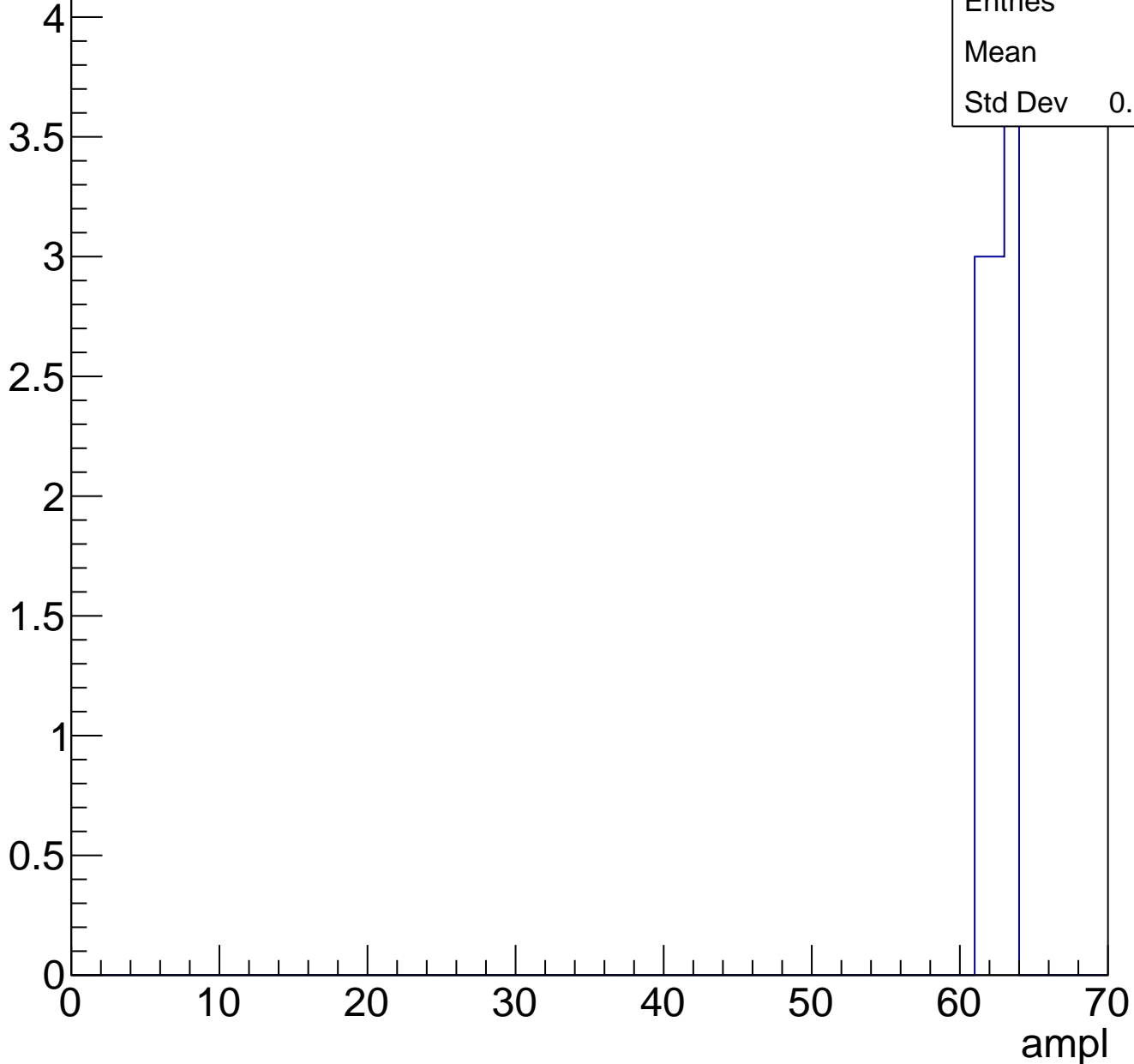
ampl



B1L001S, U26-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

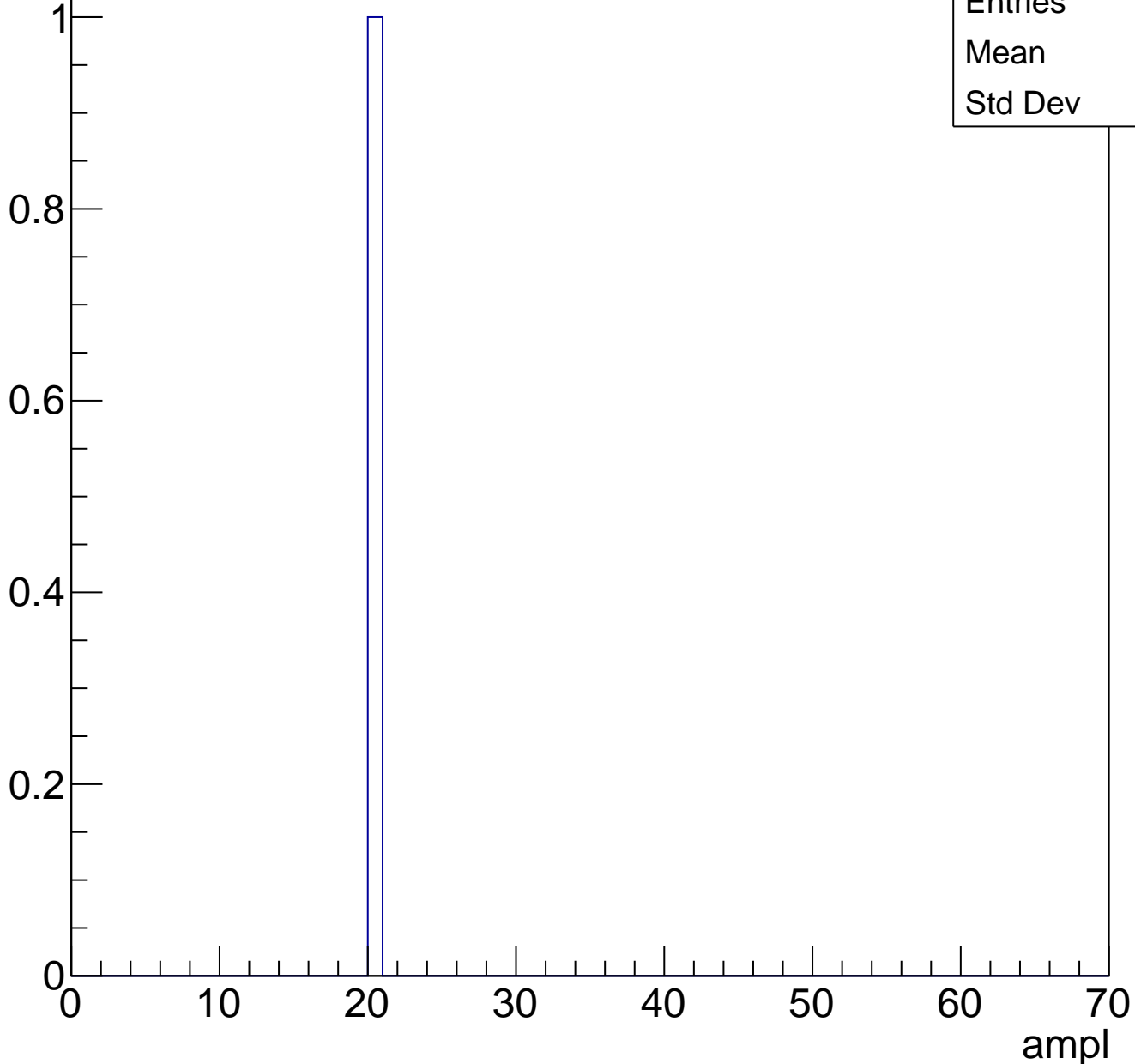
Entry



B1L001S, U26-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

B1L001S, U26-ch59, adc0

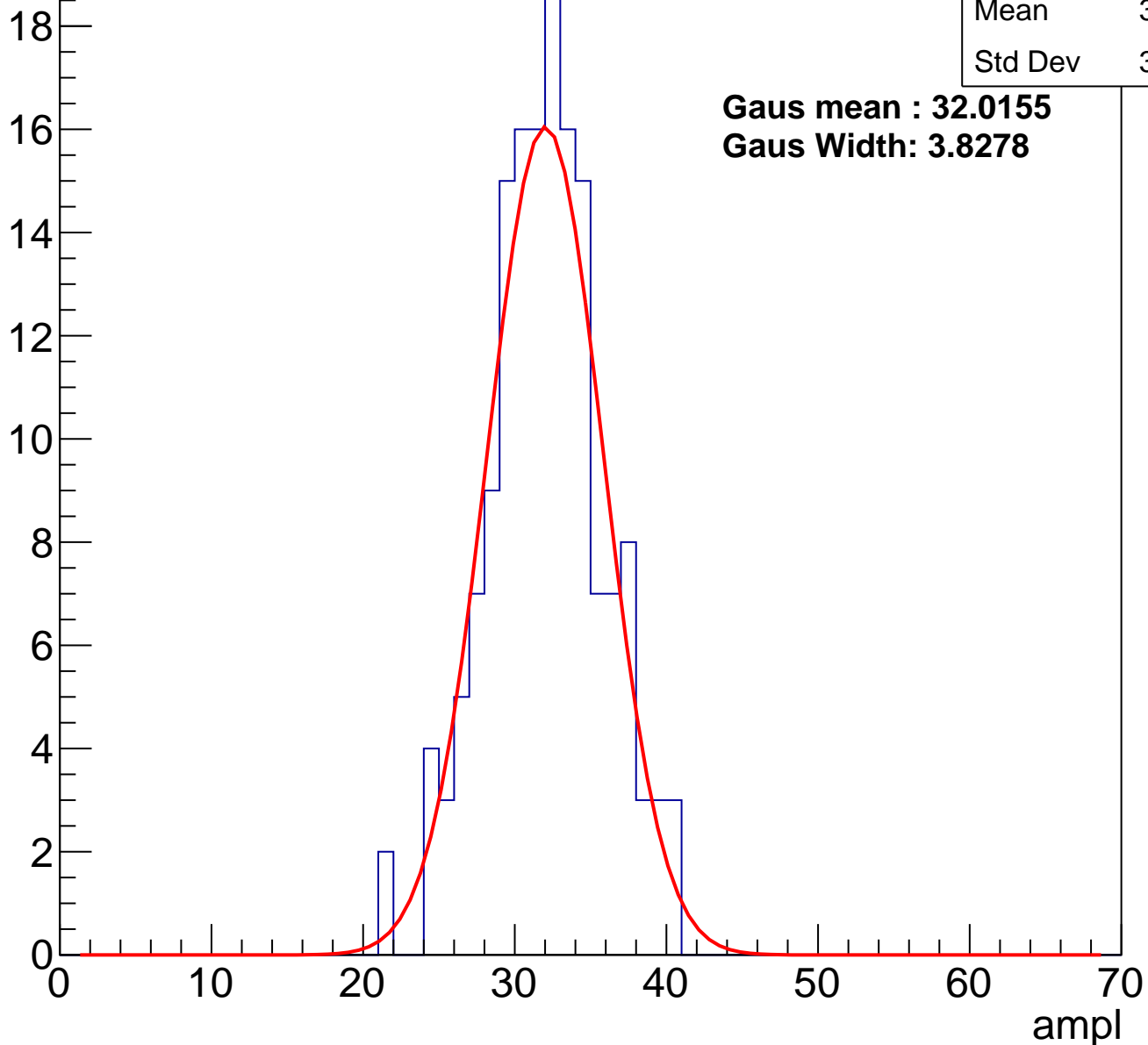
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 31.55 |
| Std Dev | 3.753 |

Gaus mean : 32.0155

Gaus Width: 3.8278

Entry



B1L001S, U26-ch59, adc1

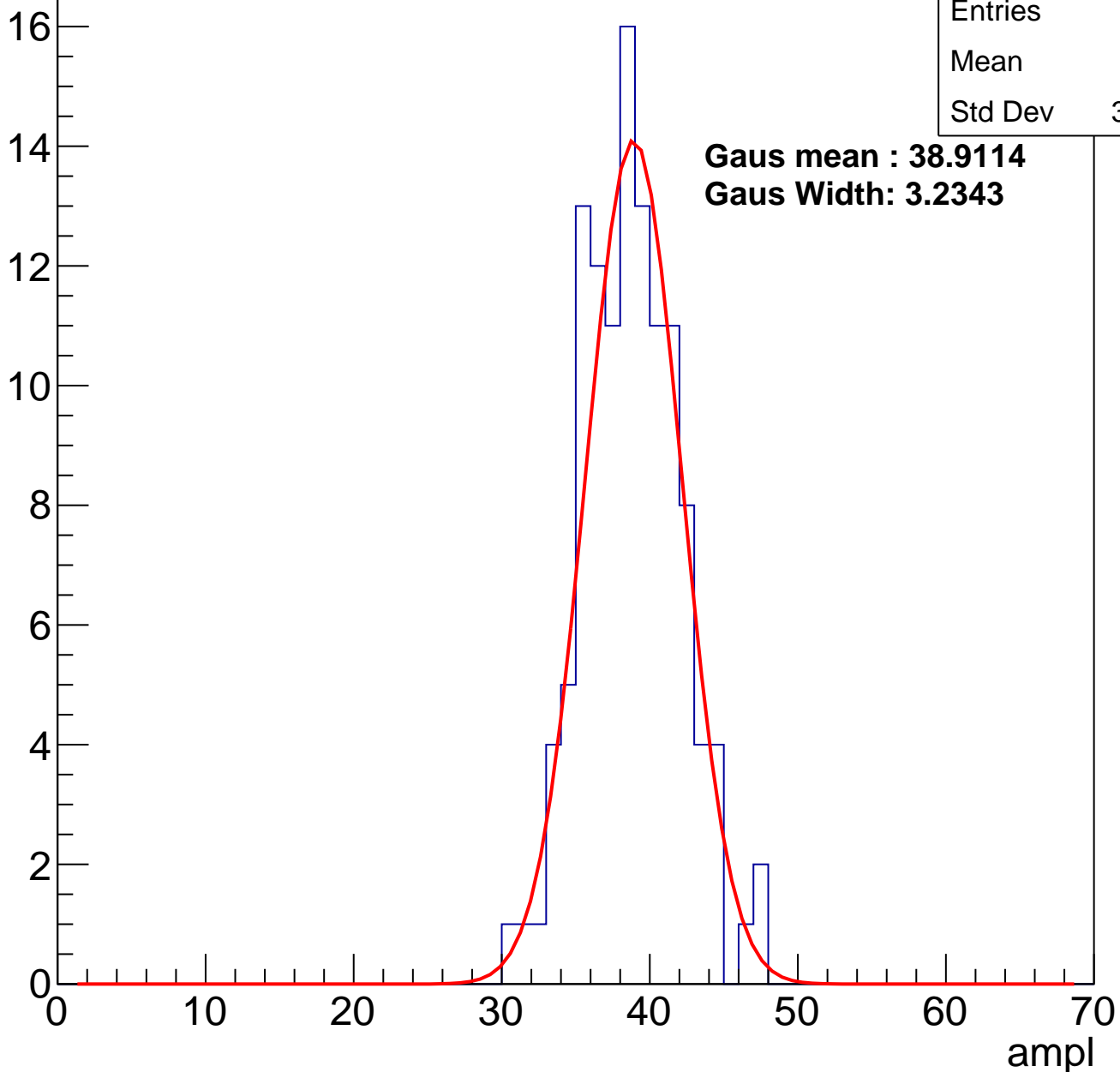
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 38.3 |
| Std Dev | 3.253 |

Gaus mean : 38.9114

Gaus Width: 3.2343

Entry



B1L001S, U26-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 44.47 |
| Std Dev | 3.067 |

Gaus mean : 44.9253

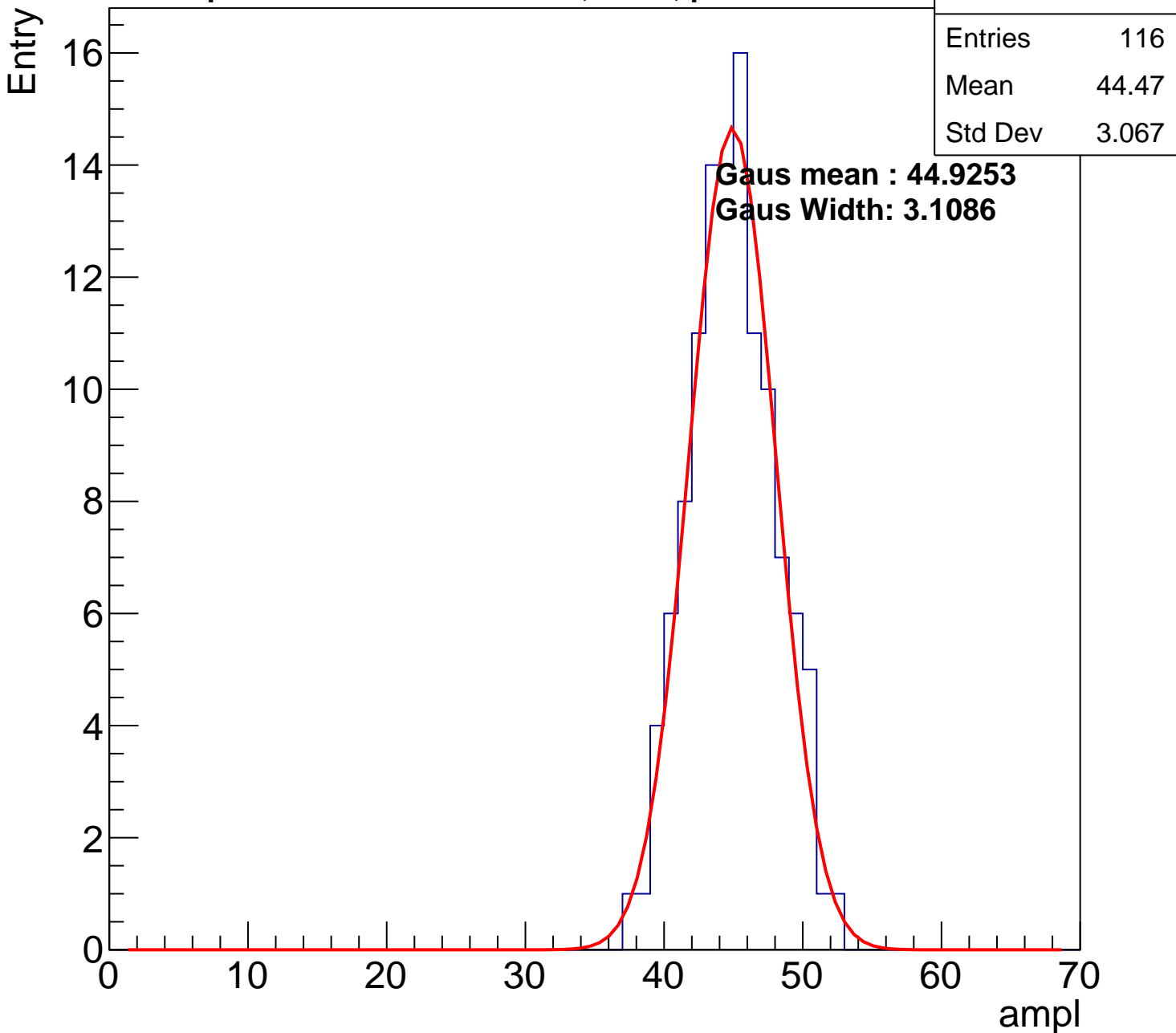
Gaus Width: 3.1086

Entry

16
14
12
10
8
6
4
2
0

ampl

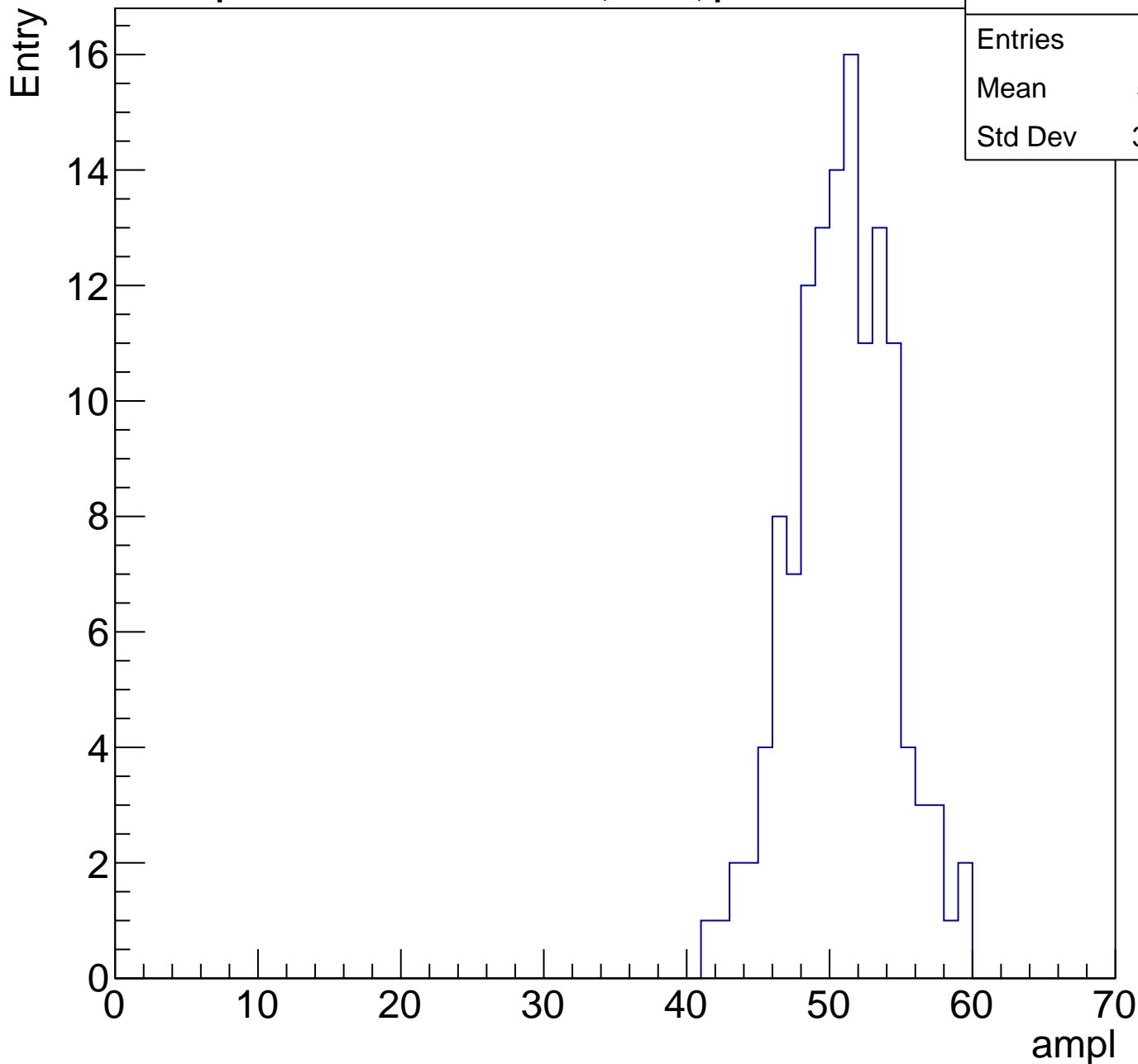
0 10 20 30 40 50 60 70



B1L001S, U26-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

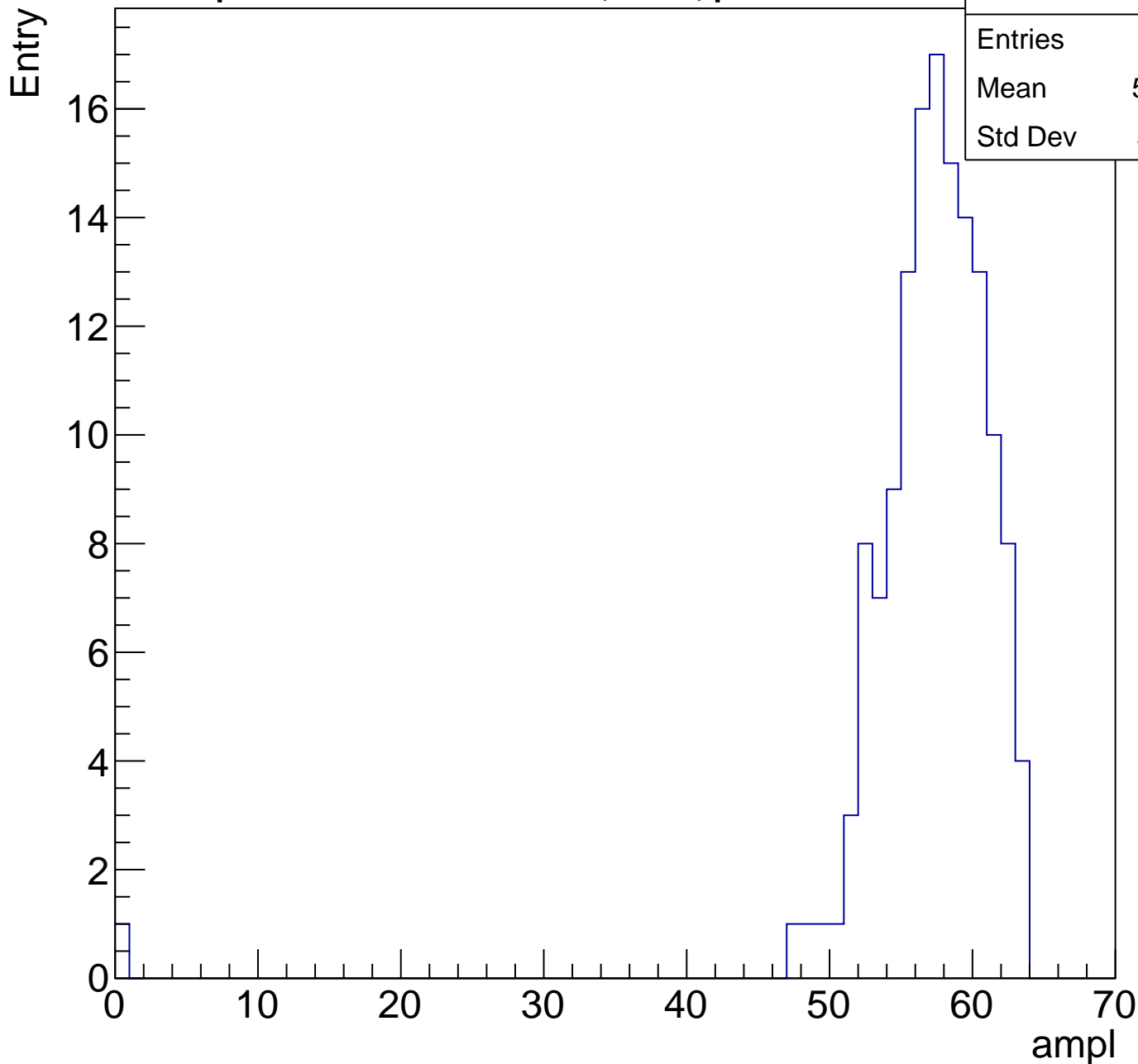
| | |
|---------|-------|
| Entries | 128 |
| Mean | 50.41 |
| Std Dev | 3.512 |



B1L001S, U26-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 56.58 |
| Std Dev | 5.801 |



B1L001S, U26-ch59, adc5

calib_packv5_042523_0143.root, FC#2, port C2

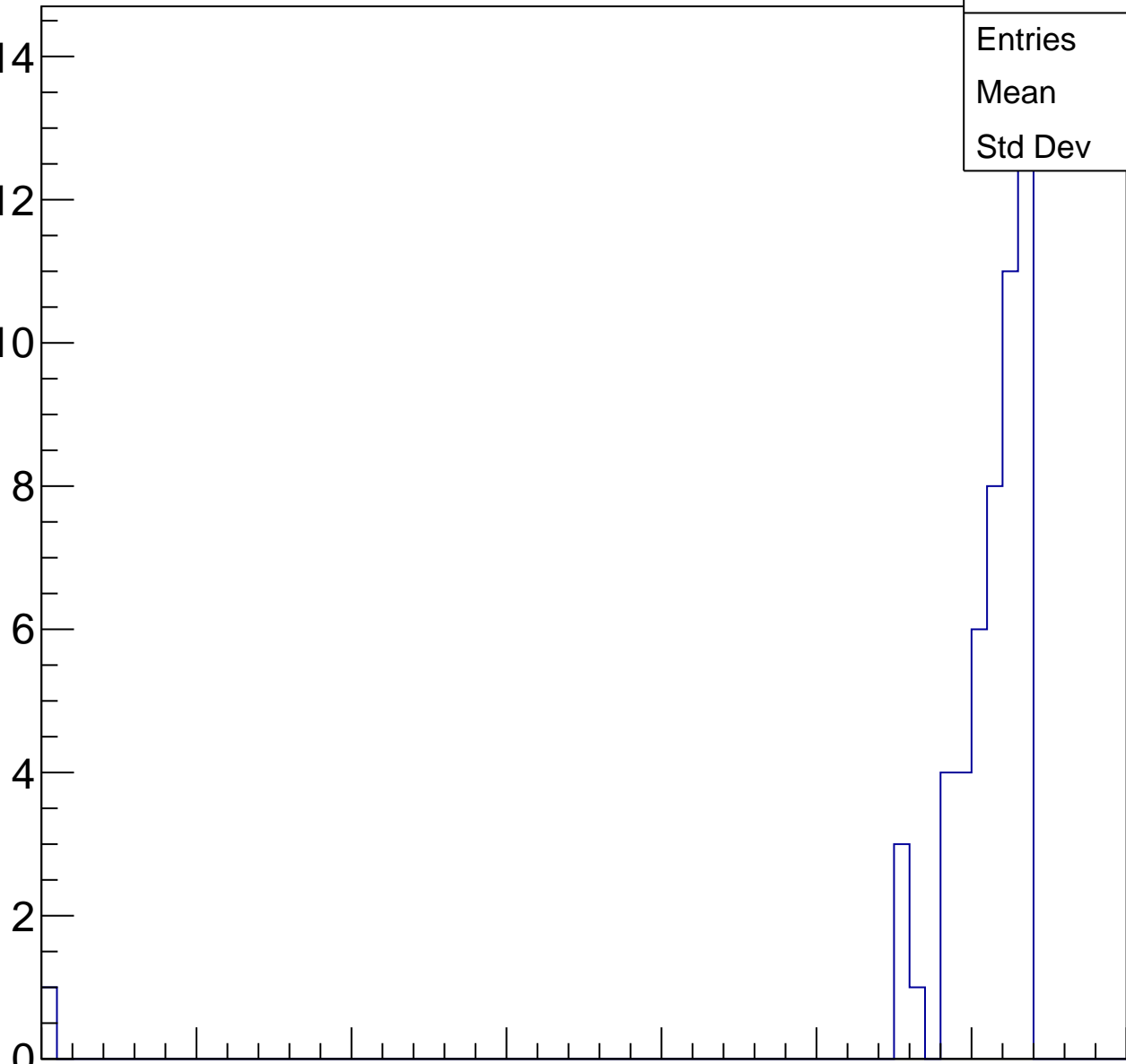
| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.63 |
| Std Dev | 8.64 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

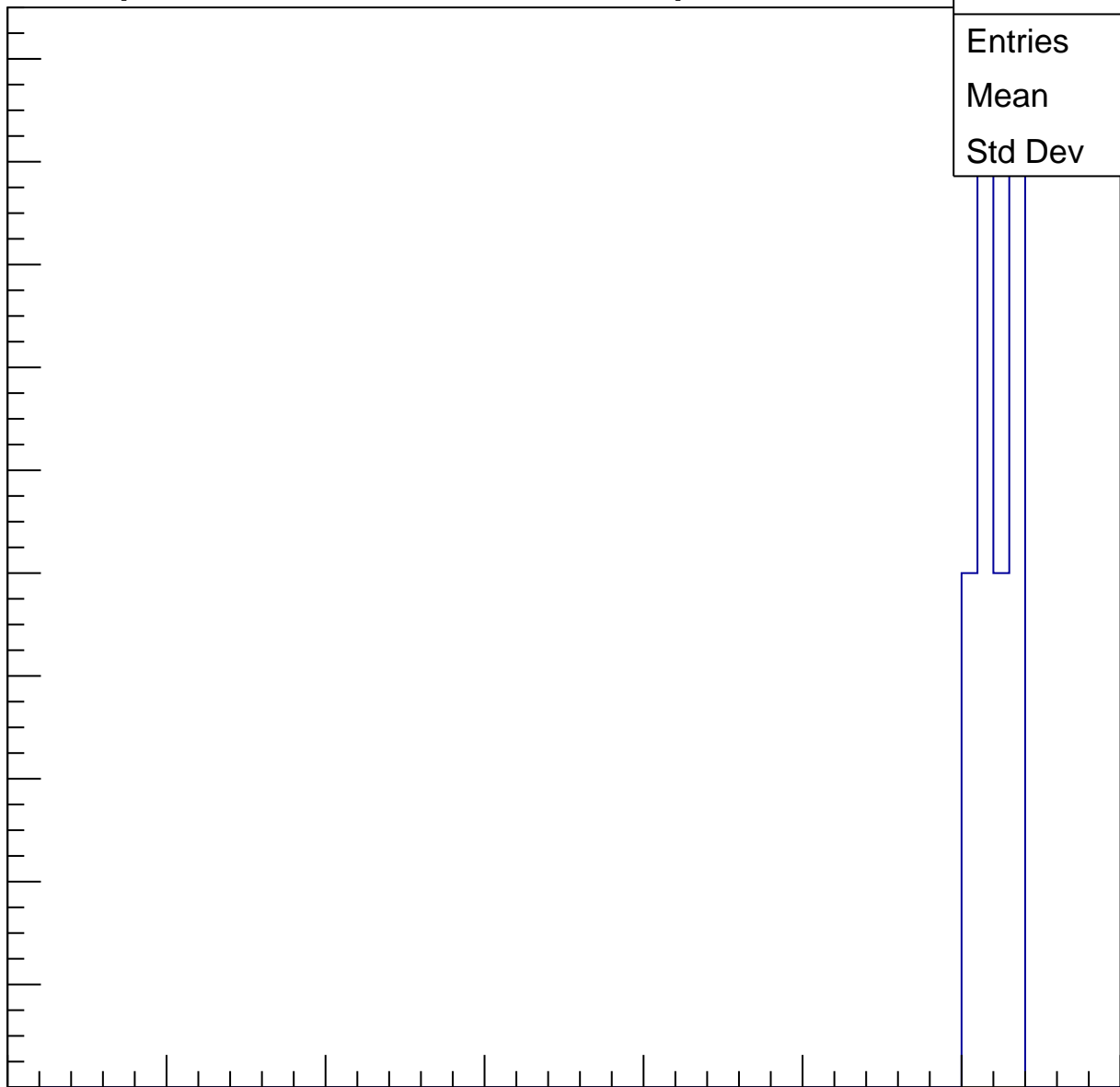
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.67 |
| Std Dev | 1.106 |

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch60, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 30.34 |
| Std Dev | 3.258 |

Gaus mean : 30.8560

Gaus Width: 3.3231

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

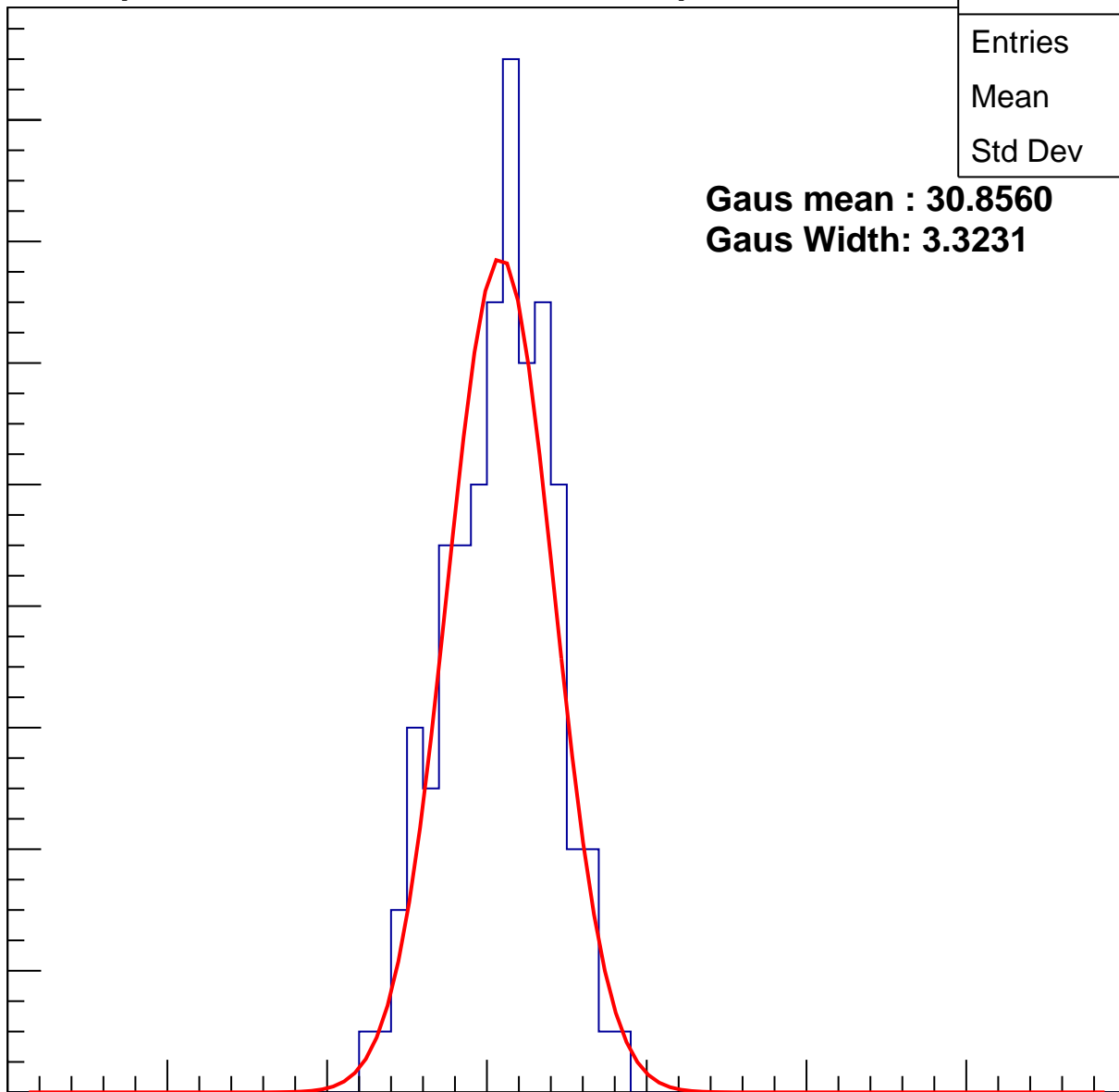
40

50

60

70

ampl



B1L001S, U26-ch60, adc1

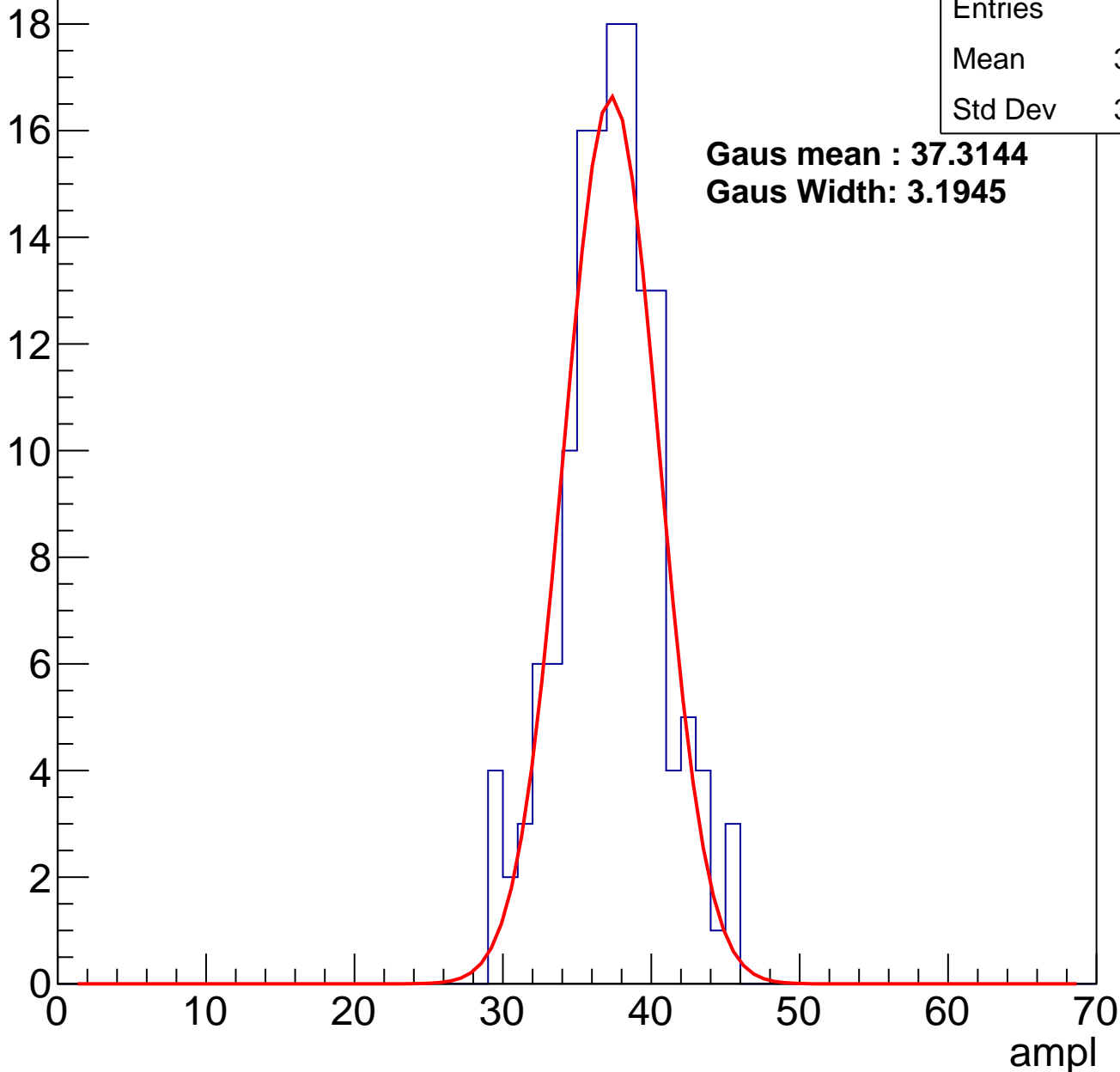
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 36.88 |
| Std Dev | 3.395 |

Gaus mean : 37.3144

Gaus Width: 3.1945

Entry



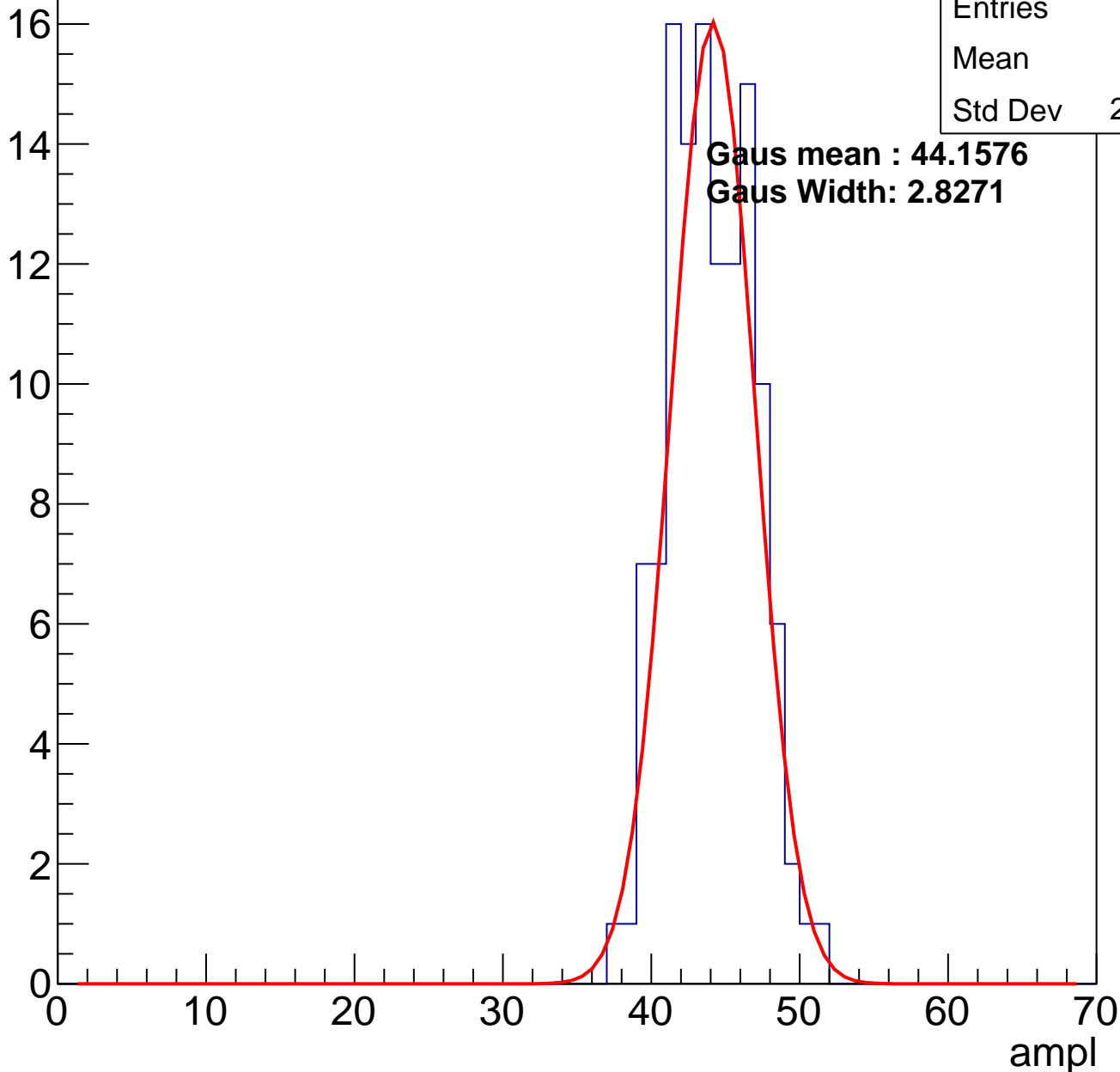
B1L001S, U26-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 43.6 |
| Std Dev | 2.818 |

Gaus mean : 44.1576
Gaus Width: 2.8271

Entry



B1L001S, U26-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 49.86 |
| Std Dev | 3.173 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

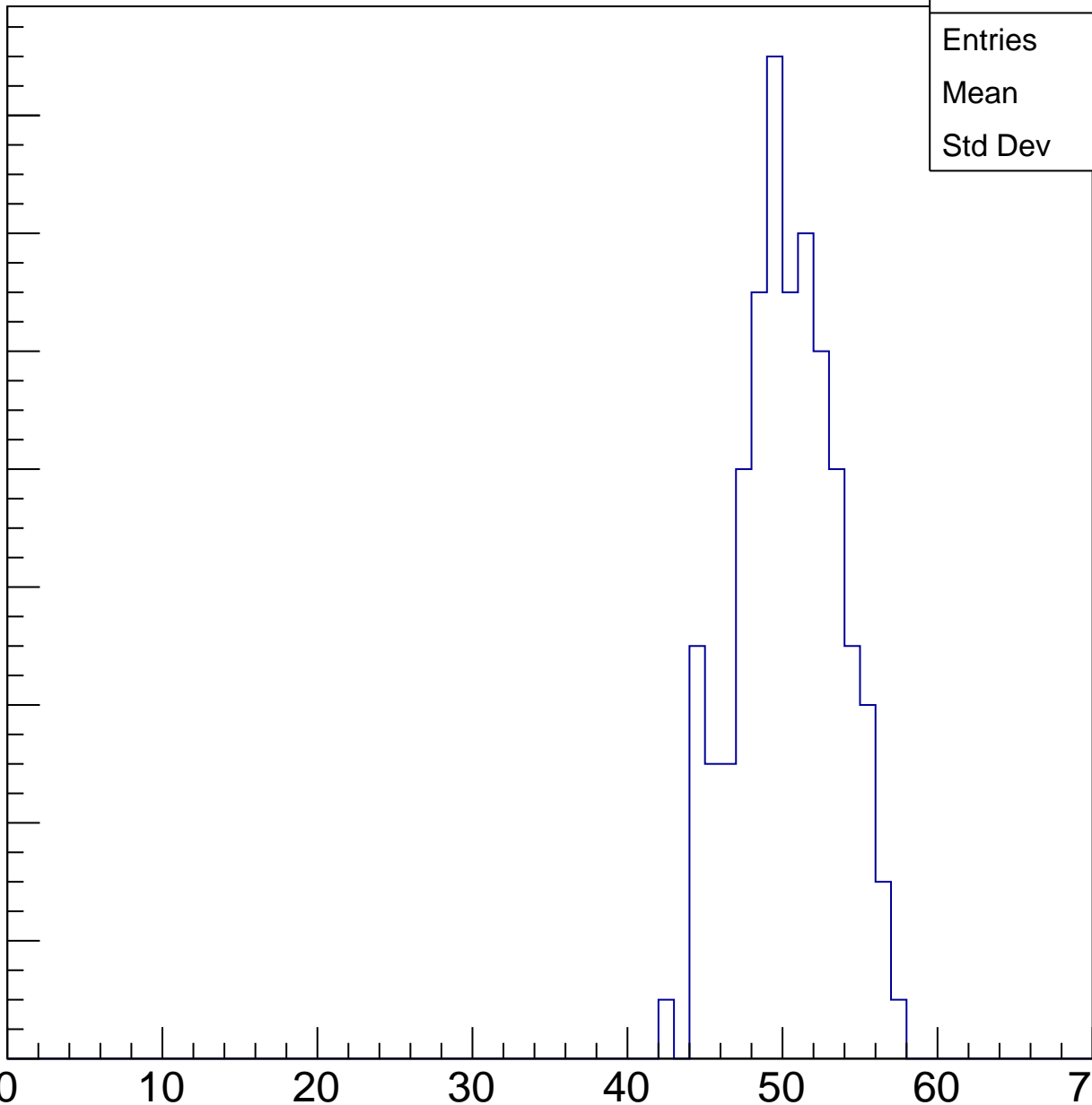
40

50

60

70

ampl



B1L001S, U26-ch60, adc4

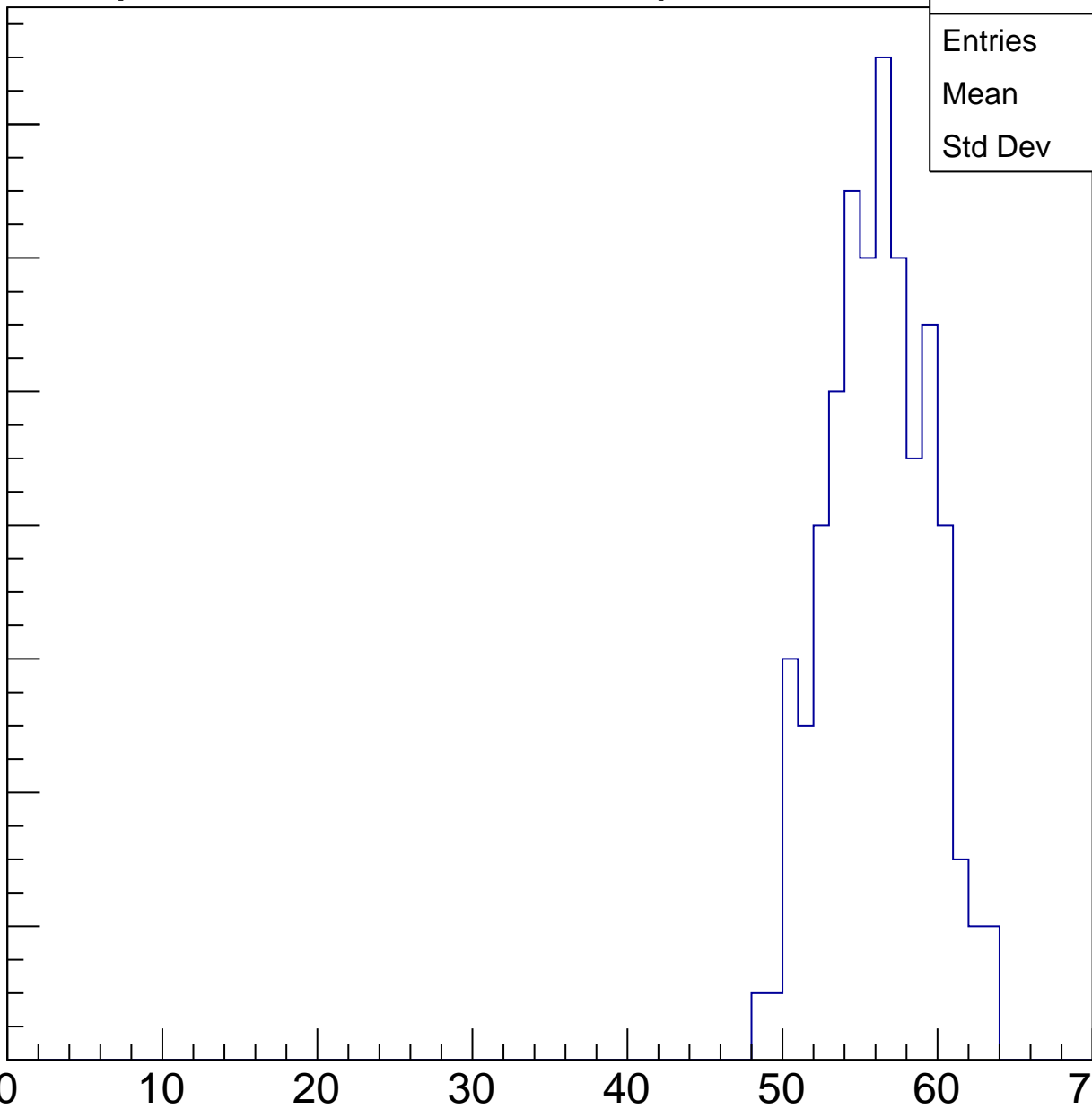
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 118 |
| Mean | 55.66 |
| Std Dev | 3.245 |

ampl



B1L001S, U26-ch60, adc5

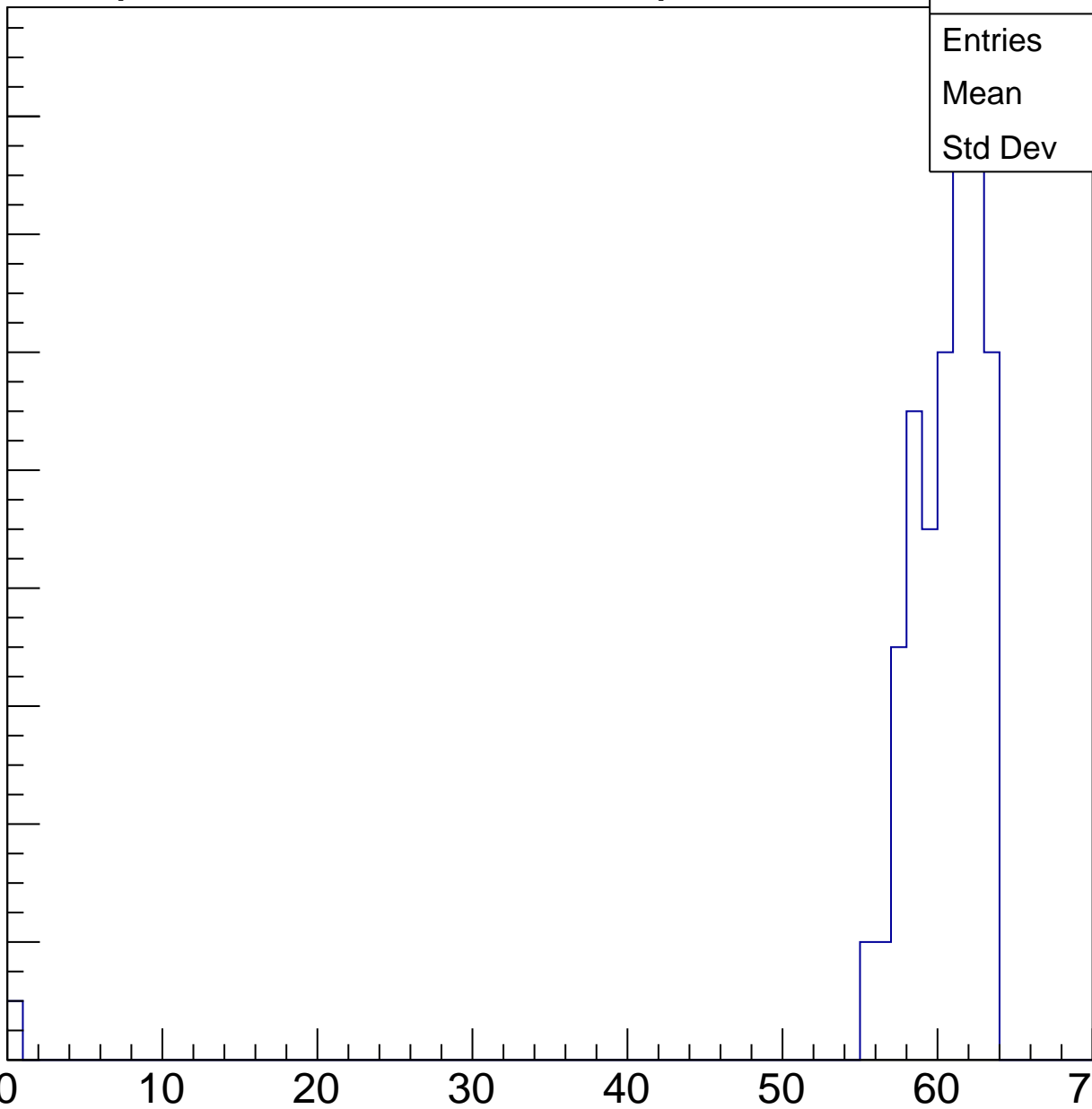
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 90 |
| Mean | 59.52 |
| Std Dev | 6.642 |

ampl

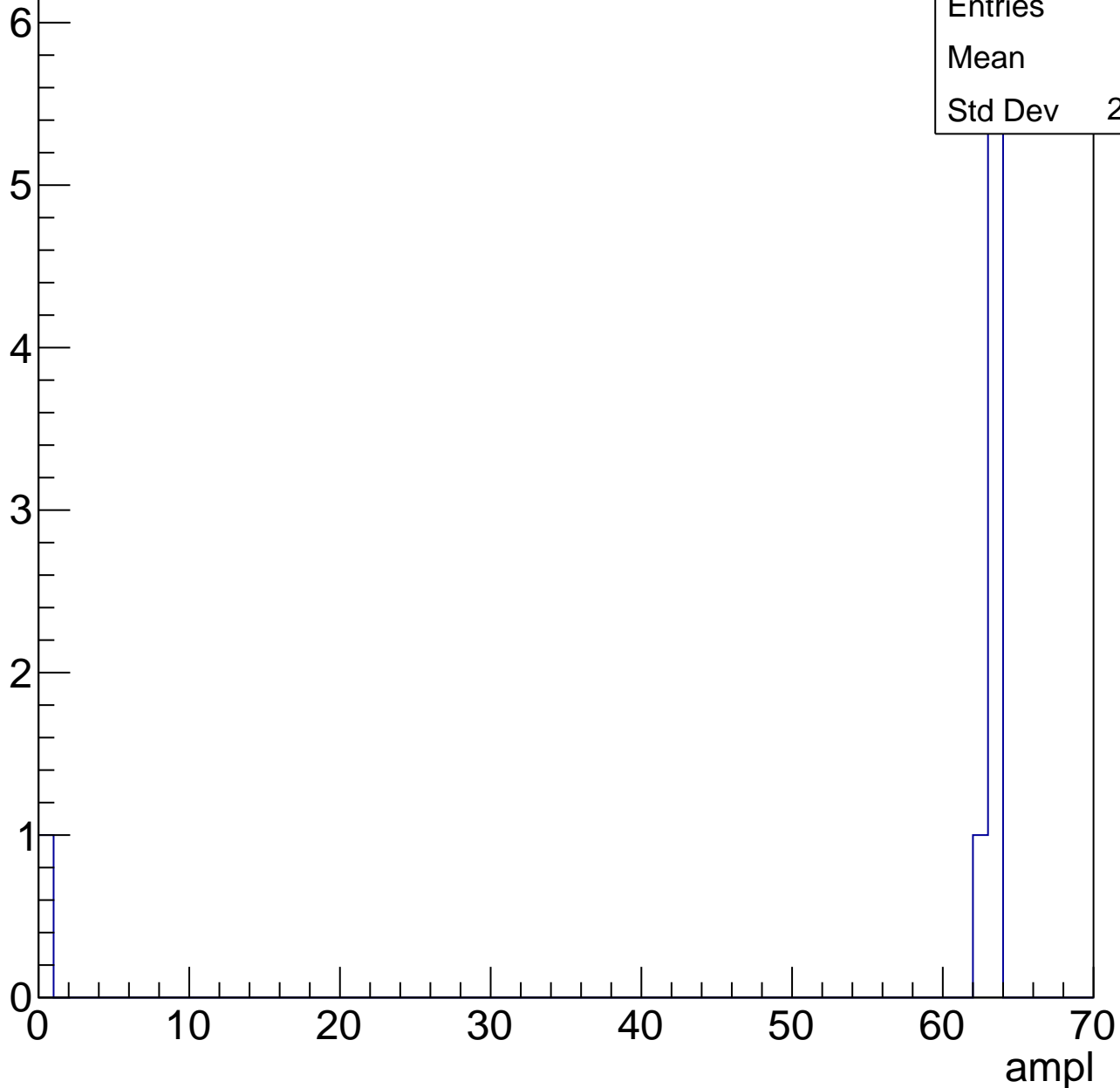


B1L001S, U26-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 55 |
| Std Dev | 20.79 |



B1L001S, U26-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



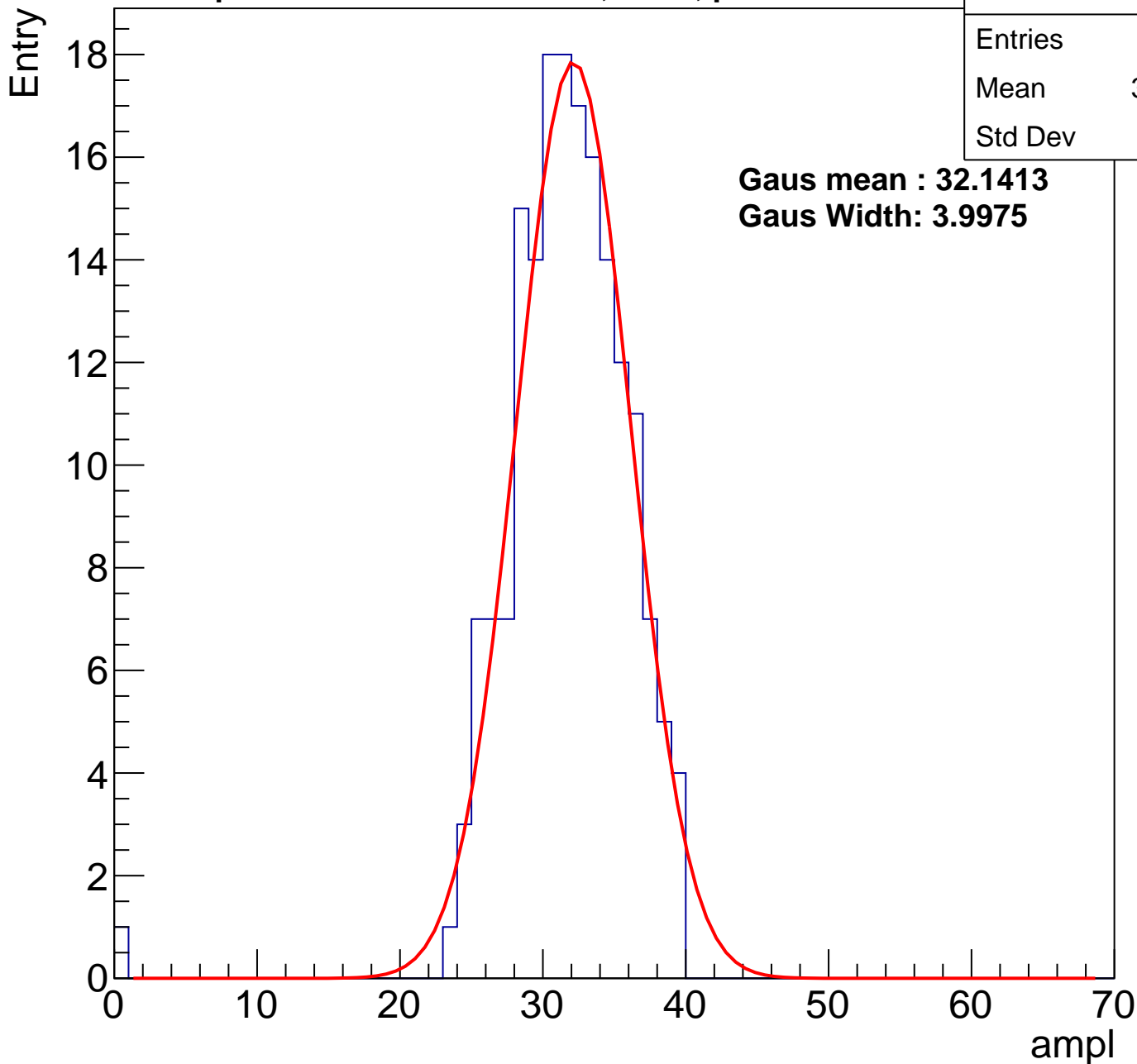
B1L001S, U26-ch61, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 177 |
| Mean | 31.27 |
| Std Dev | 4.34 |

Gaus mean : 32.1413

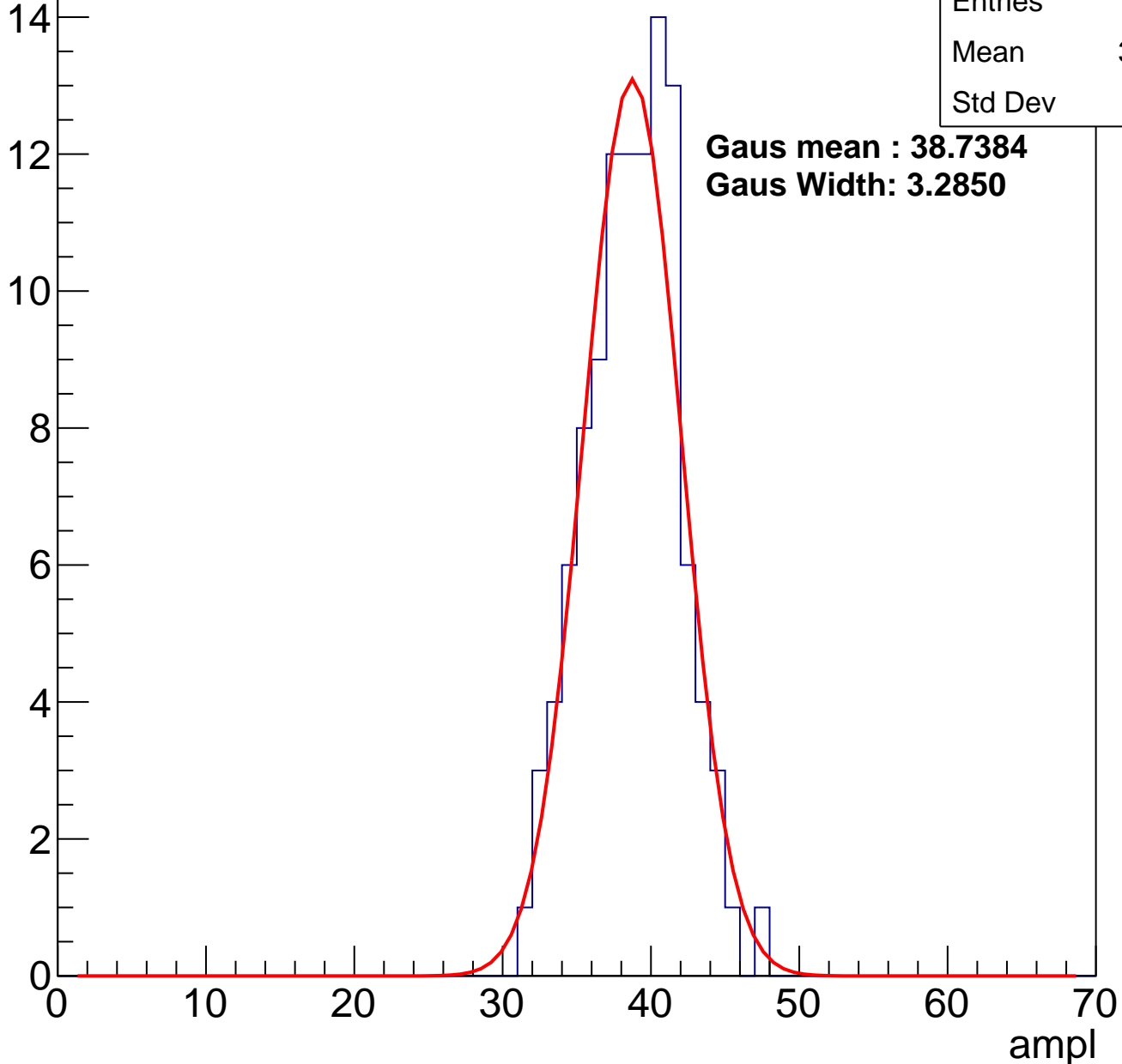
Gaus Width: 3.9975



B1L001S, U26-ch61, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch61, adc2

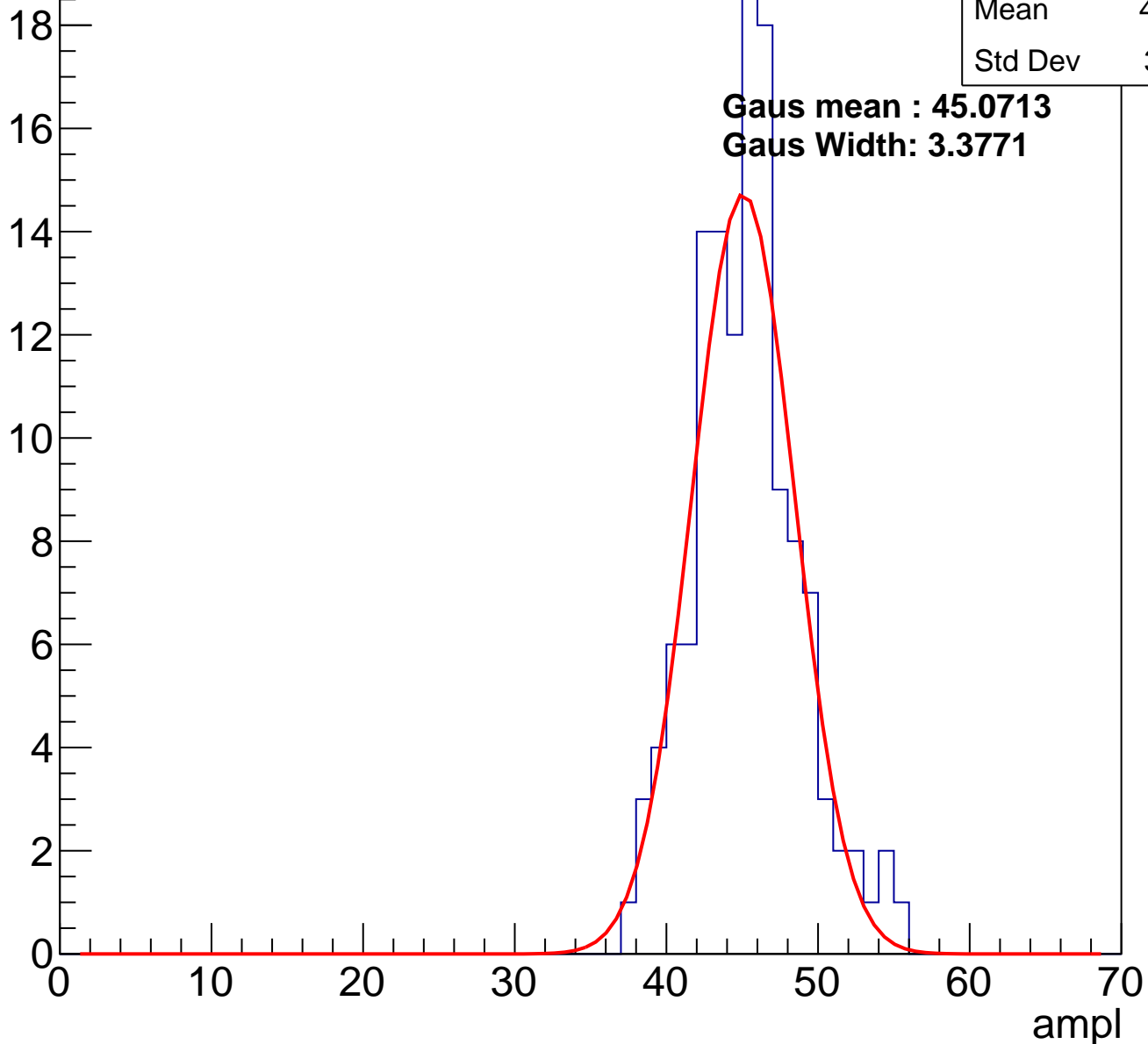
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 44.82 |
| Std Dev | 3.481 |

Gaus mean : 45.0713

Gaus Width: 3.3771

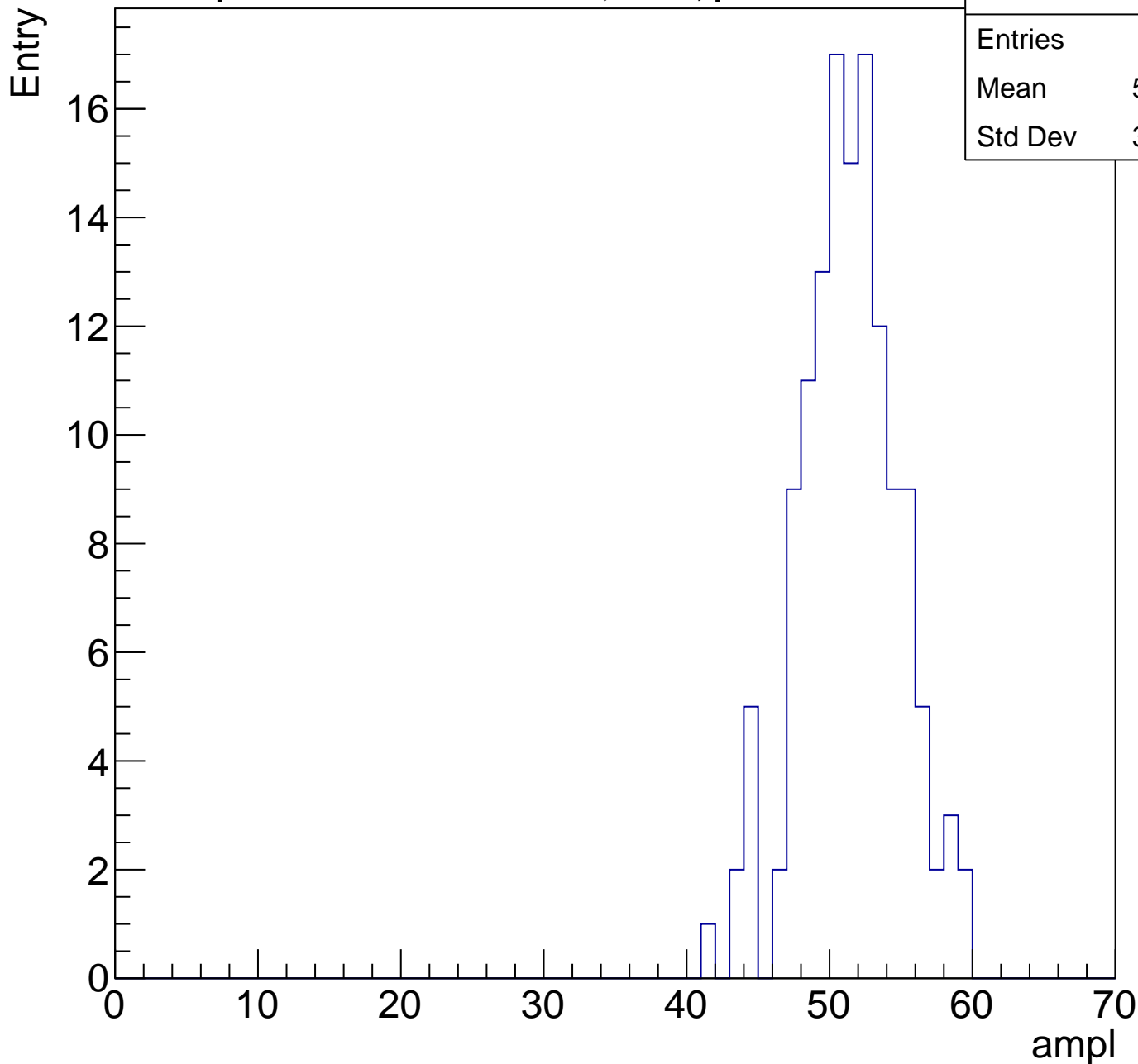
Entry



B1L001S, U26-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

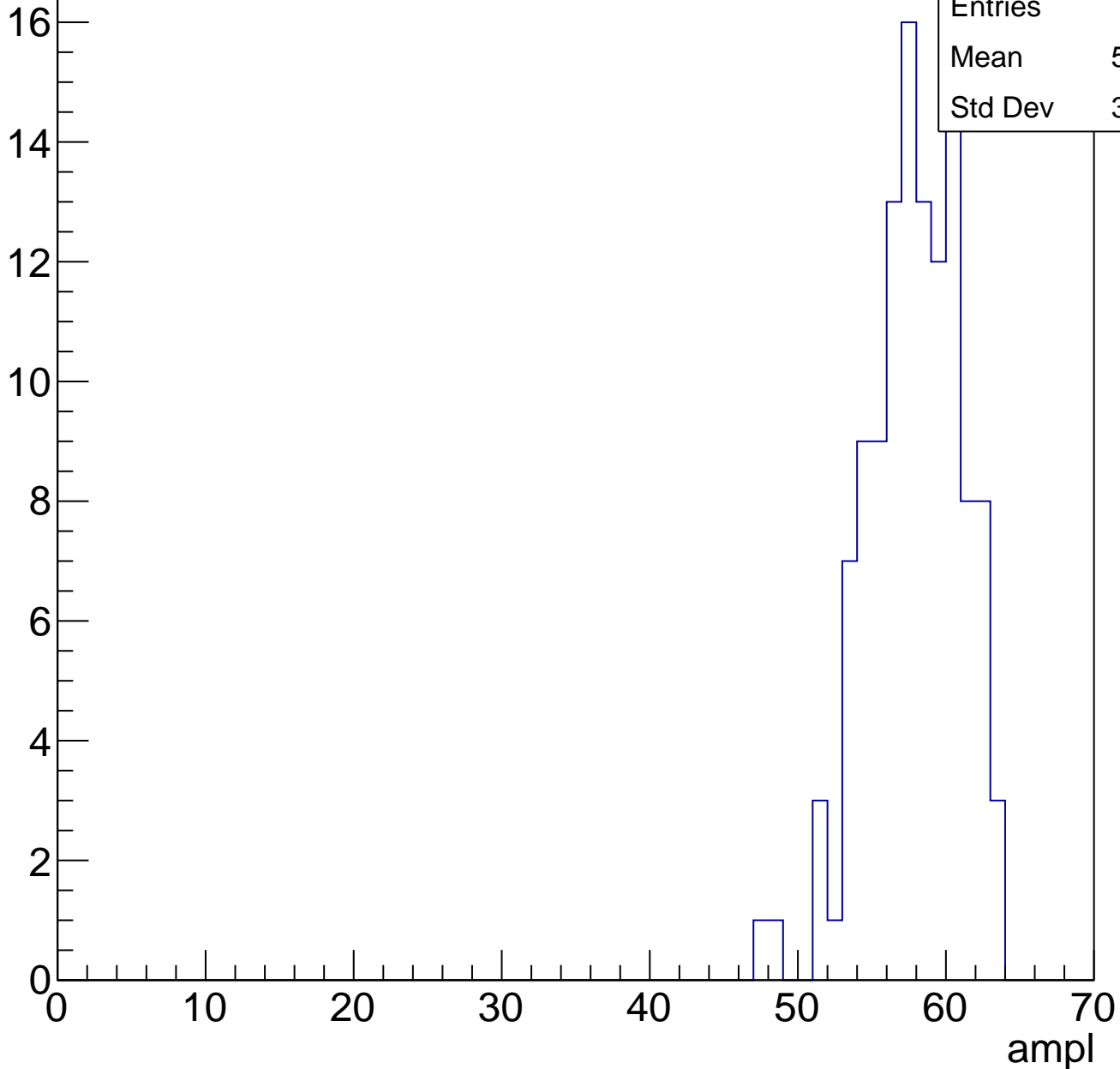
| | |
|---------|-------|
| Entries | 134 |
| Mean | 50.96 |
| Std Dev | 3.452 |



B1L001S, U26-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch61, adc5

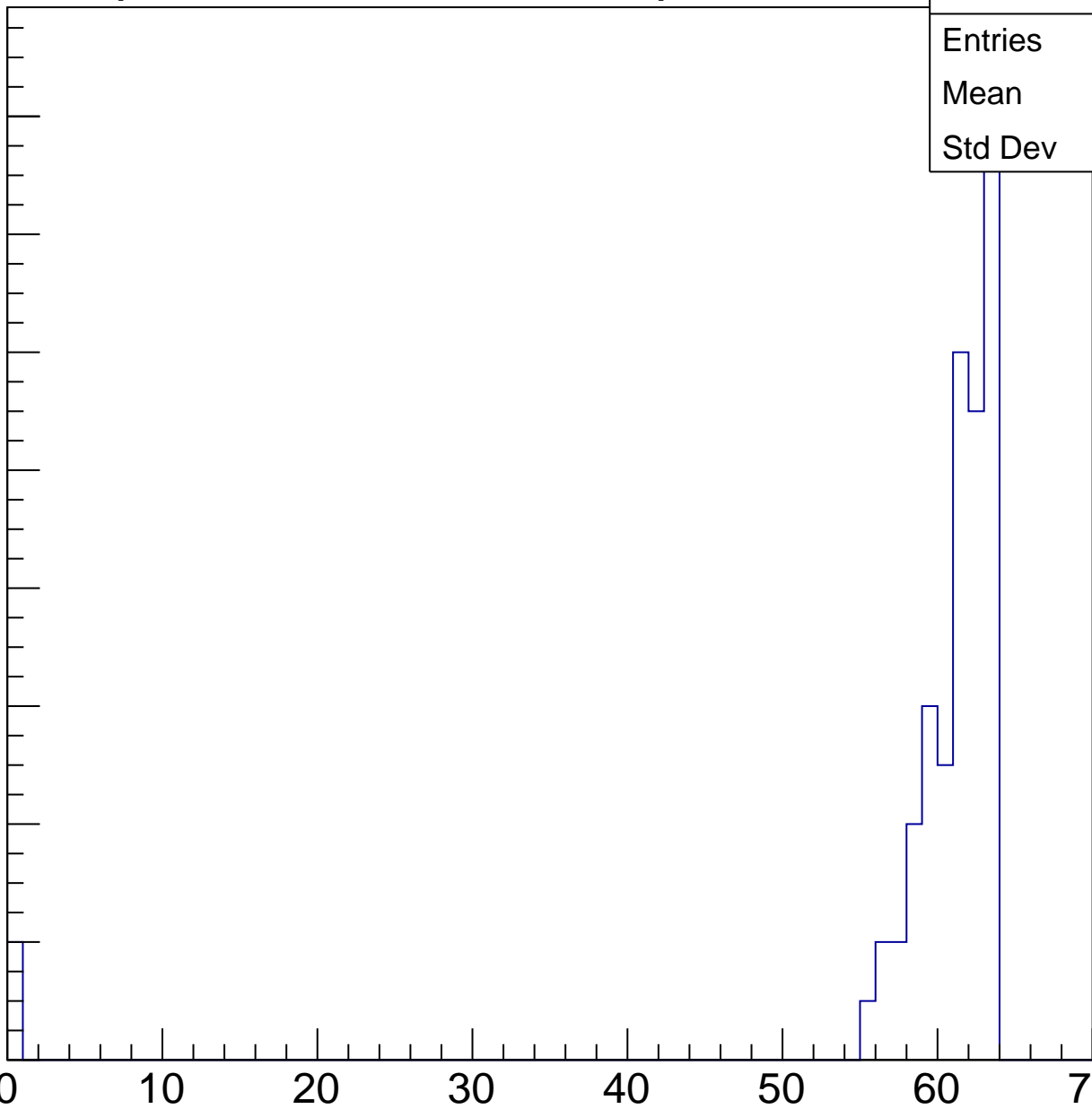
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 58.9 |
| Std Dev | 10.95 |

ampl



B1L001S, U26-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch62, adc0

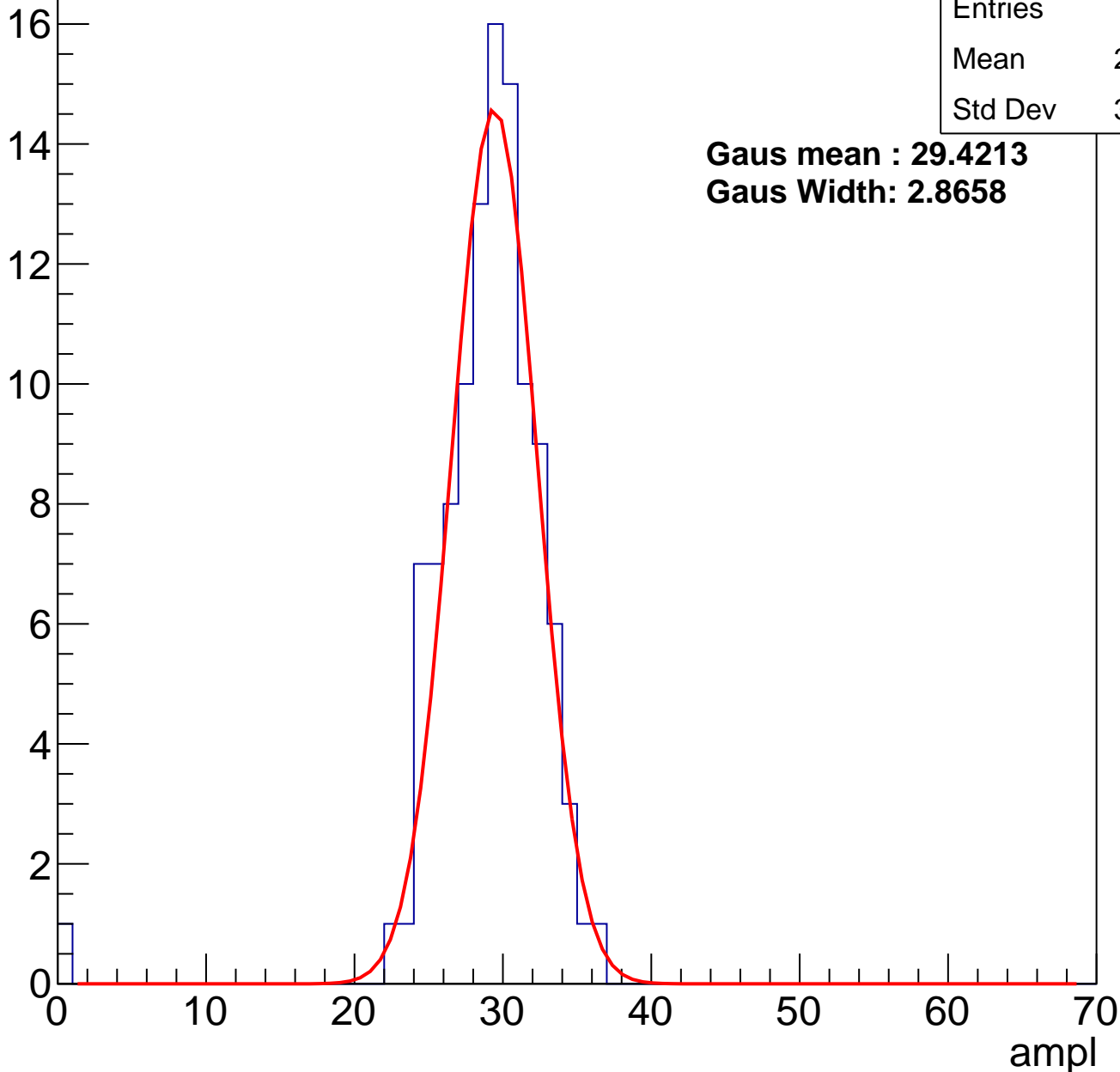
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 109 |
| Mean | 28.56 |
| Std Dev | 3.959 |

Gaus mean : 29.4213

Gaus Width: 2.8658

Entry



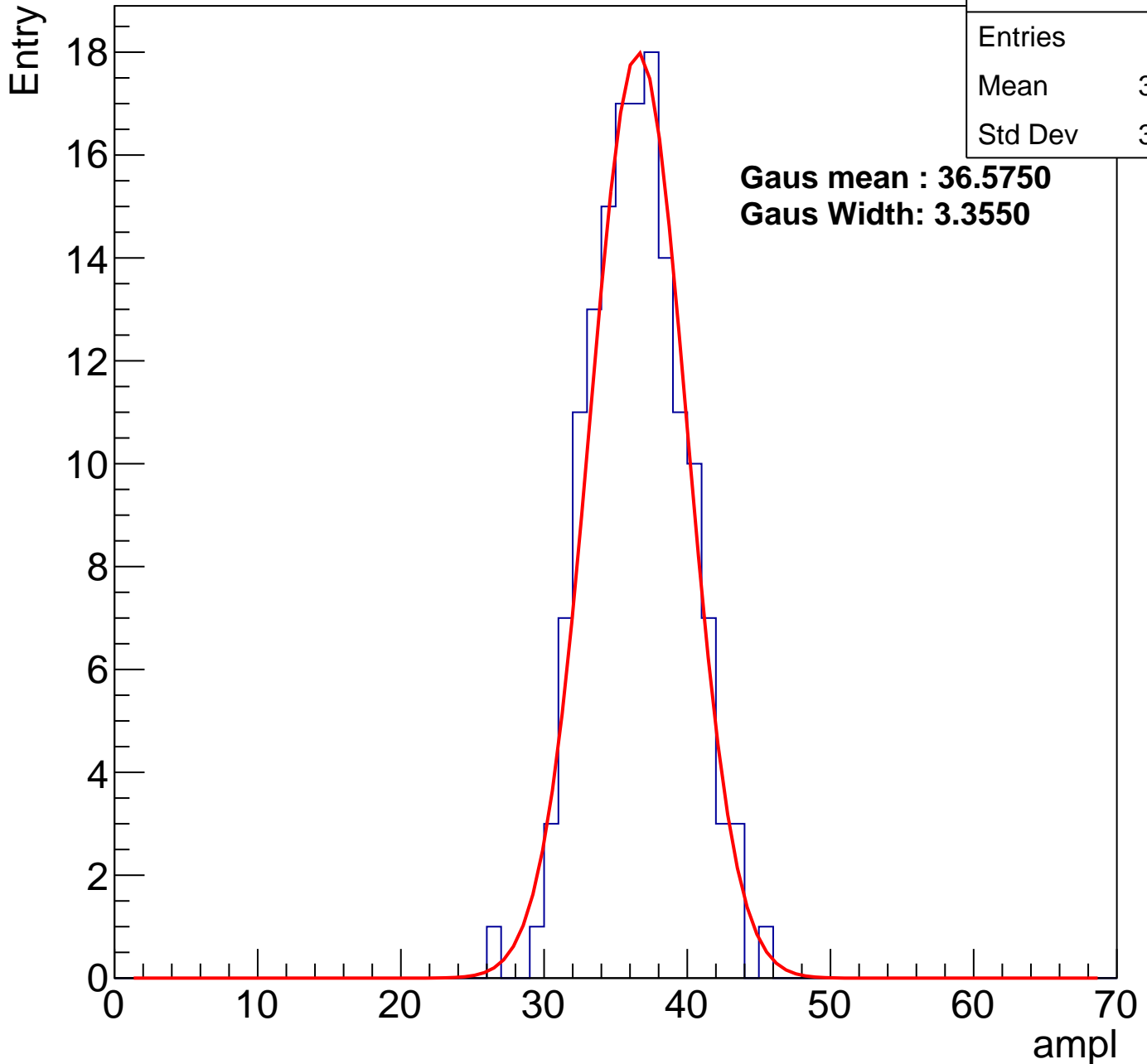
B1L001S, U26-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 36.01 |
| Std Dev | 3.291 |

Gaus mean : 36.5750

Gaus Width: 3.3550



B1L001S, U26-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 42.46 |
| Std Dev | 3.447 |

Gaus mean : 43.2151

Gaus Width: 3.3707

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

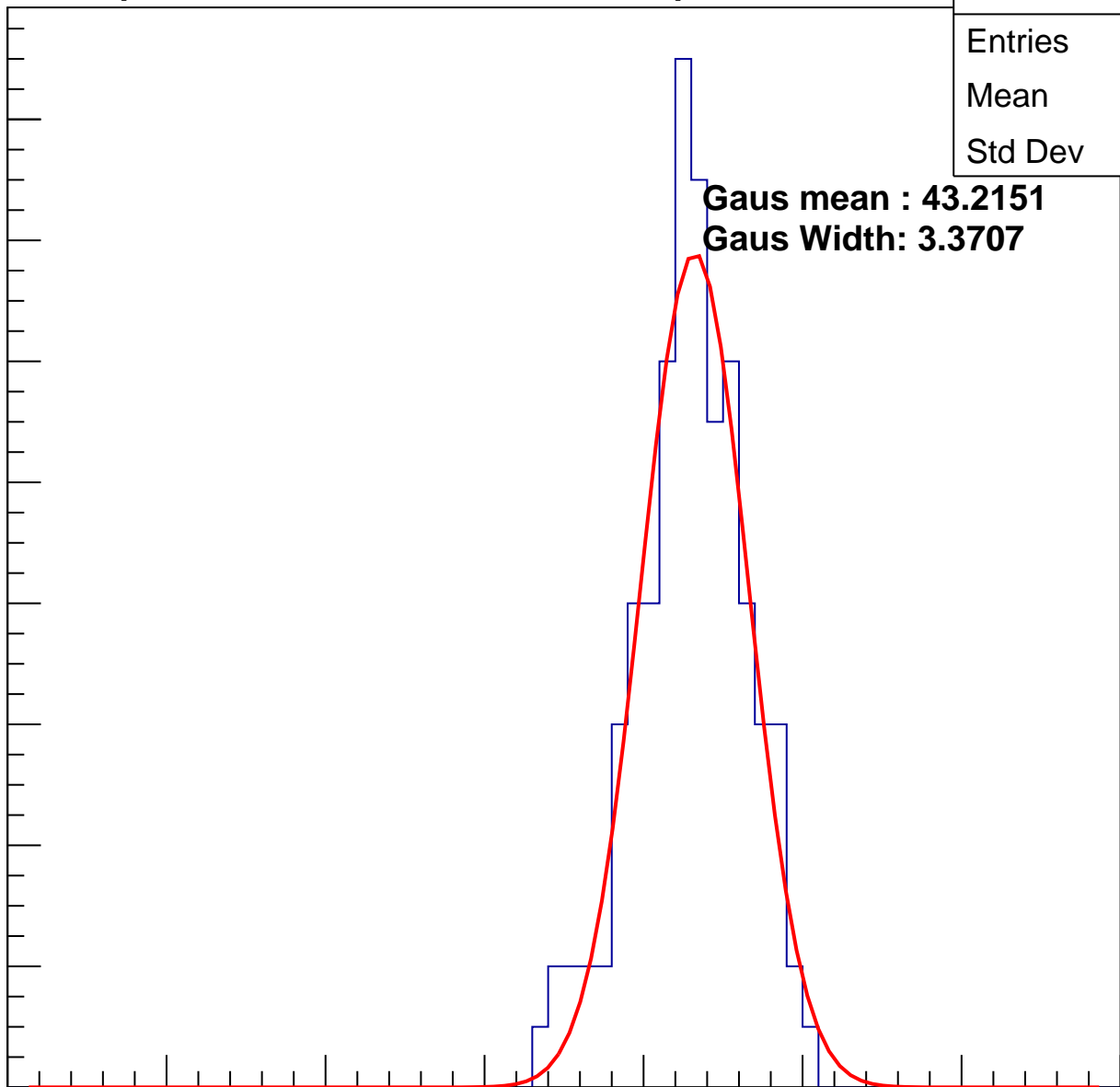
40

50

60

70

ampl



B1L001S, U26-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 48.85 |
| Std Dev | 3.637 |

Entry

12

10

8

6

4

2

0

0

10

20

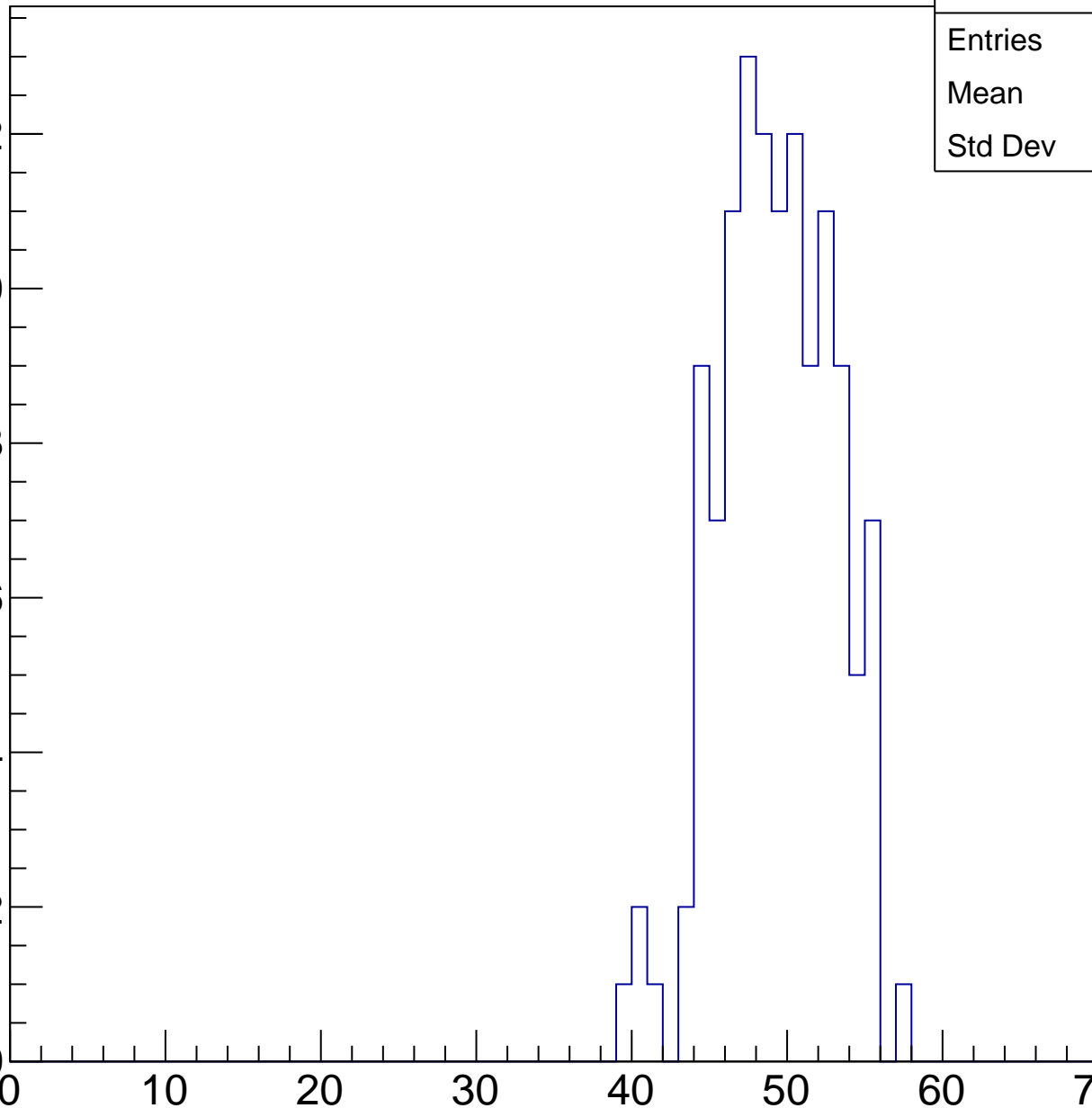
30

40

50

60

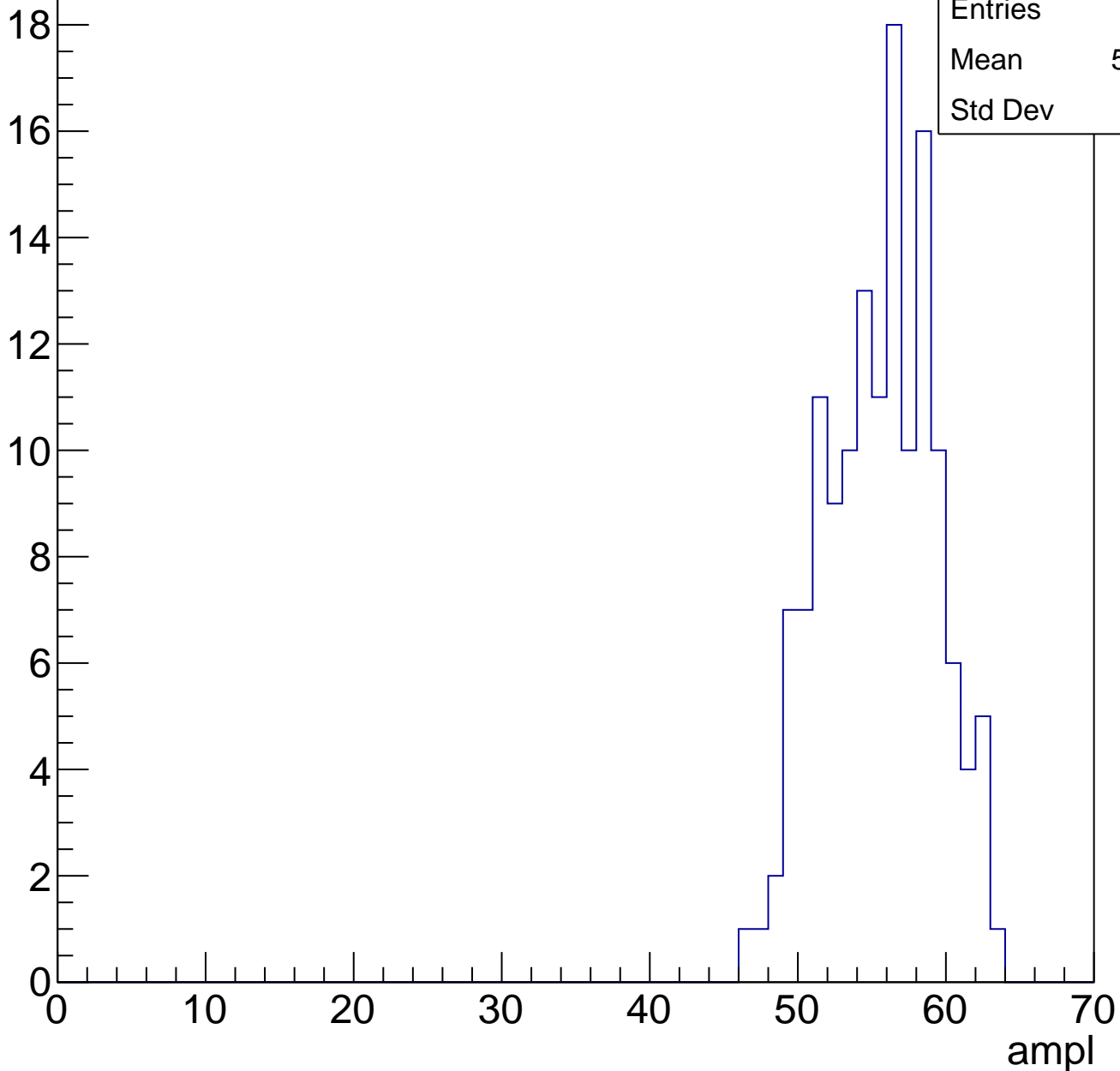
ampl



B1L001S, U26-ch62, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch62, adc5

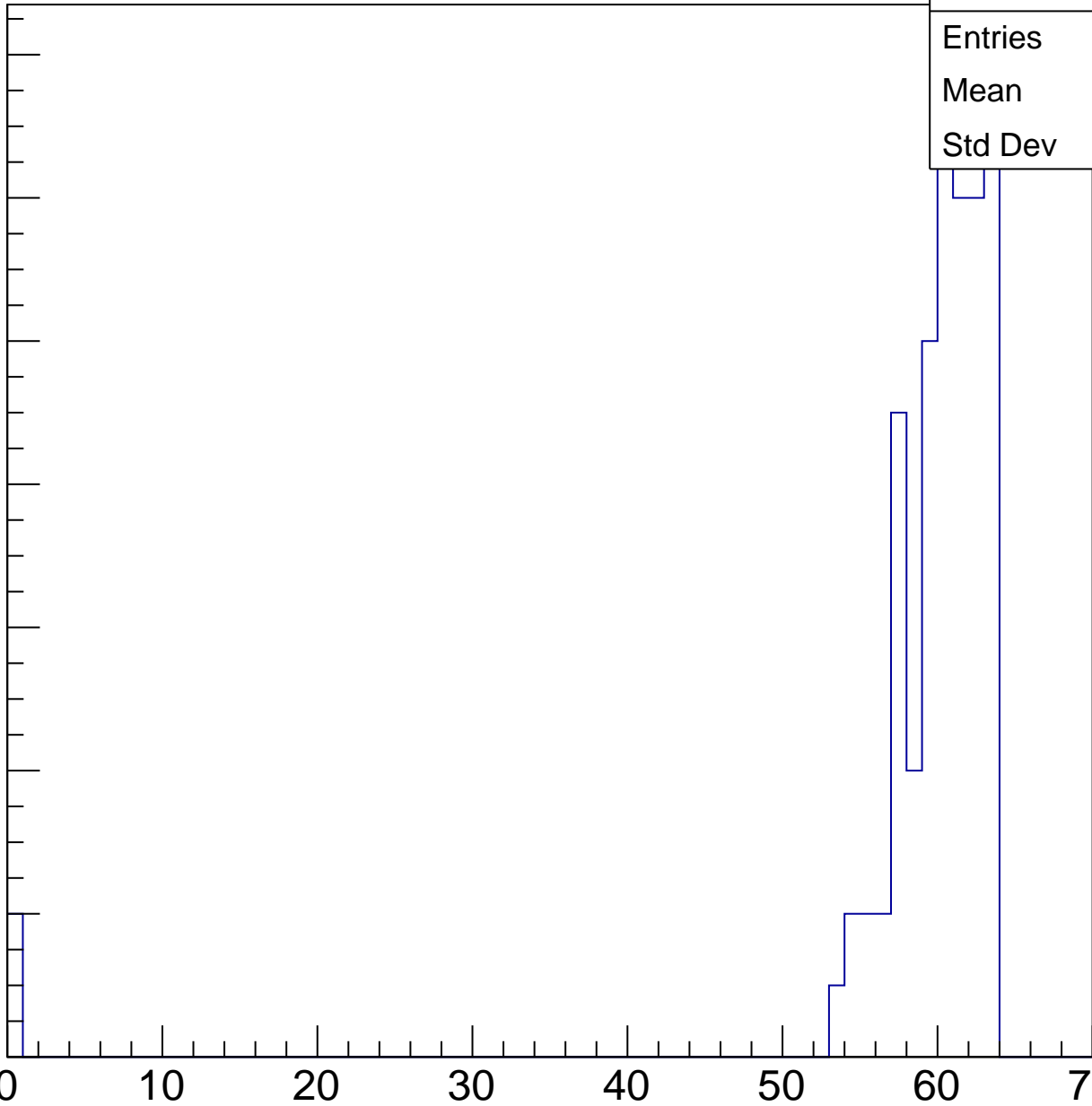
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 84 |
| Mean | 58.52 |
| Std Dev | 9.457 |

ampl

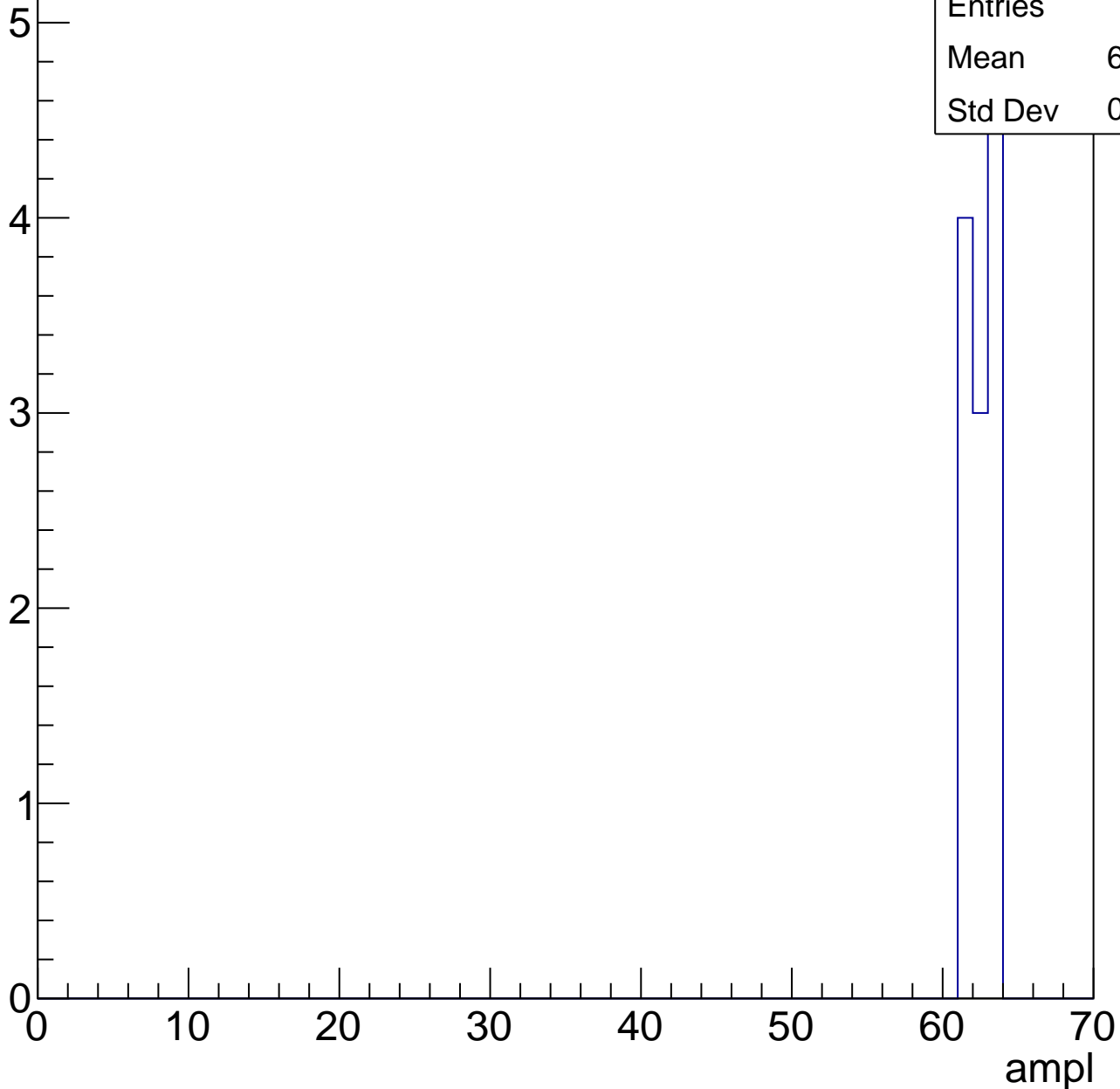


B1L001S, U26-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

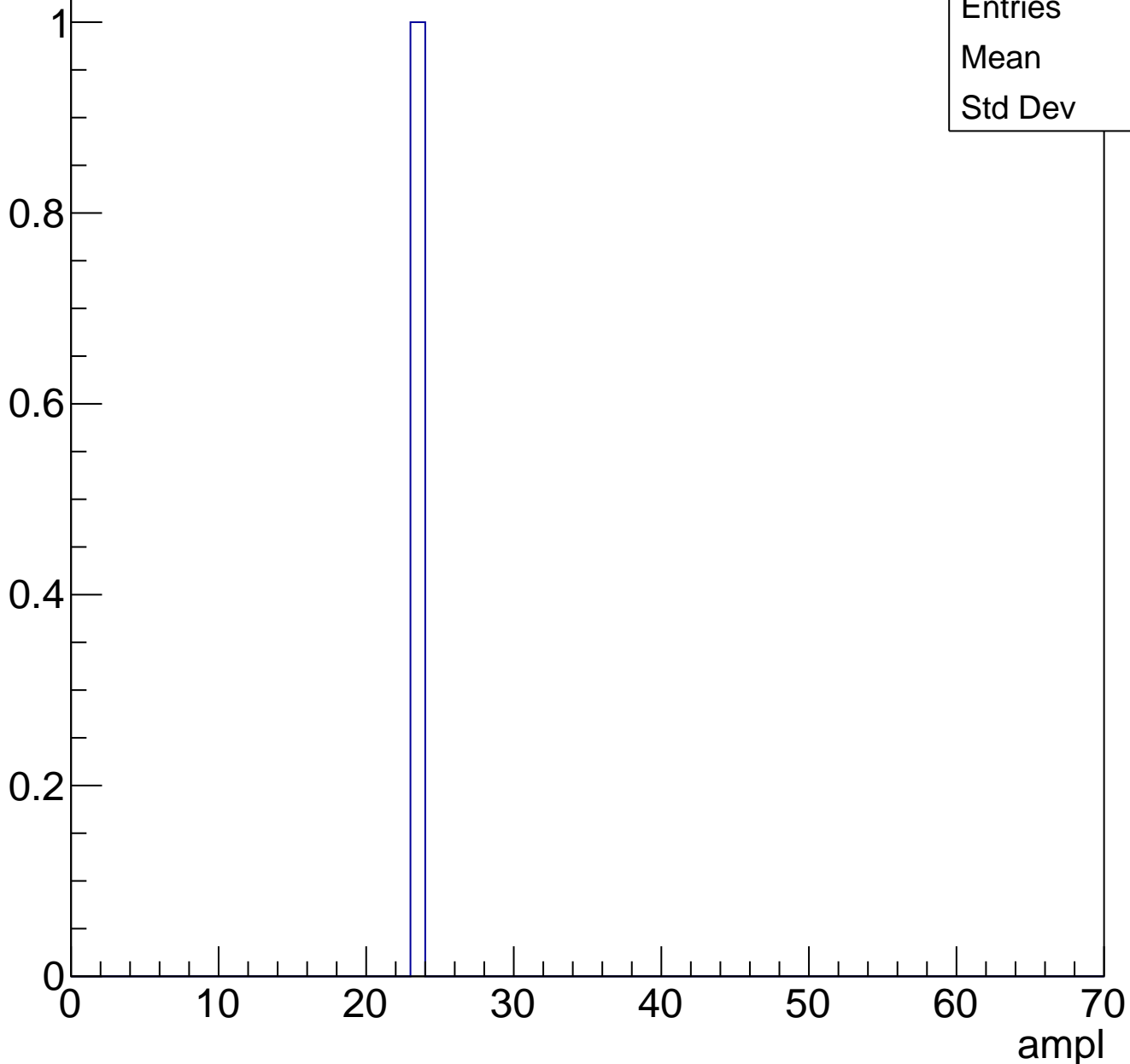
| | |
|---------|-------|
| Entries | 12 |
| Mean | 62.08 |
| Std Dev | 0.862 |



B1L001S, U26-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch63, adc0

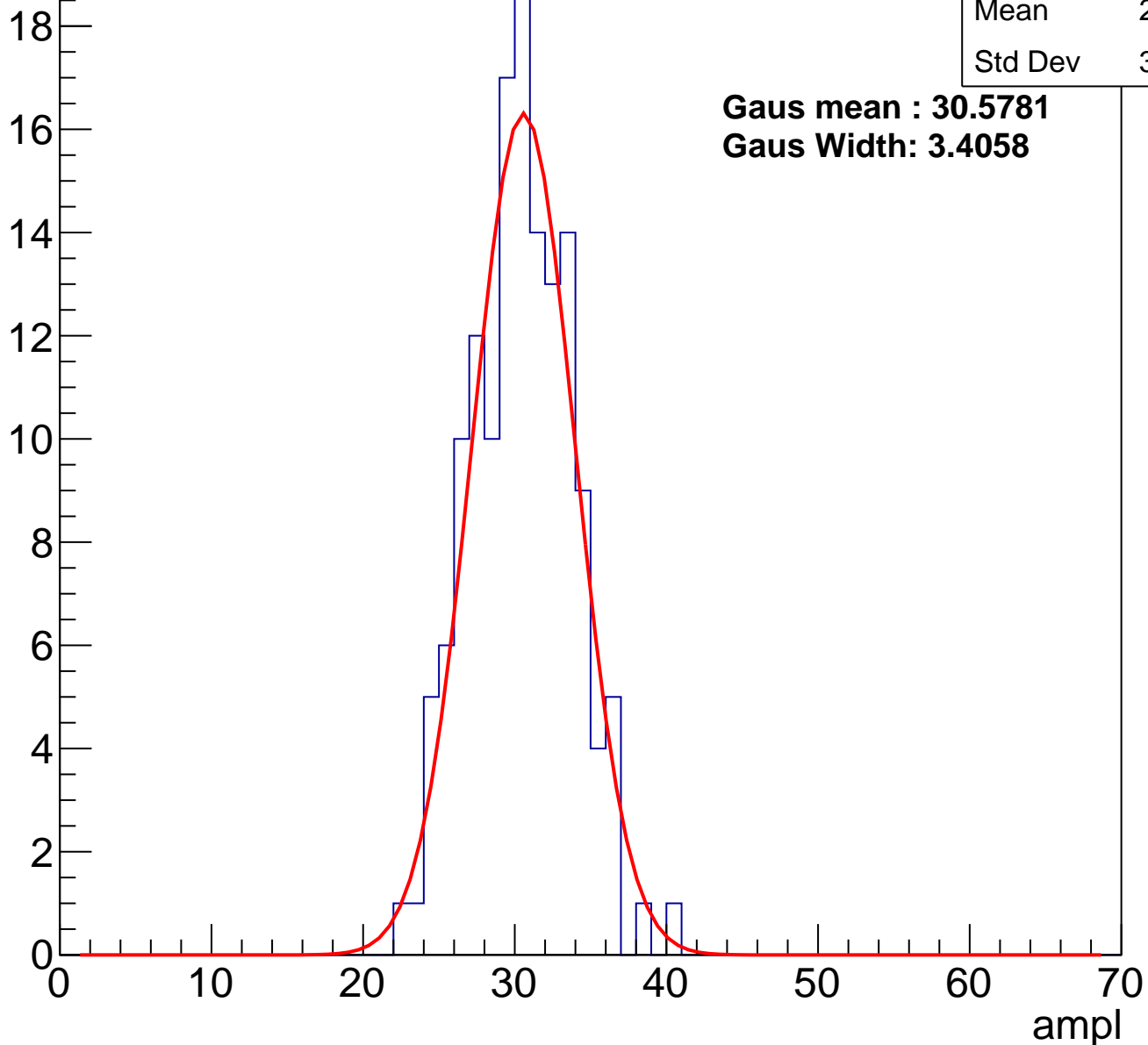
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 29.99 |
| Std Dev | 3.302 |

Gaus mean : 30.5781

Gaus Width: 3.4058

Entry

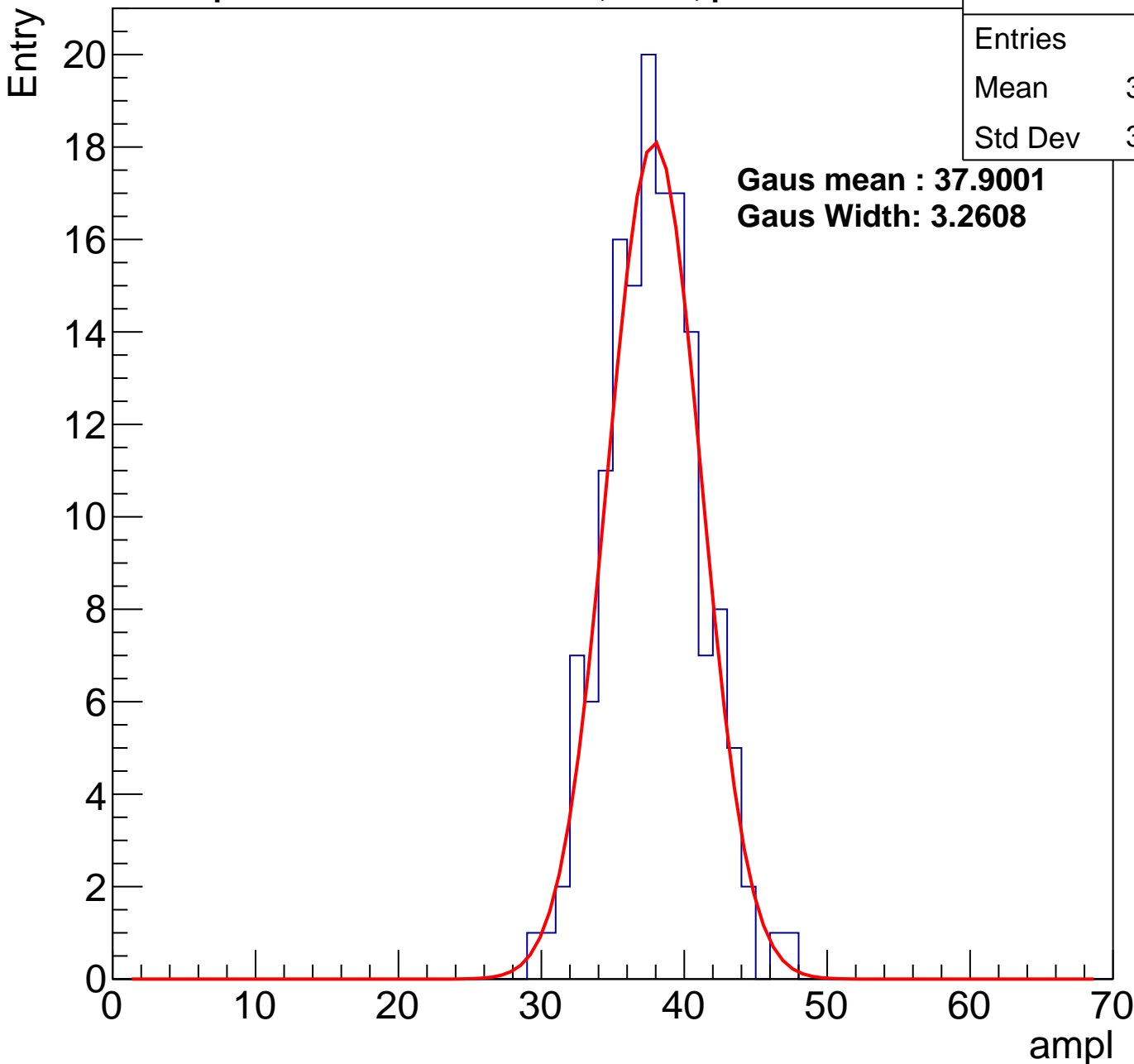


B1L001S, U26-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 37.38 |
| Std Dev | 3.249 |

Gaus mean : 37.9001
Gaus Width: 3.2608



B1L001S, U26-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

116

Mean

44.28

Std Dev

3.064

Gaus mean : 44.4882

Gaus Width: 2.9513

0

10

20

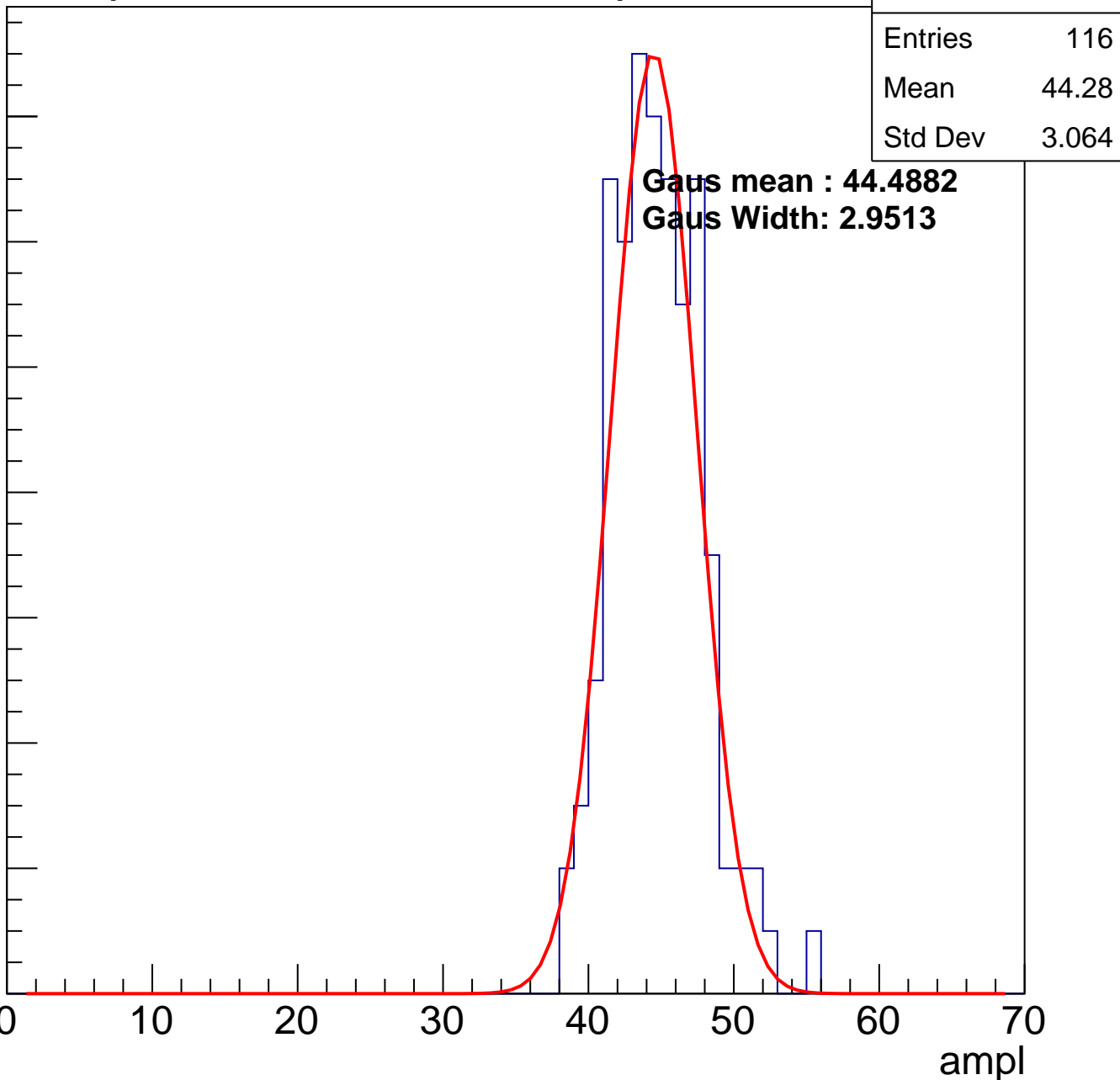
30

40

50

60

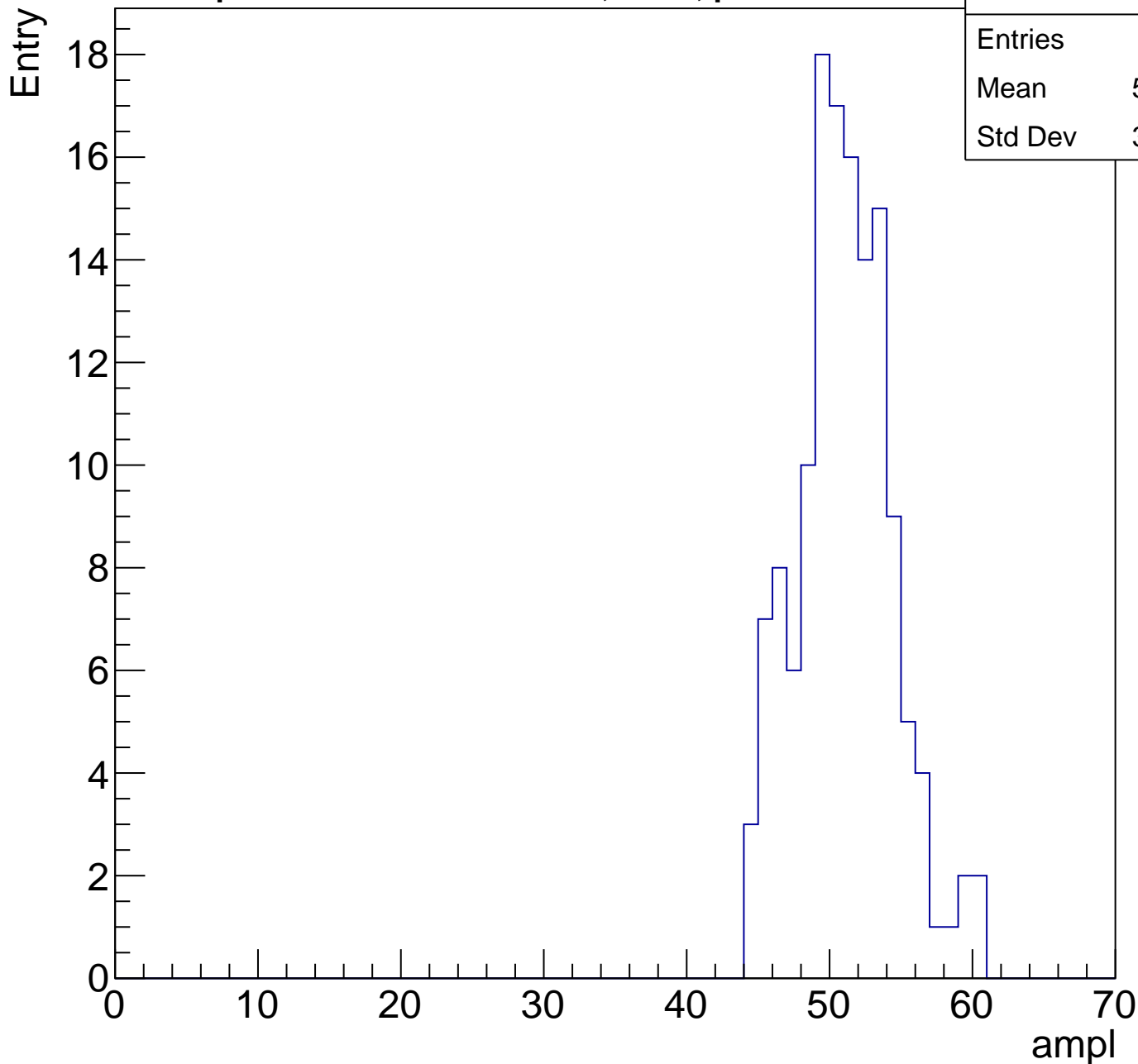
ampl



B1L001S, U26-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 50.62 |
| Std Dev | 3.367 |



B1L001S, U26-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 105 |
| Mean | 56.44 |
| Std Dev | 3.461 |

Entry

12

10

8

6

4

2

0

0

10

20

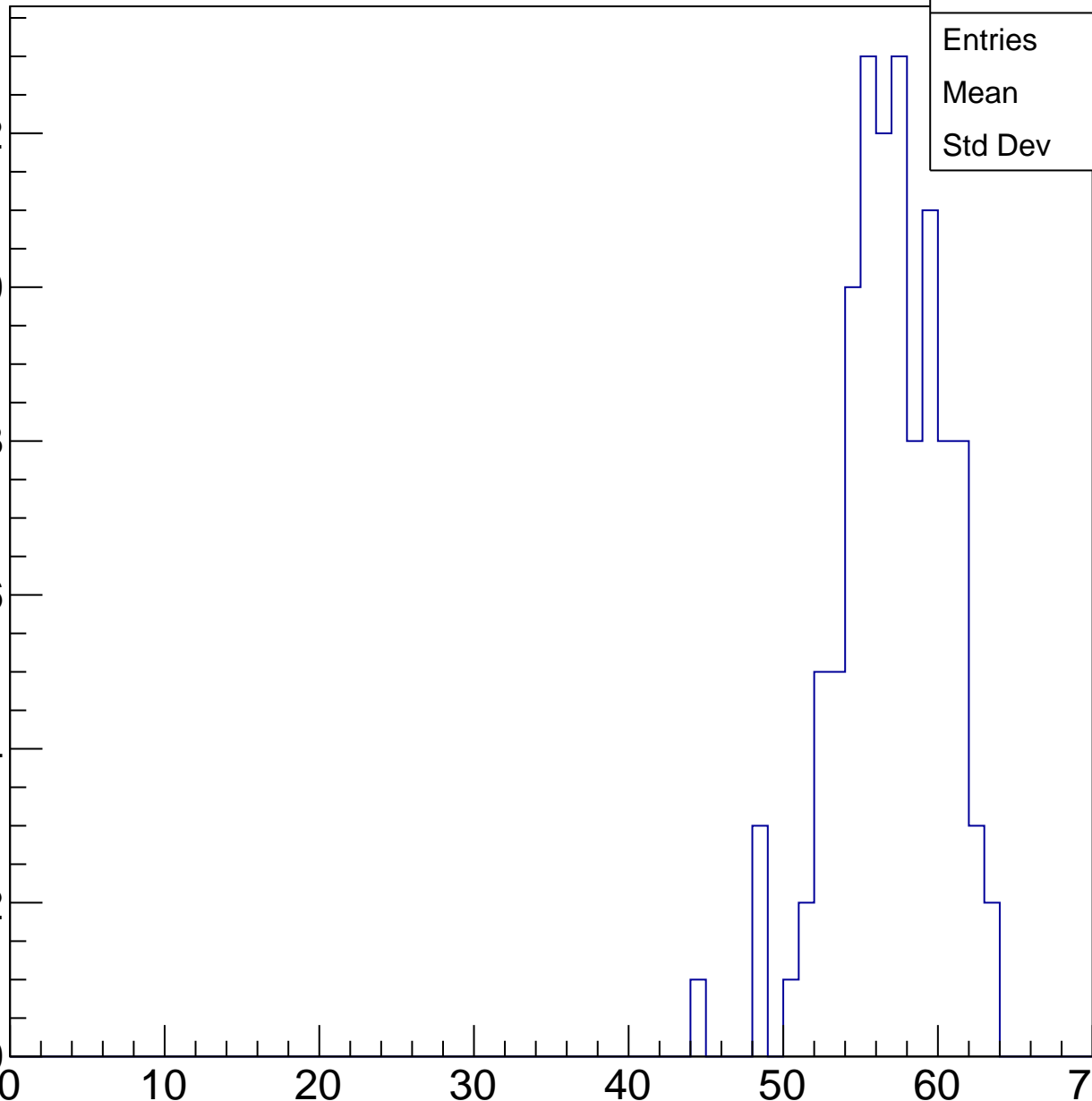
30

40

50

60

ampl



B1L001S, U26-ch63, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

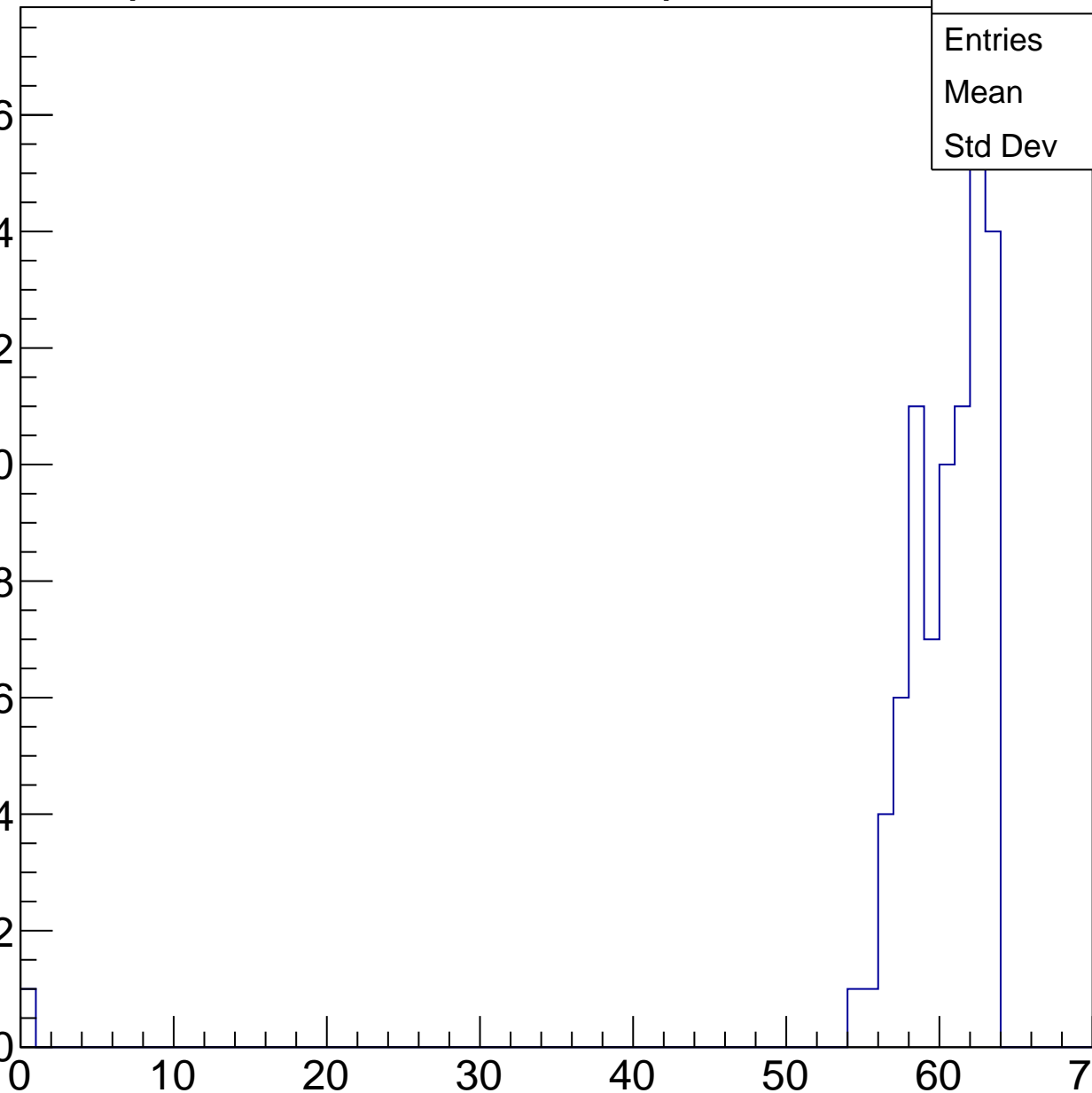
4

2

0

| | |
|---------|-------|
| Entries | 83 |
| Mean | 59.43 |
| Std Dev | 6.949 |

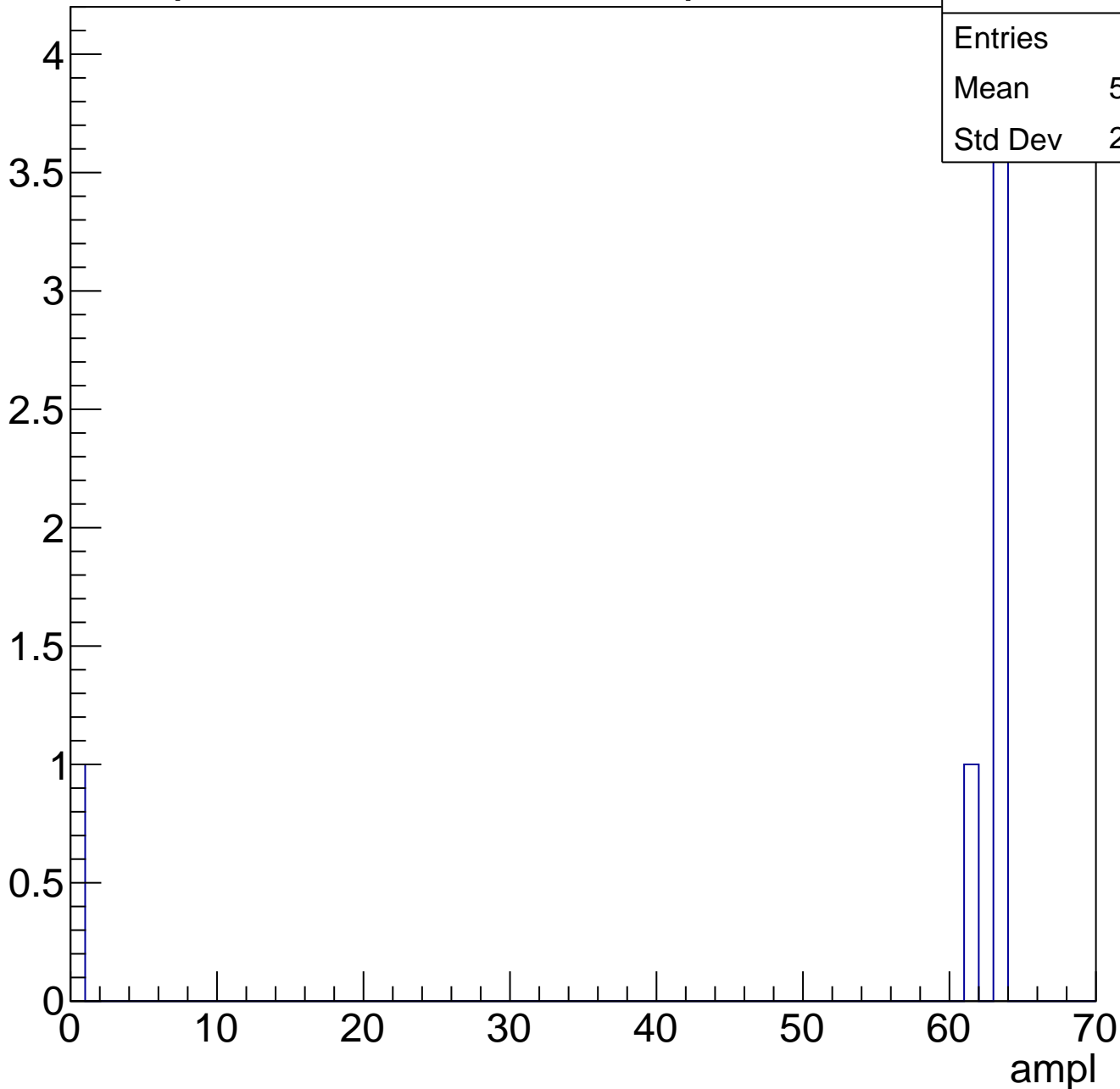
ampl



B1L001S, U26-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

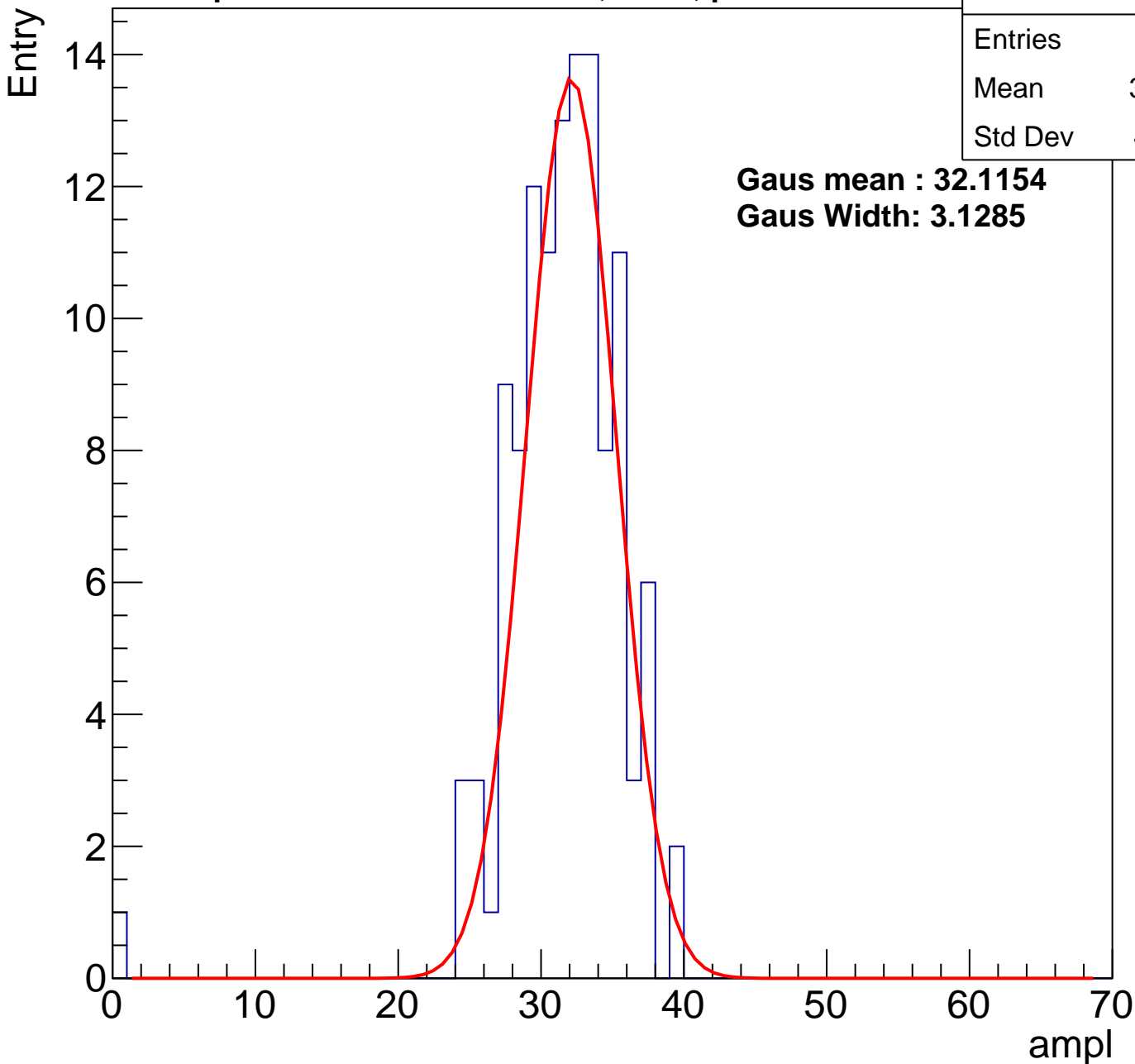


B1L001S, U26-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 31.06 |
| Std Dev | 4.351 |

Gaus mean : 32.1154
Gaus Width: 3.1285



B1L001S, U26-ch64, adc1

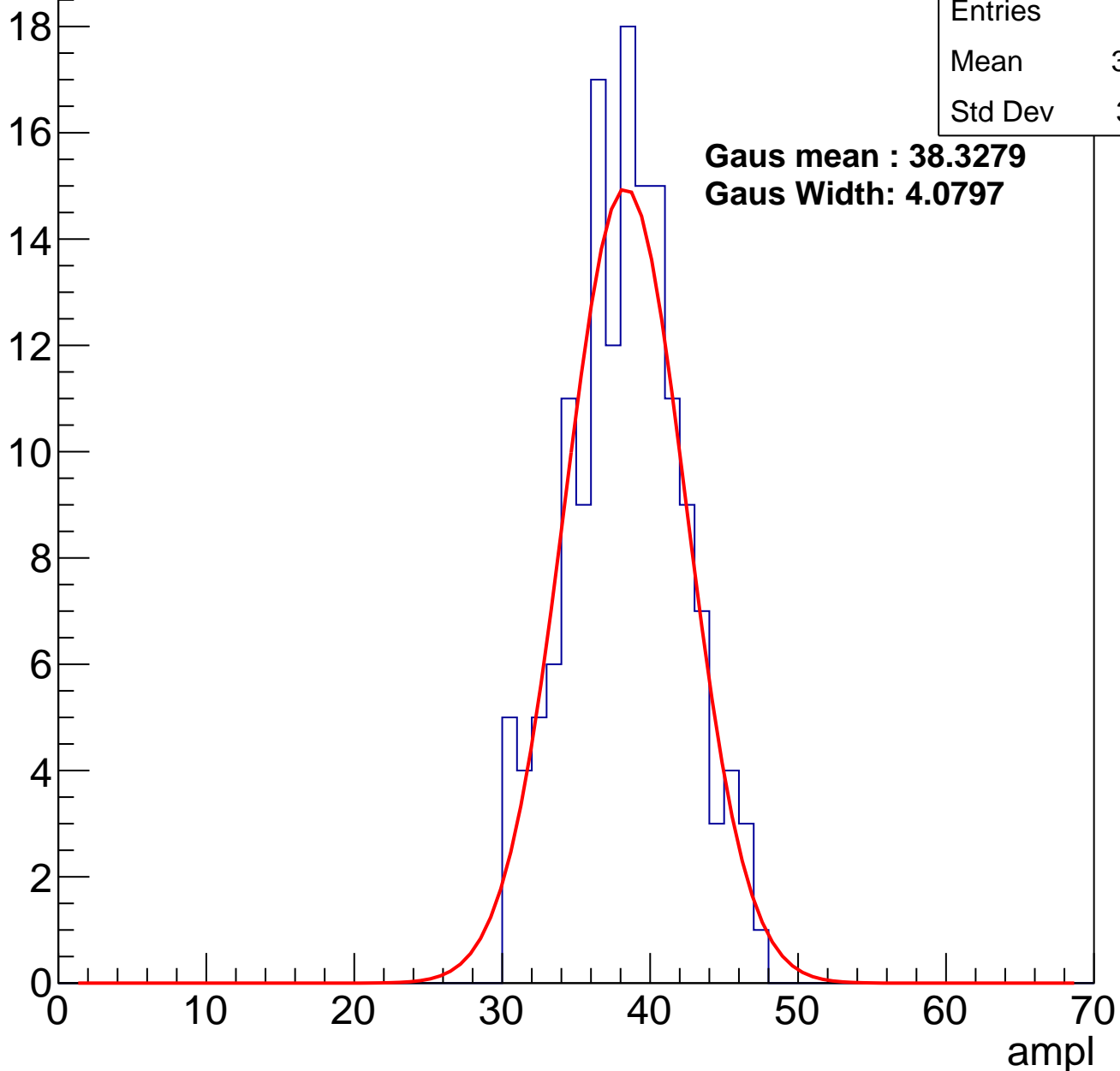
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 155 |
| Mean | 37.89 |
| Std Dev | 3.811 |

Gaus mean : 38.3279

Gaus Width: 4.0797

Entry



B1L001S, U26-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 97 |
| Mean | 44.08 |
| Std Dev | 3.105 |

Gaus mean : 44.4102

Gaus Width: 3.0485

0

10

20

30

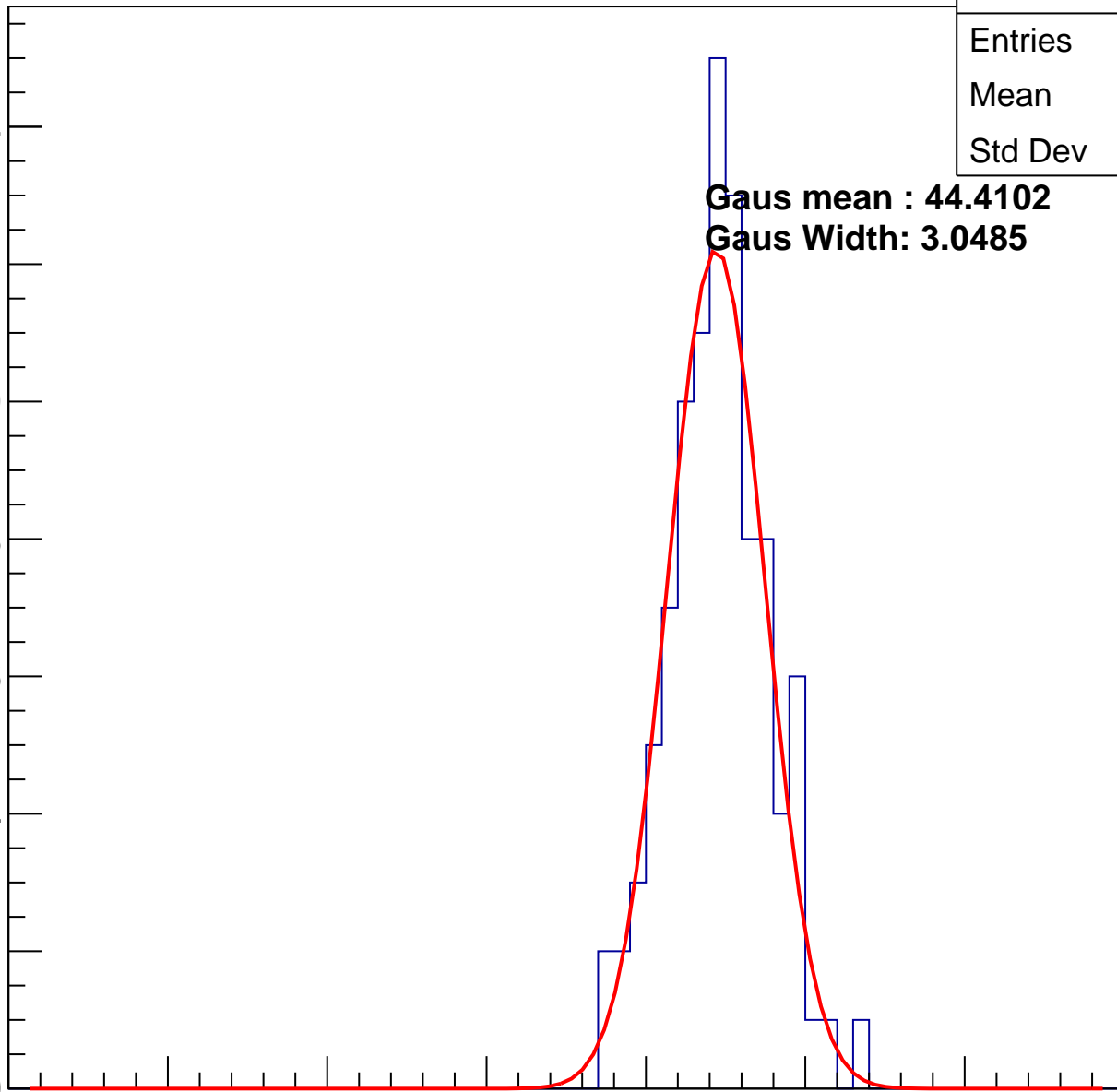
40

50

60

70

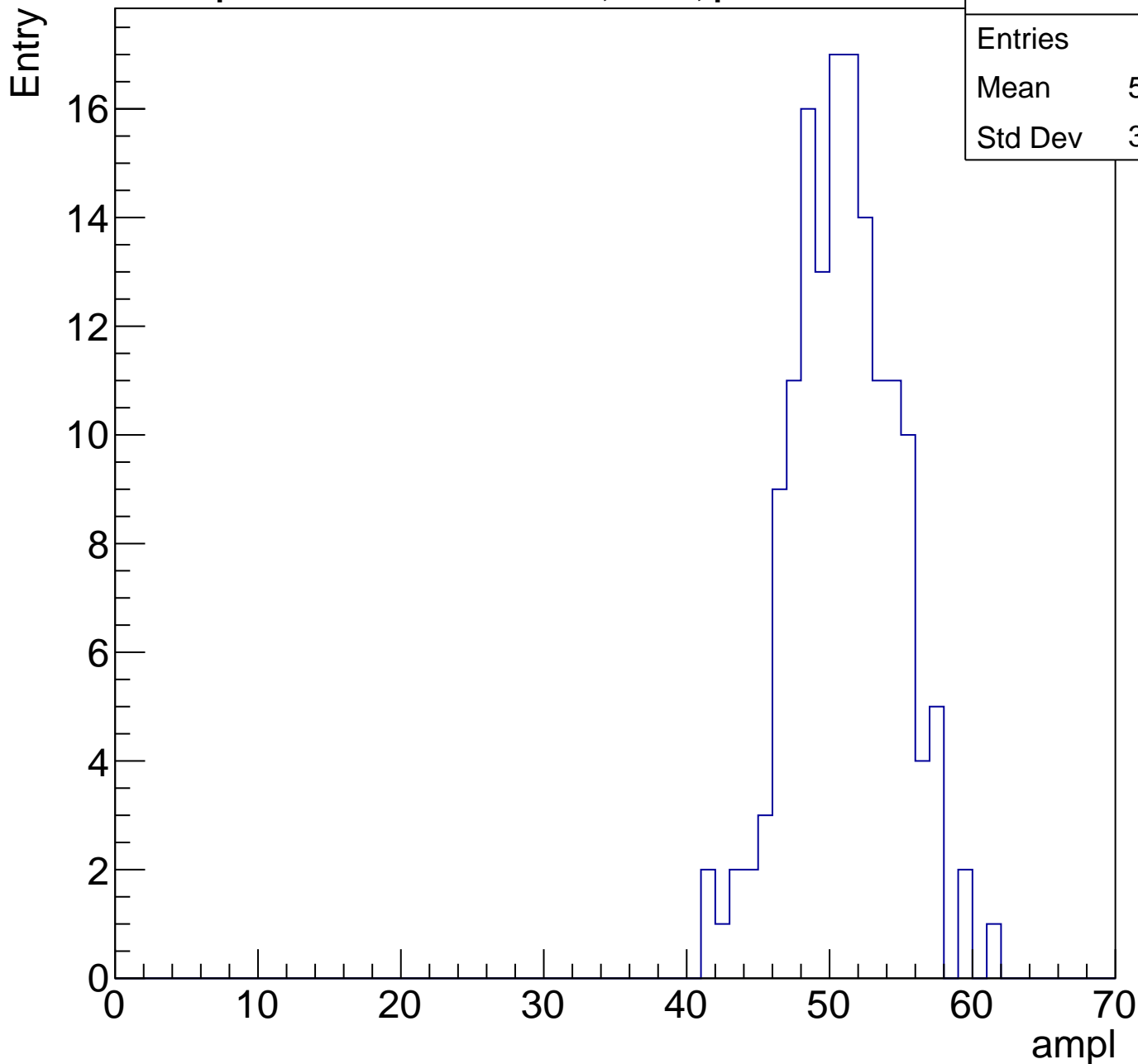
ampl



B1L001S, U26-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 50.52 |
| Std Dev | 3.657 |



B1L001S, U26-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

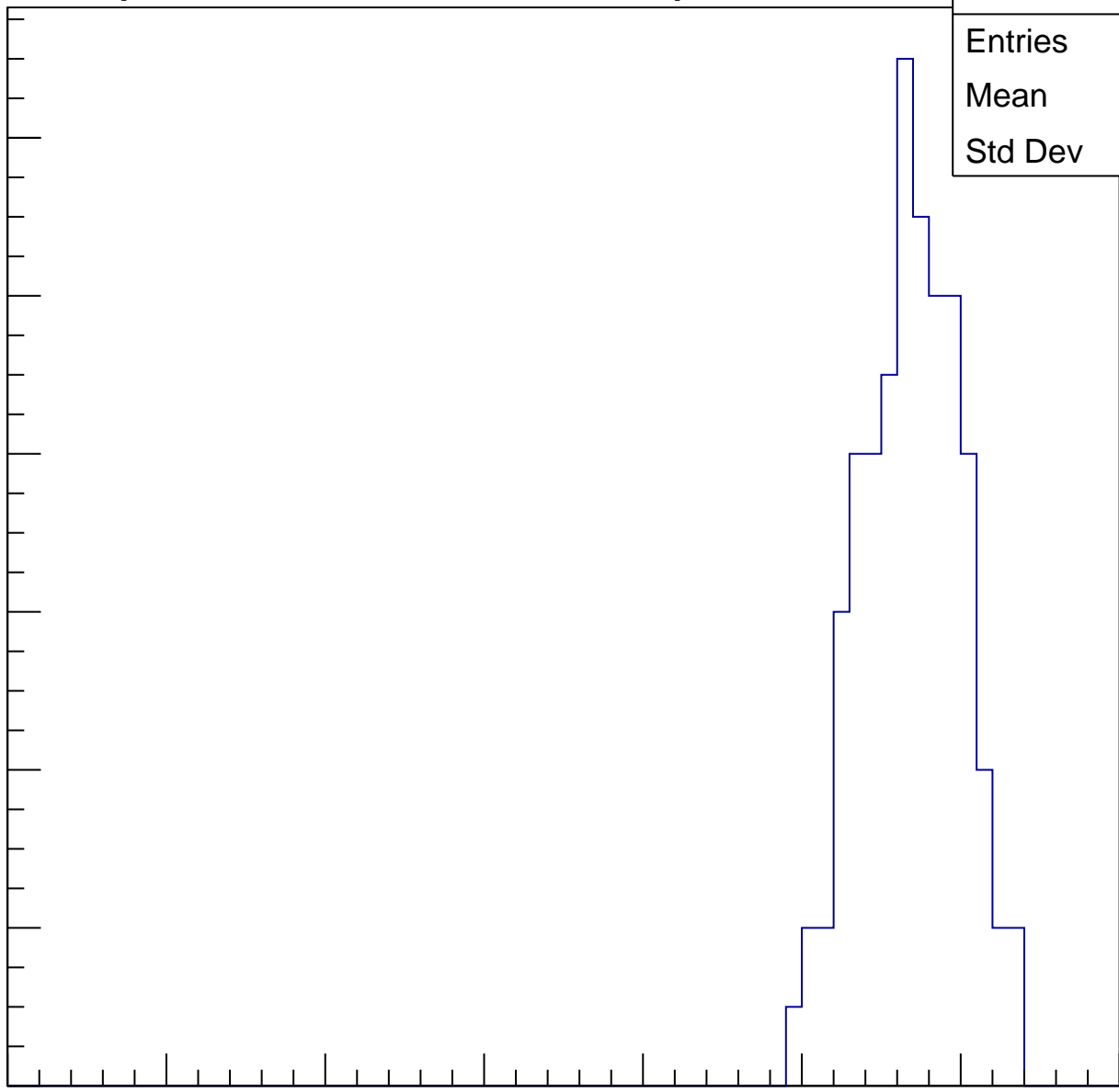
50

60

70

ampl

| | |
|---------|-------|
| Entries | 96 |
| Mean | 56.39 |
| Std Dev | 3.07 |



B1L001S, U26-ch64, adc5

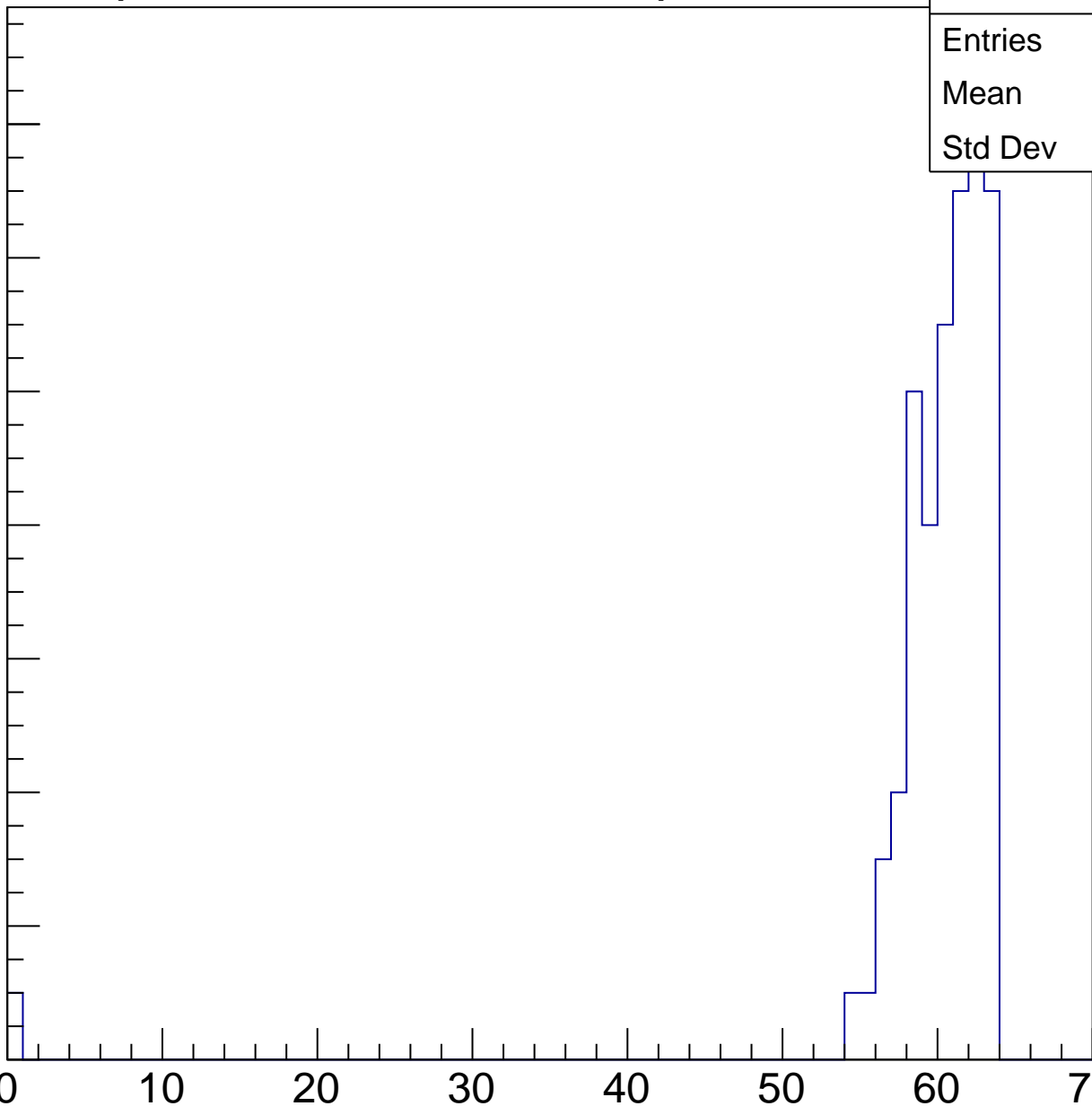
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 80 |
| Mean | 59.49 |
| Std Dev | 7.037 |

ampl

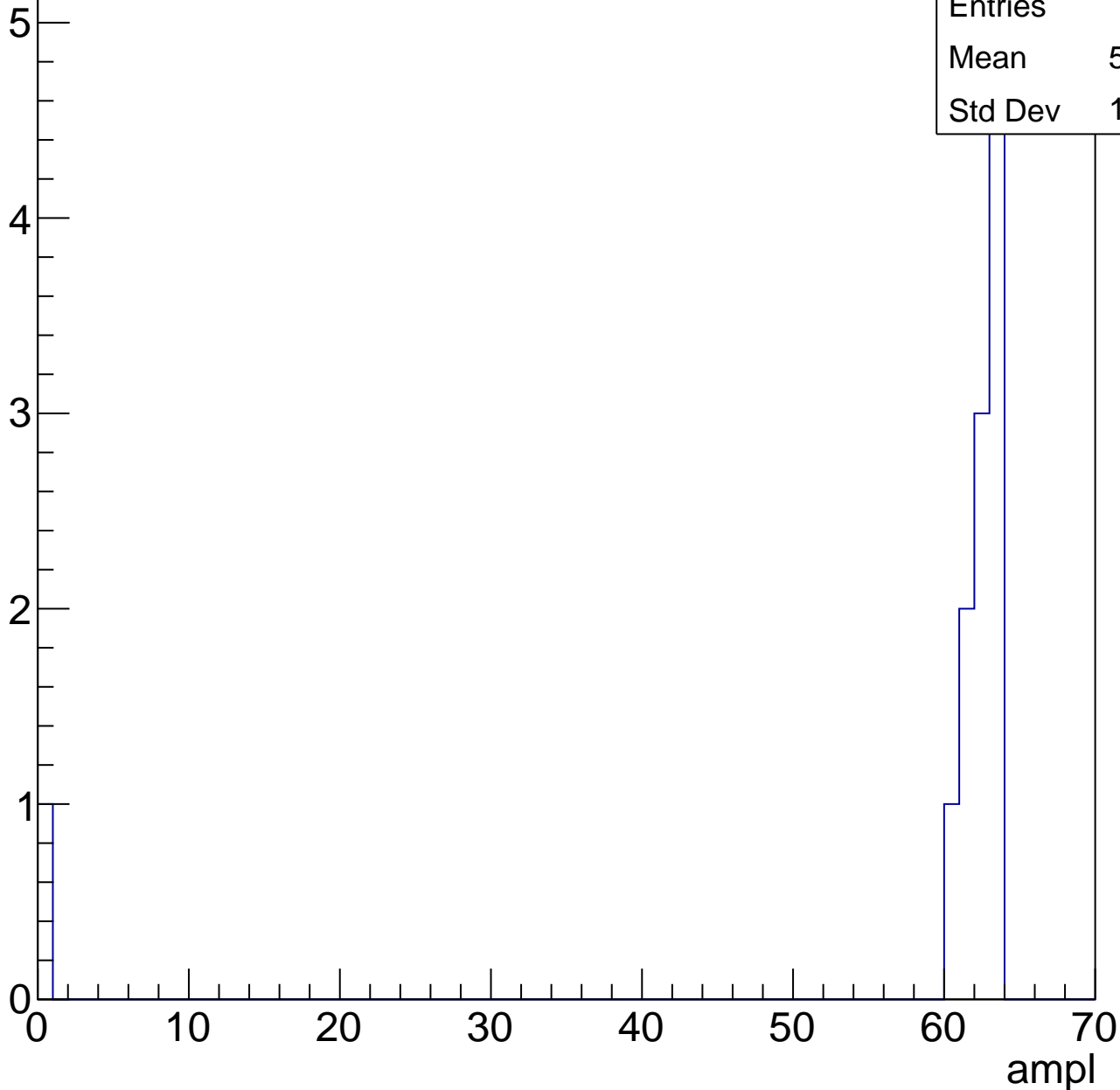


B1L001S, U26-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

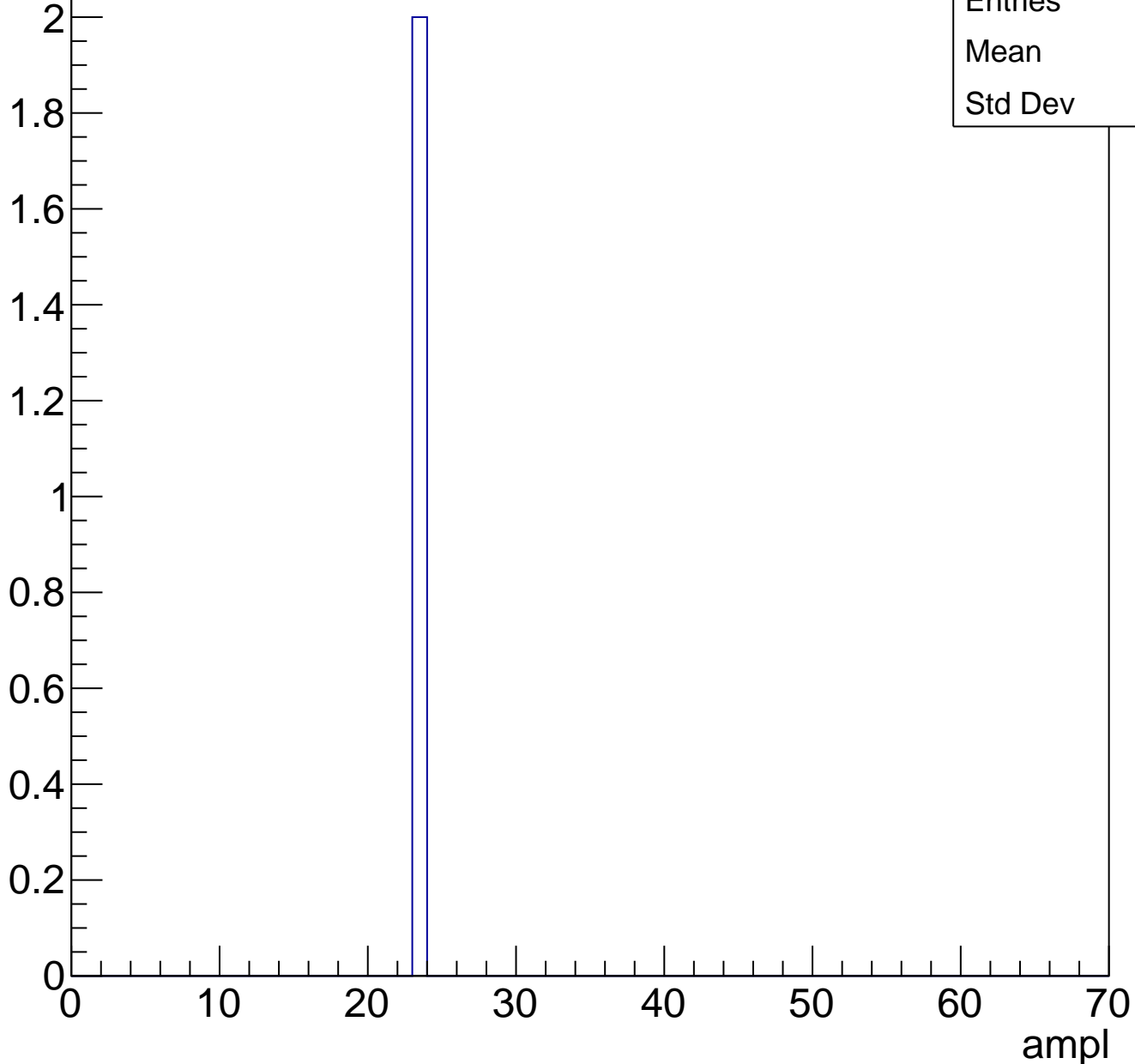
| | |
|---------|-------|
| Entries | 12 |
| Mean | 56.92 |
| Std Dev | 17.19 |



B1L001S, U26-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch65, adc0

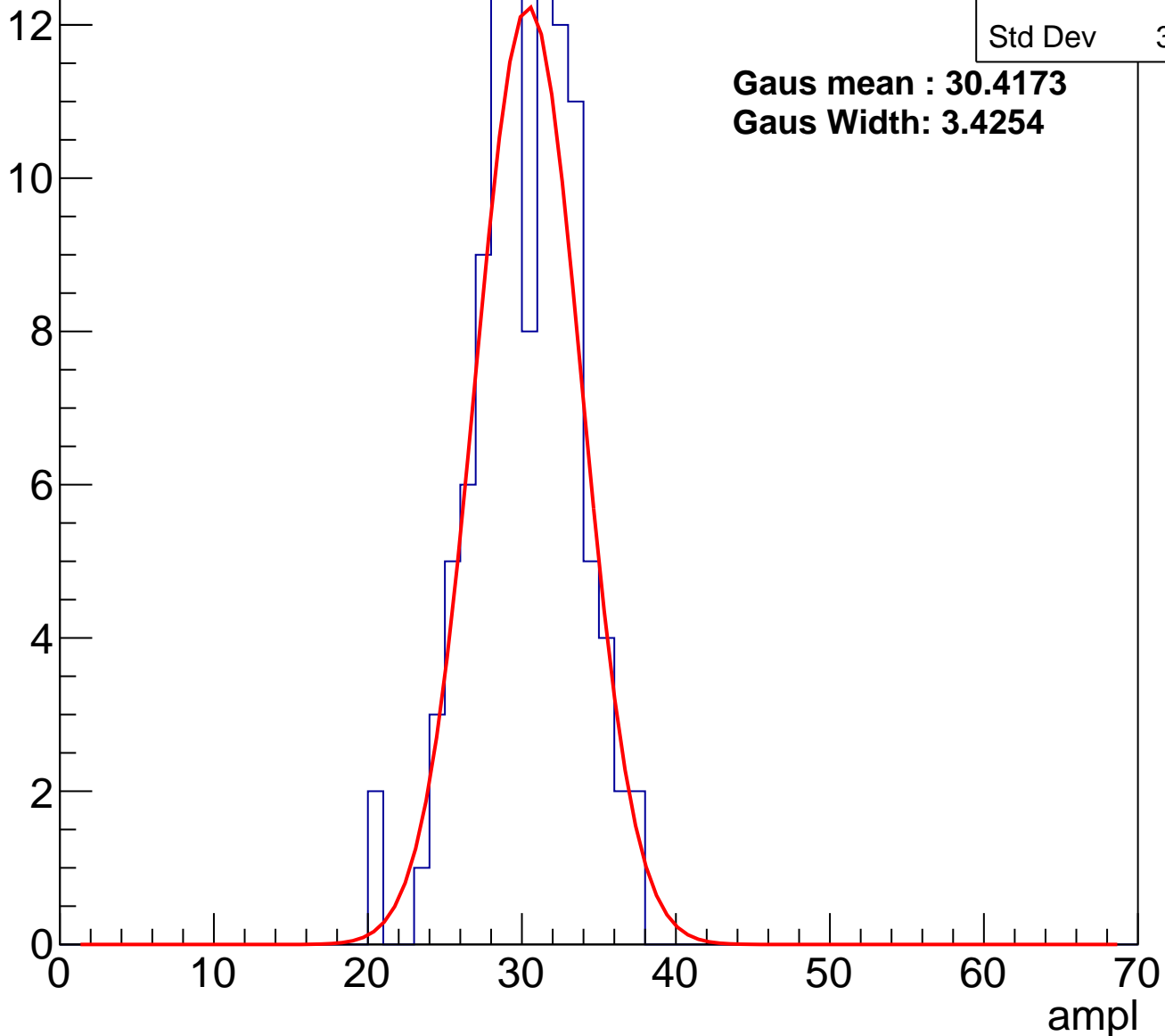
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 109 |
| Mean | 29.78 |
| Std Dev | 3.357 |

Gaus mean : 30.4173

Gaus Width: 3.4254

Entry



B1L001S, U26-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

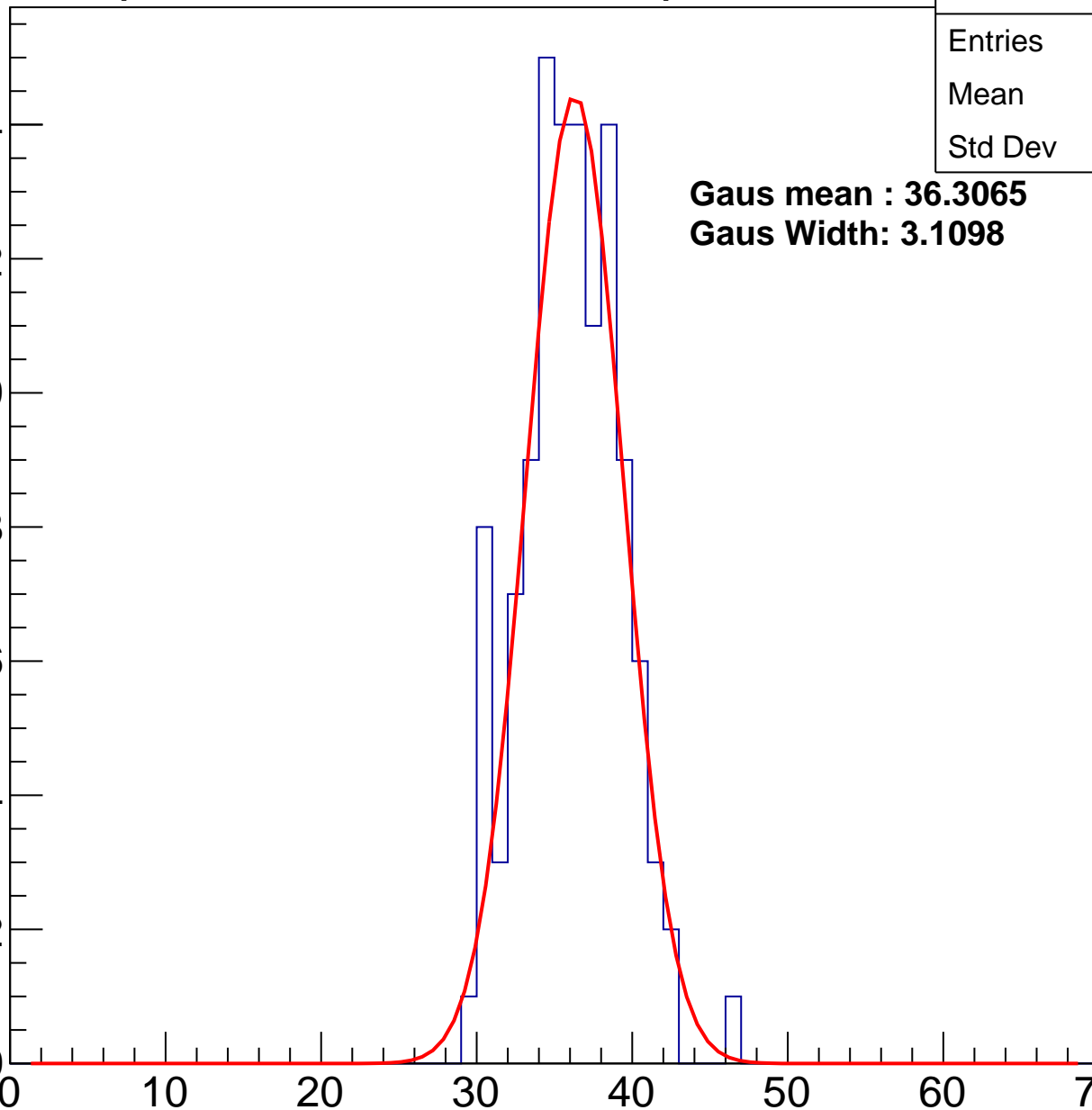
35.64

Std Dev

3.142

Gaus mean : 36.3065

Gaus Width: 3.1098



B1L001S, U26-ch65, adc2

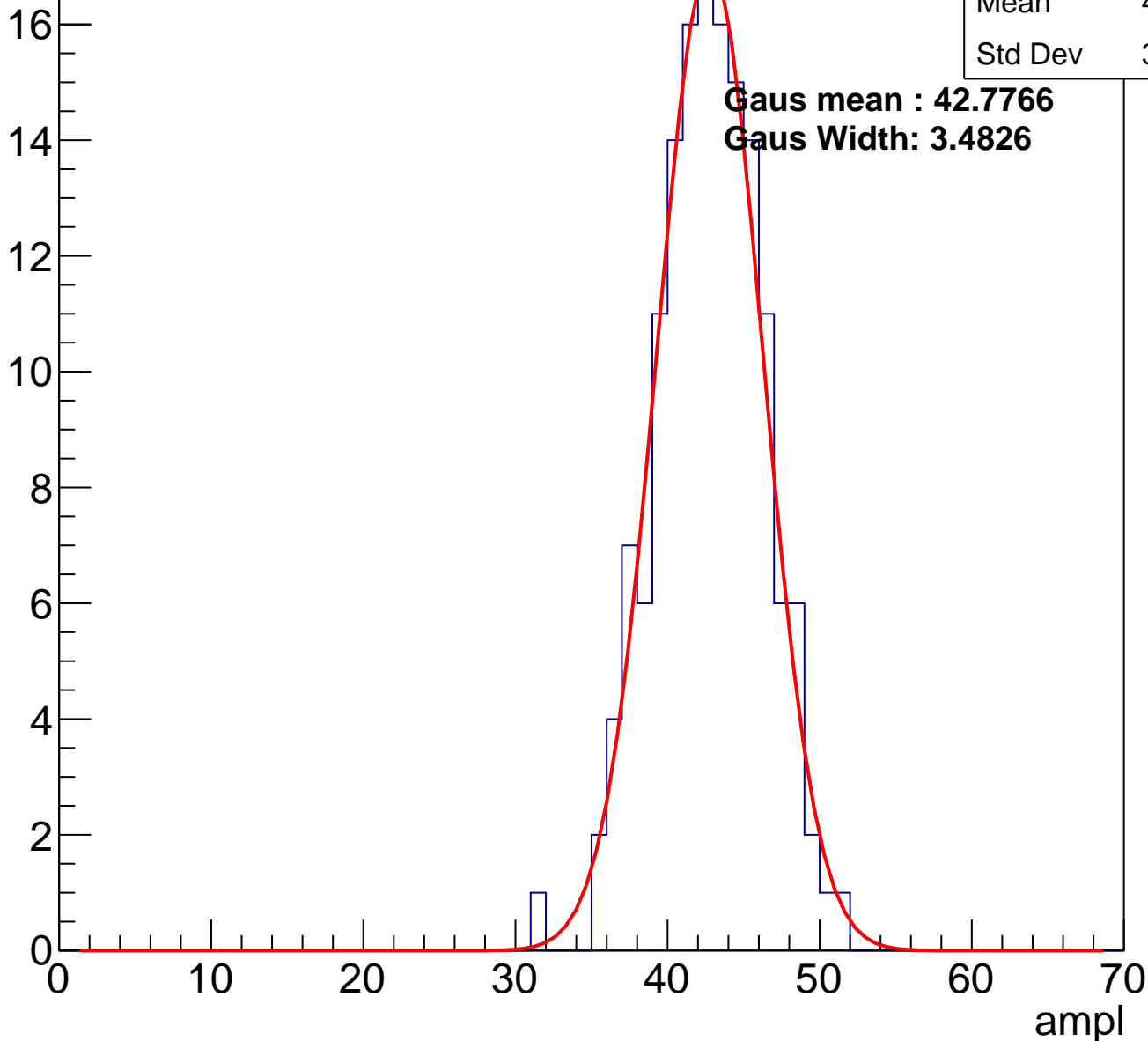
calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 150 |
| Mean | 42.29 |
| Std Dev | 3.444 |

Gaus mean : 42.7766

Gaus Width: 3.4826

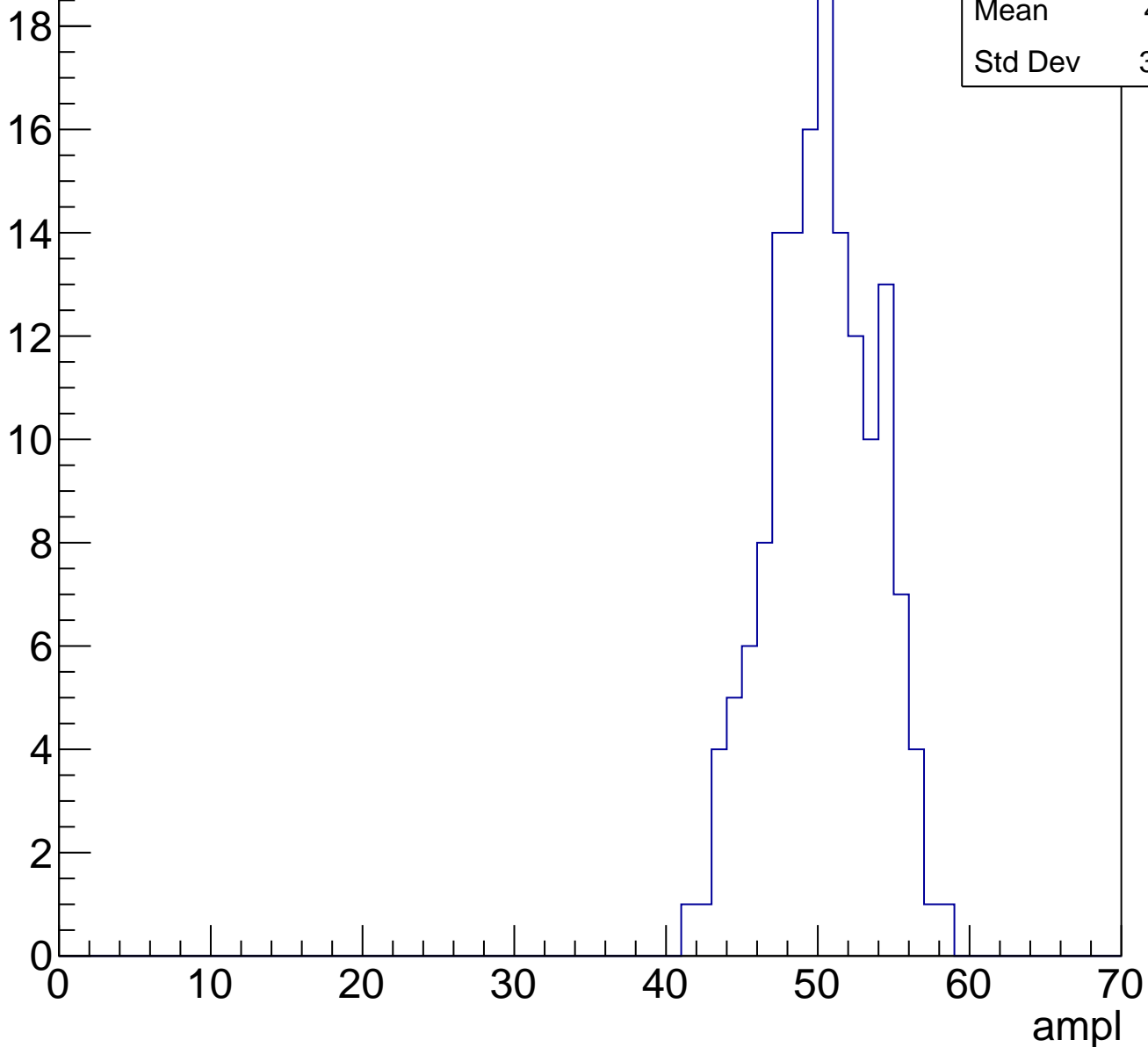


B1L001S, U26-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 49.81 |
| Std Dev | 3.448 |

Entry



B1L001S, U26-ch65, adc4

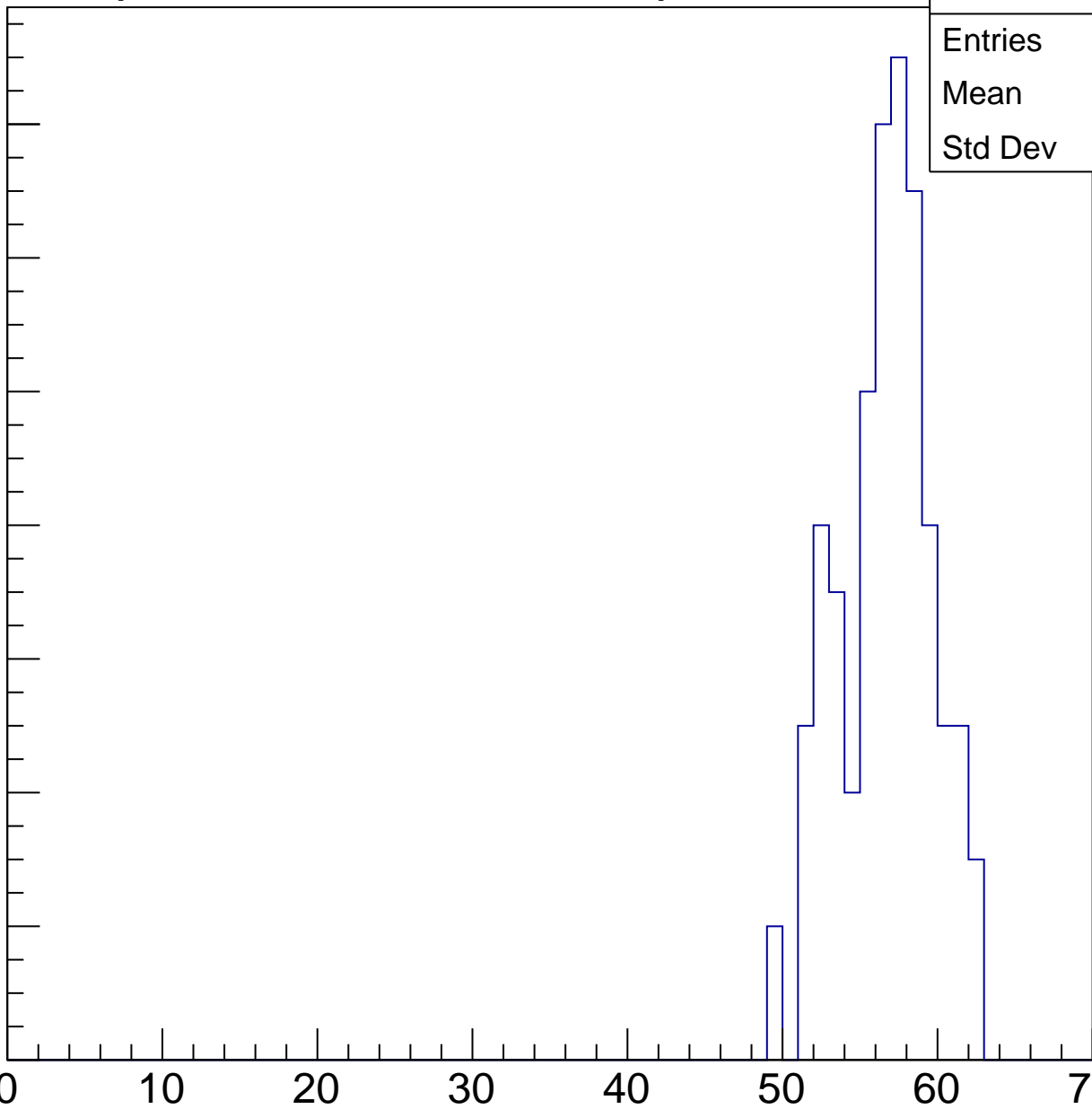
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 99 |
| Mean | 56.18 |
| Std Dev | 2.996 |

ampl



B1L001S, U26-ch65, adc5

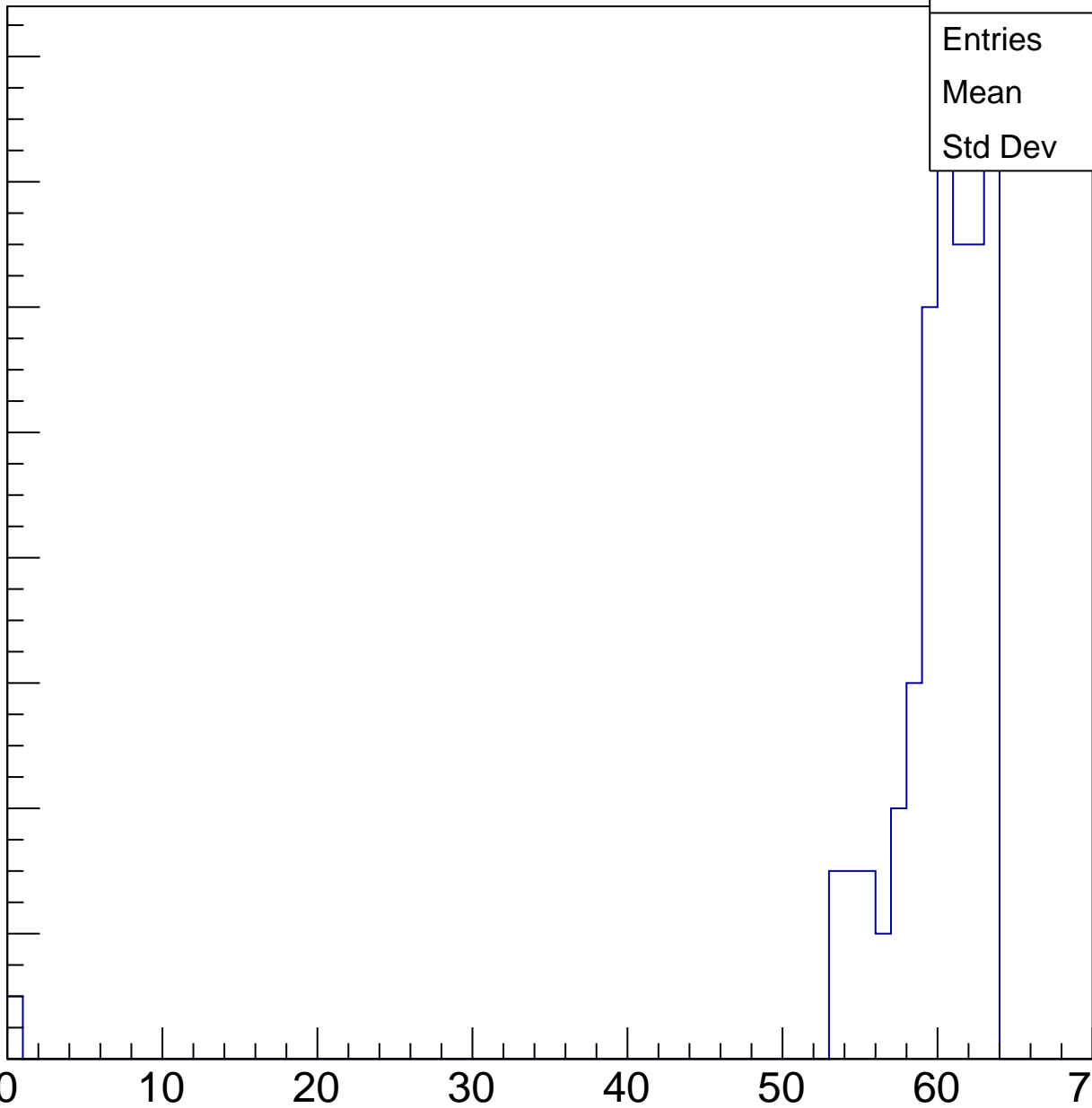
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 59.22 |
| Std Dev | 6.784 |

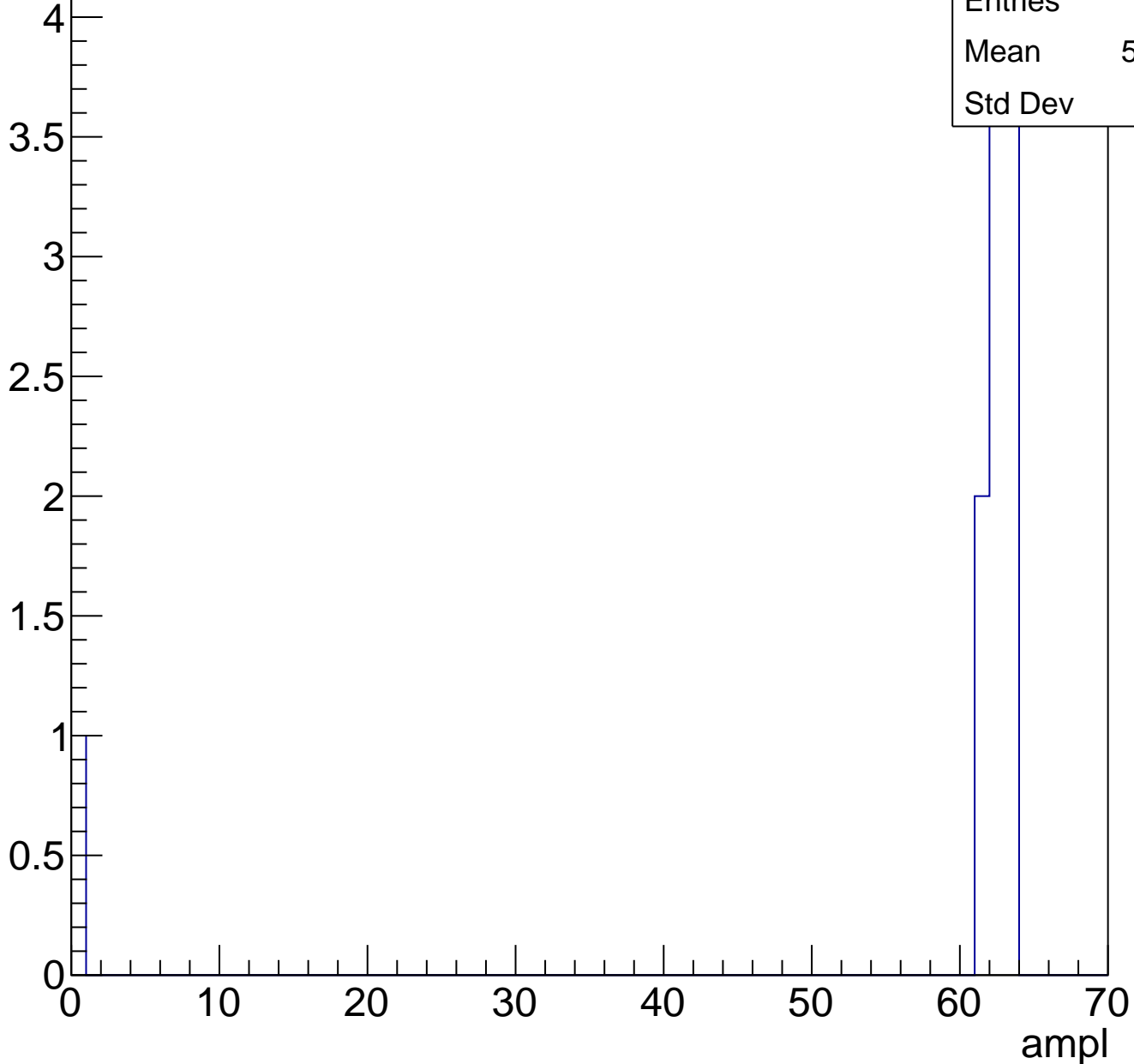
ampl



B1L001S, U26-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch66, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 29.77 |
| Std Dev | 3.375 |

Gaus mean : 30.0517

Gaus Width: 3.6225

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

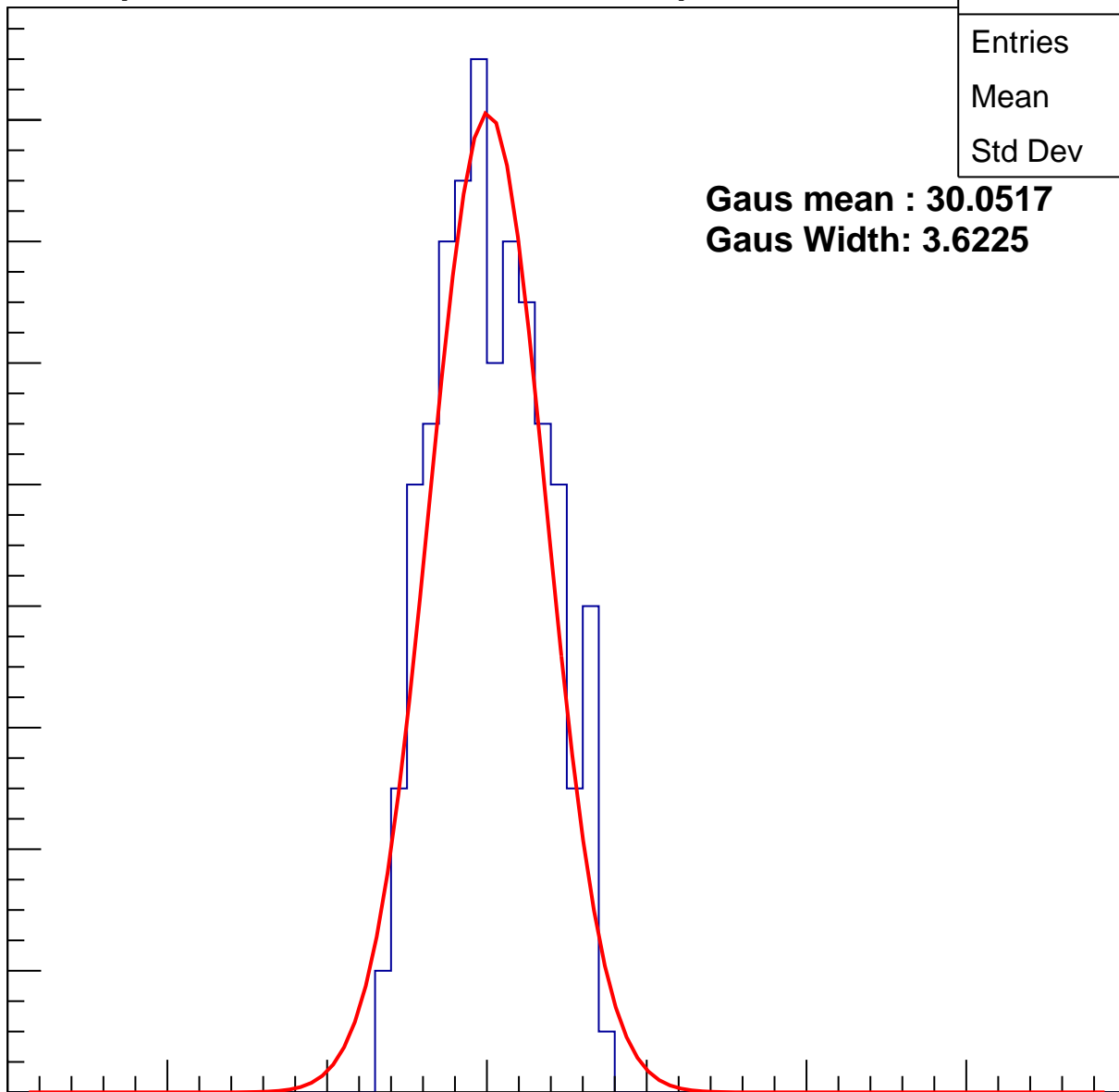
40

50

60

70

ampl



B1L001S, U26-ch66, adc1

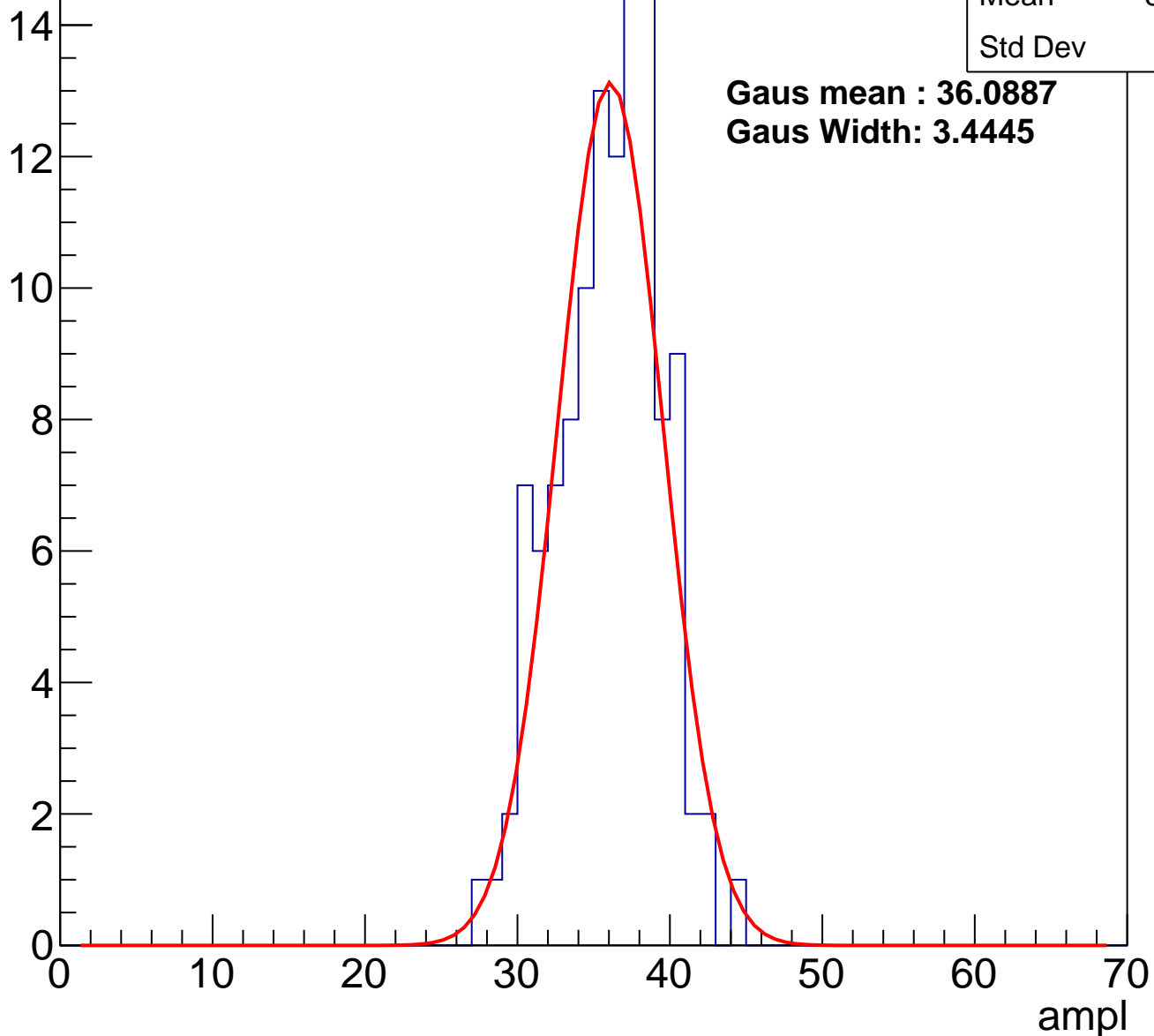
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 35.55 |
| Std Dev | 3.35 |

Gaus mean : 36.0887

Gaus Width: 3.4445

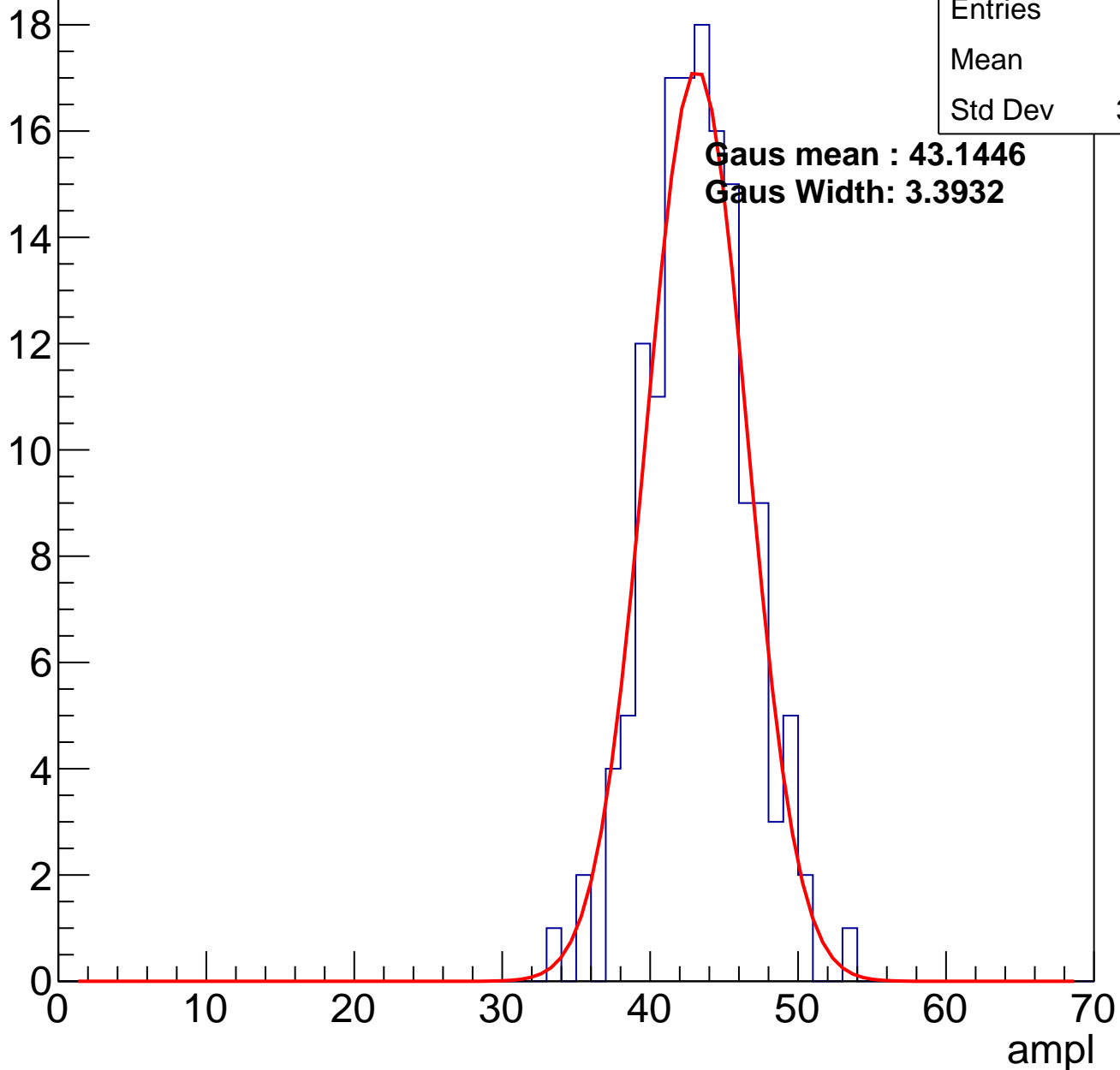
Entry



B1L001S, U26-ch66, adc2

calib_packv5_042523_0143.root, FC#2, port C2

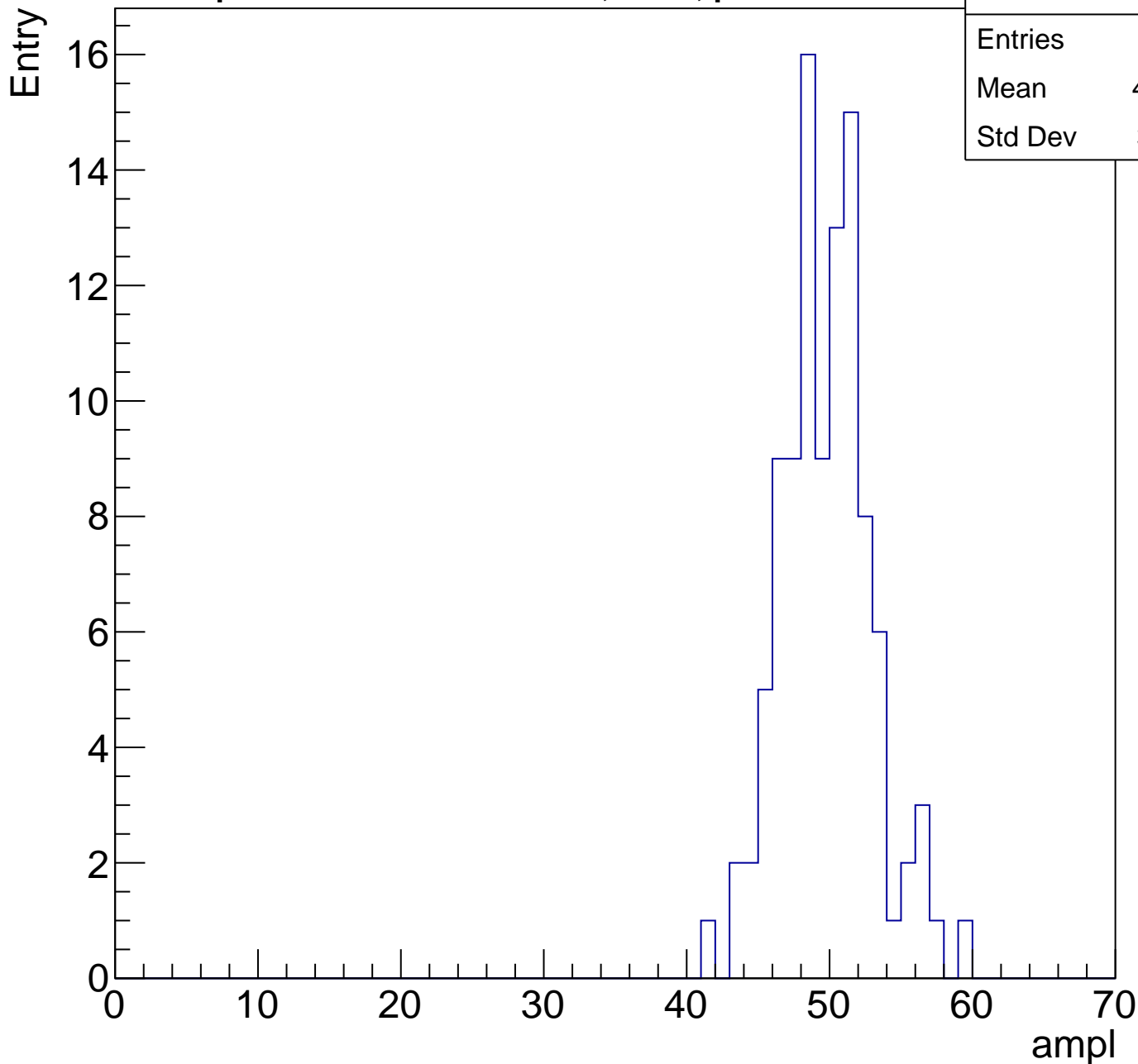
Entry



B1L001S, U26-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 103 |
| Mean | 49.35 |
| Std Dev | 3.171 |



B1L001S, U26-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

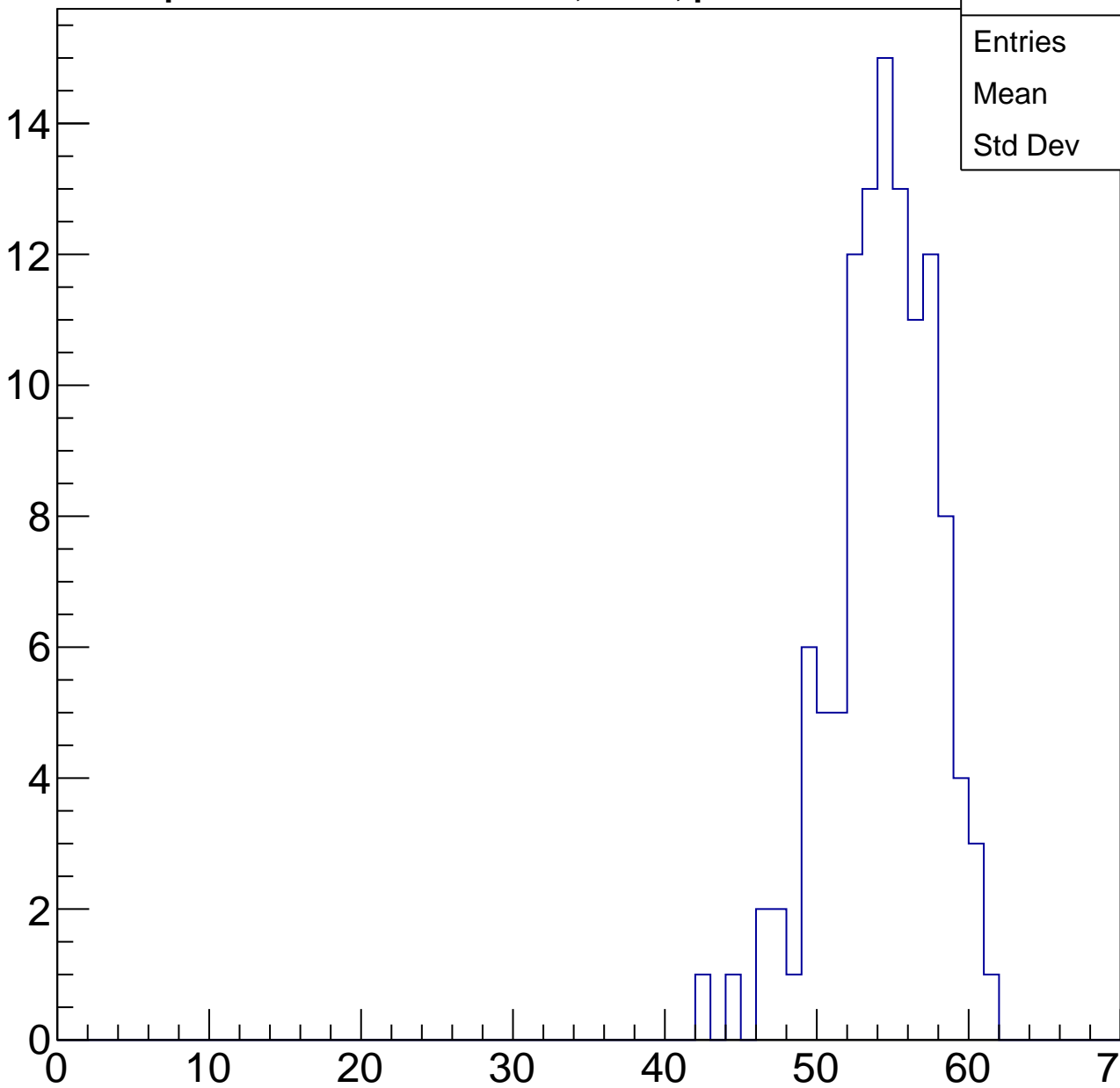
| | |
|---------|-------|
| Entries | 115 |
| Mean | 53.9 |
| Std Dev | 3.478 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

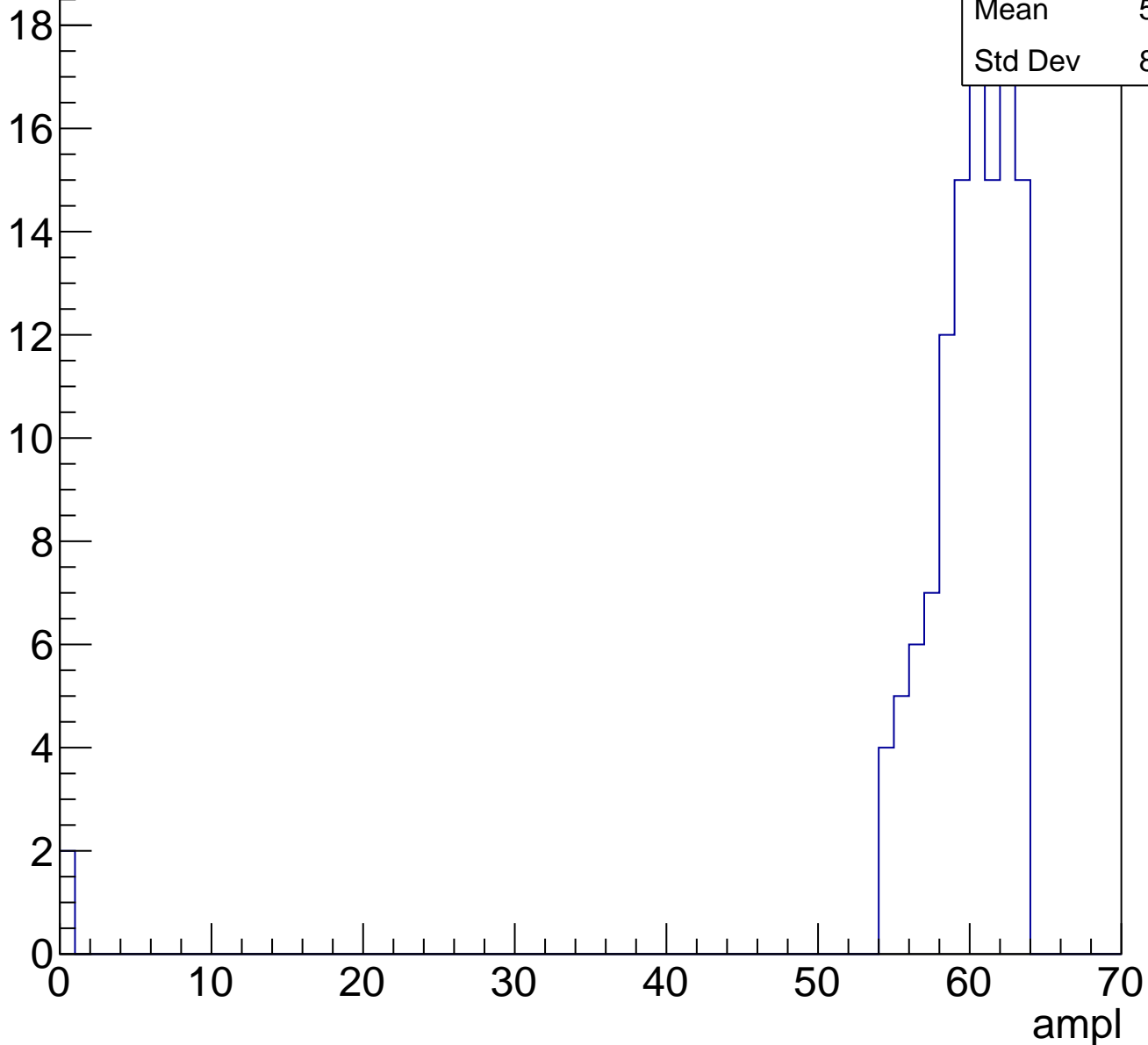
ampl



B1L001S, U26-ch66, adc5

calib_packv5_042523_0143.root, FC#2, port C2

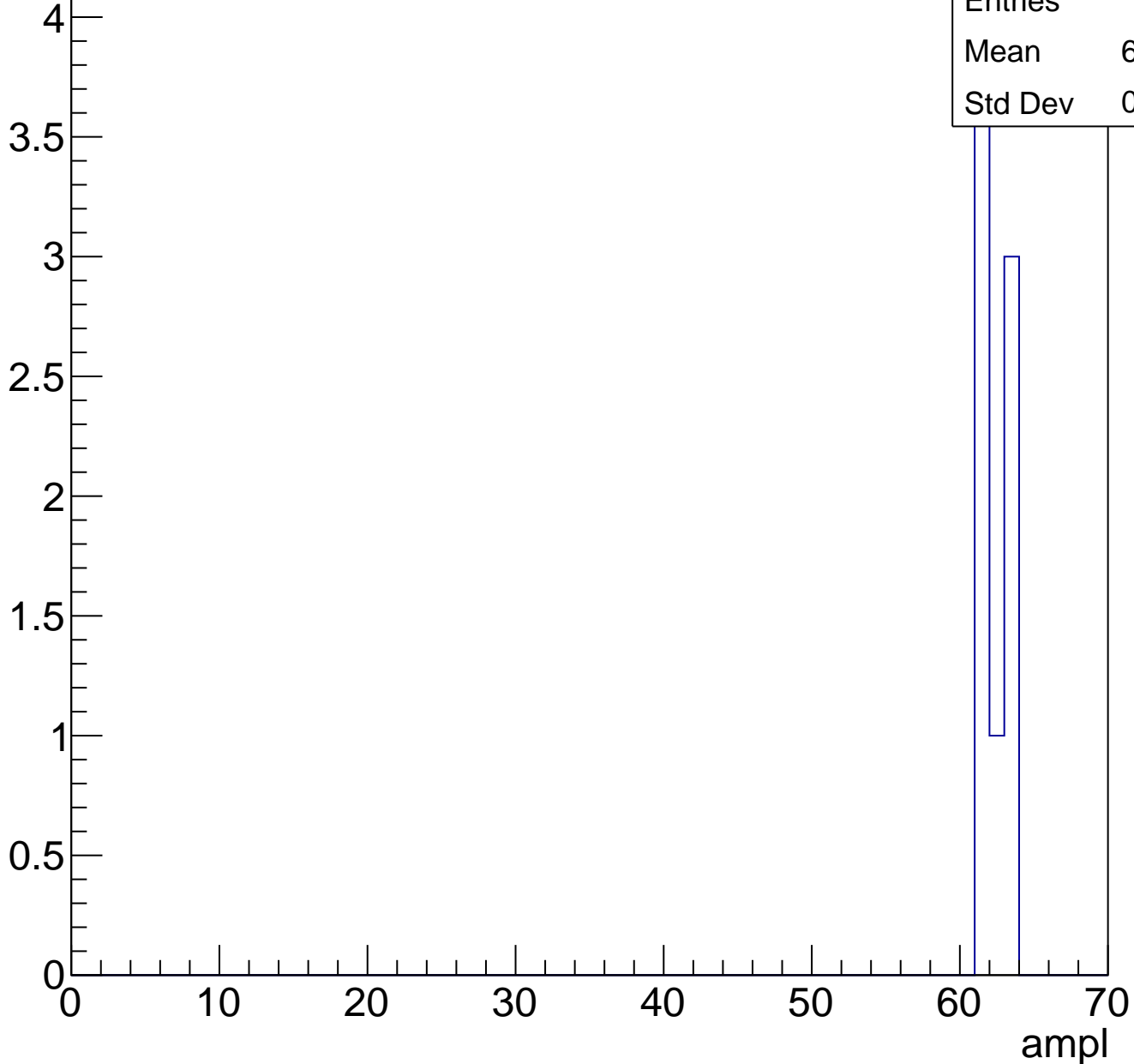
Entry



B1L001S, U26-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

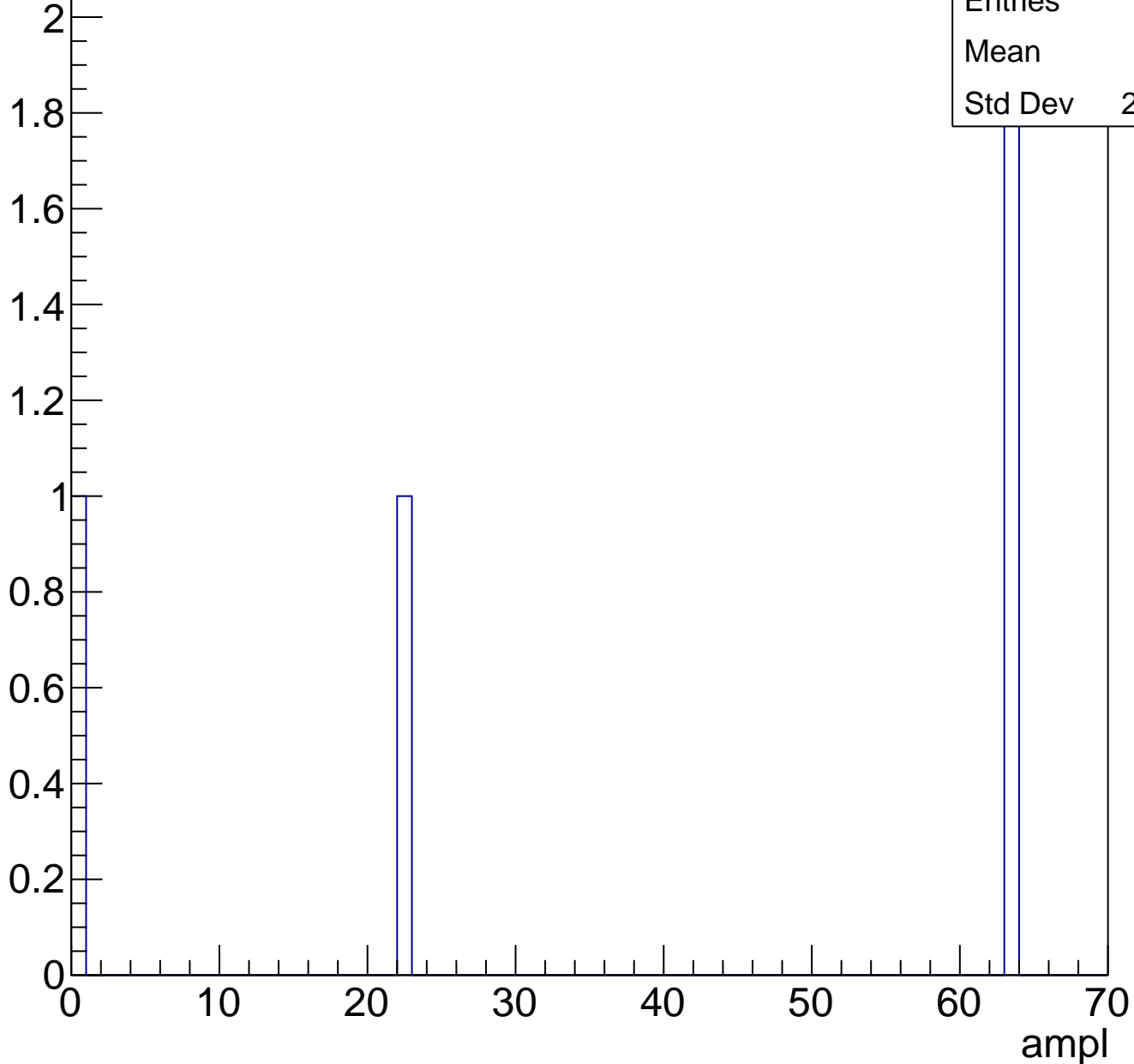
Entry



B1L001S, U26-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

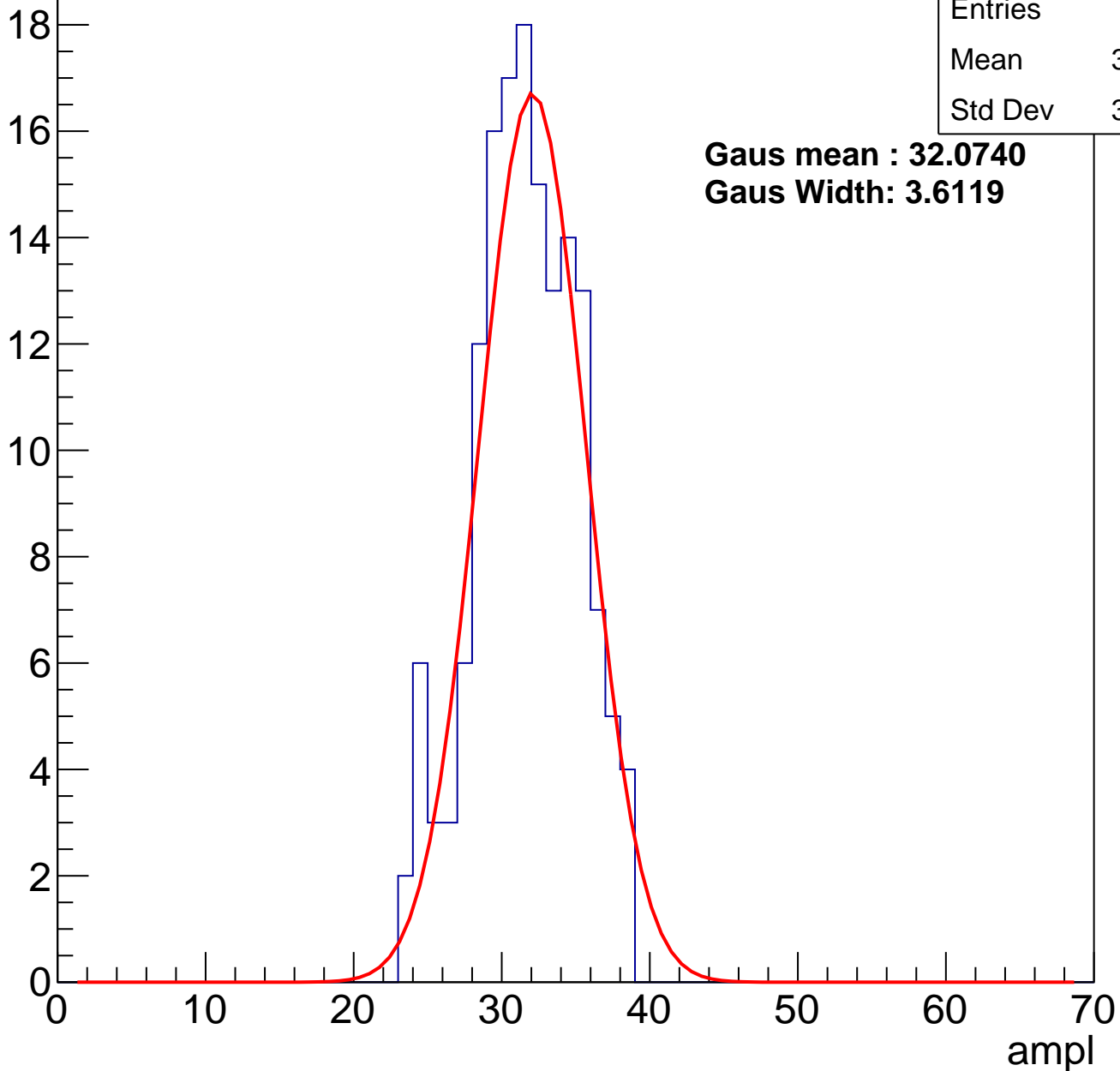
Entry



B1L001S, U26-ch67, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch67, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 38.26 |
| Std Dev | 3.324 |

Gaus mean : 38.4545

Gaus Width: 3.2758

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

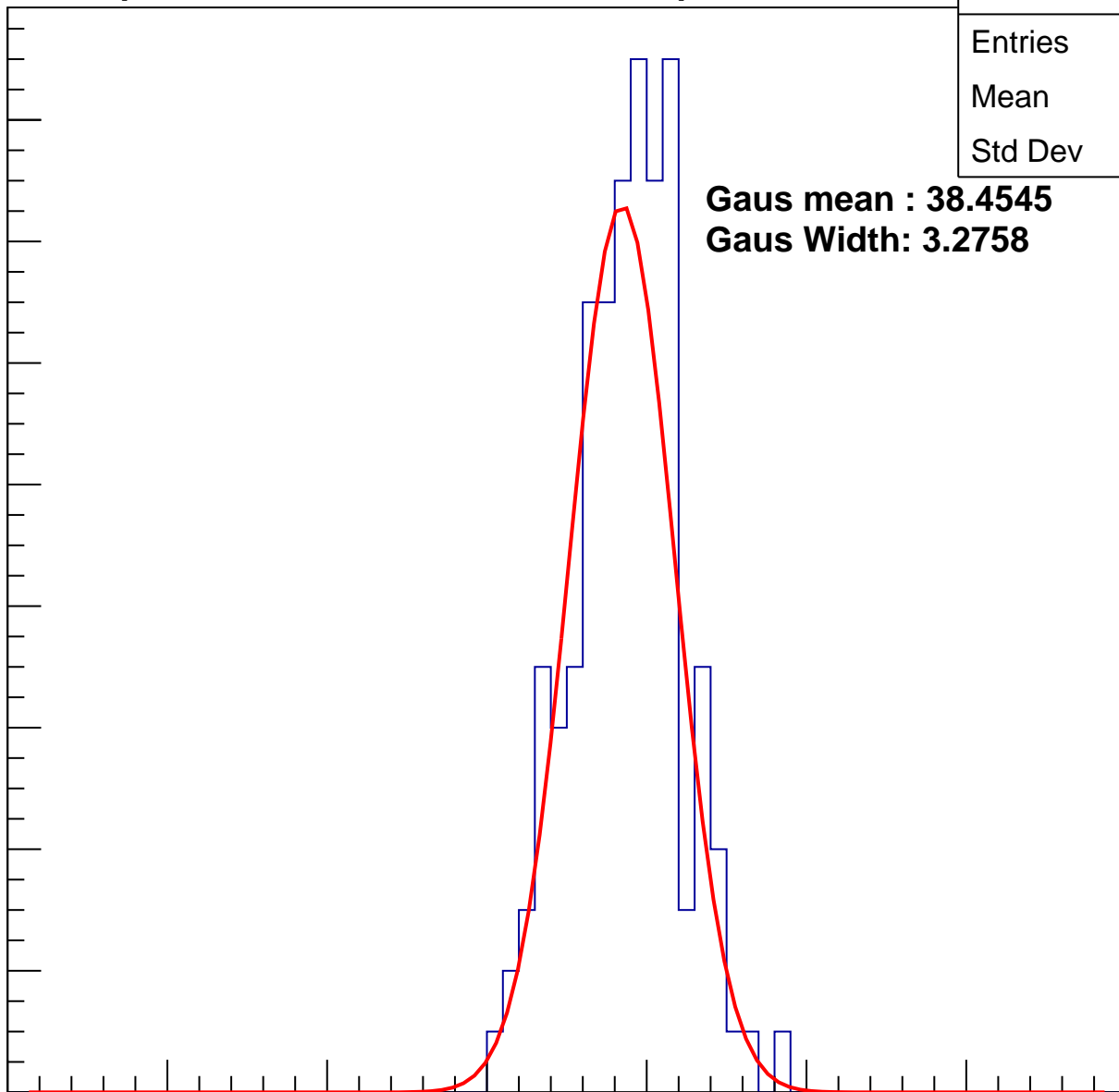
40

50

60

70

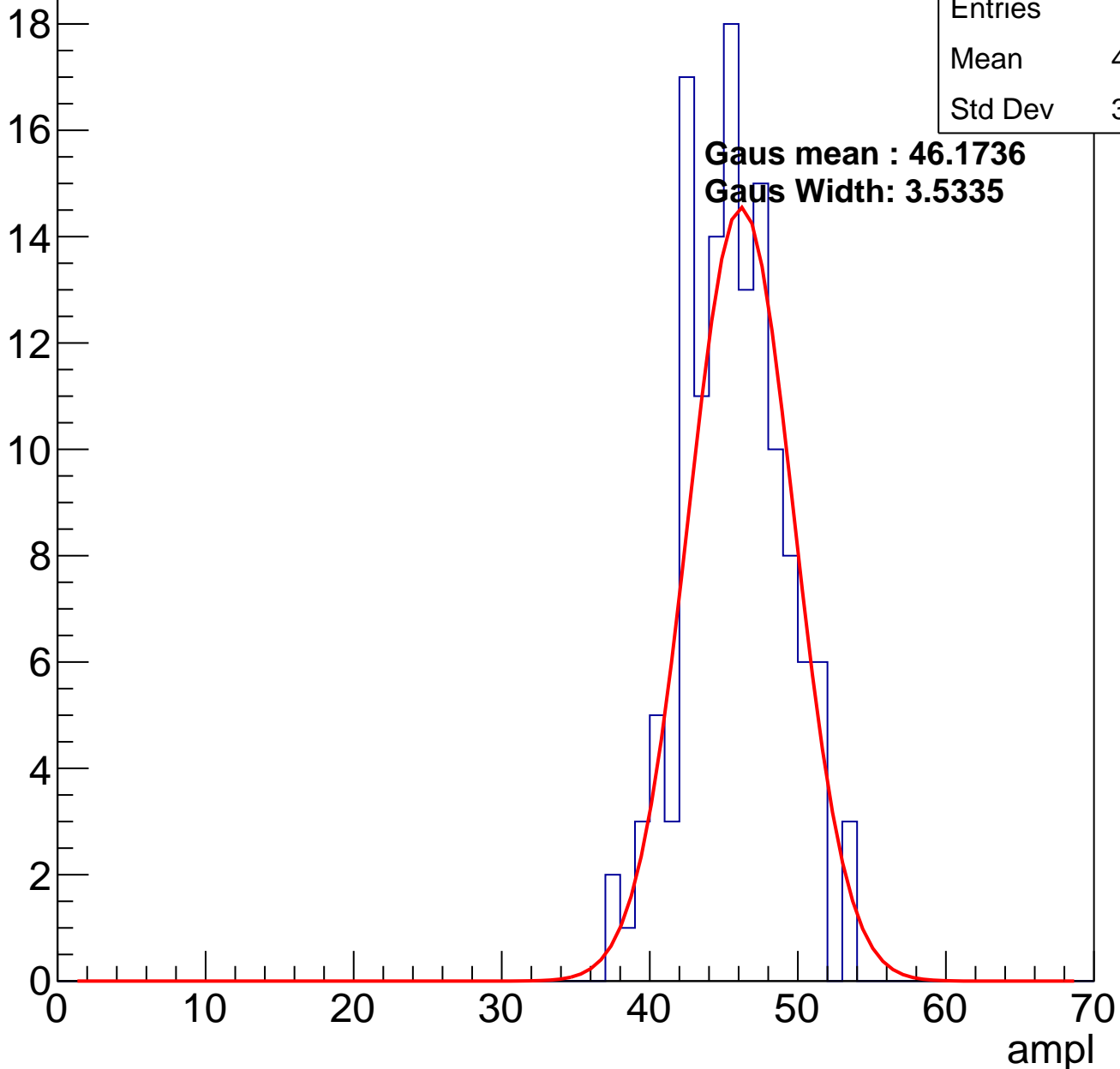
ampl



B1L001S, U26-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

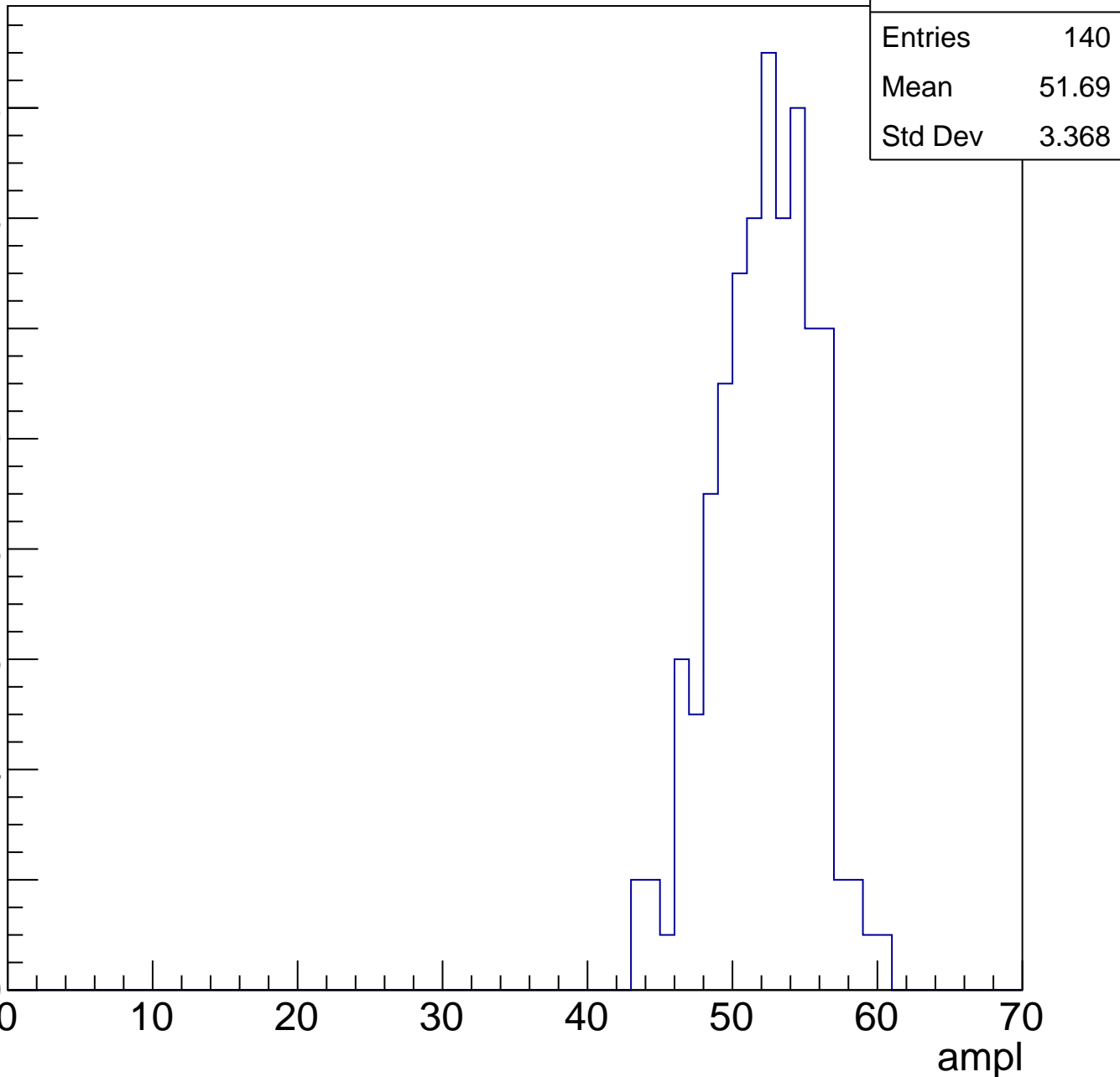
140

Mean

51.69

Std Dev

3.368



B1L001S, U26-ch67, adc4

calib_packv5_042523_0143.root, FC#2, port C2

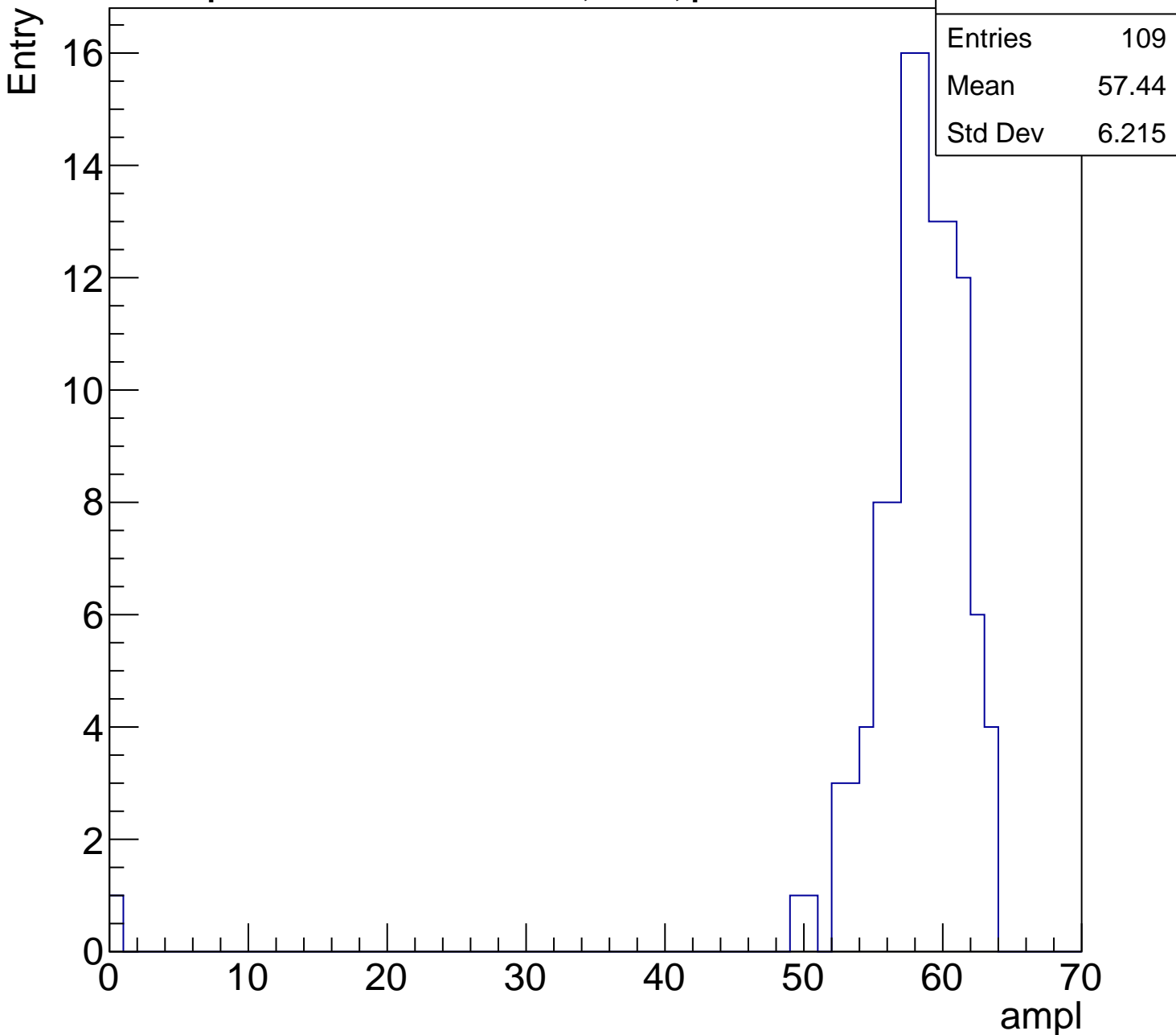
| | |
|---------|-------|
| Entries | 109 |
| Mean | 57.44 |
| Std Dev | 6.215 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch67, adc5

calib_packv5_042523_0143.root, FC#2, port C2

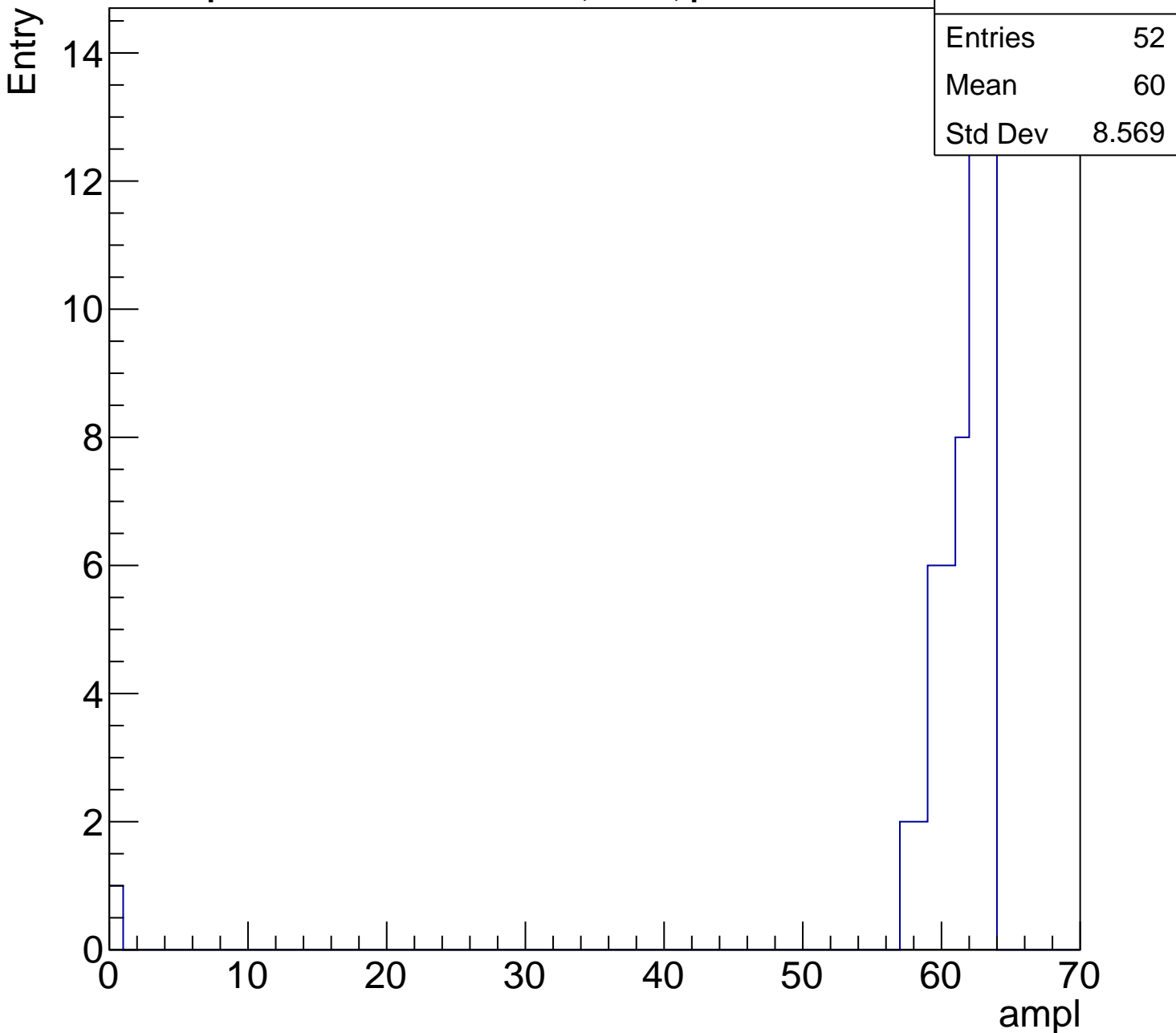
Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 52 |
| Mean | 60 |
| Std Dev | 8.569 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch67, adc7

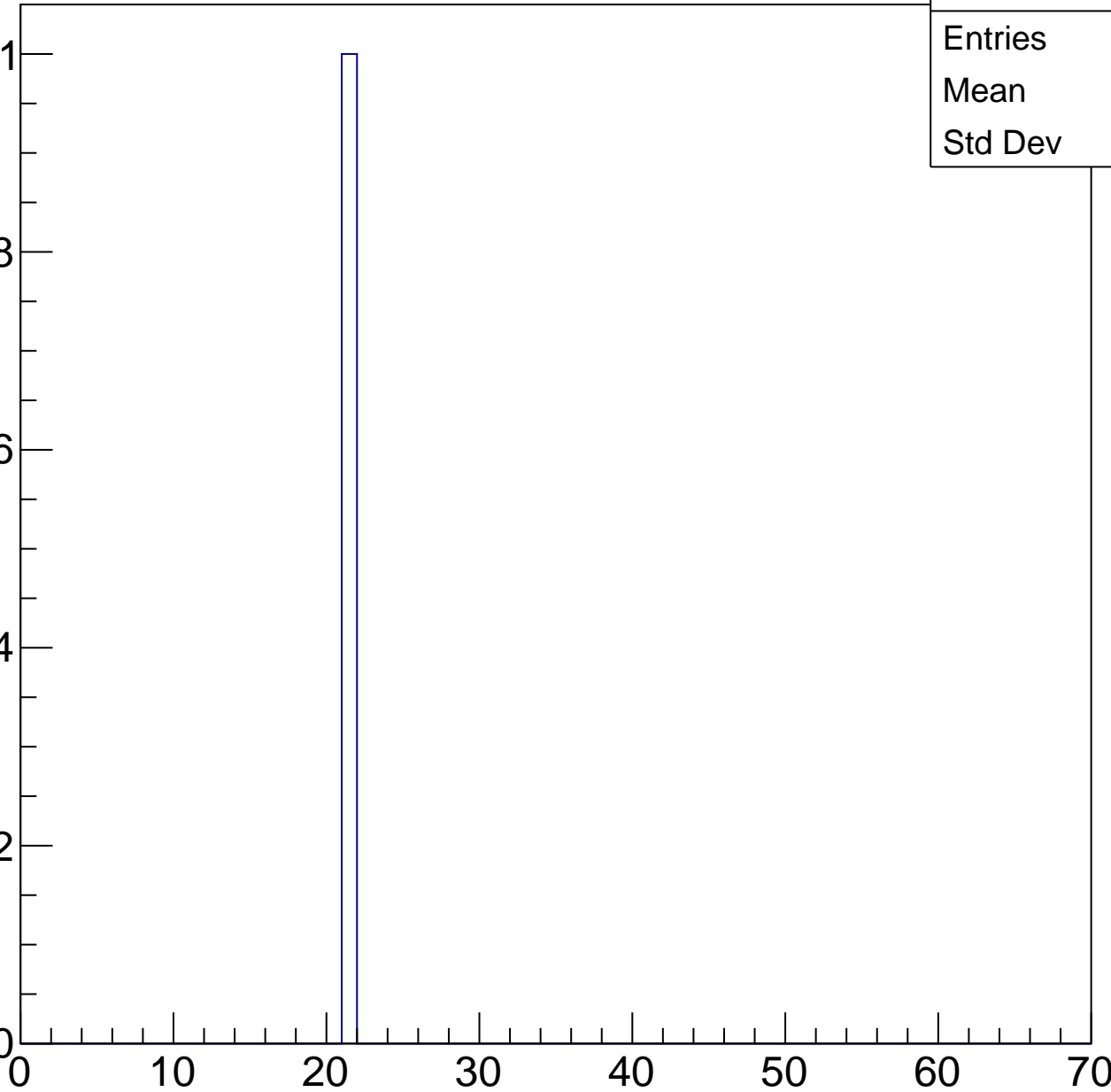
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl



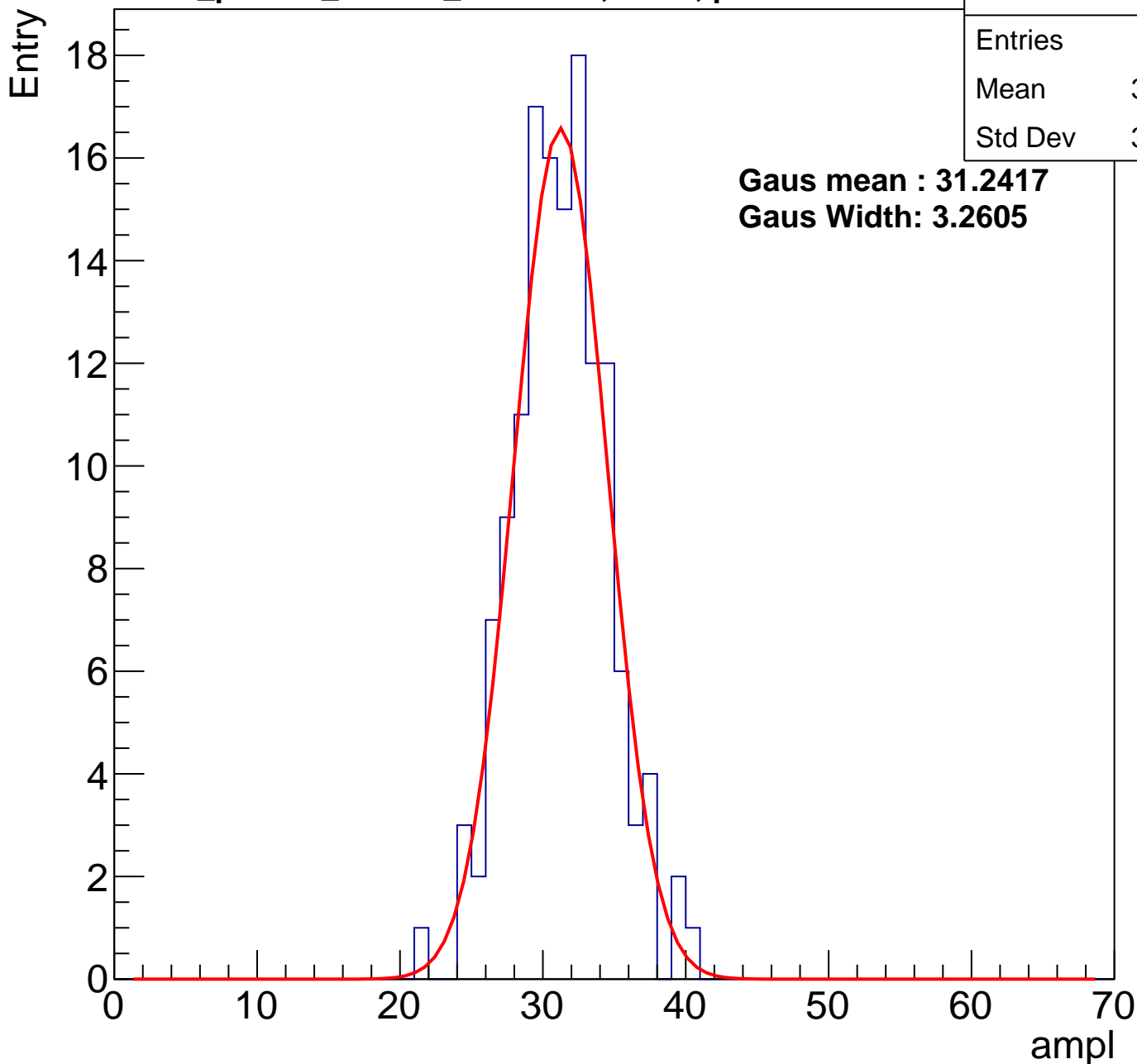
B1L001S, U26-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 30.78 |
| Std Dev | 3.298 |

Gaus mean : 31.2417

Gaus Width: 3.2605



B1L001S, U26-ch68, adc1

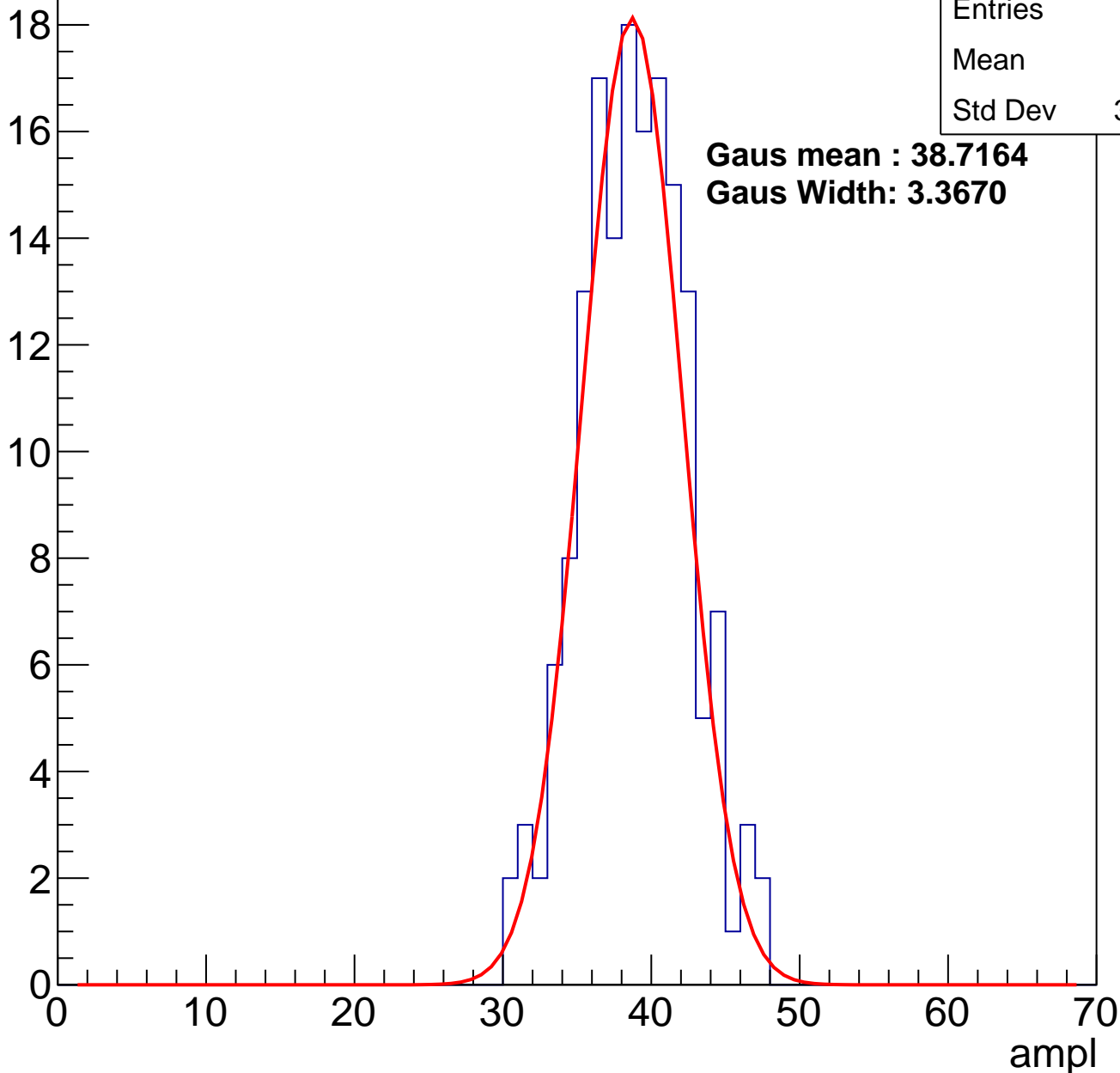
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 162 |
| Mean | 38.4 |
| Std Dev | 3.518 |

Gaus mean : 38.7164

Gaus Width: 3.3670

Entry



B1L001S, U26-ch68, adc2

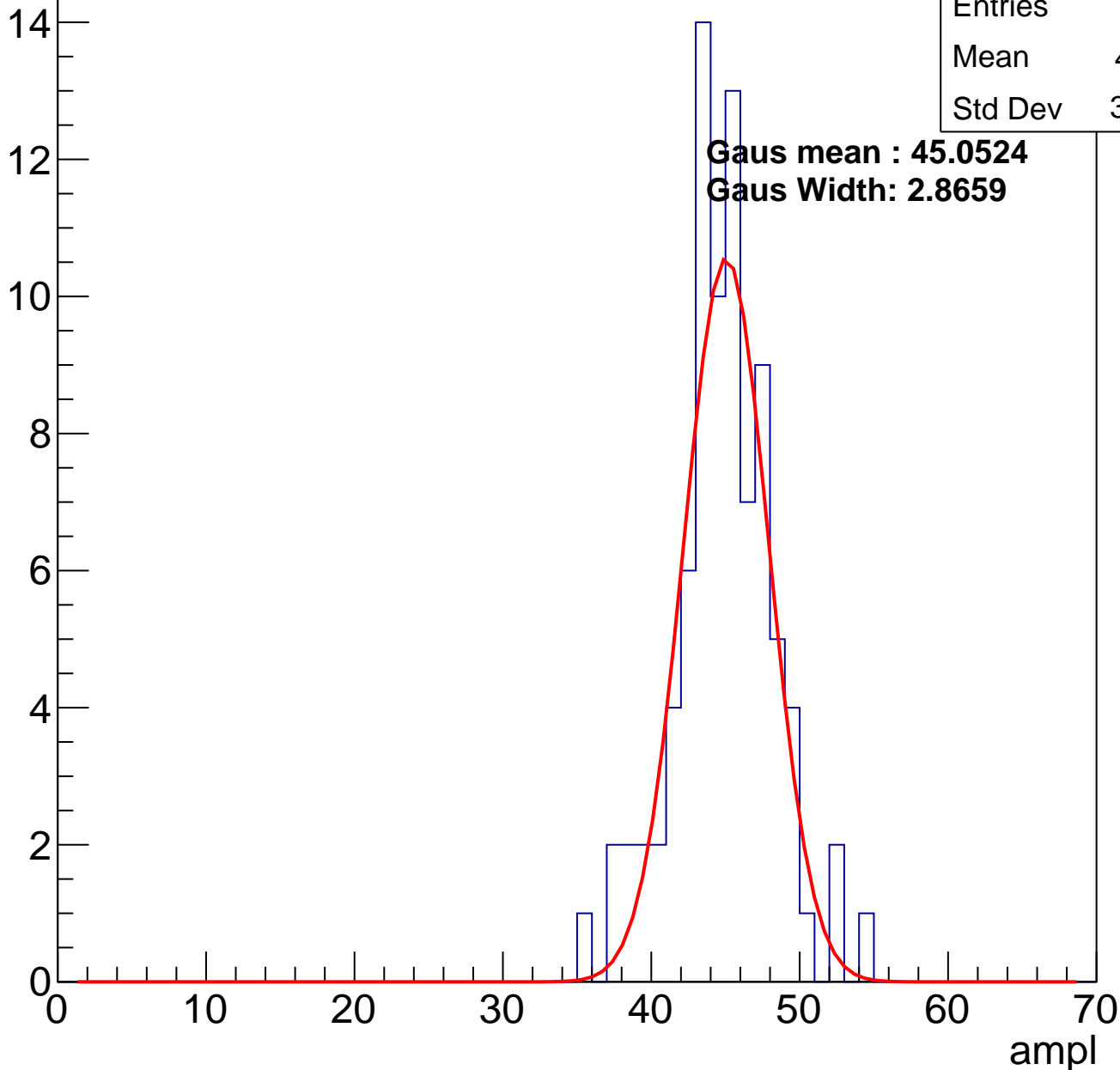
calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 44.41 |
| Std Dev | 3.358 |

Gaus mean : 45.0524

Gaus Width: 2.8659

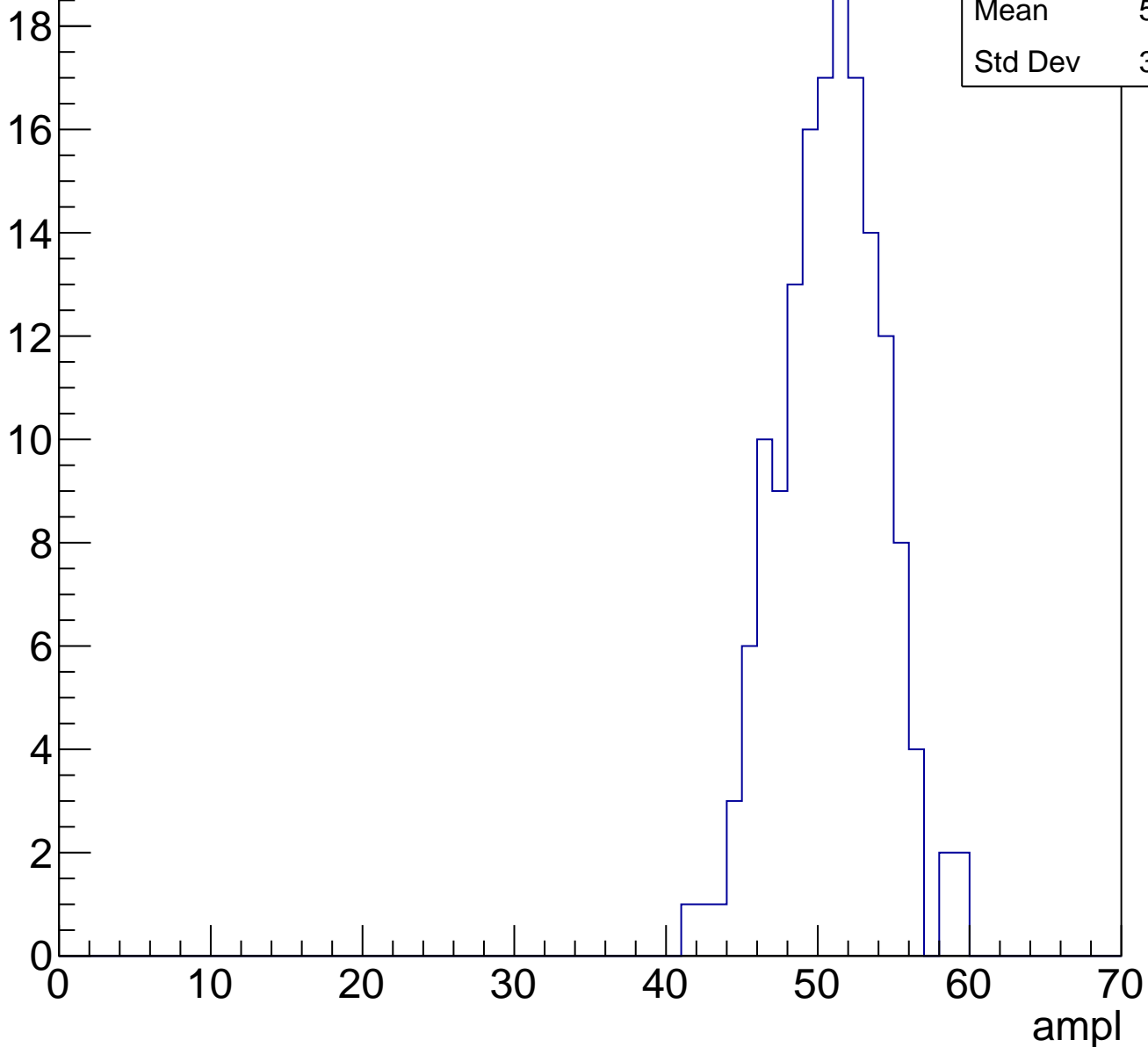


B1L001S, U26-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 155 |
| Mean | 50.39 |
| Std Dev | 3.386 |

Entry



B1L001S, U26-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

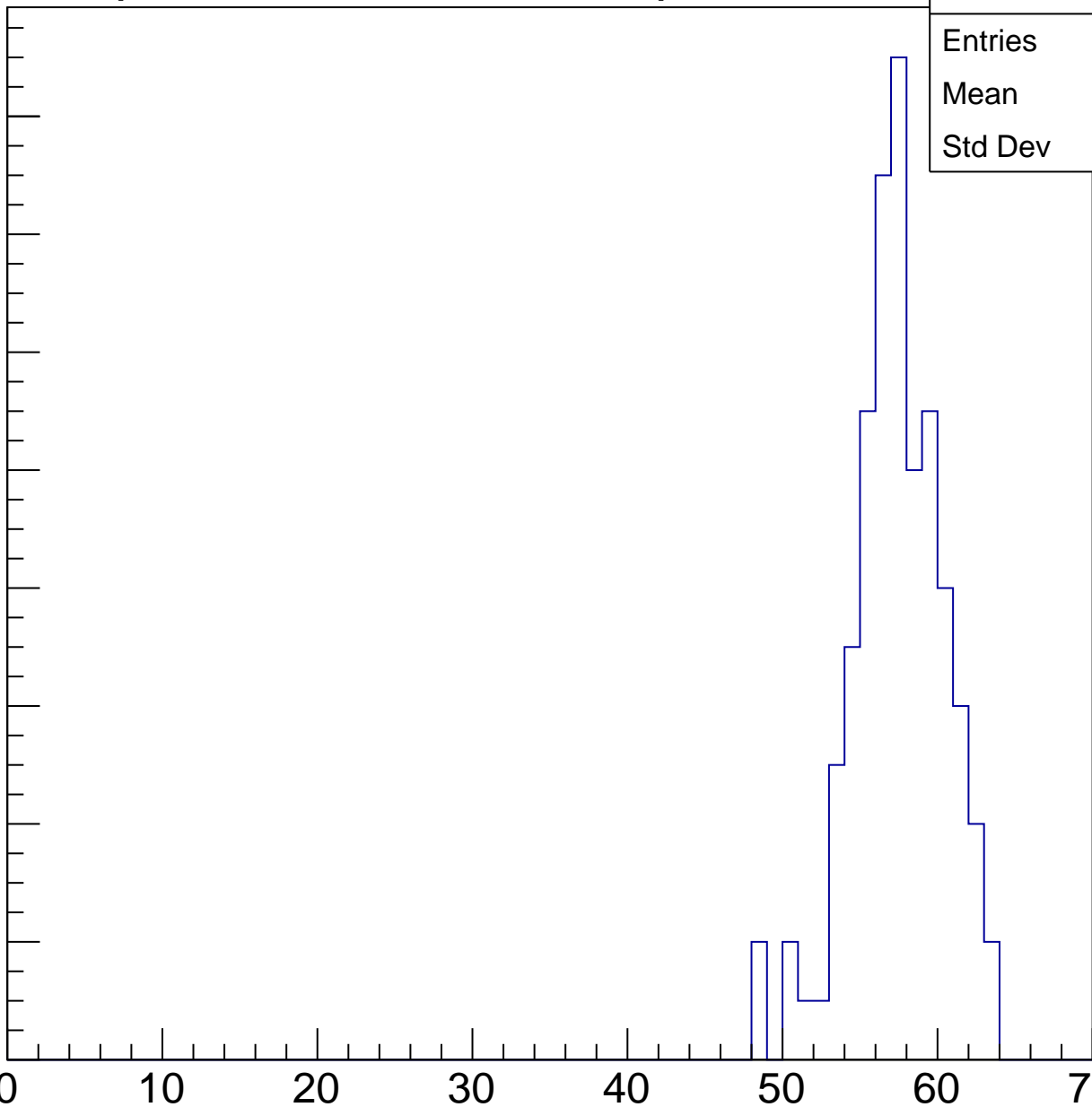
| | |
|---------|-------|
| Entries | 102 |
| Mean | 56.91 |
| Std Dev | 2.997 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

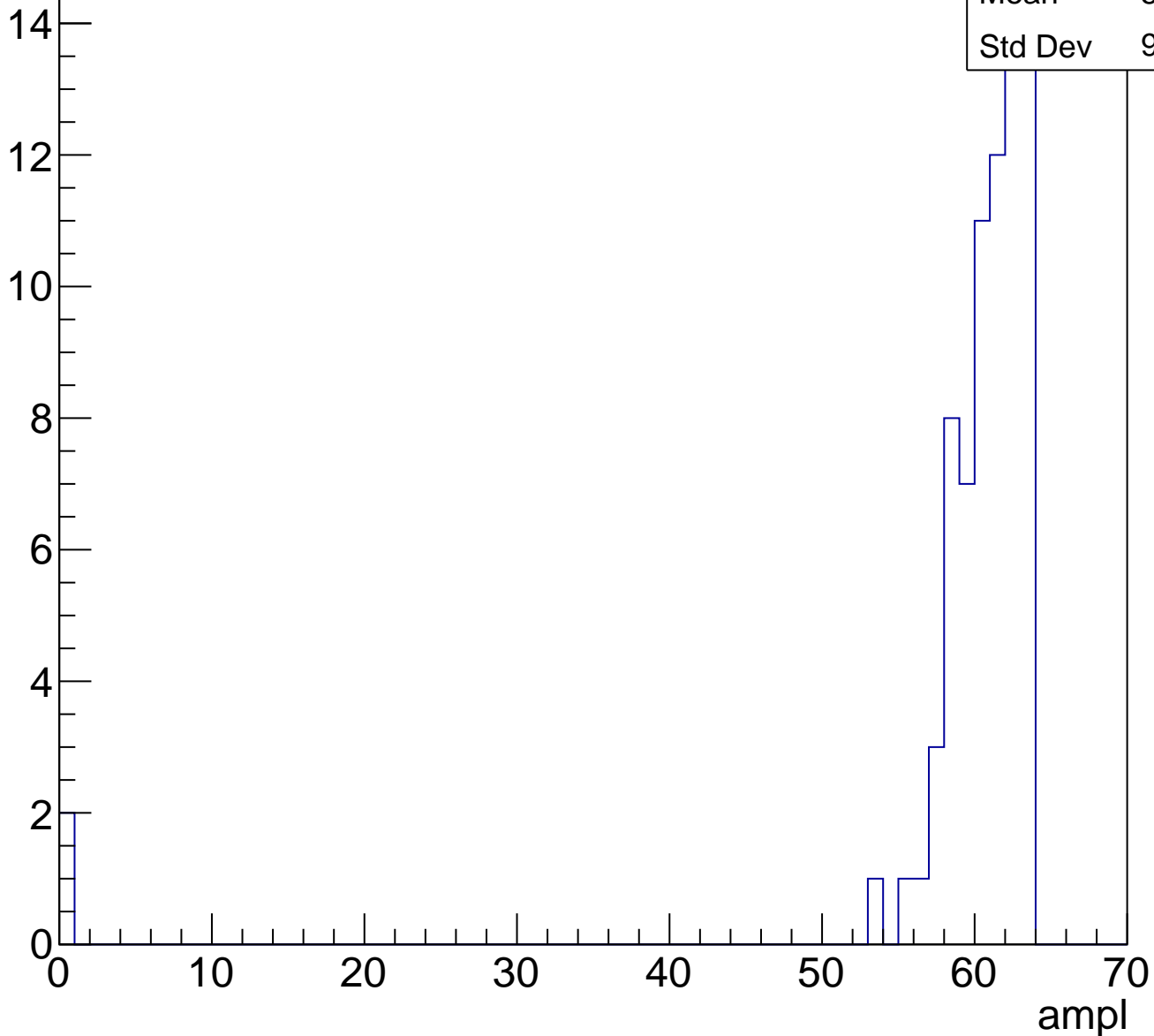


B1L001S, U26-ch68, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 58.89 |
| Std Dev | 9.978 |

Entry



B1L001S, U26-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch69, adc0

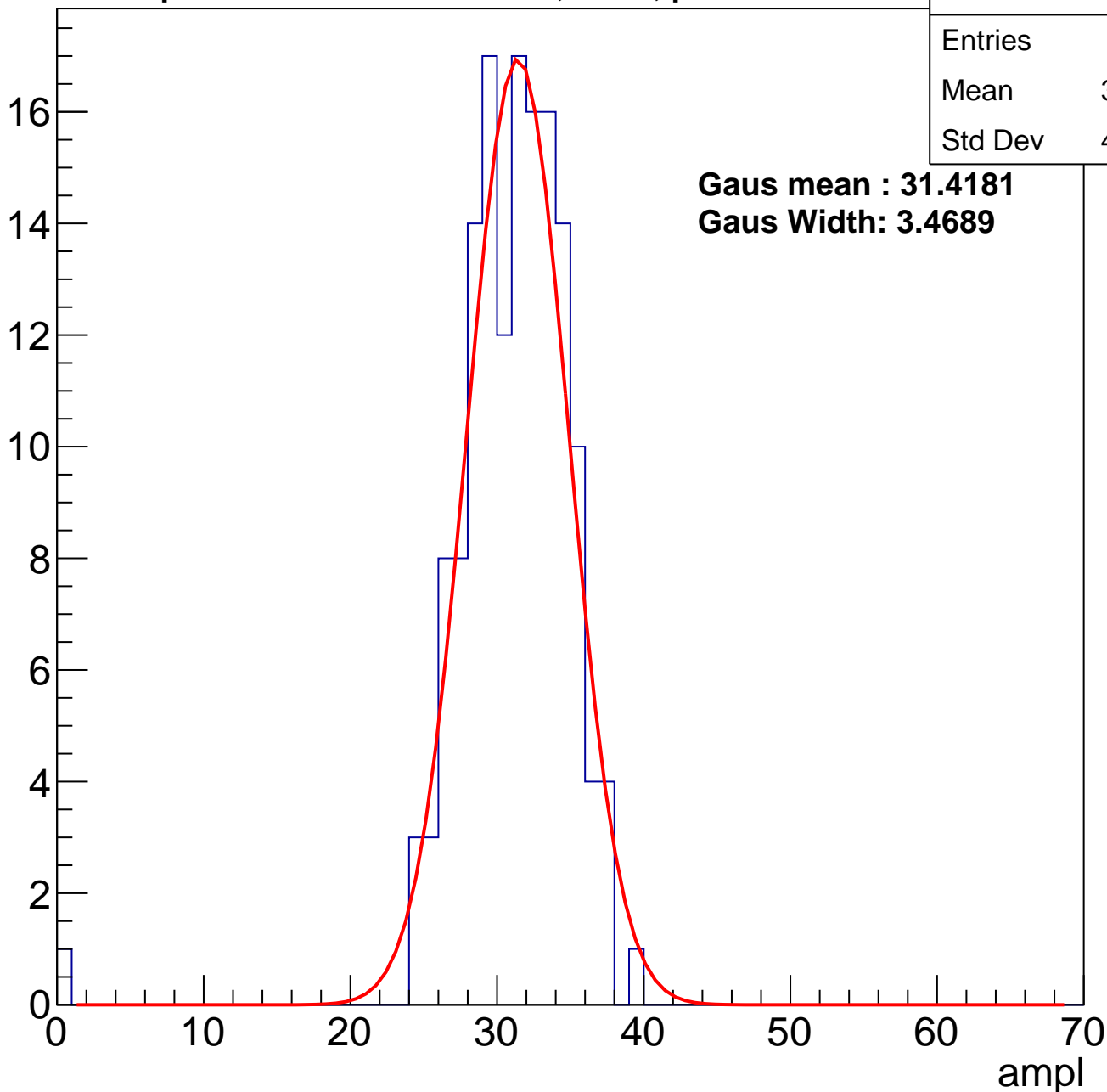
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 30.68 |
| Std Dev | 4.034 |

Gaus mean : 31.4181

Gaus Width: 3.4689

Entry



B1L001S, U26-ch69, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 37.41 |
| Std Dev | 3.363 |

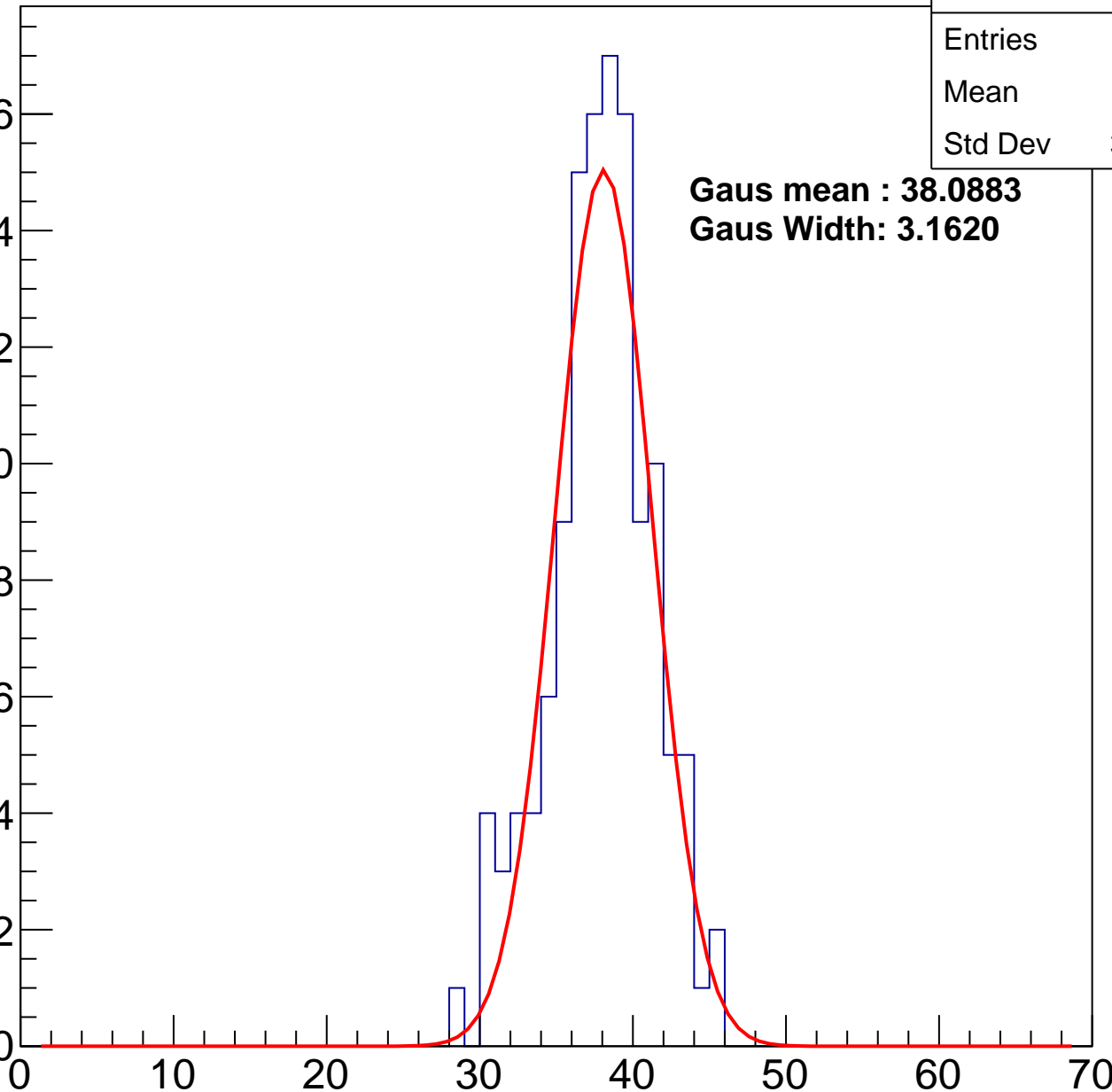
Gaus mean : 38.0883

Gaus Width: 3.1620

Entry

16
14
12
10
8
6
4
2
0

ampl

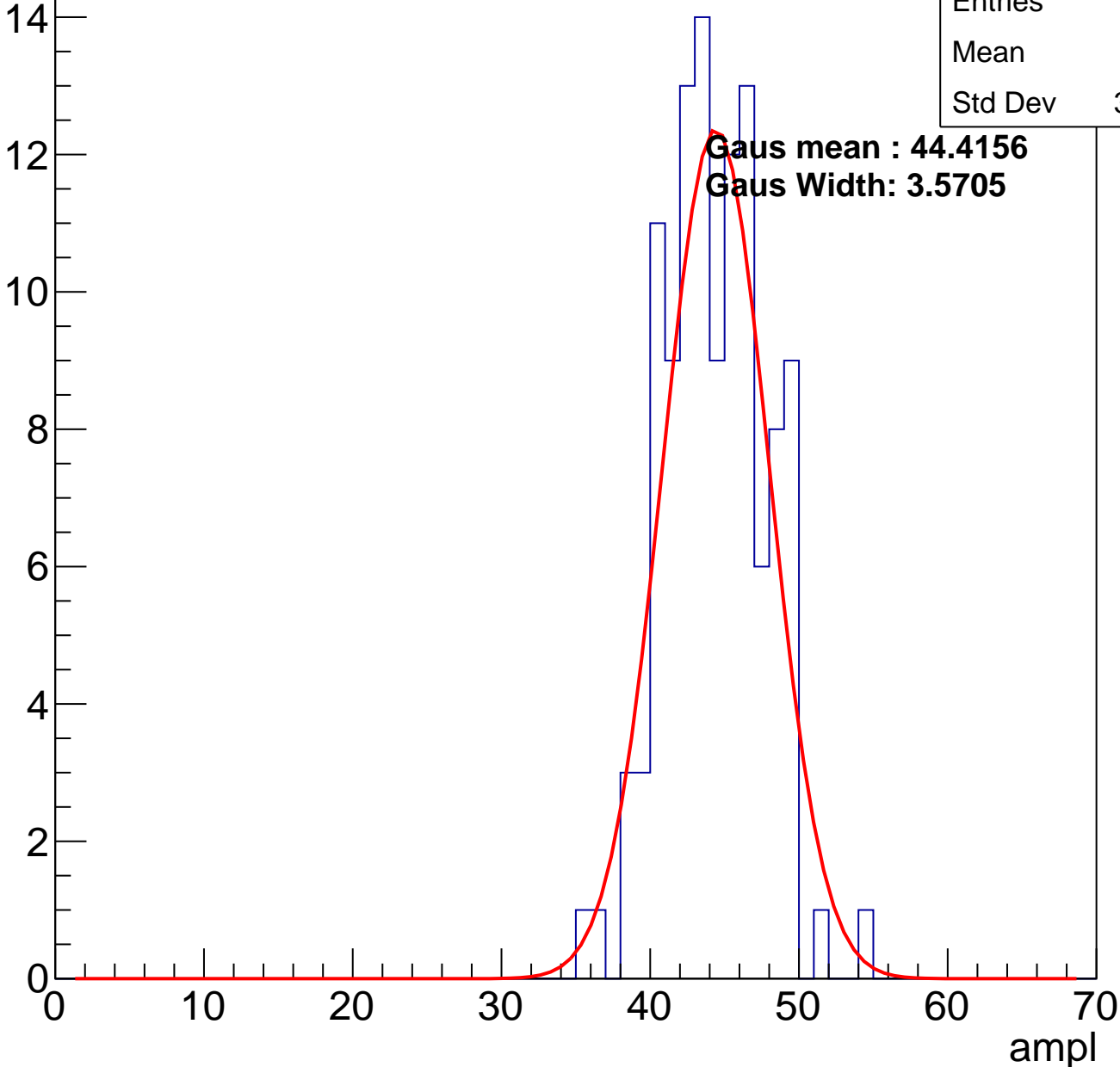


B1L001S, U26-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 114 |
| Mean | 43.9 |
| Std Dev | 3.338 |

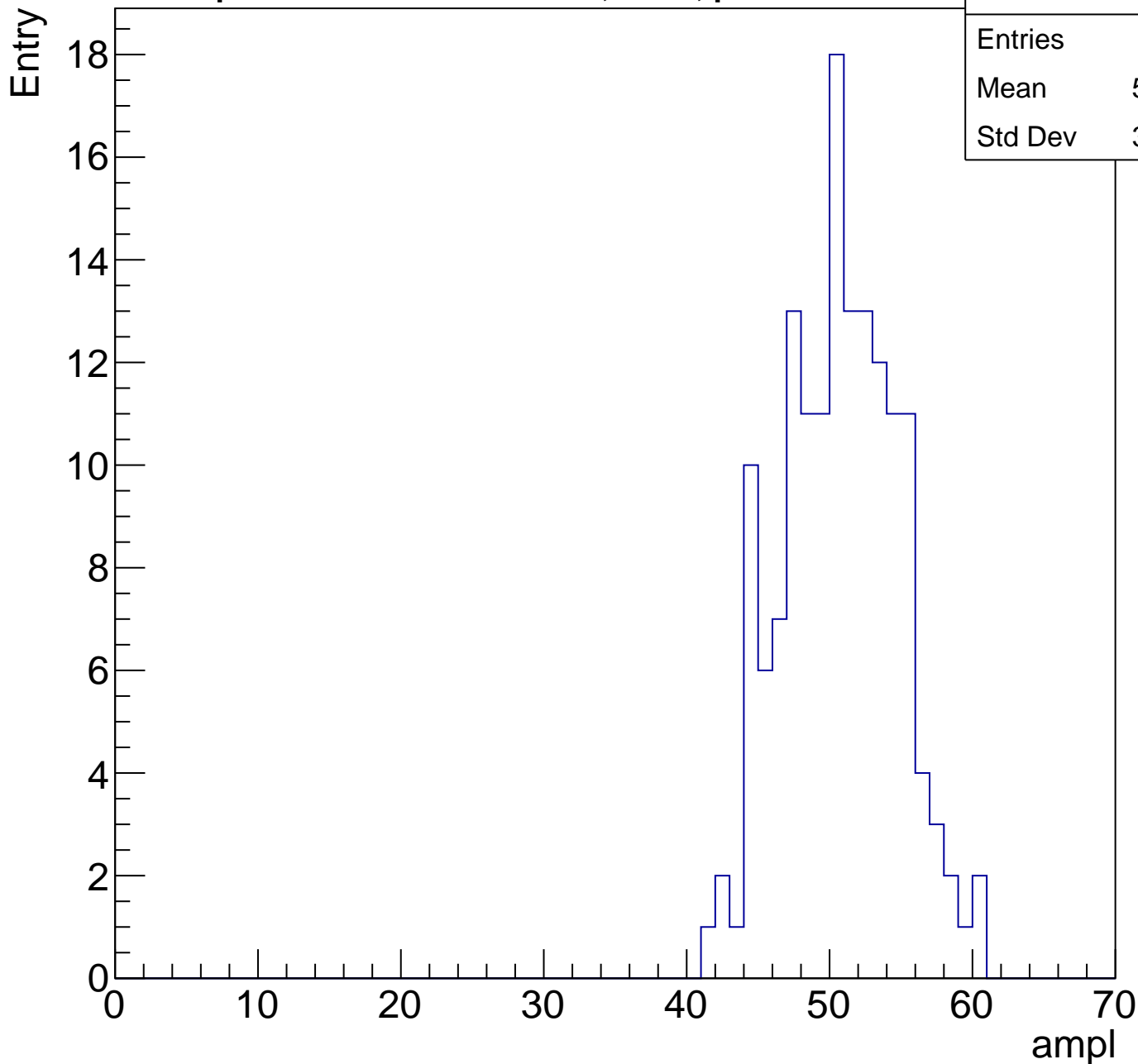
Entry



B1L001S, U26-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 50.28 |
| Std Dev | 3.944 |

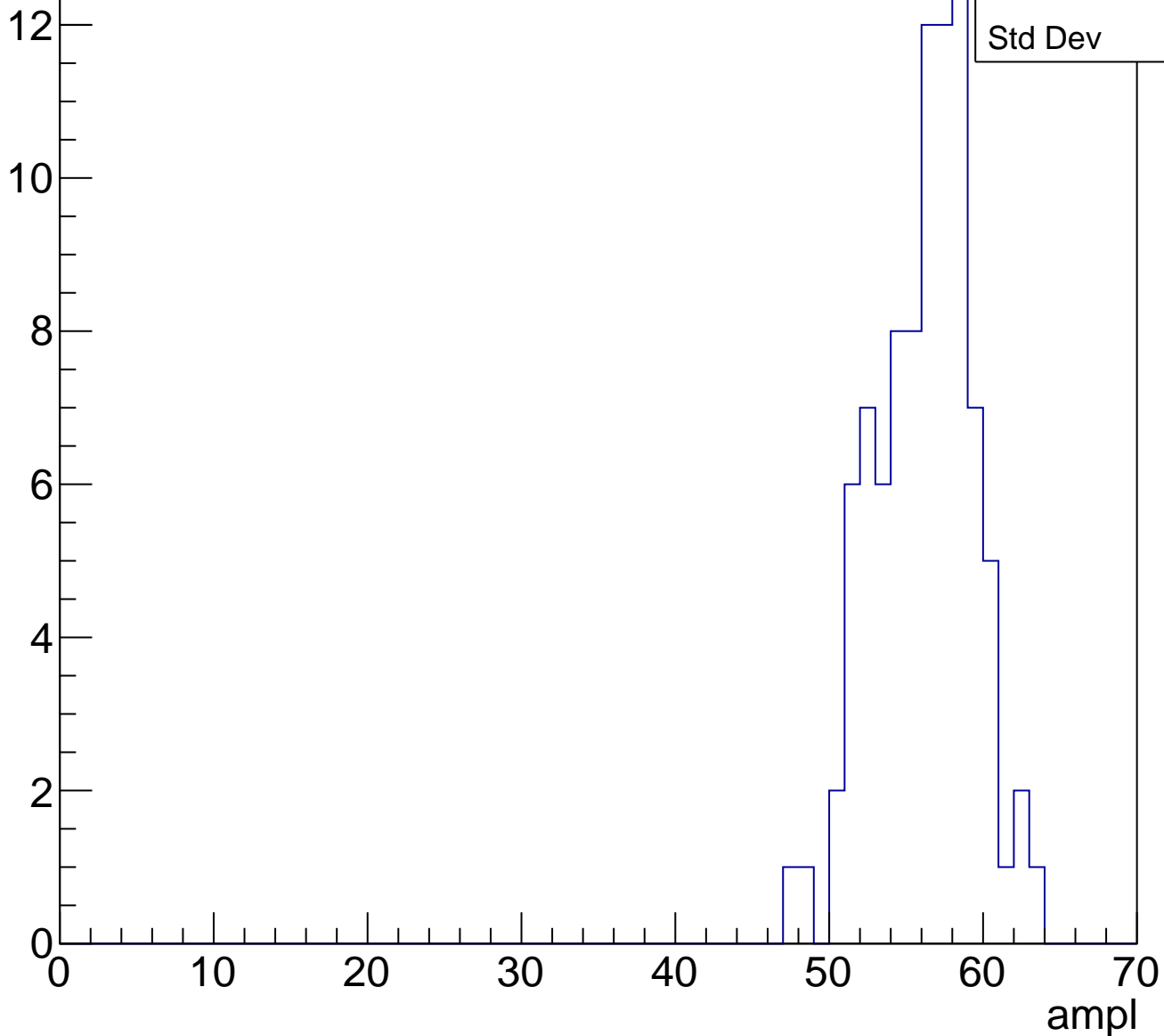


B1L001S, U26-ch69, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 92 |
| Mean | 55.72 |
| Std Dev | 3.16 |

Entry



B1L001S, U26-ch69, adc5

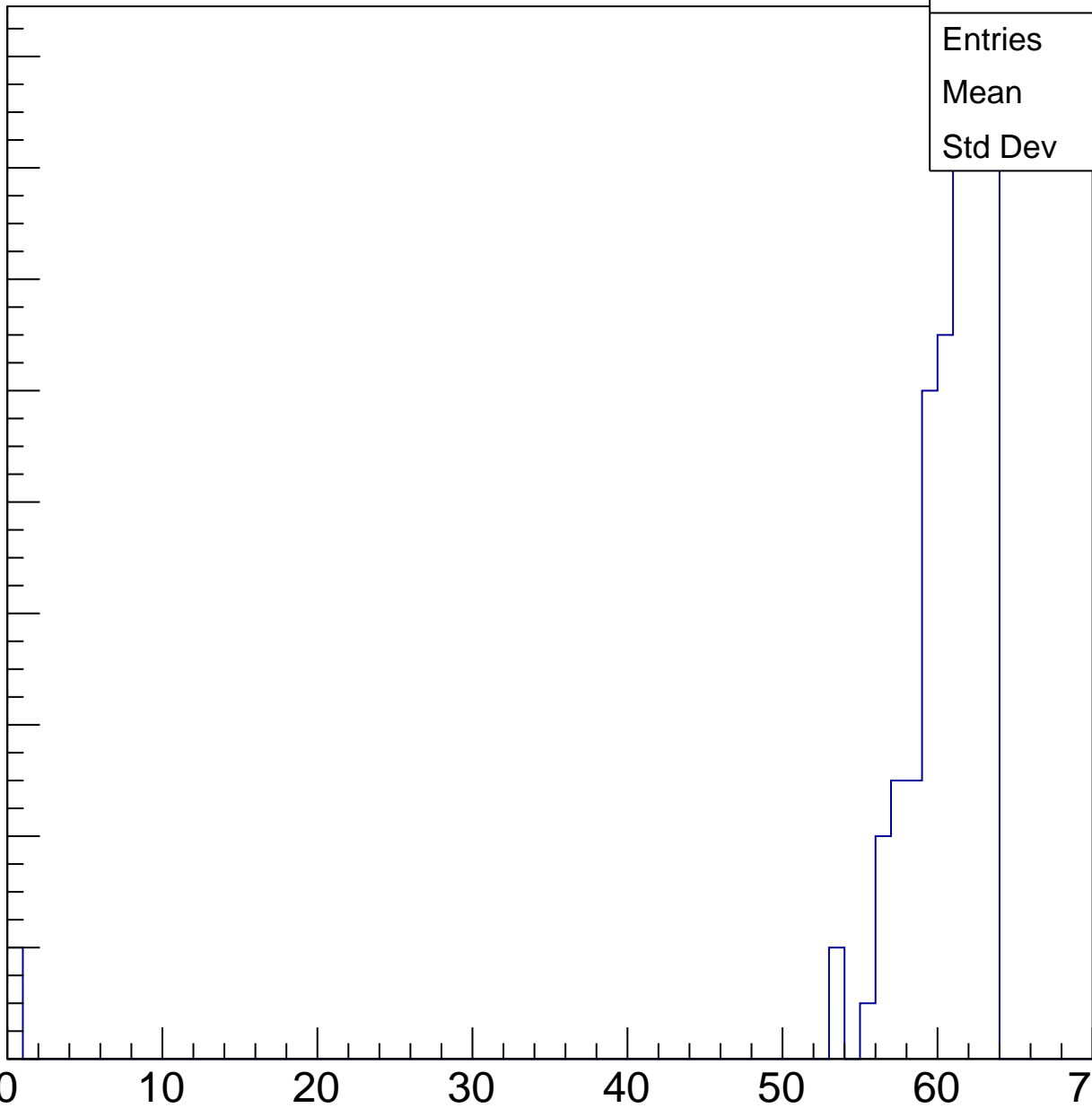
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 97 |
| Mean | 59.1 |
| Std Dev | 8.868 |

ampl



B1L001S, U26-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch70, adc0

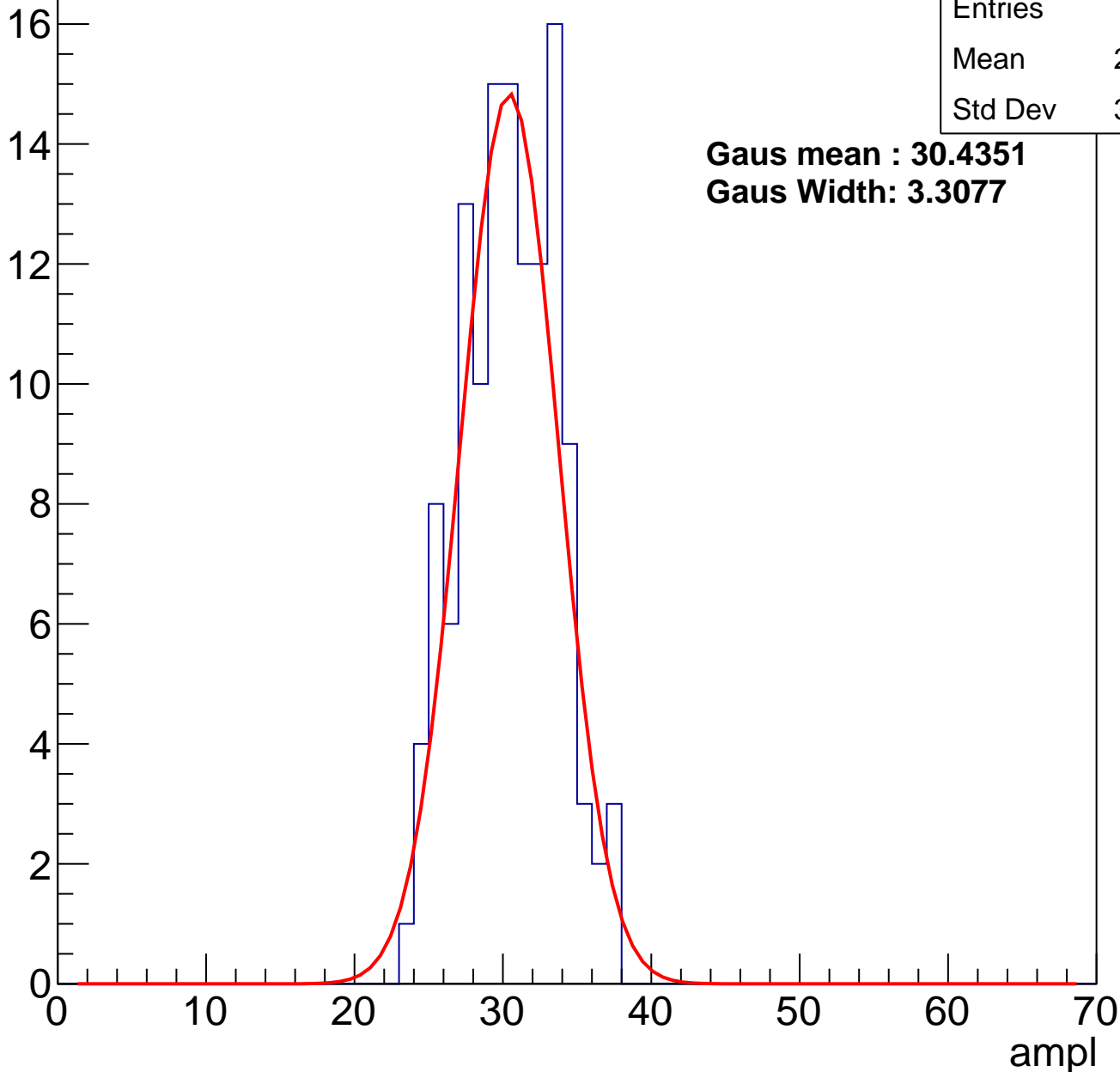
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 29.99 |
| Std Dev | 3.178 |

Gaus mean : 30.4351

Gaus Width: 3.3077

Entry



B1L001S, U26-ch70, adc1

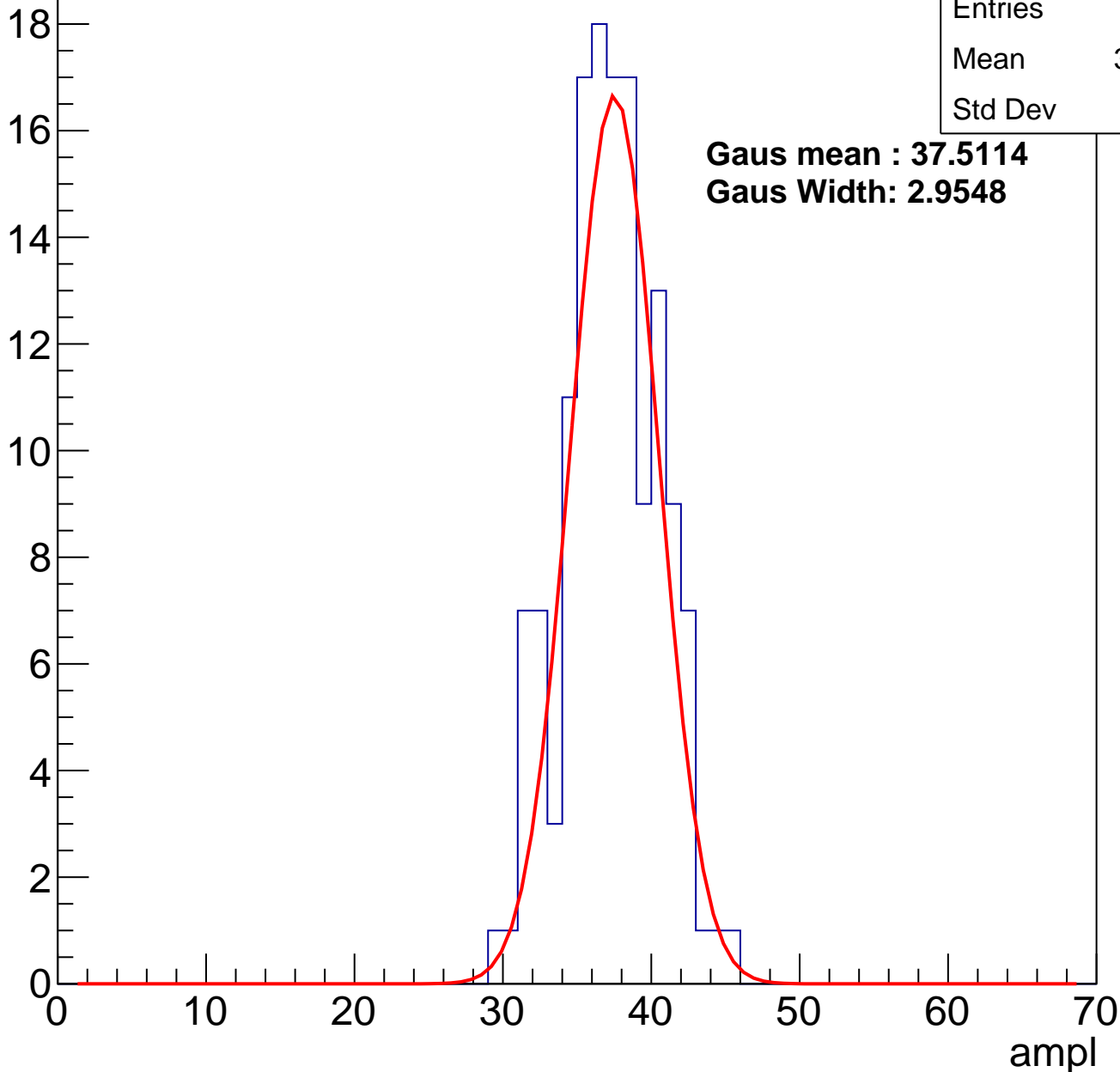
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 36.84 |
| Std Dev | 3.17 |

Gaus mean : 37.5114

Gaus Width: 2.9548

Entry



B1L001S, U26-ch70, adc2

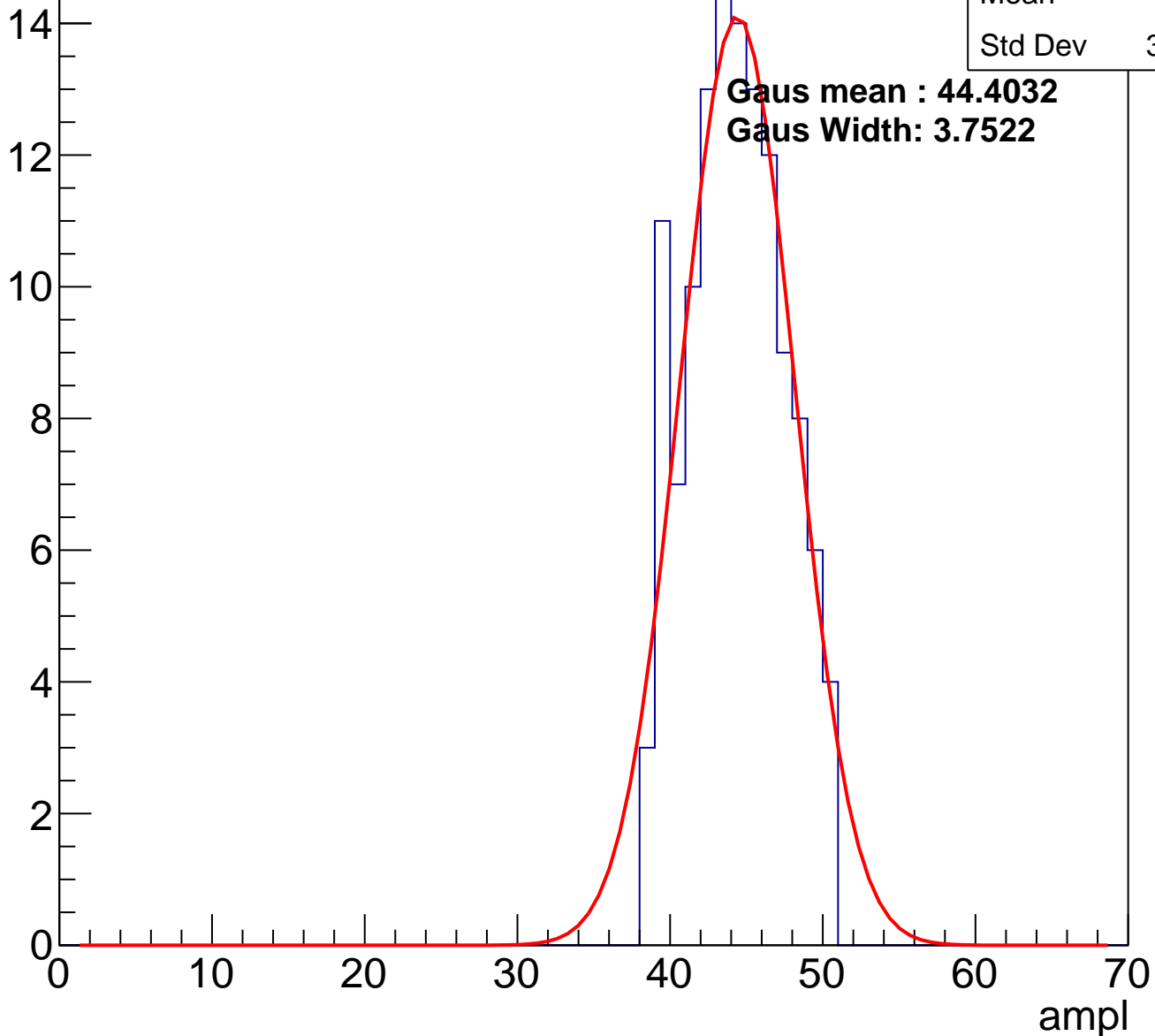
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 43.82 |
| Std Dev | 3.114 |

Gaus mean : 44.4032

Gaus Width: 3.7522

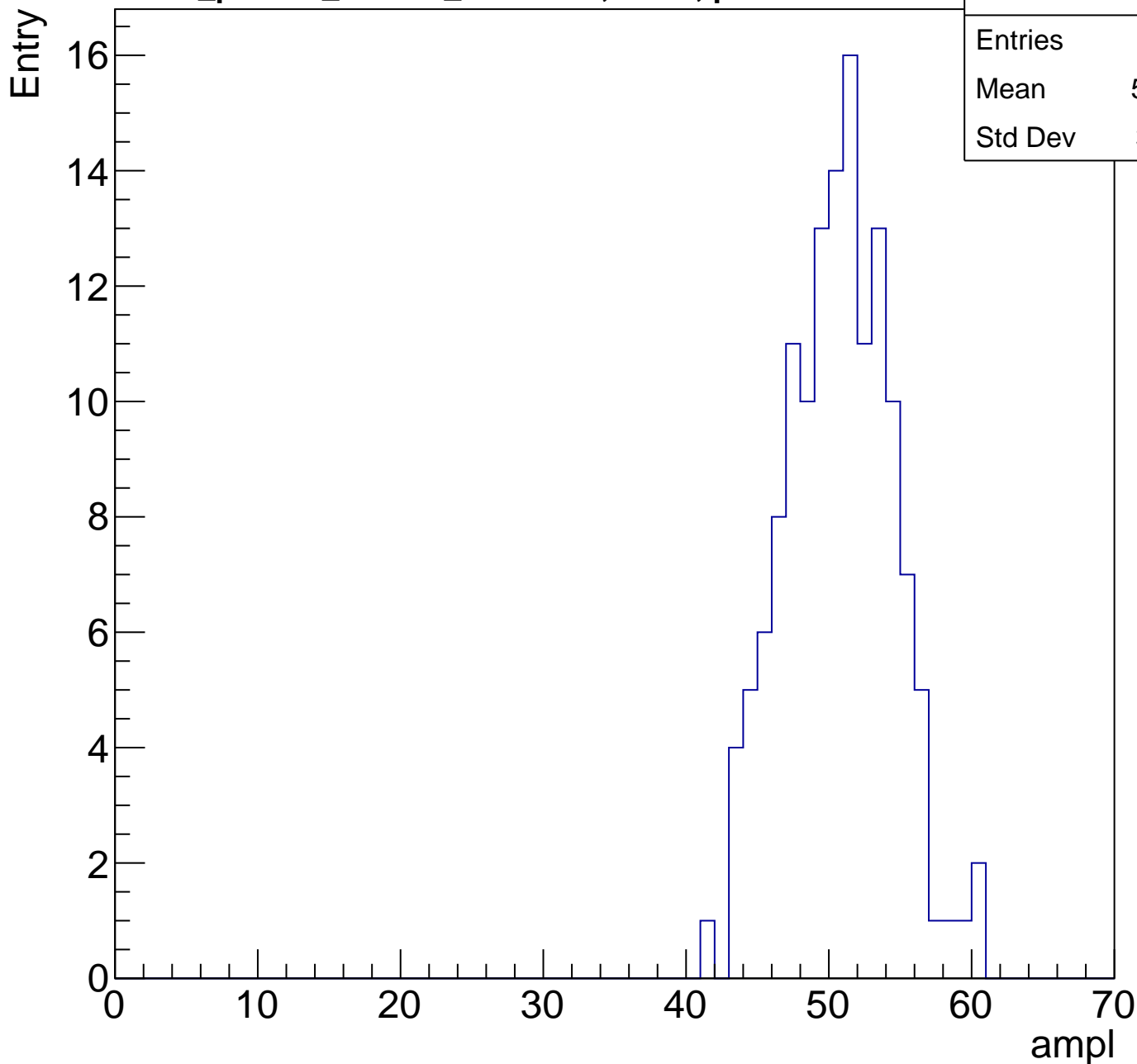
Entry



B1L001S, U26-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 50.22 |
| Std Dev | 3.761 |



B1L001S, U26-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 102 |
| Mean | 55.81 |
| Std Dev | 3.223 |

Entry

12

10

8

6

4

2

0

0

10

20

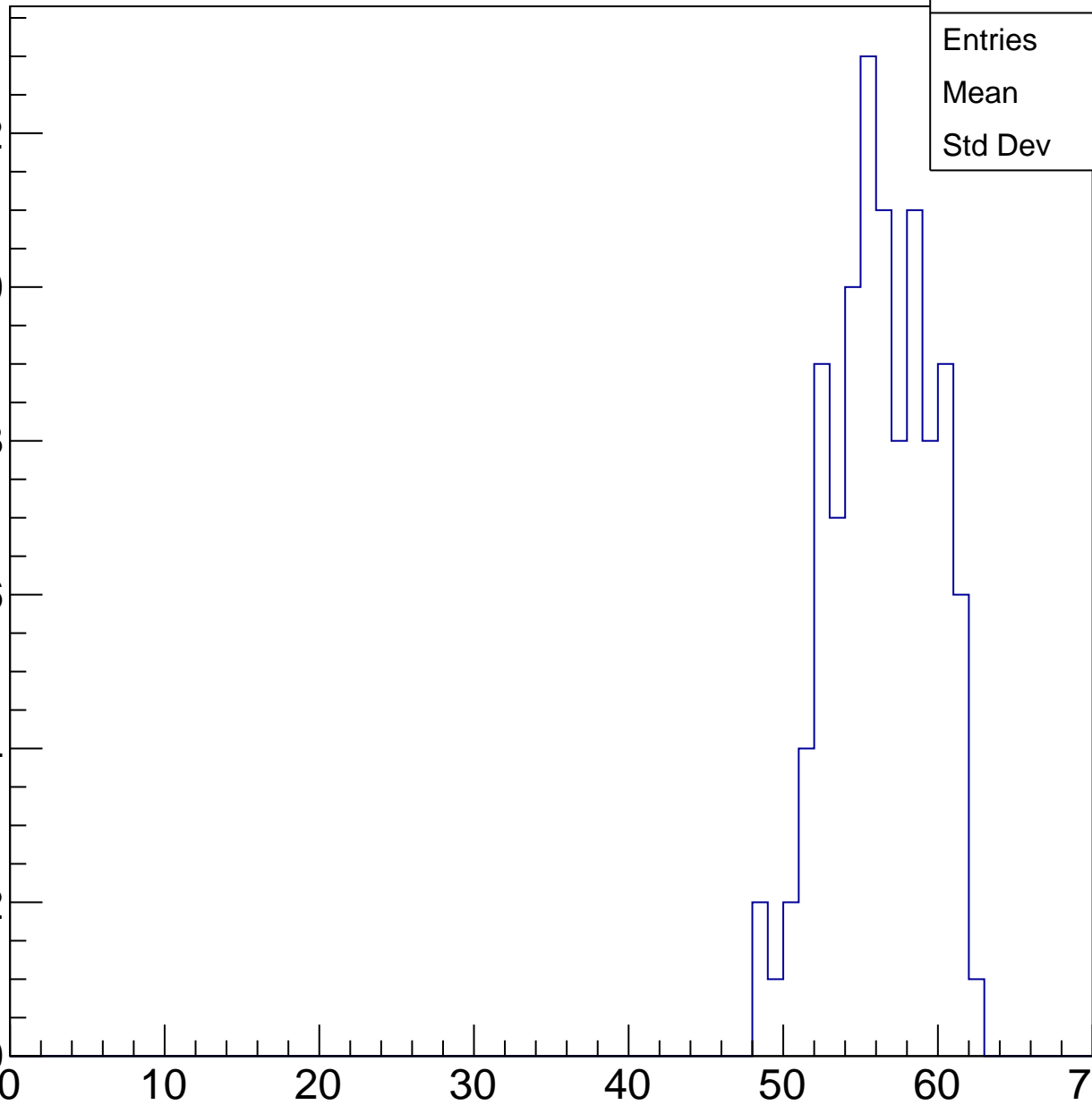
30

40

50

60

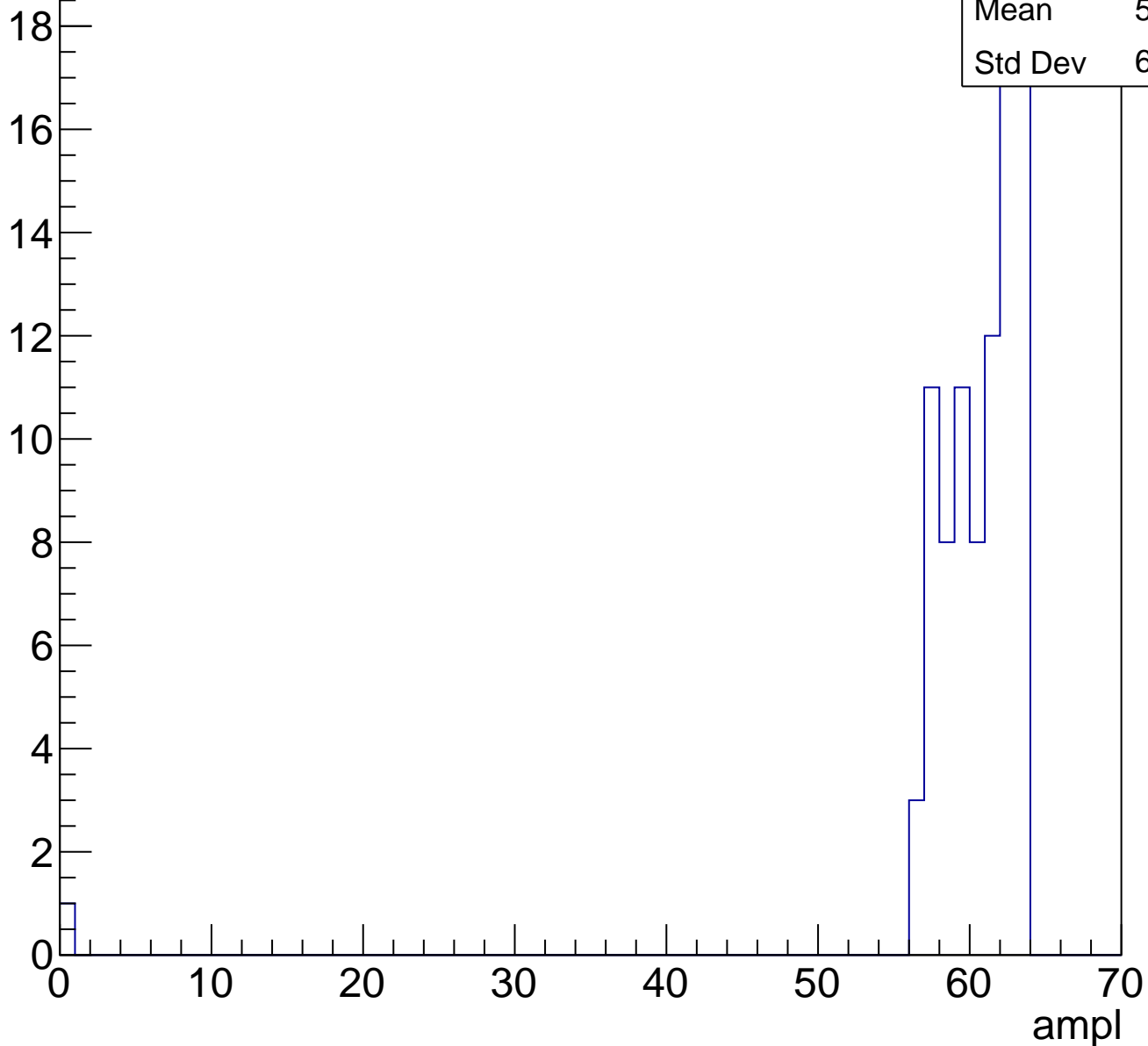
ampl



B1L001S, U26-ch70, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

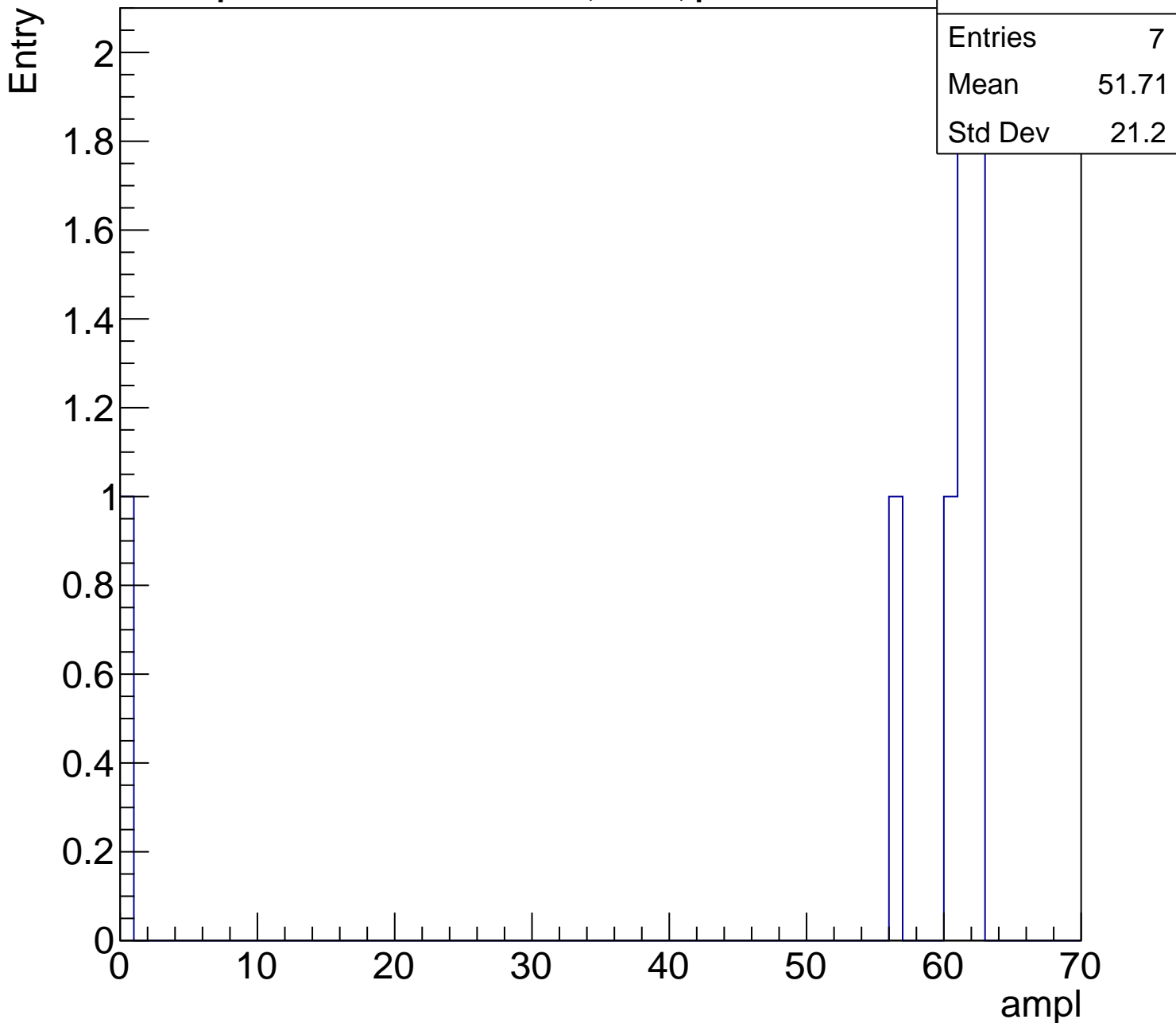
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 7 |
| Mean | 51.71 |
| Std Dev | 21.2 |

0 10 20 30 40 50 60 70

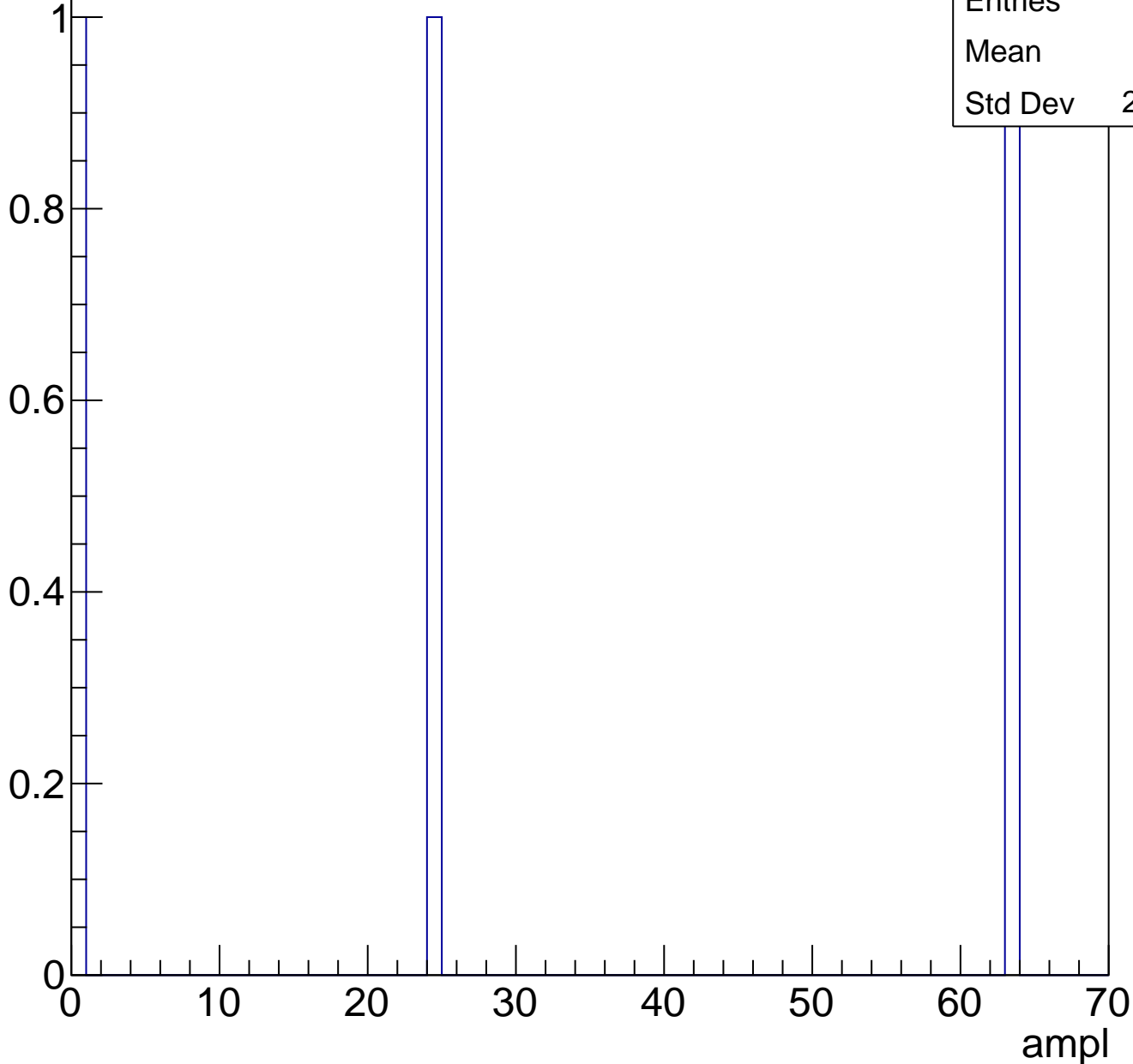
ampl



B1L001S, U26-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 29.2 |
| Std Dev | 4.394 |

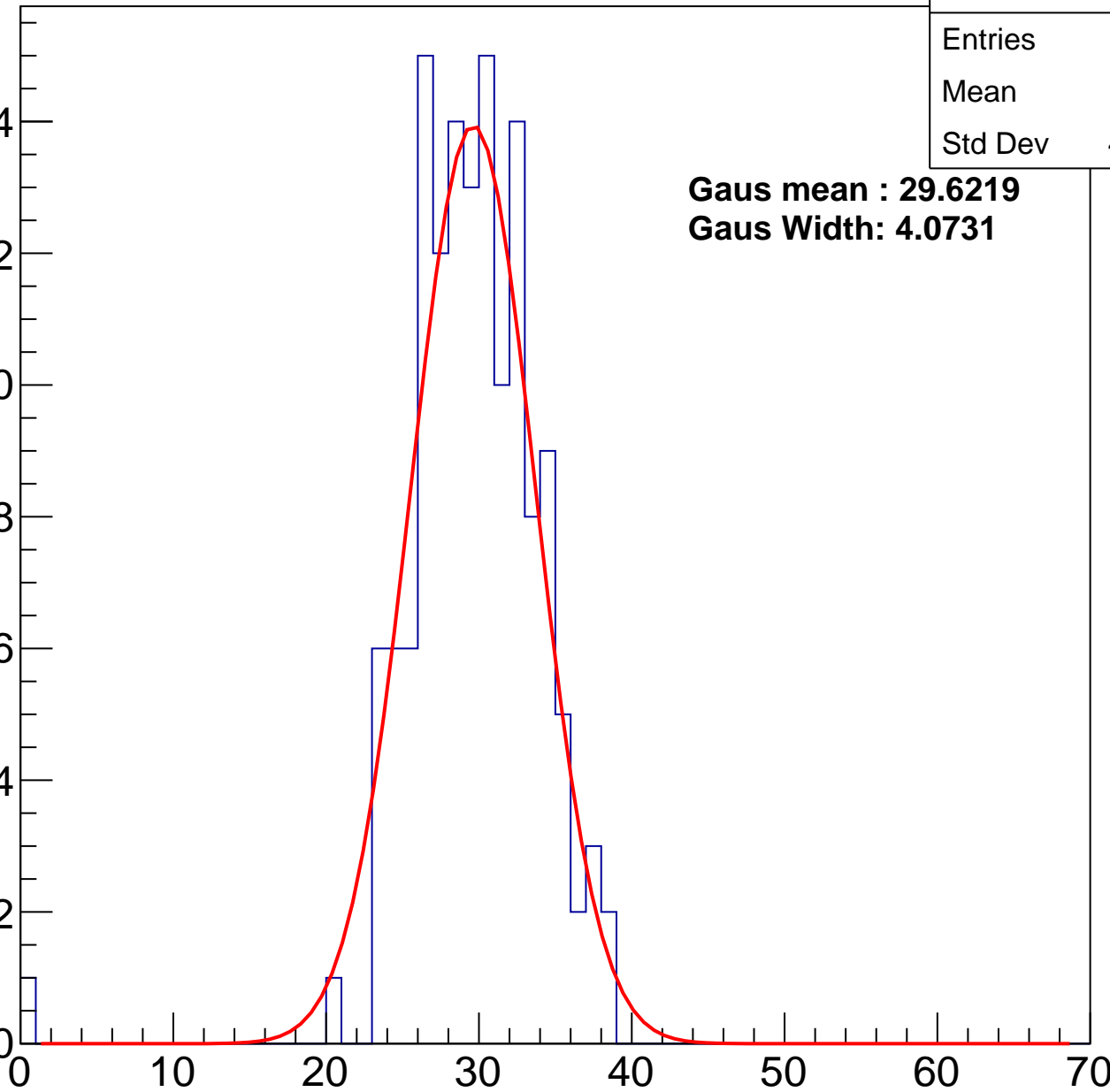
Gaus mean : 29.6219

Gaus Width: 4.0731

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch71, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 36.22 |
| Std Dev | 3.318 |

Gaus mean : 36.8418

Gaus Width: 3.1799

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

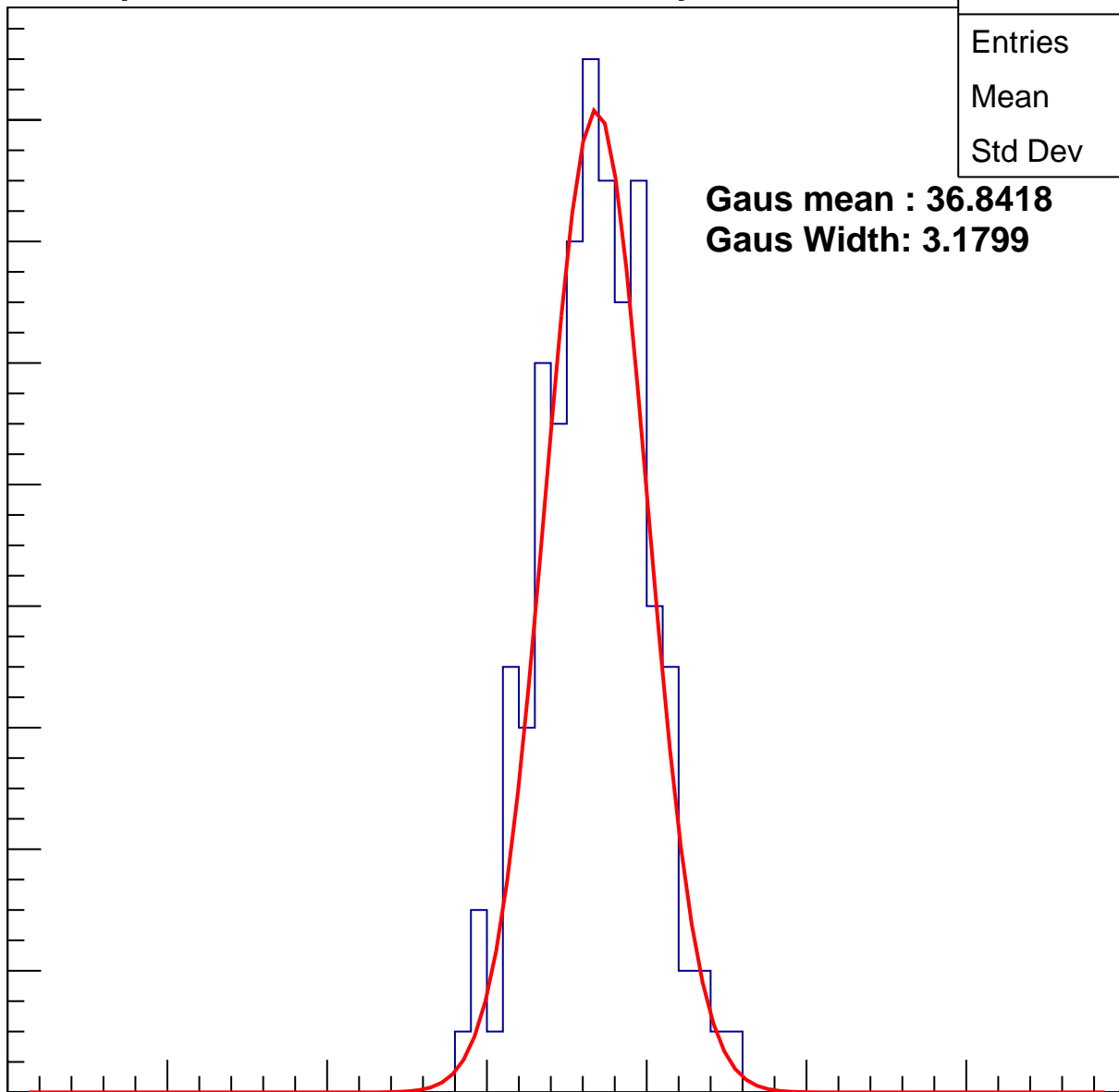
40

50

60

70

ampl



B1L001S, U26-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

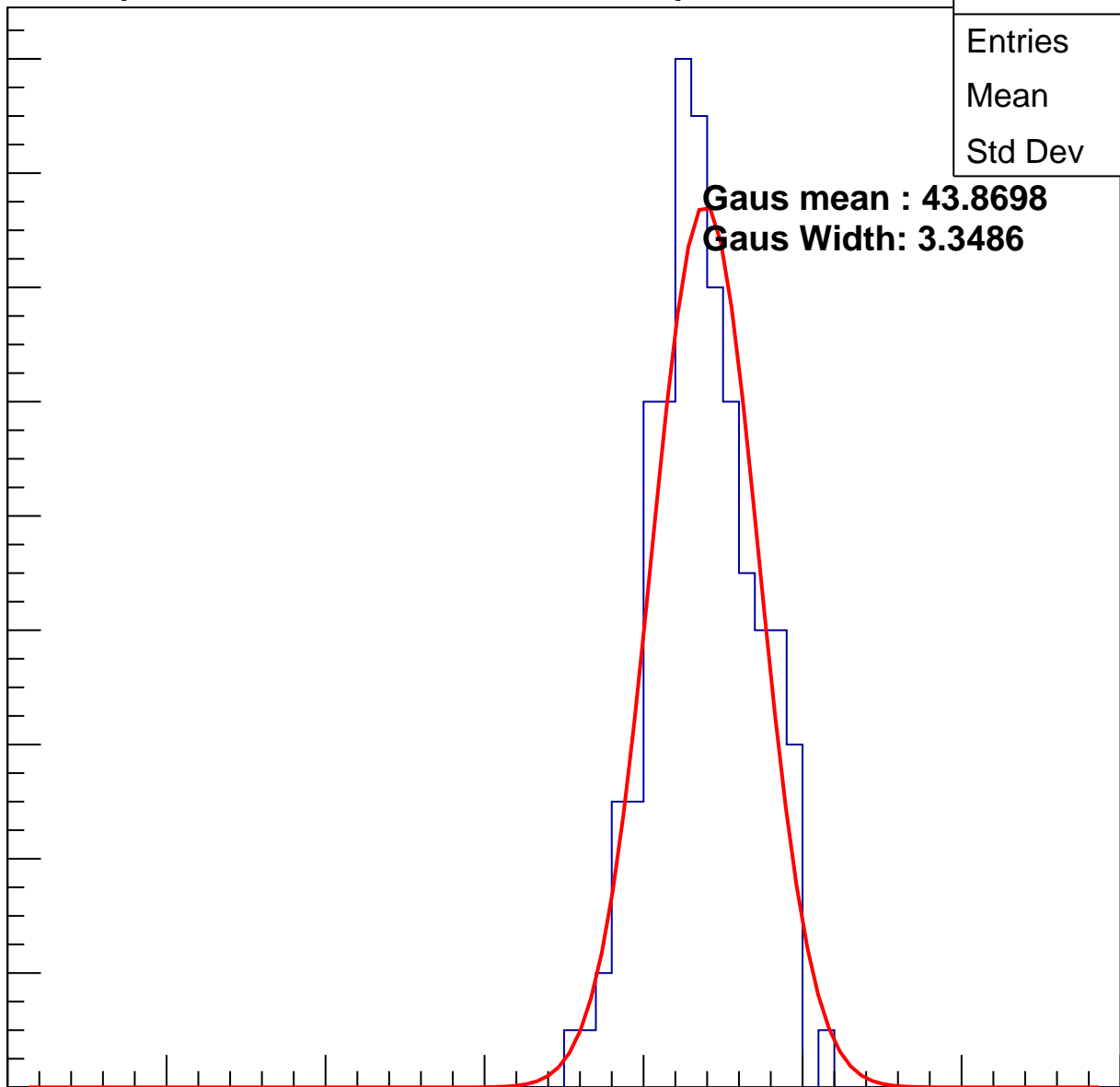
| | |
|---------|-------|
| Entries | 131 |
| Mean | 43.24 |
| Std Dev | 3.159 |

Gaus mean : 43.8698

Gaus Width: 3.3486

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

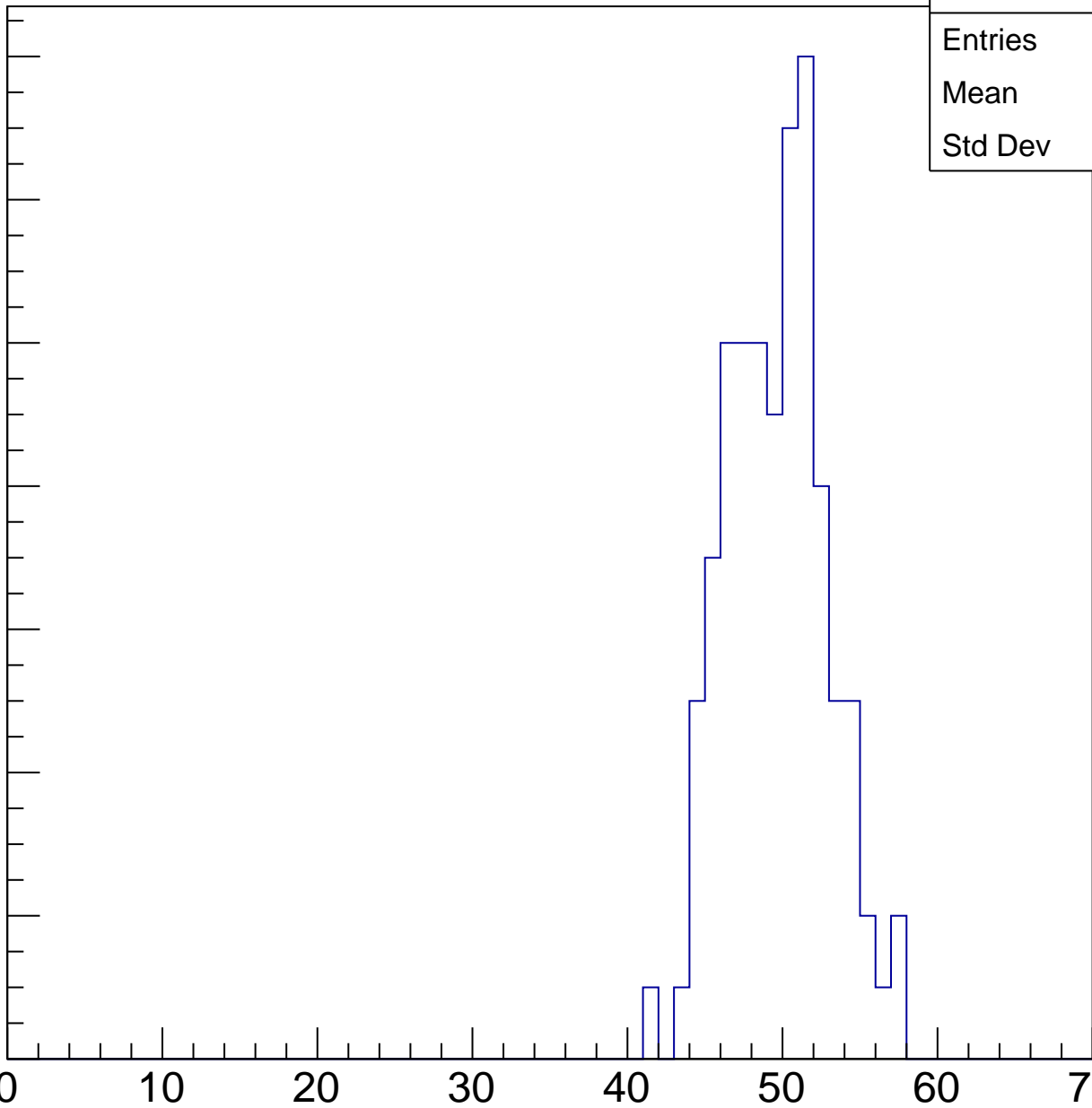
| | |
|---------|-------|
| Entries | 103 |
| Mean | 49.17 |
| Std Dev | 3.221 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

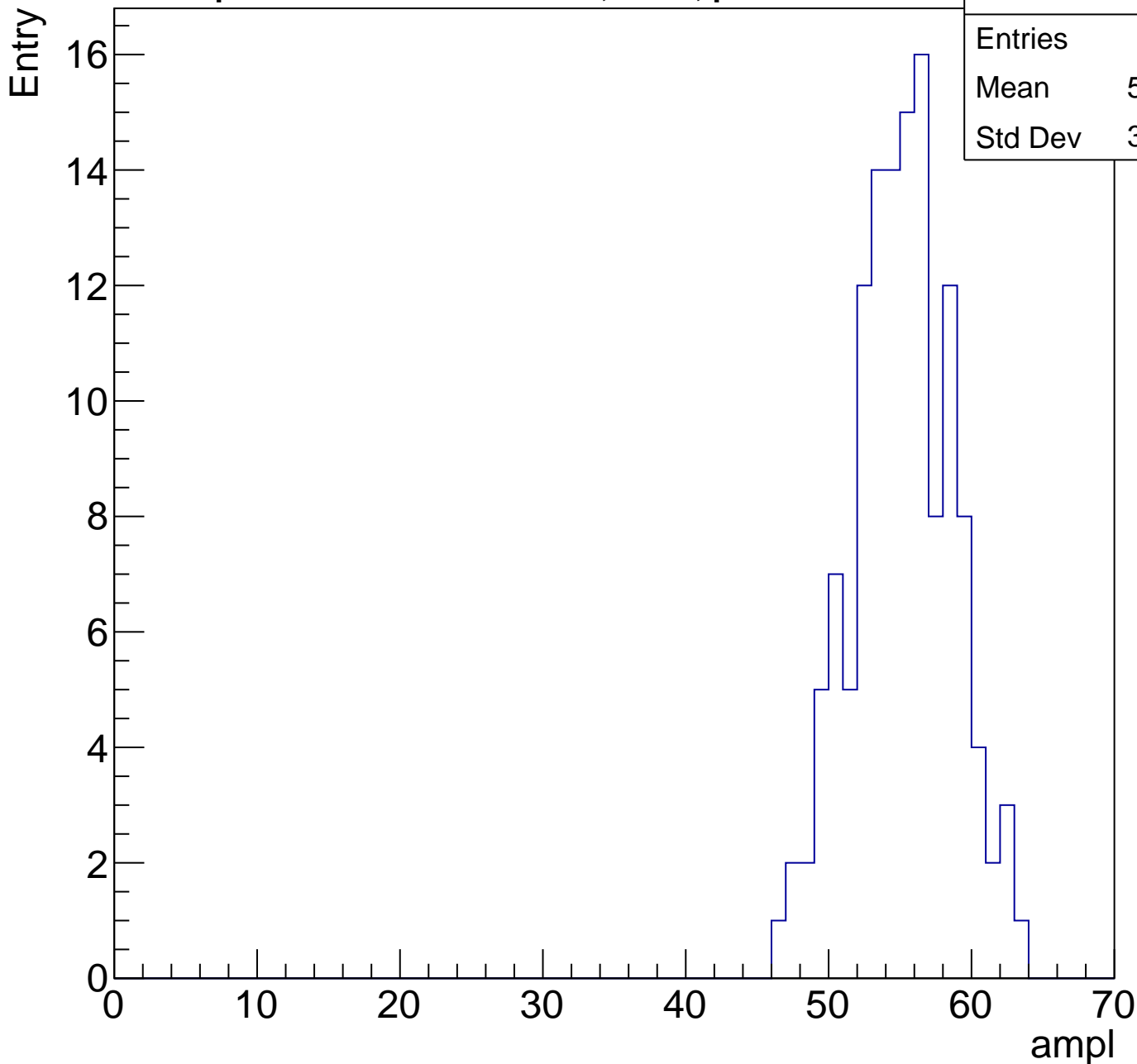
ampl



B1L001S, U26-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 54.69 |
| Std Dev | 3.466 |



B1L001S, U26-ch71, adc5

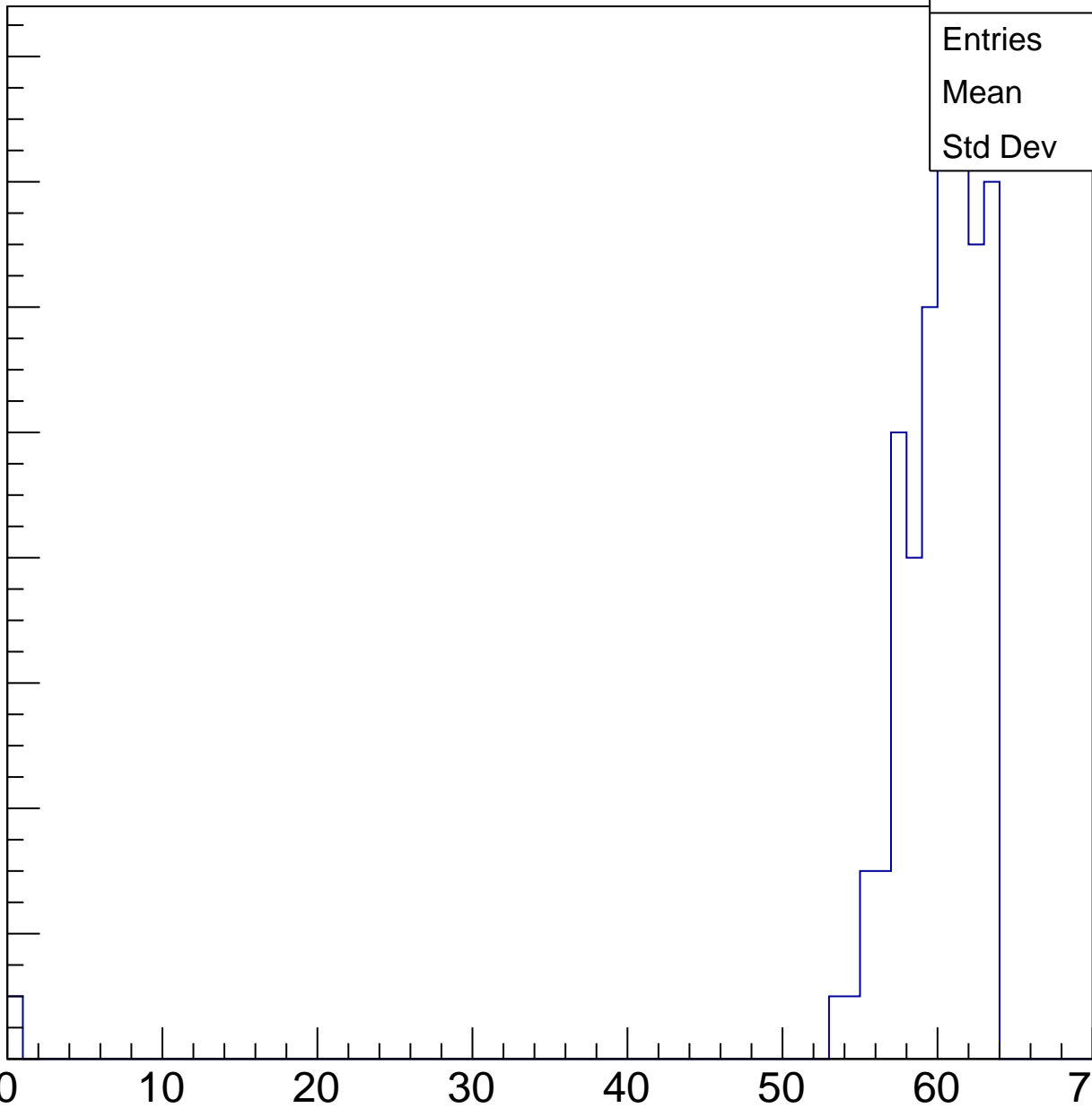
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 59.24 |
| Std Dev | 6.452 |

ampl

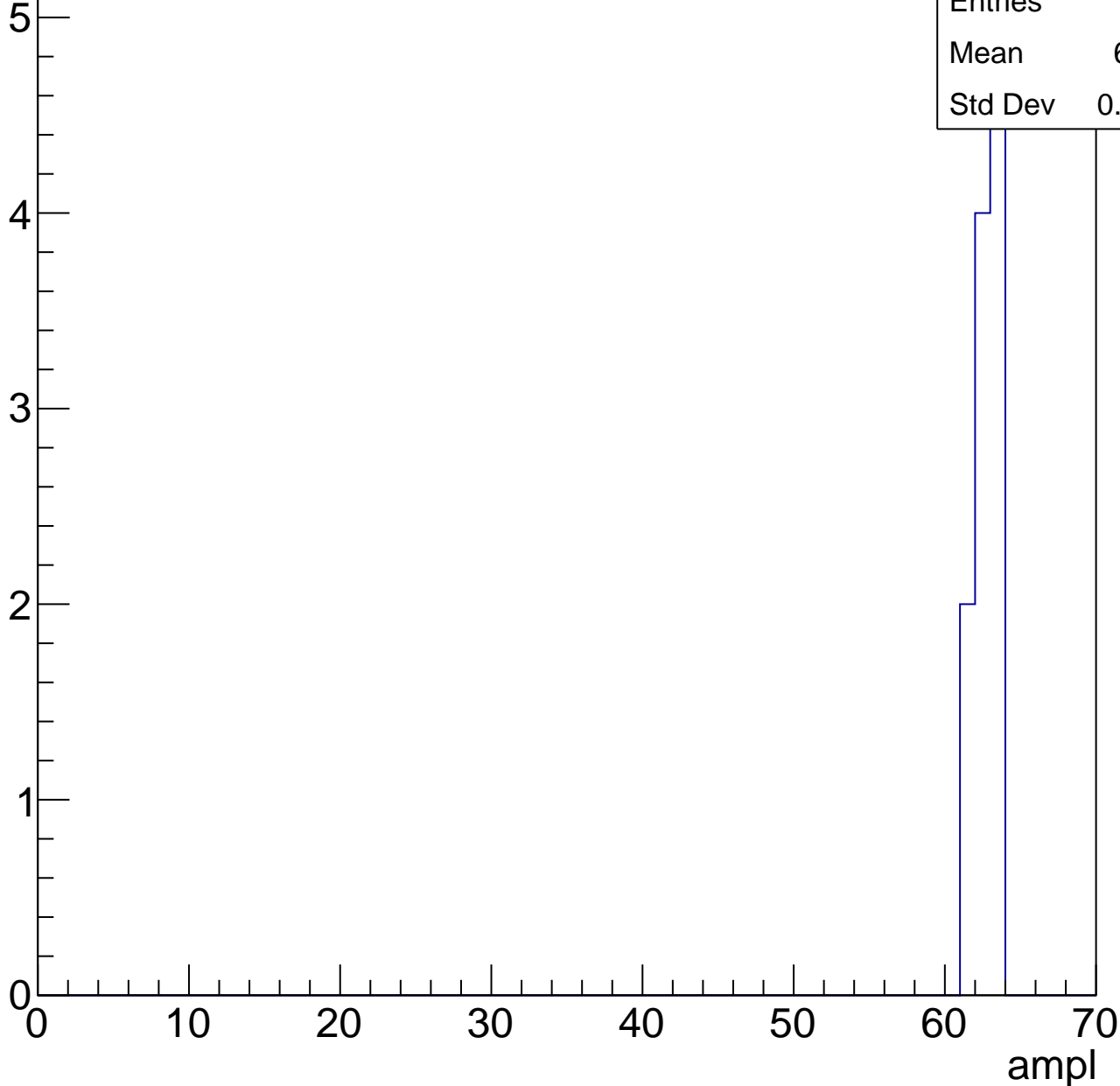


B1L001S, U26-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|--------|
| Entries | 11 |
| Mean | 62.27 |
| Std Dev | 0.7497 |



B1L001S, U26-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

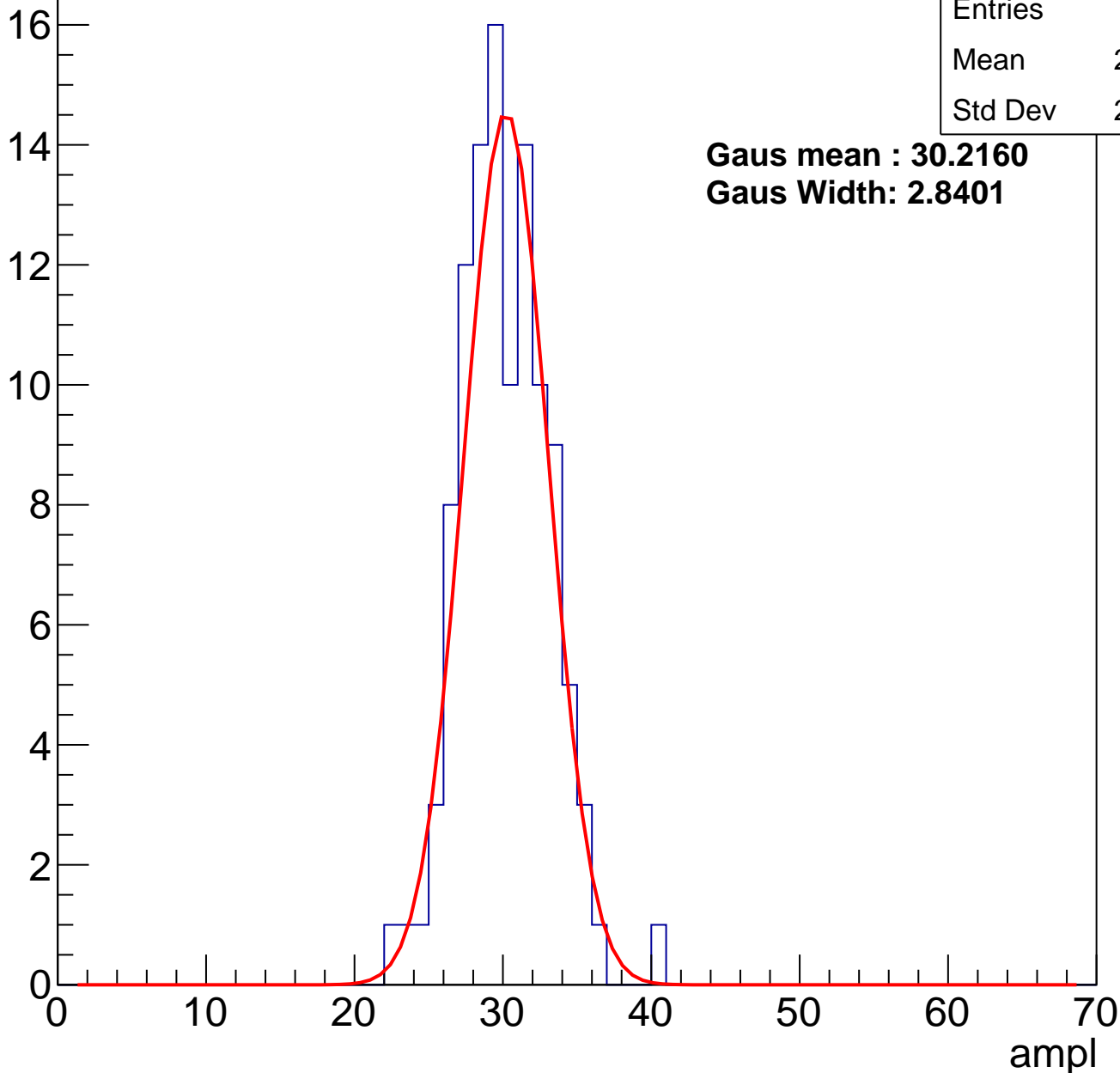


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch72, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



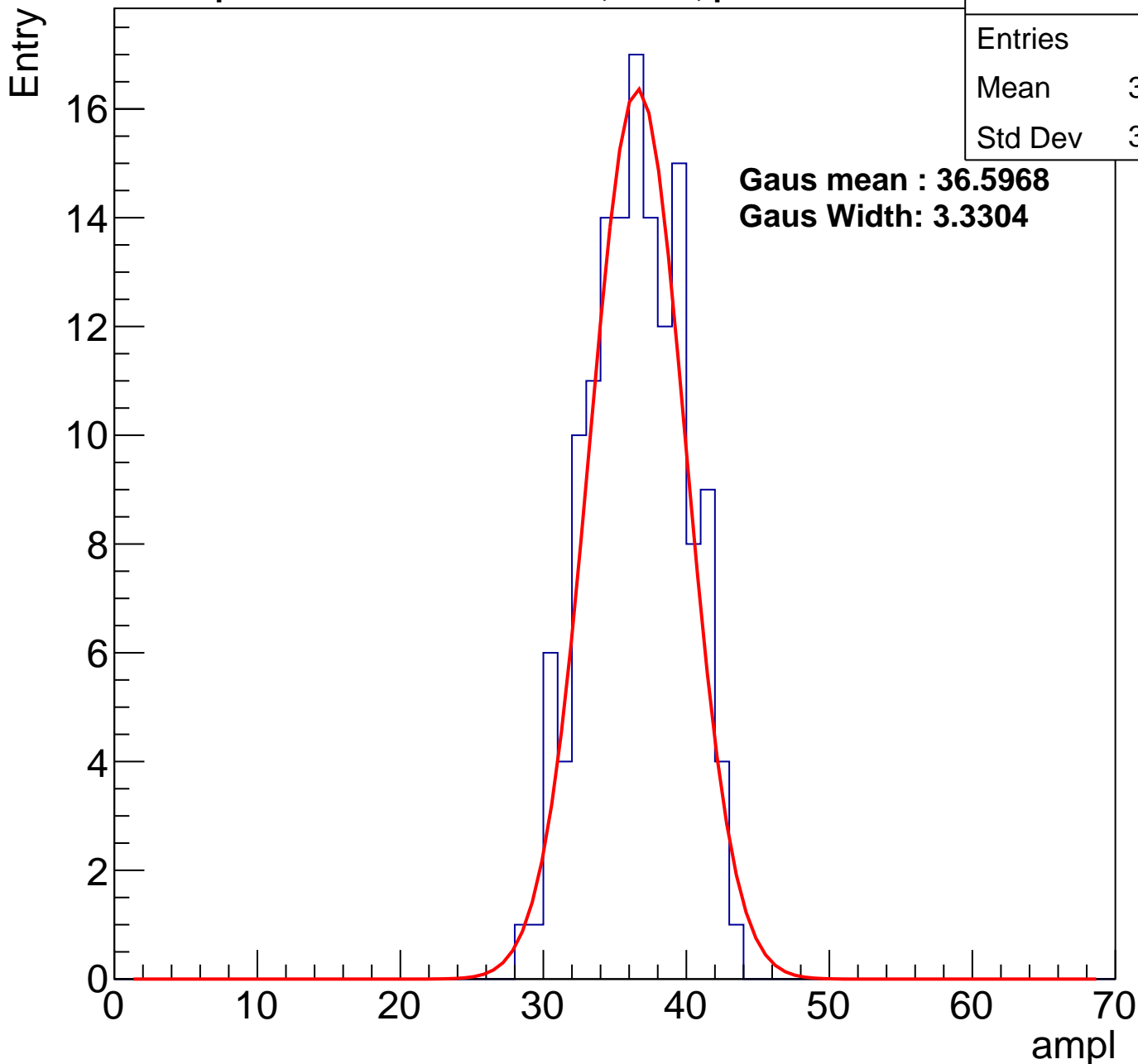
B1L001S, U26-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 36.04 |
| Std Dev | 3.263 |

Gaus mean : 36.5968

Gaus Width: 3.3304



B1L001S, U26-ch72, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 105 |
| Mean | 41.74 |
| Std Dev | 3.18 |

Gaus mean : 42.3739

Gaus Width: 3.2210

Entry

14

12

10

8

6

4

2

0

0

10

20

30

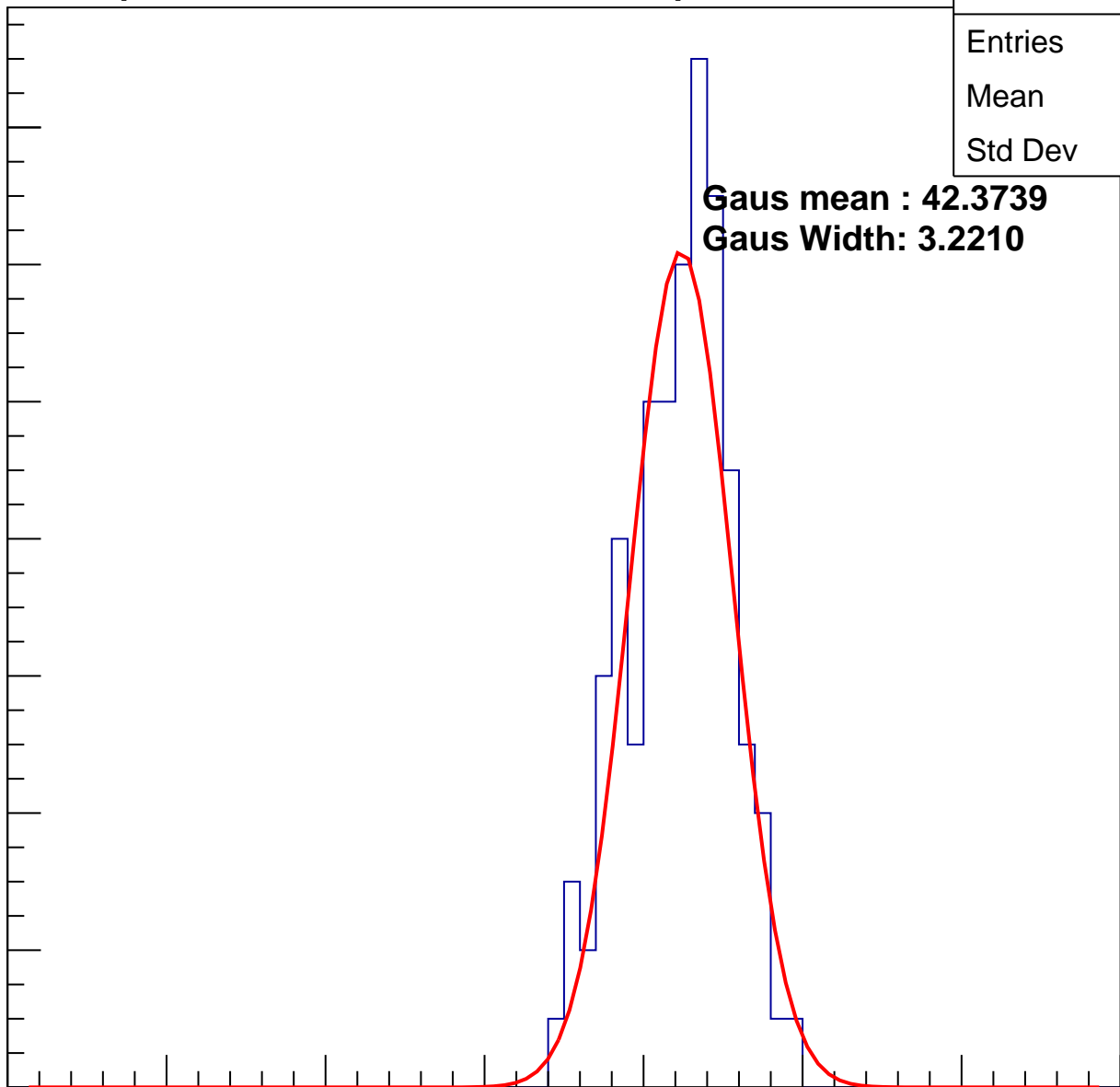
40

50

60

70

ampl



B1L001S, U26-ch72, adc3

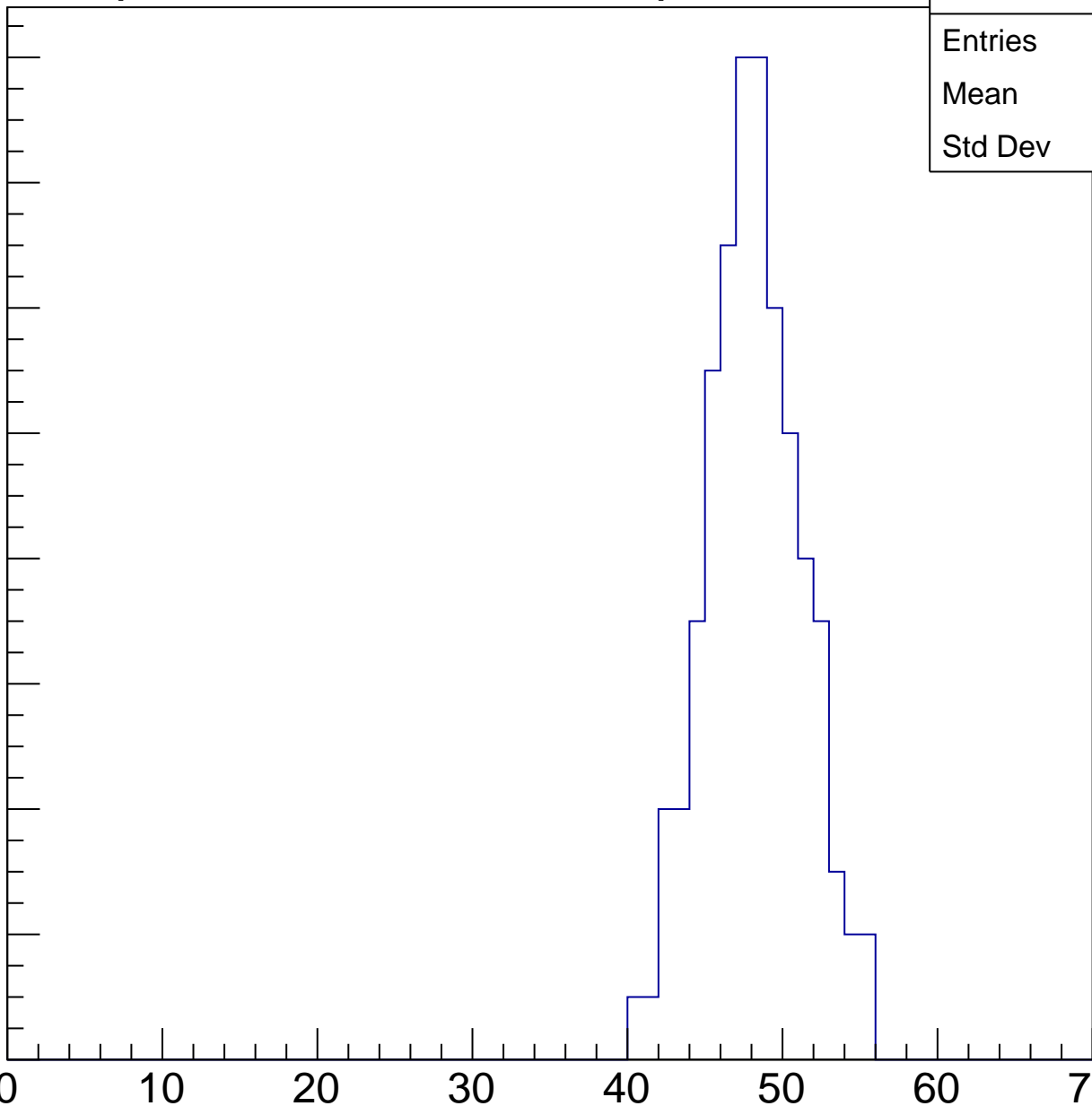
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 117 |
| Mean | 47.68 |
| Std Dev | 3.074 |

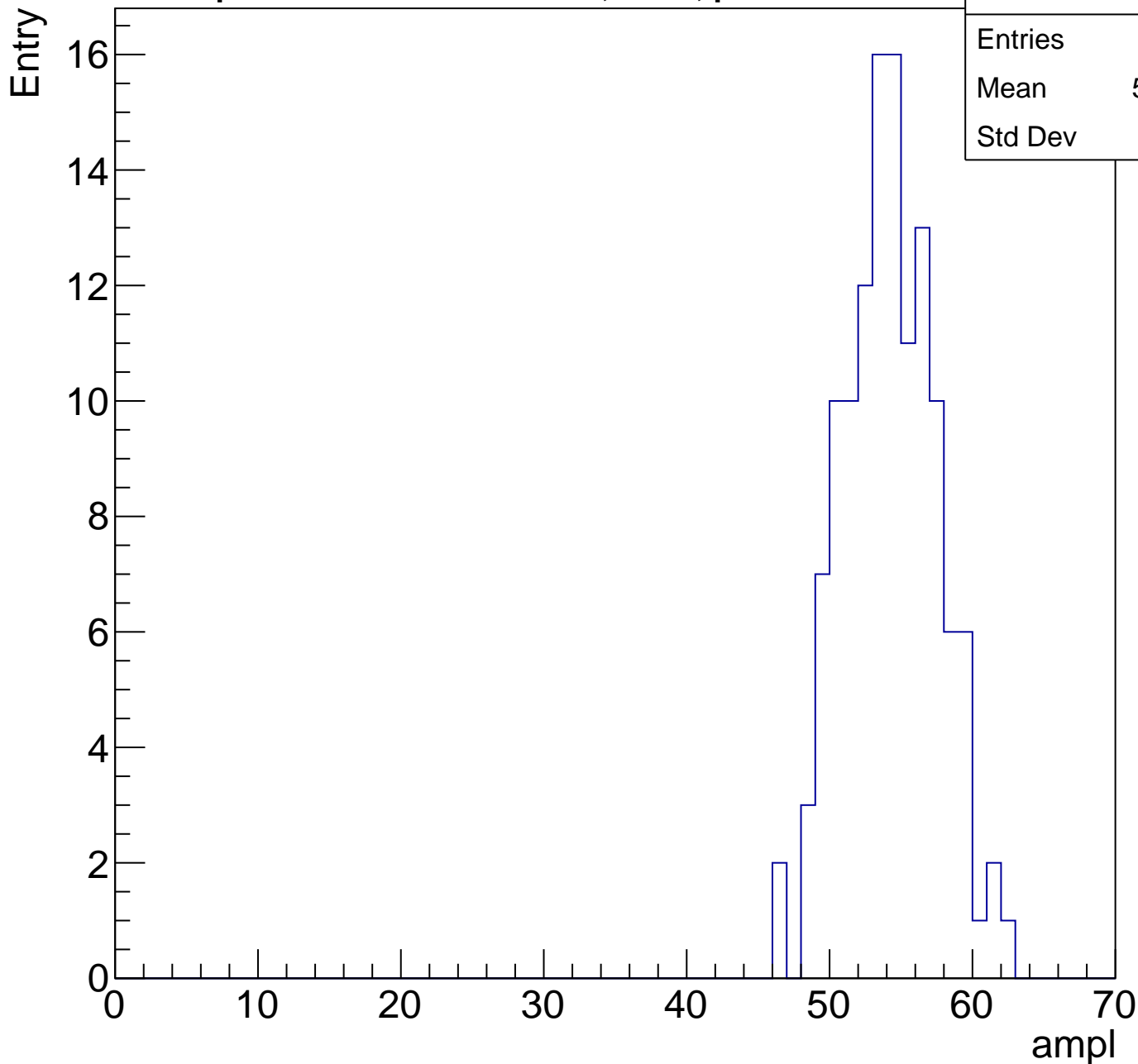
ampl



B1L001S, U26-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 126 |
| Mean | 53.76 |
| Std Dev | 3.23 |



B1L001S, U26-ch72, adc5

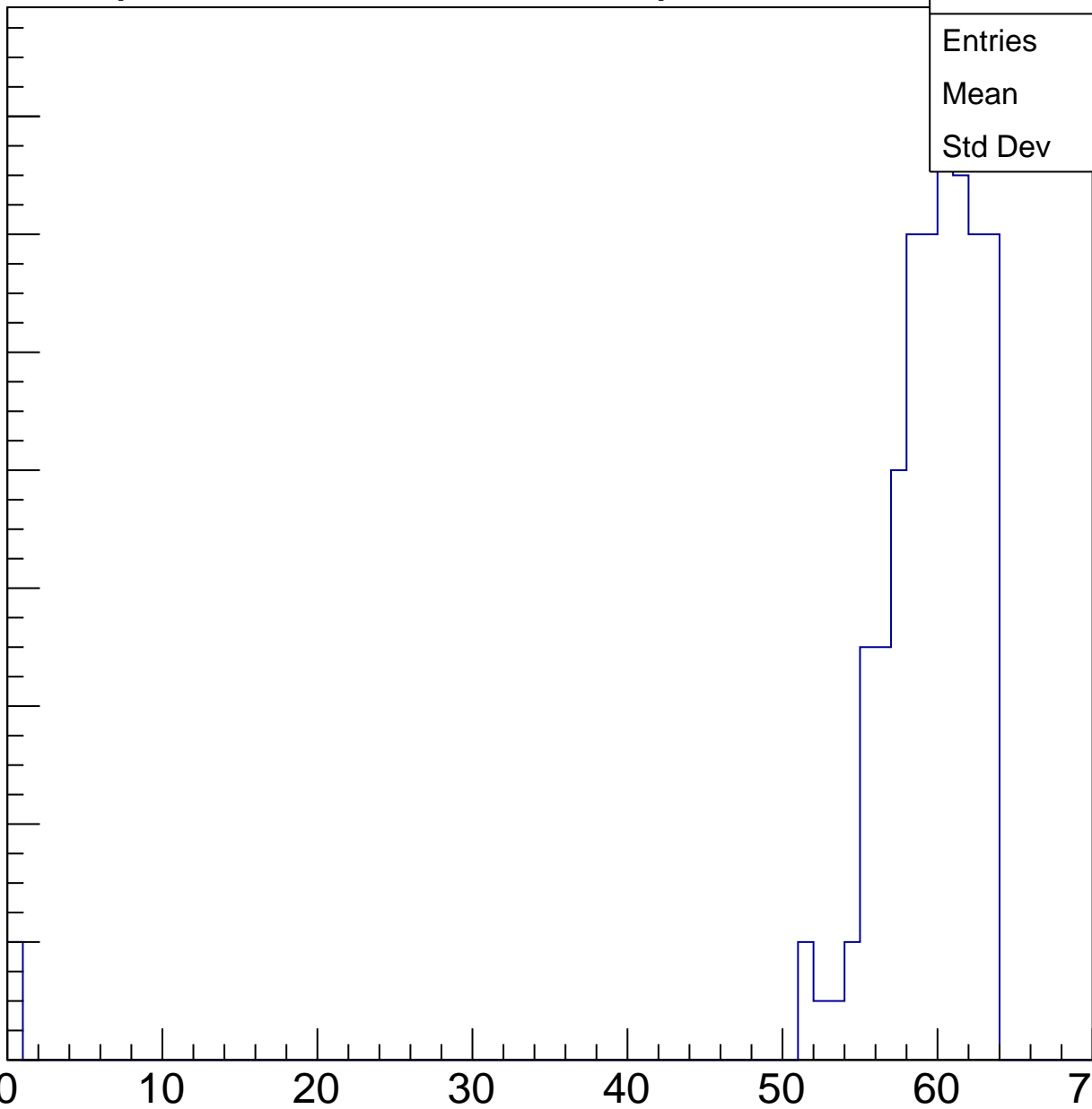
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 120 |
| Mean | 58.21 |
| Std Dev | 8.065 |

ampl

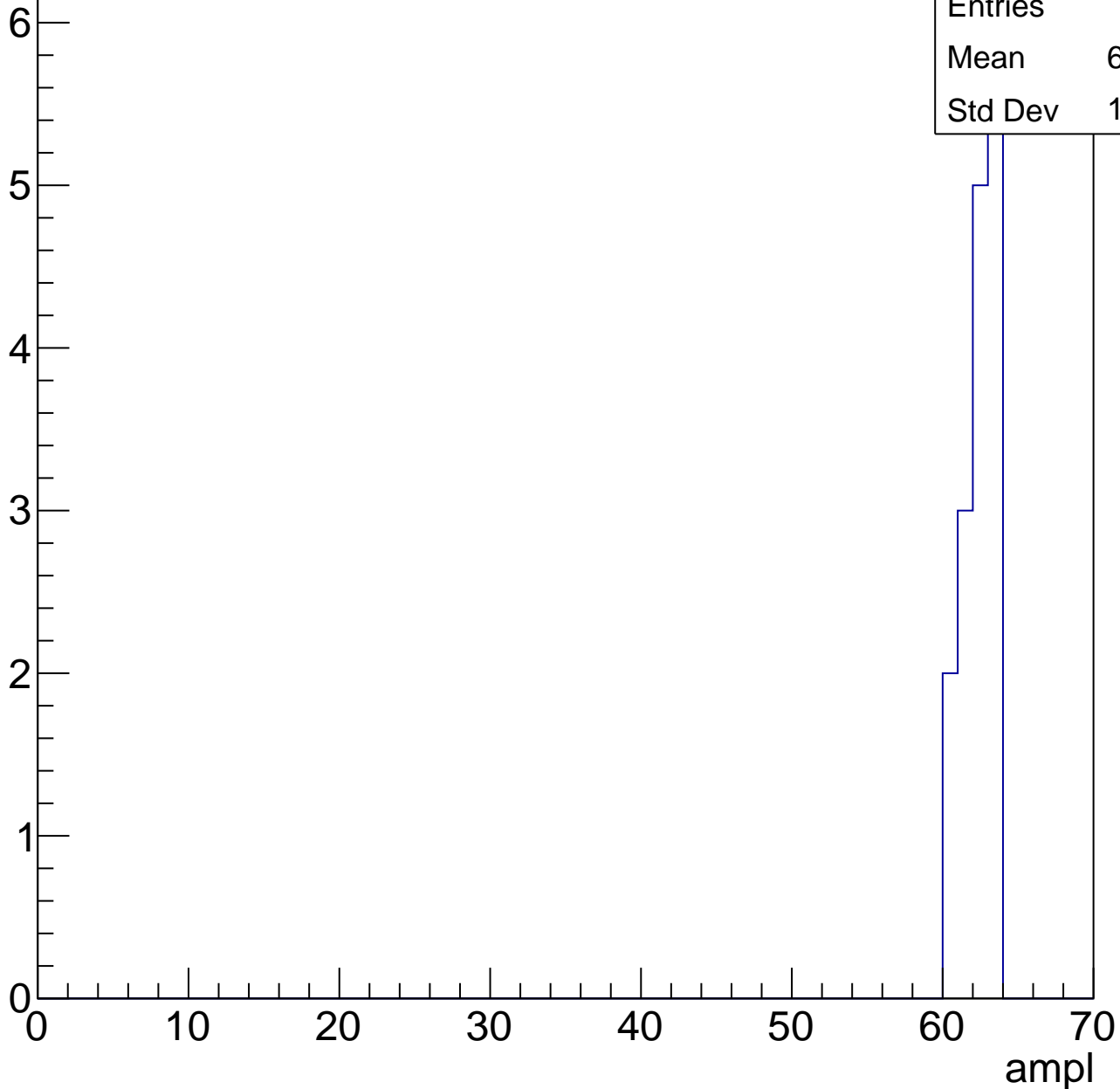


B1L001S, U26-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 16 |
| Mean | 61.94 |
| Std Dev | 1.029 |



B1L001S, U26-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 30.34 |
| Std Dev | 3.235 |

Gaus mean : 30.7491

Gaus Width: 3.4077

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

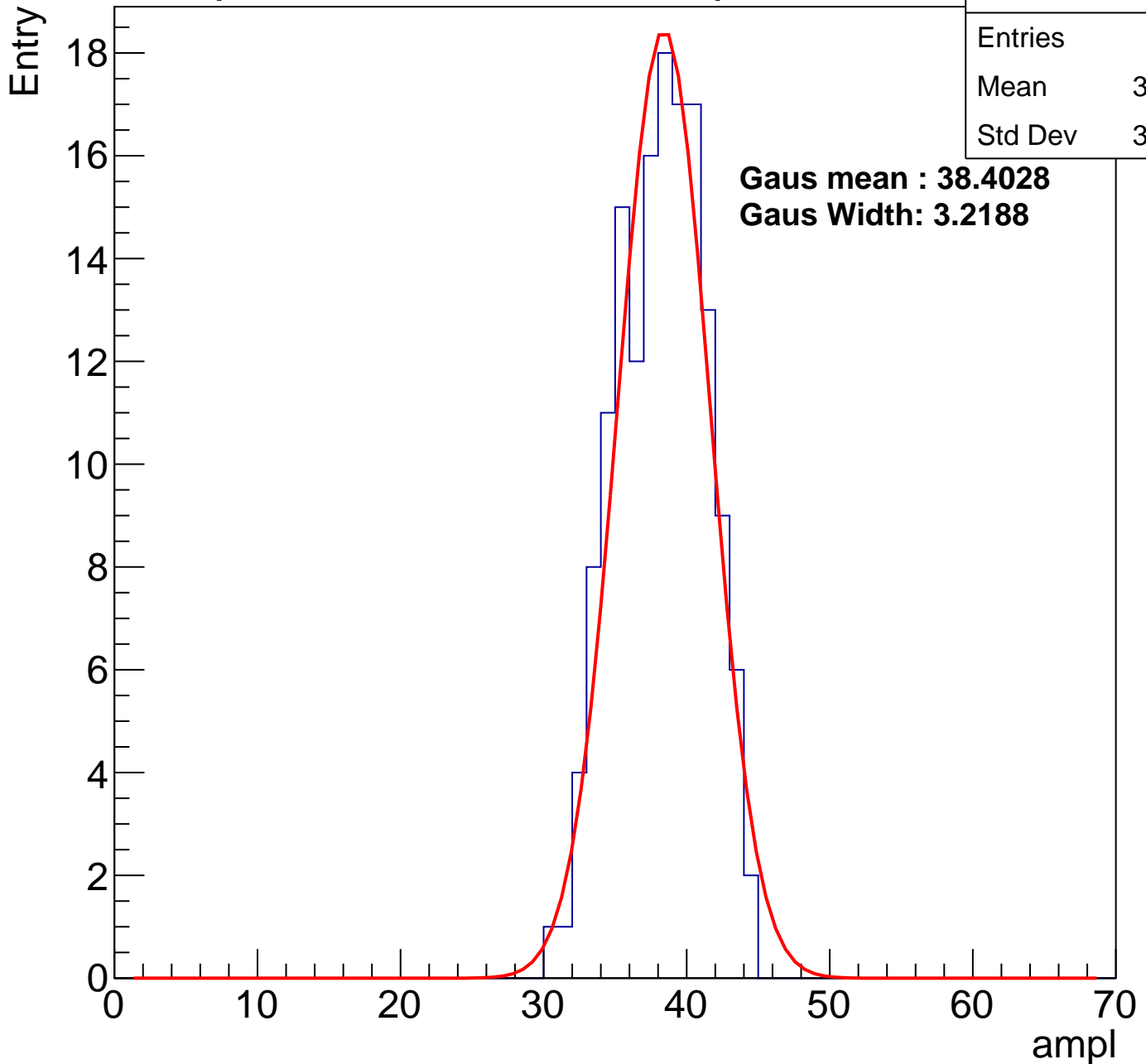
70

B1L001S, U26-ch73, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 37.73 |
| Std Dev | 3.043 |

Gaus mean : 38.4028
Gaus Width: 3.2188



B1L001S, U26-ch73, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

112

Mean

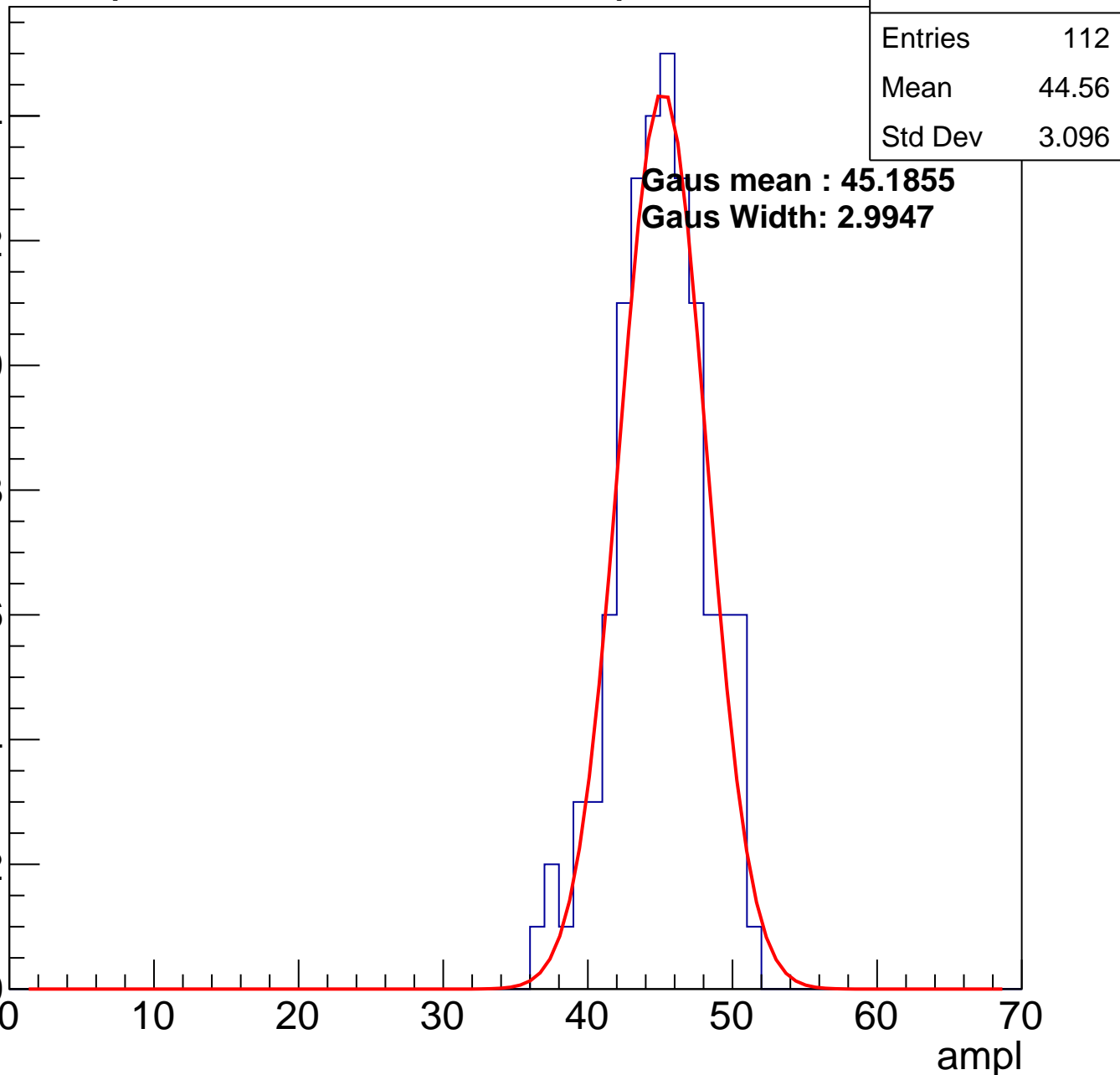
44.56

Std Dev

3.096

Gaus mean : 45.1855

Gaus Width: 2.9947



B1L001S, U26-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

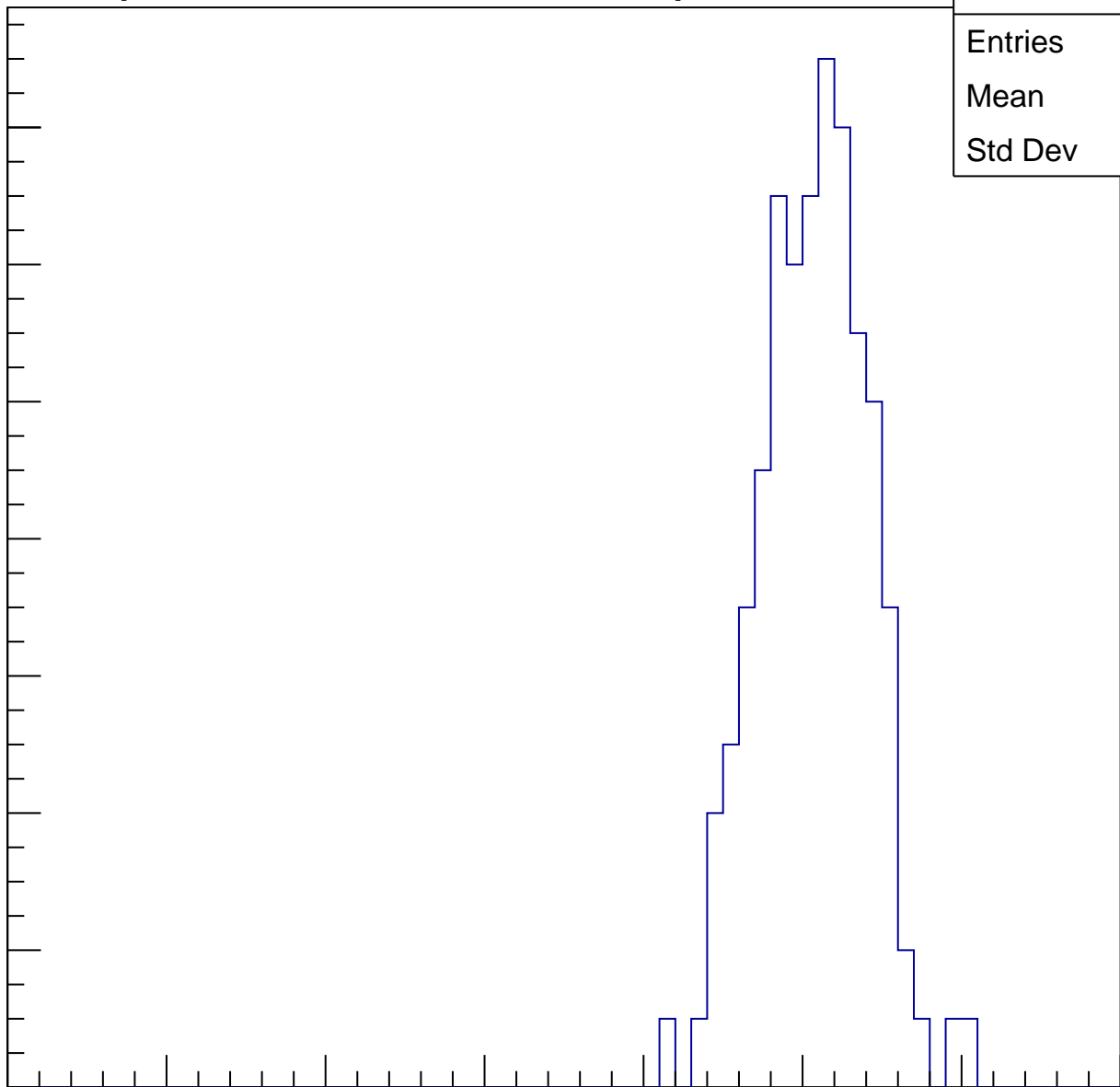
| | |
|---------|-------|
| Entries | 127 |
| Mean | 50.24 |
| Std Dev | 3.385 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

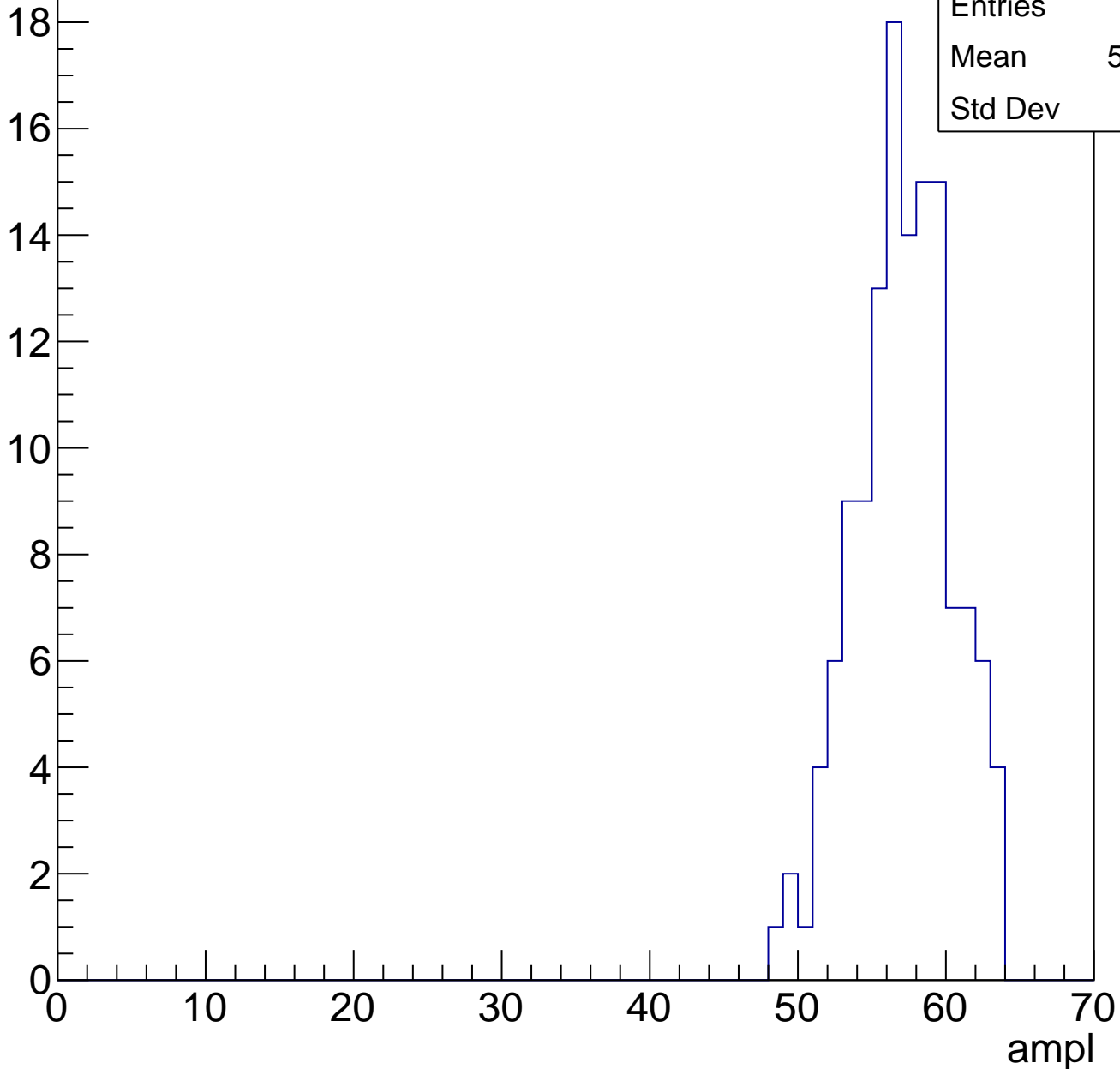


B1L001S, U26-ch73, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 56.66 |
| Std Dev | 3.24 |

Entry



B1L001S, U26-ch73, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 63 |
| Mean | 58.65 |
| Std Dev | 10.81 |

0

2

4

6

8

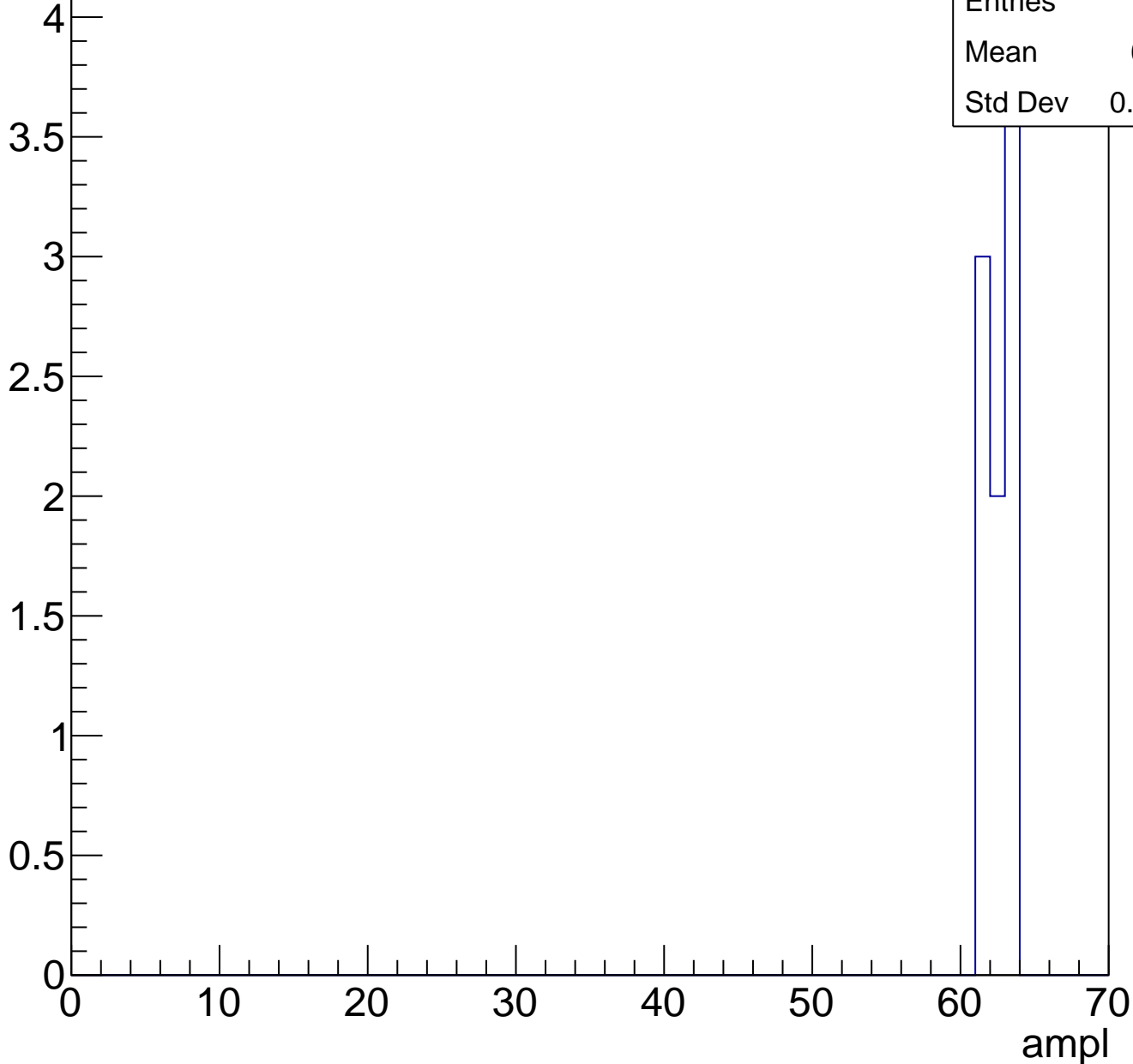
10

12

B1L001S, U26-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch73, adc7

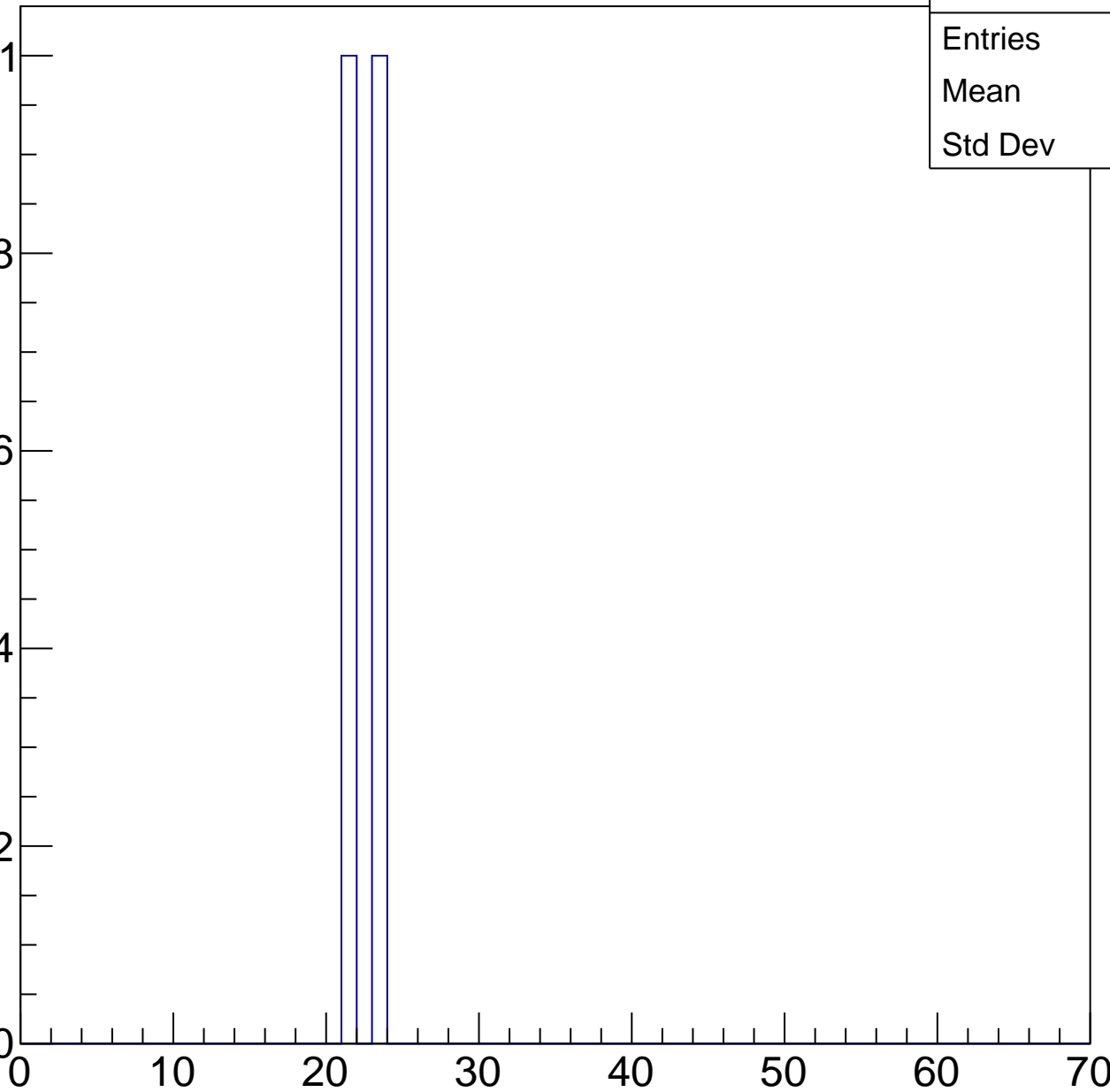
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 22 |
| Std Dev | 1 |

ampl



B1L001S, U26-ch74, adc0

calib_packv5_042523_0143.root, FC#2, port C2

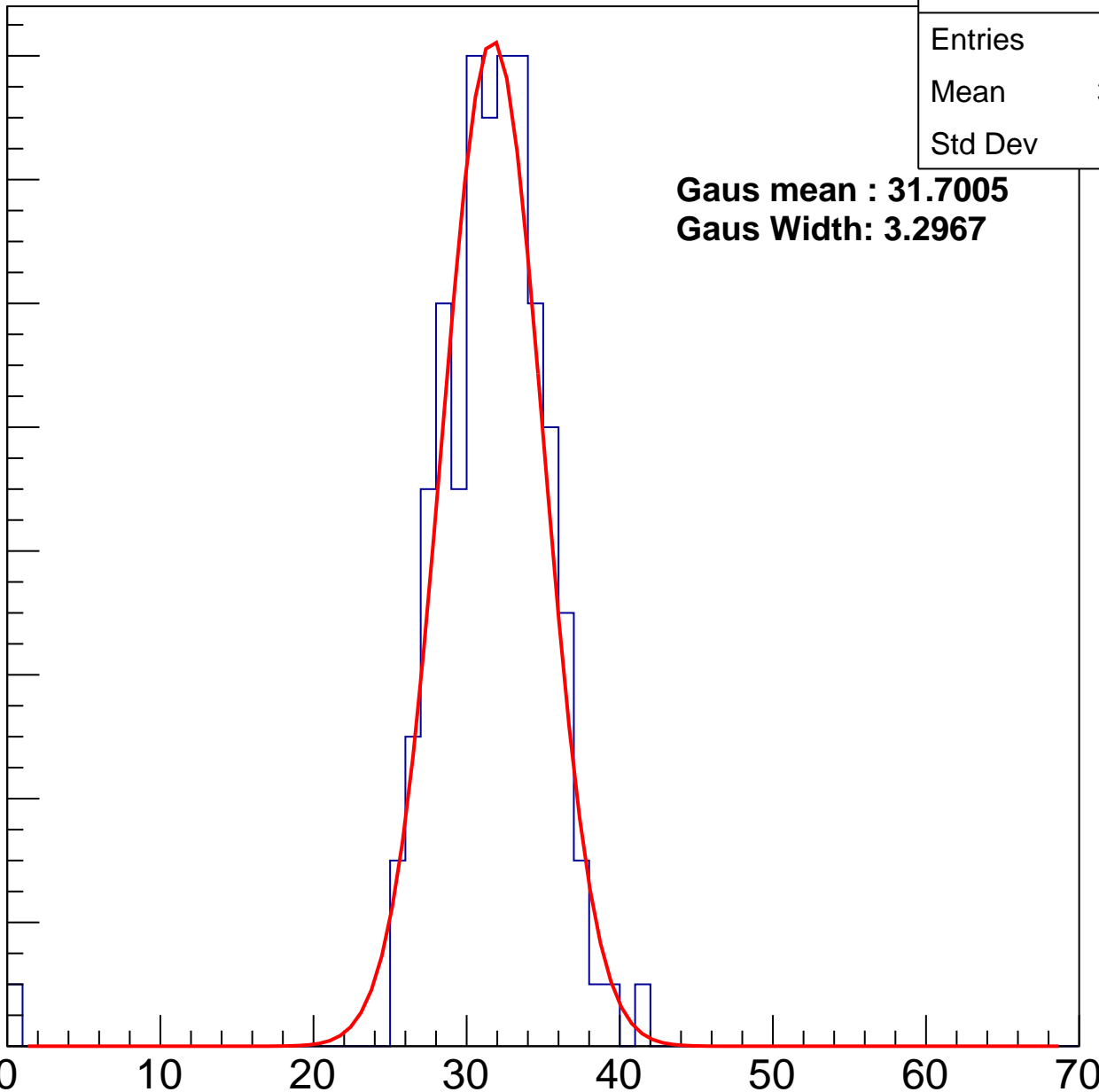
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 137 |
| Mean | 31.16 |
| Std Dev | 4.131 |

Gaus mean : 31.7005
Gaus Width: 3.2967

ampl



B1L001S, U26-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 38.17 |
| Std Dev | 3.519 |

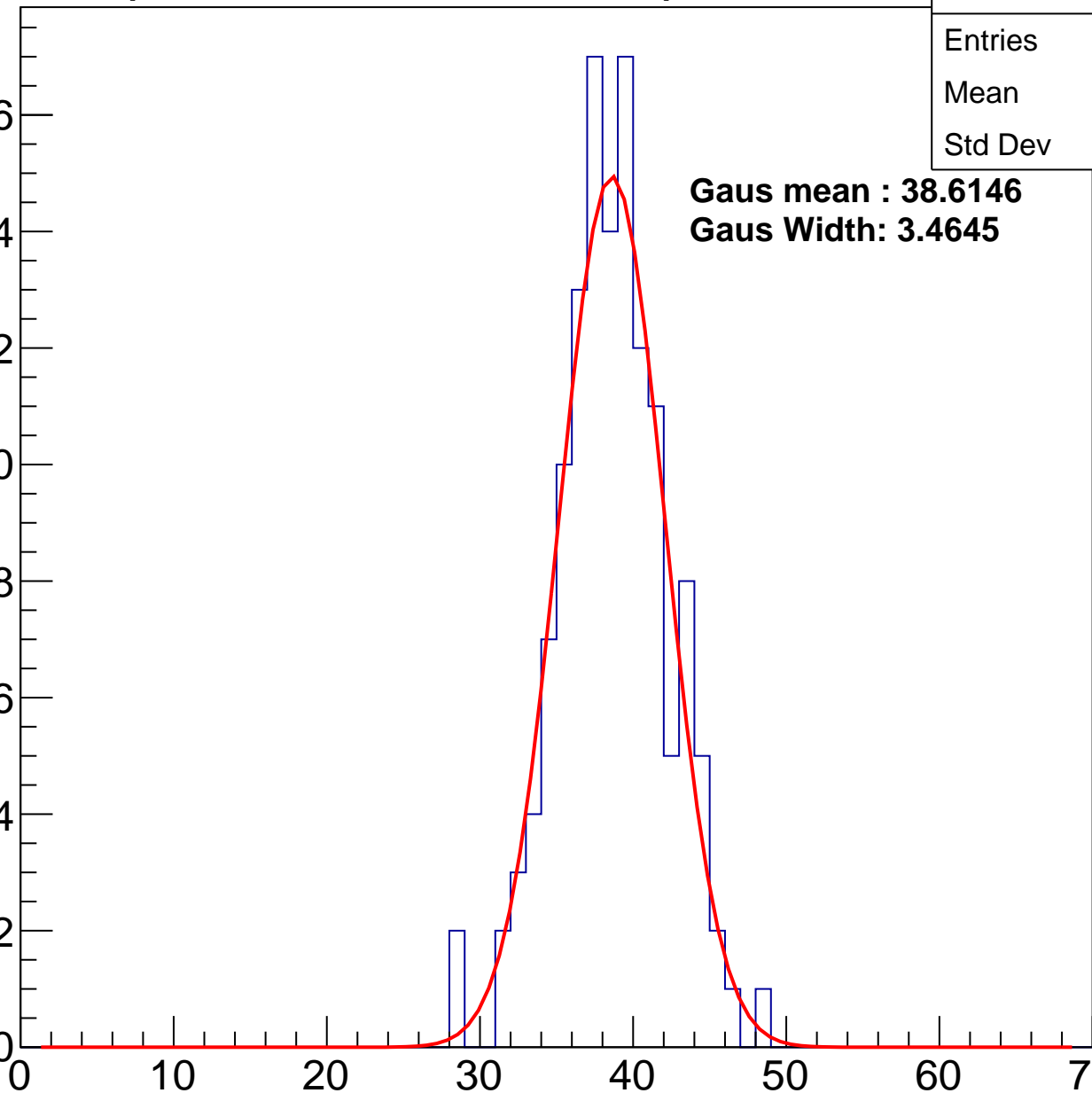
Gaus mean : 38.6146

Gaus Width: 3.4645

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

128

Mean

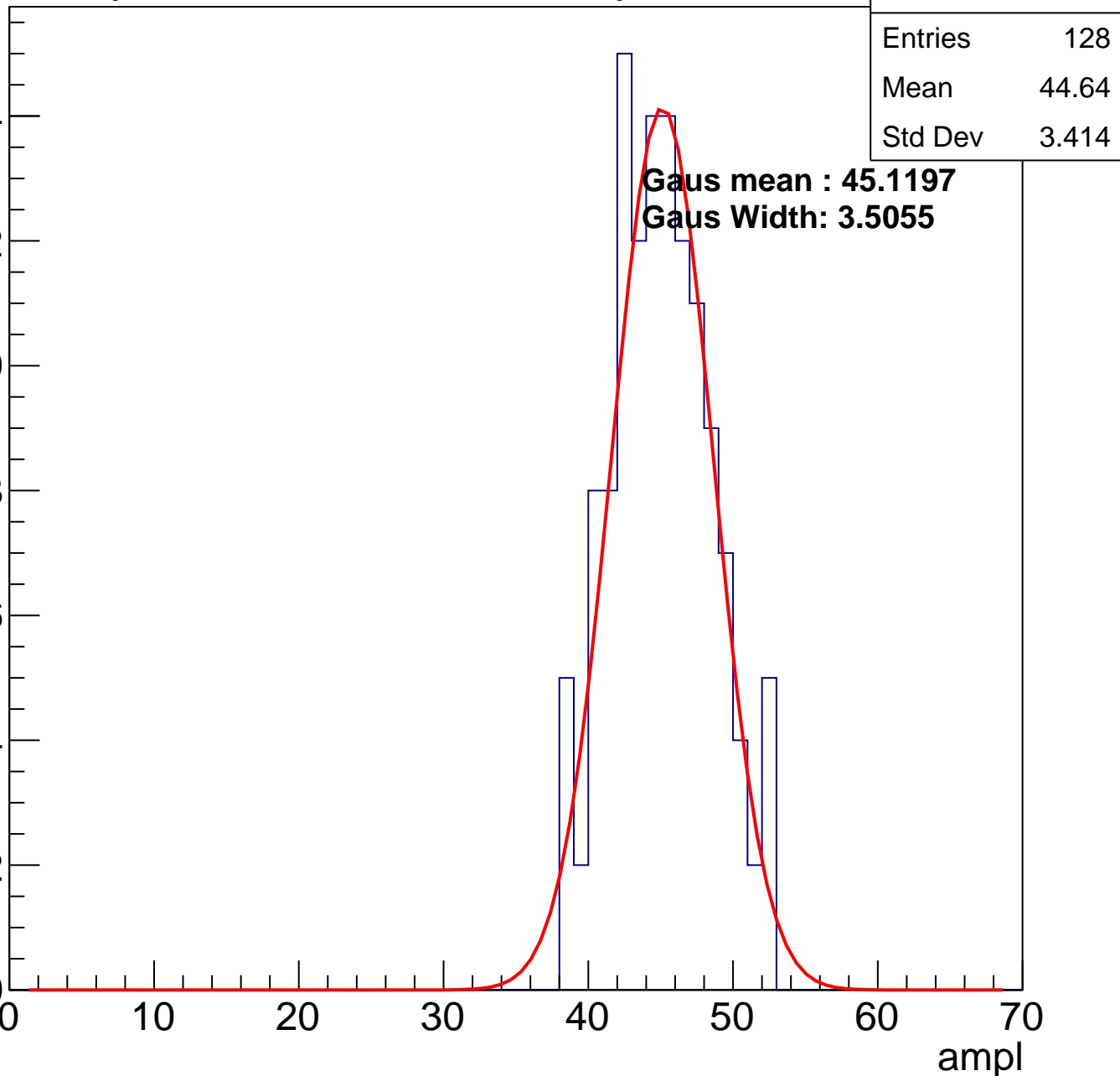
44.64

Std Dev

3.414

Gaus mean : 45.1197

Gaus Width: 3.5055



B1L001S, U26-ch74, adc3

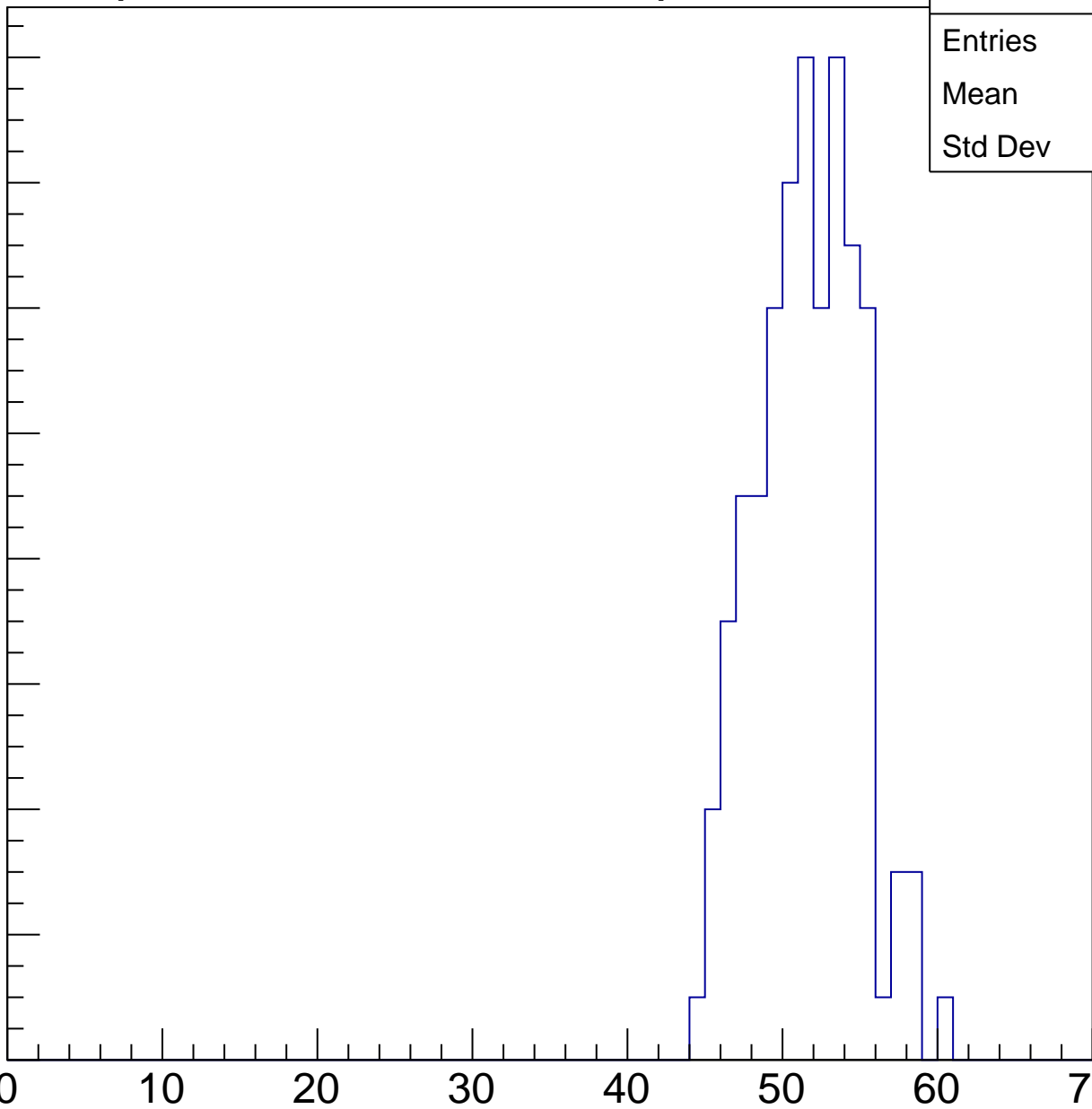
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 133 |
| Mean | 51.13 |
| Std Dev | 3.243 |

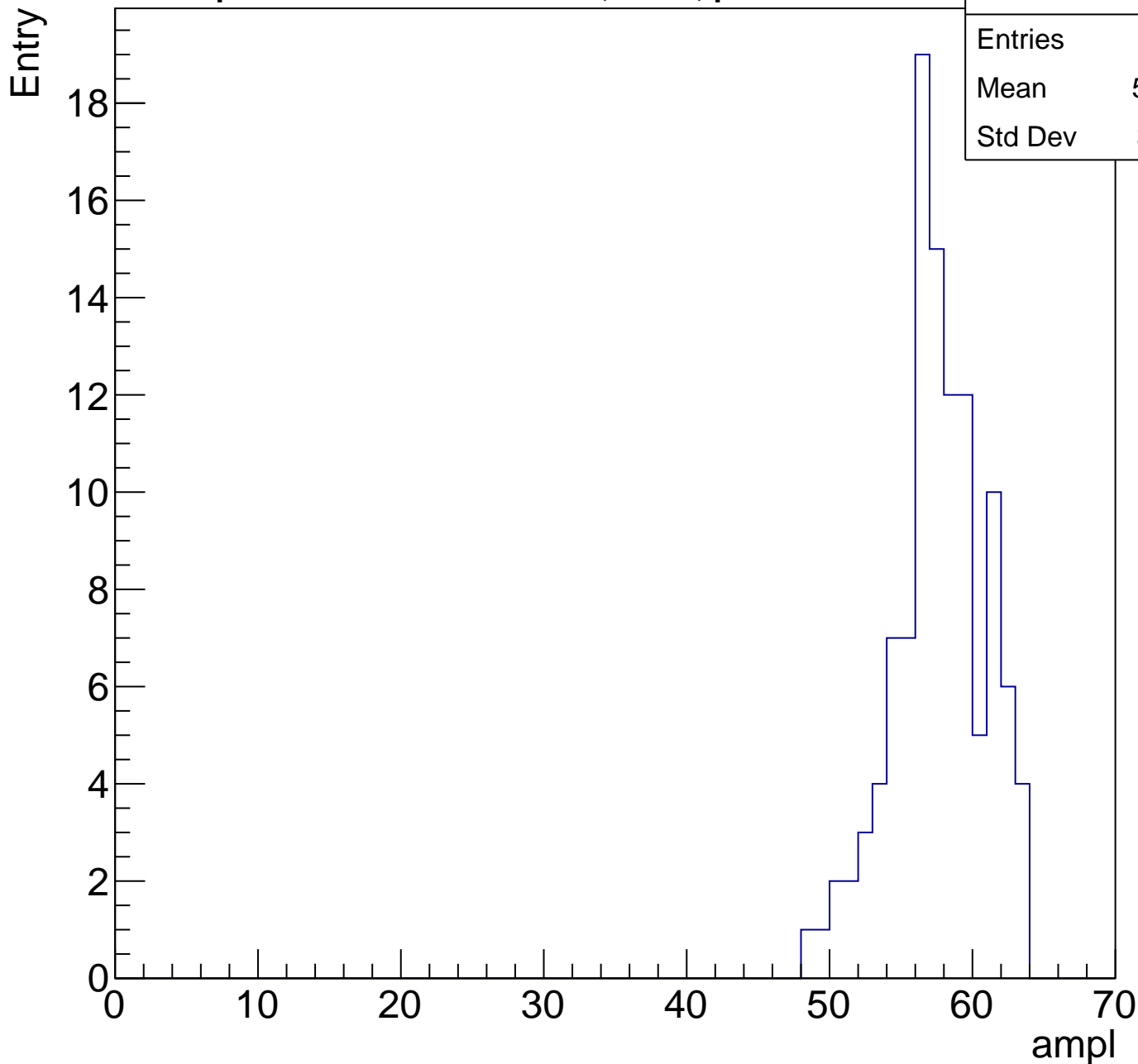
ampl



B1L001S, U26-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 110 |
| Mean | 57.15 |
| Std Dev | 3.191 |



B1L001S, U26-ch74, adc5

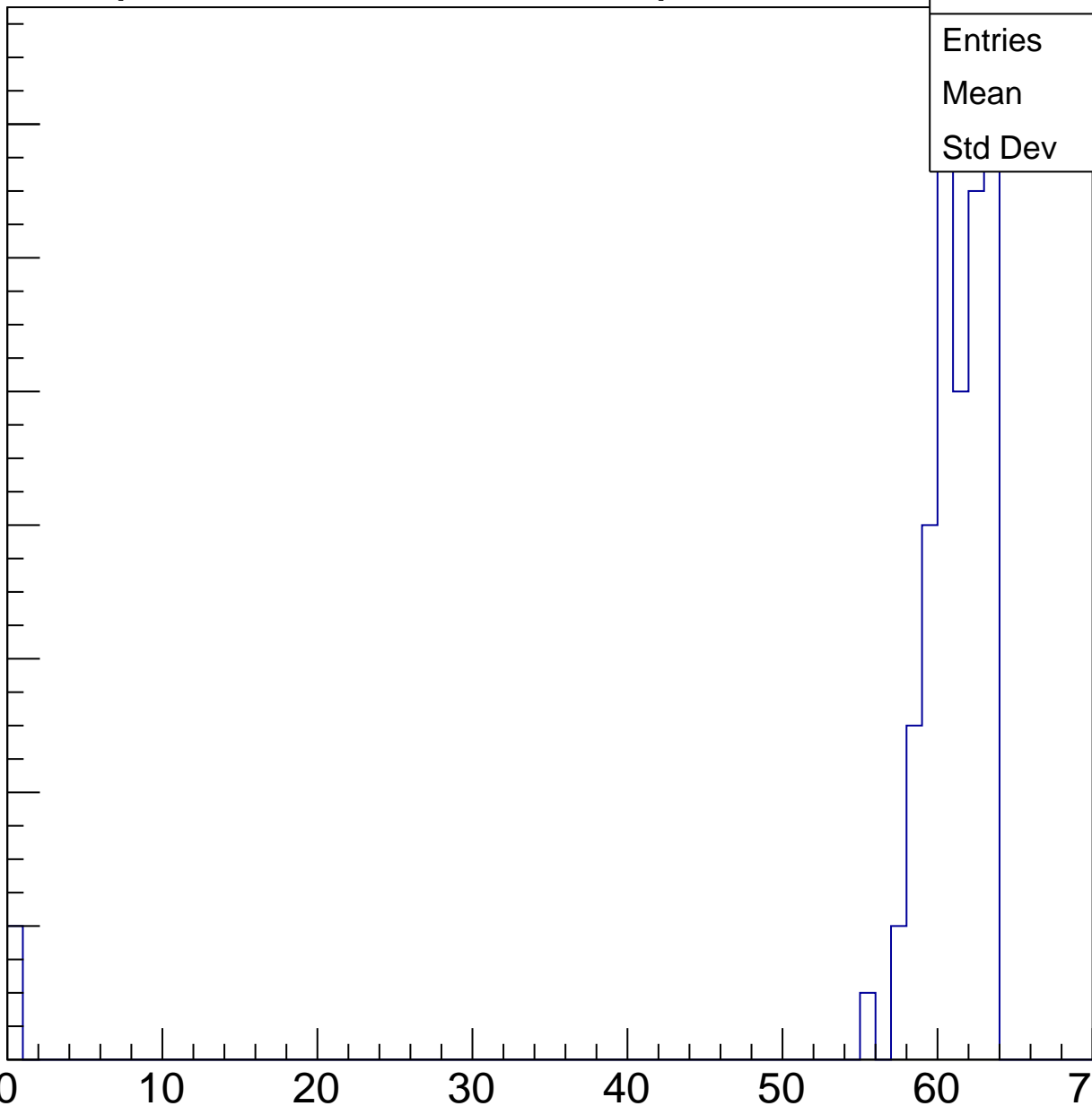
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 70 |
| Mean | 59.03 |
| Std Dev | 10.28 |

ampl



B1L001S, U26-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch75, adc0

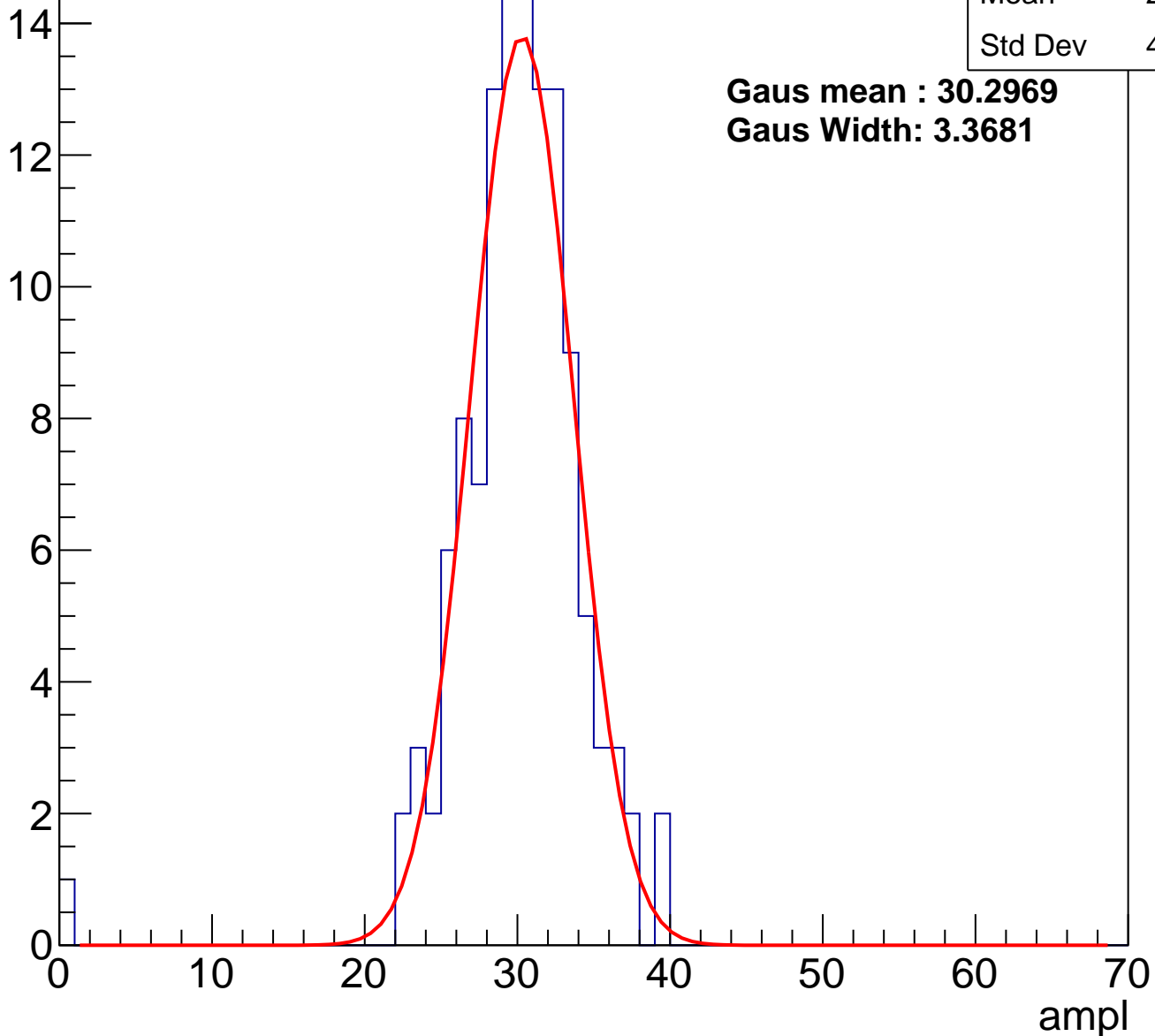
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 29.57 |
| Std Dev | 4.344 |

Gaus mean : 30.2969

Gaus Width: 3.3681

Entry

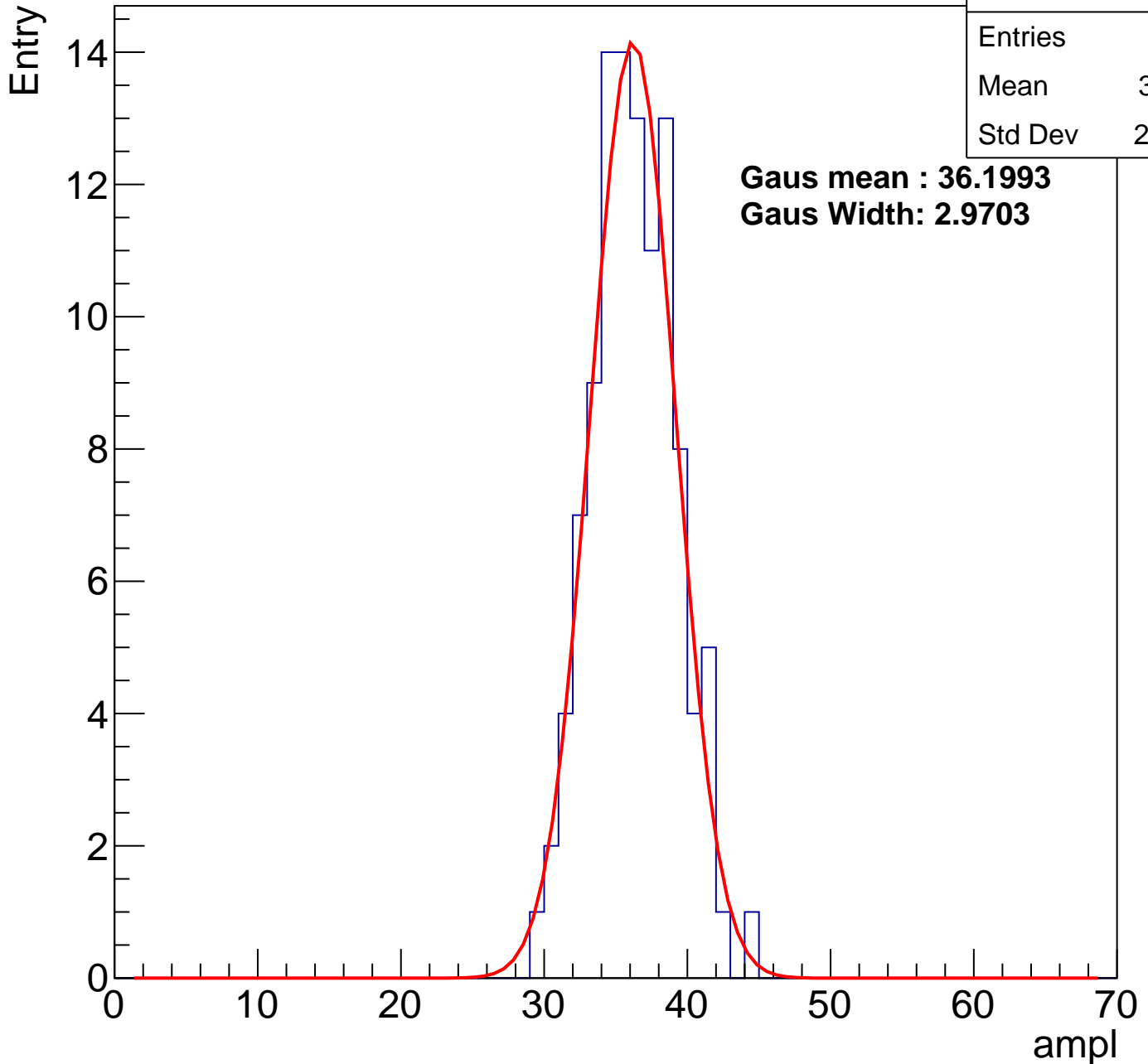


B1L001S, U26-ch75, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 107 |
| Mean | 35.81 |
| Std Dev | 2.907 |

Gaus mean : 36.1993
Gaus Width: 2.9703



B1L001S, U26-ch75, adc2

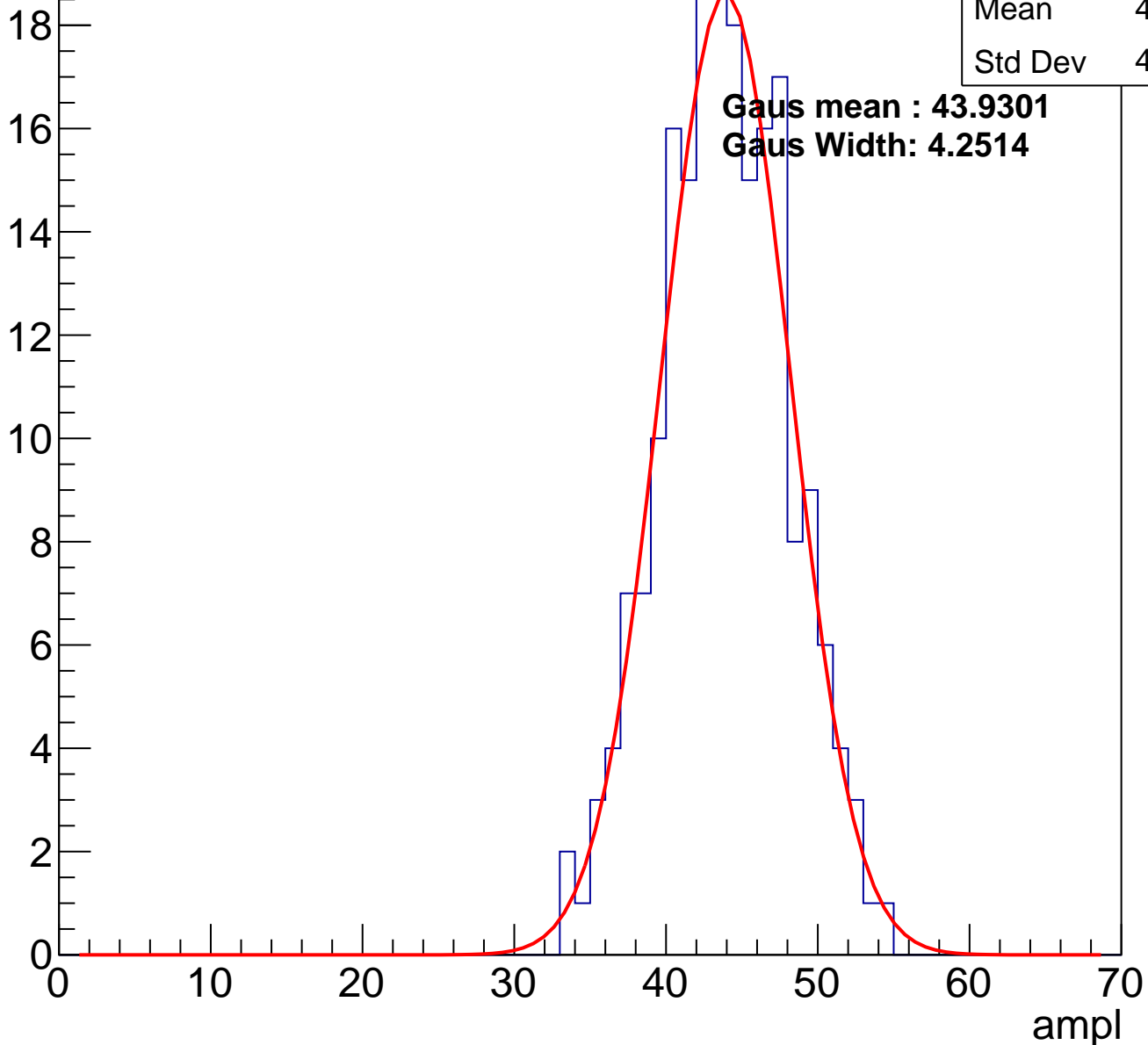
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 201 |
| Mean | 43.42 |
| Std Dev | 4.158 |

Gaus mean : 43.9301

Gaus Width: 4.2514

Entry

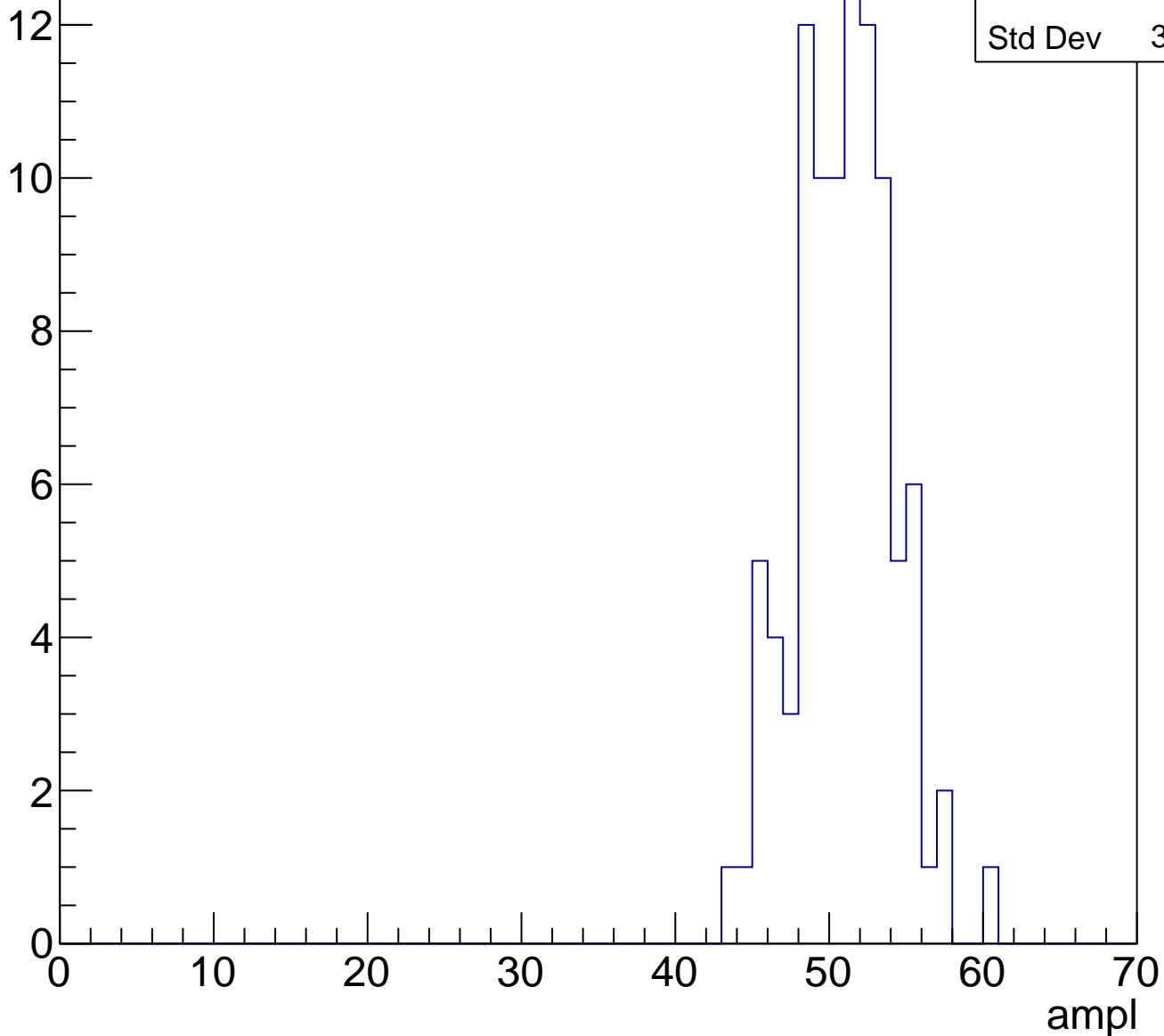


B1L001S, U26-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 96 |
| Mean | 50.52 |
| Std Dev | 3.142 |

Entry



B1L001S, U26-ch75, adc4

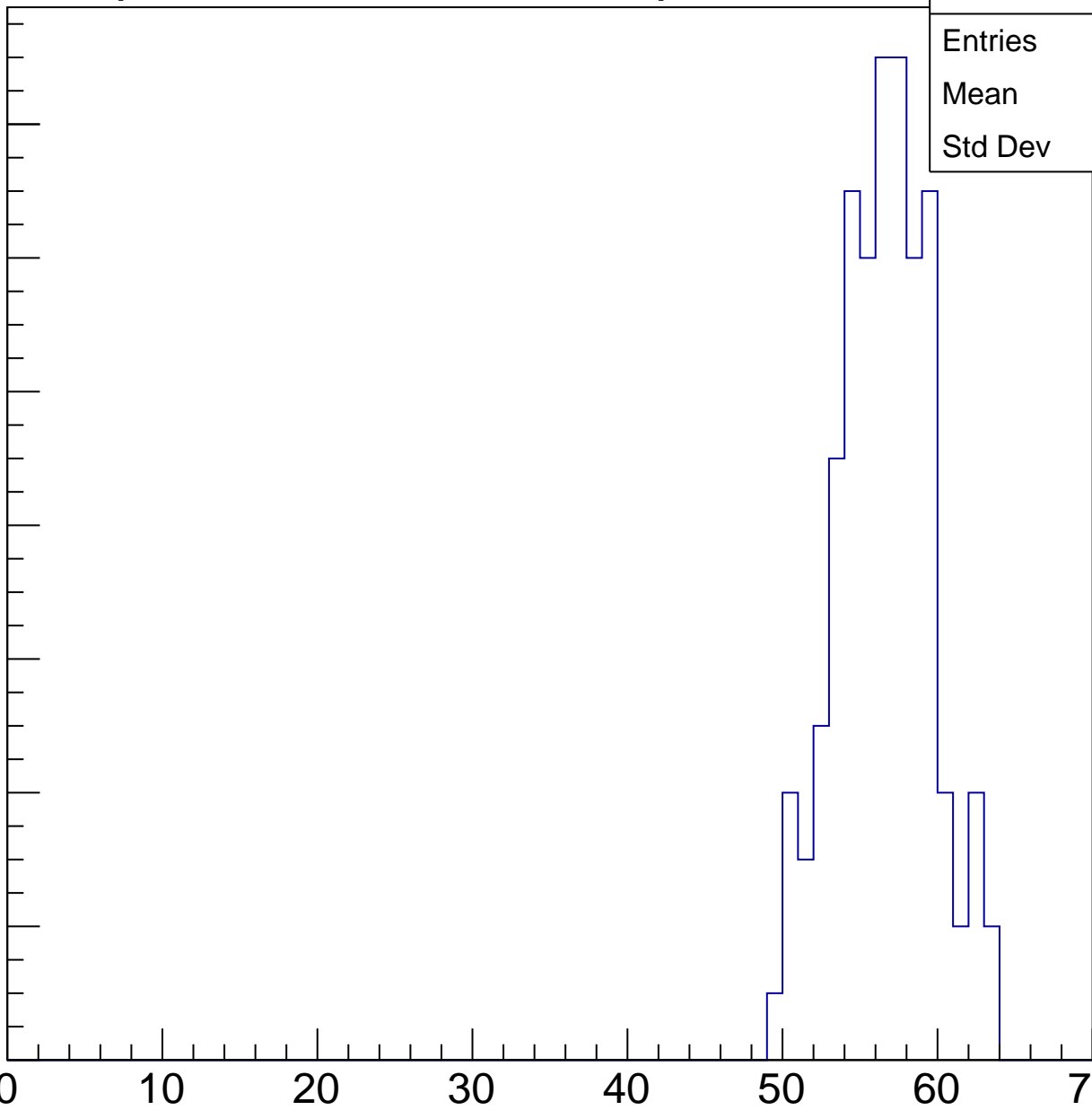
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 114 |
| Mean | 56.1 |
| Std Dev | 3.003 |

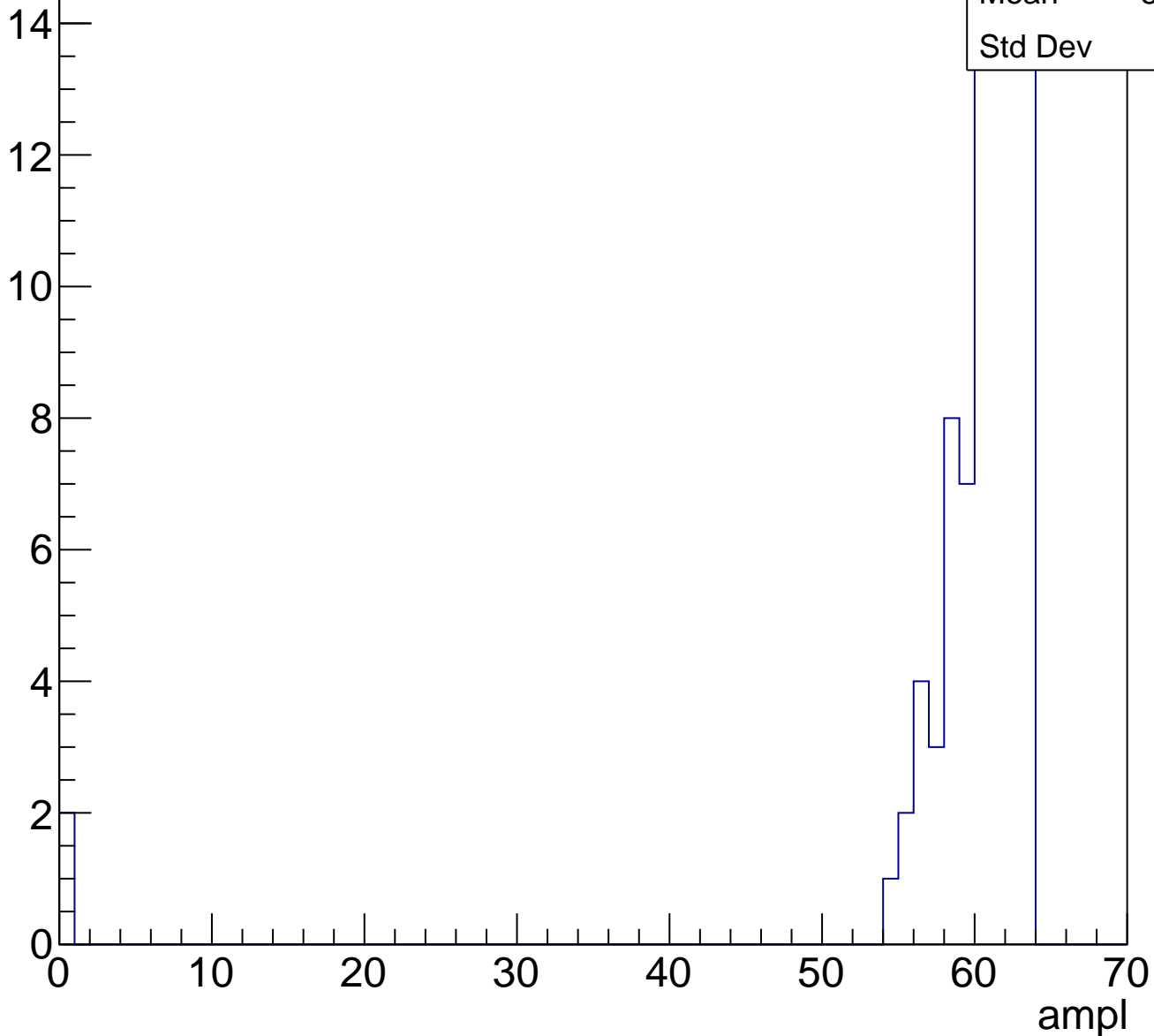
ampl



B1L001S, U26-ch75, adc5

calib_packv5_042523_0143.root, FC#2, port C2

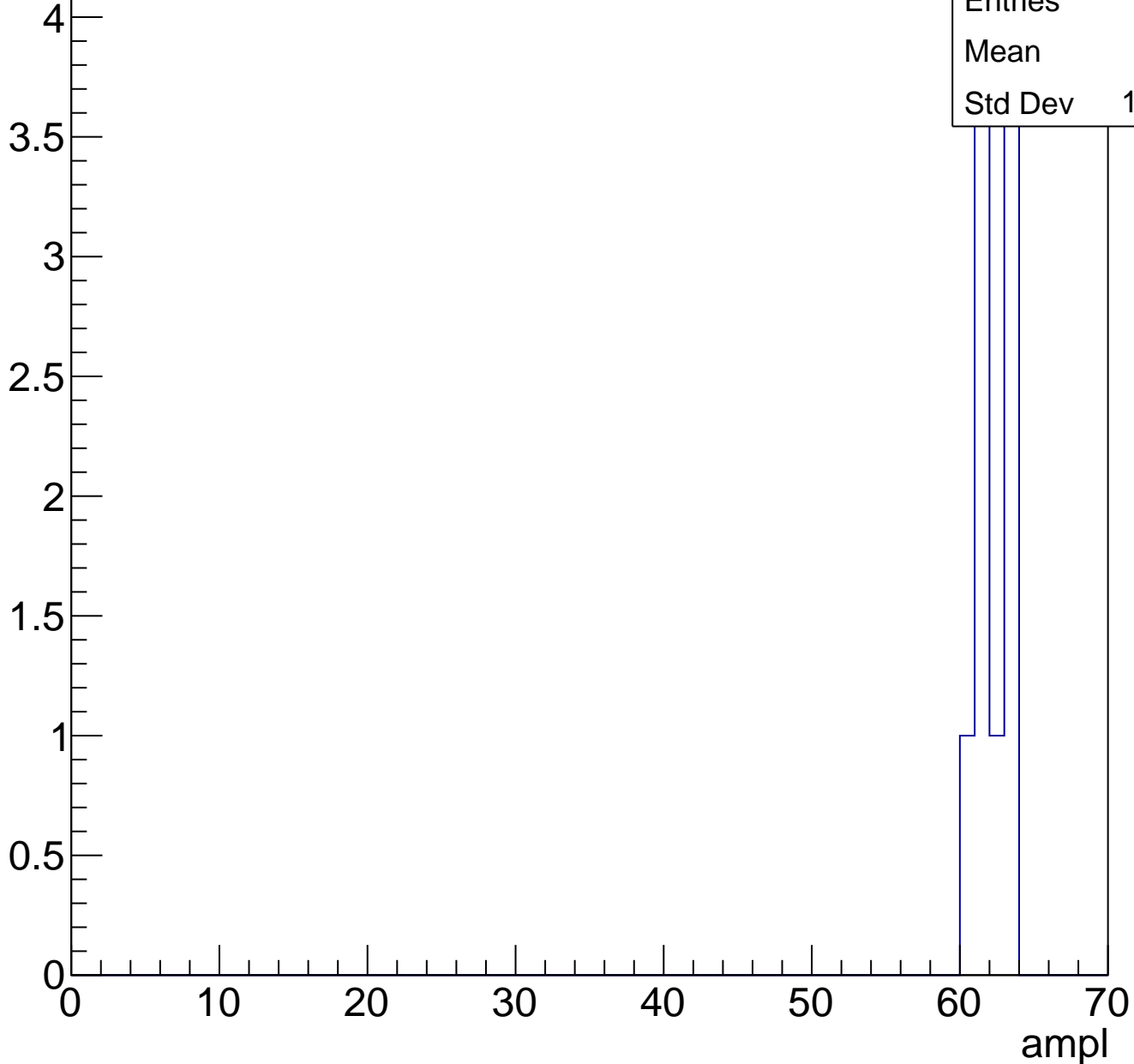
Entry



B1L001S, U26-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch76, adc0

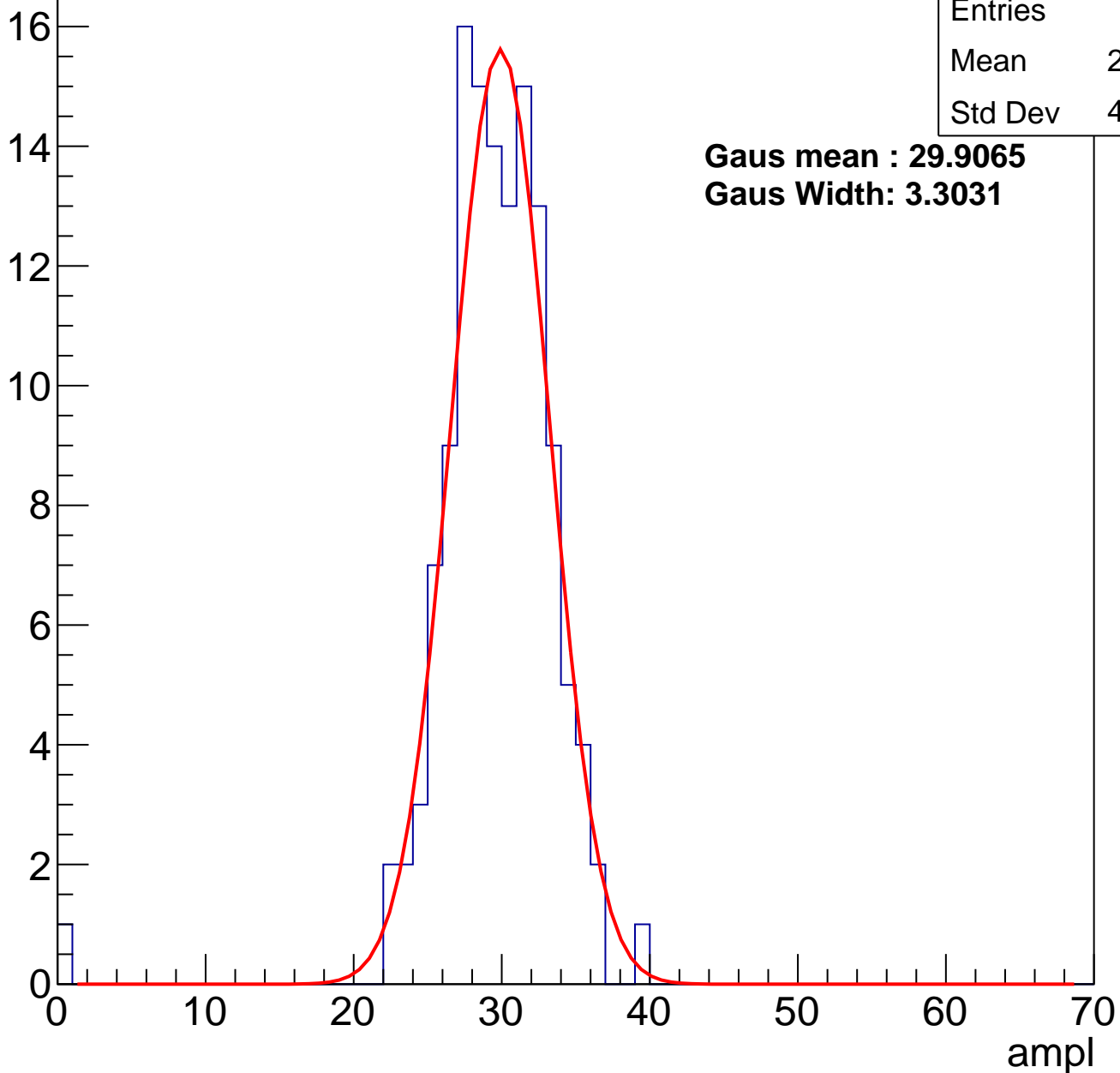
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 29.15 |
| Std Dev | 4.057 |

Gaus mean : 29.9065

Gaus Width: 3.3031

Entry



B1L001S, U26-ch76, adc1

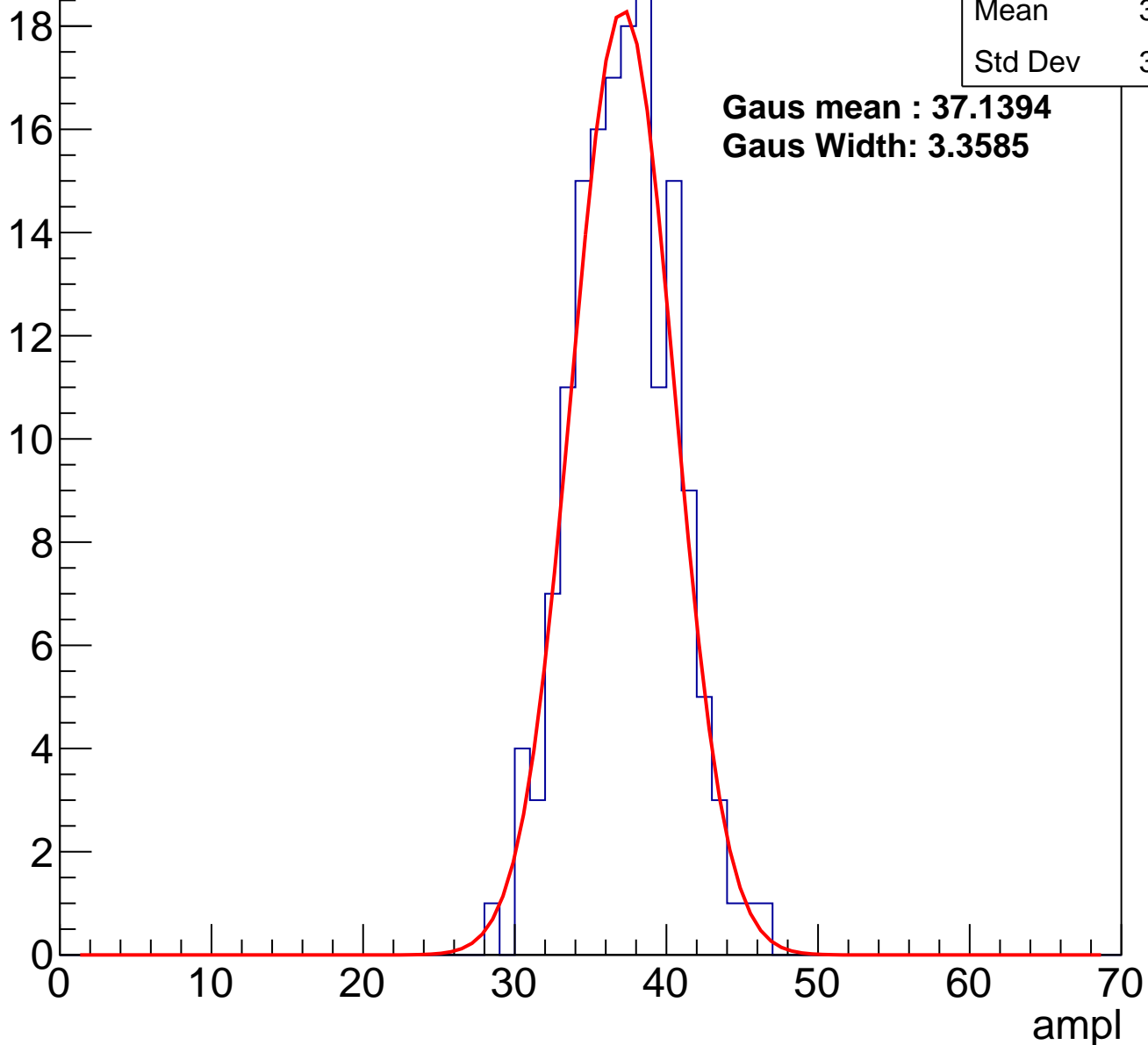
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 157 |
| Mean | 36.75 |
| Std Dev | 3.302 |

Gaus mean : 37.1394

Gaus Width: 3.3585

Entry



B1L001S, U26-ch76, adc2

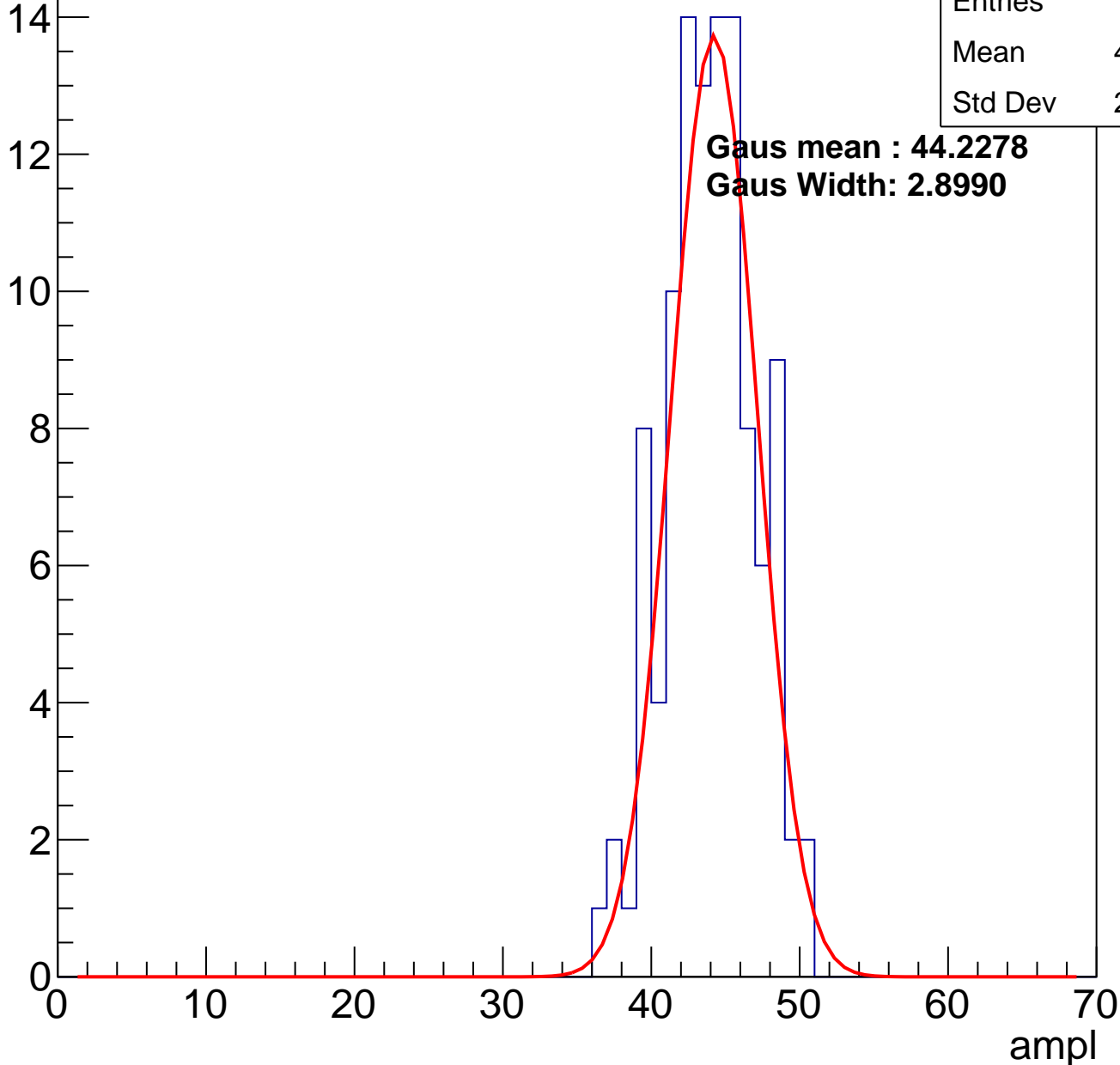
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 108 |
| Mean | 43.55 |
| Std Dev | 2.989 |

Gaus mean : 44.2278

Gaus Width: 2.8990

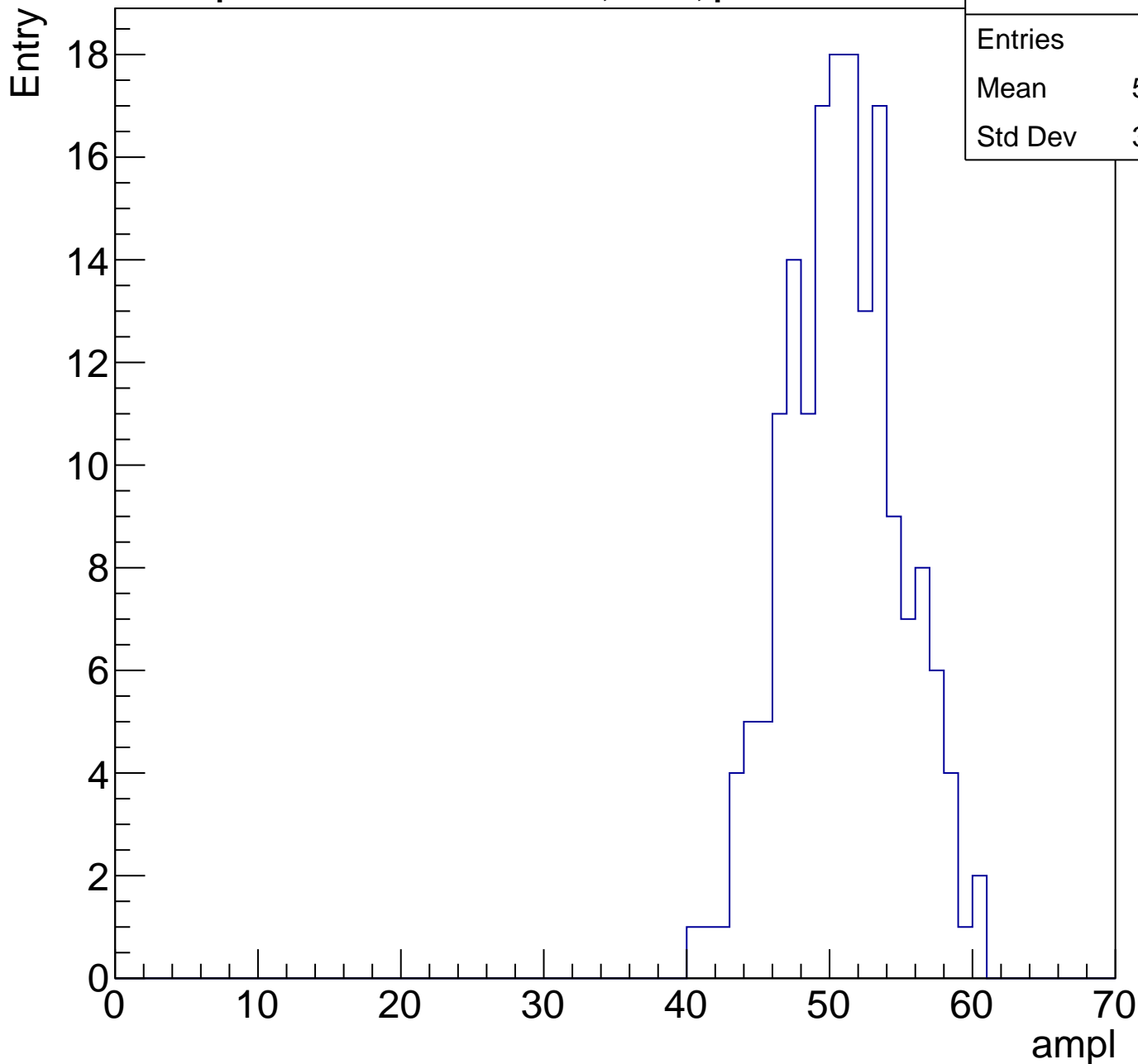
Entry



B1L001S, U26-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 173 |
| Mean | 50.47 |
| Std Dev | 3.973 |



B1L001S, U26-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 100 |
| Mean | 56.89 |
| Std Dev | 3.046 |

Entry

12

10

8

6

4

2

0

0

10

20

30

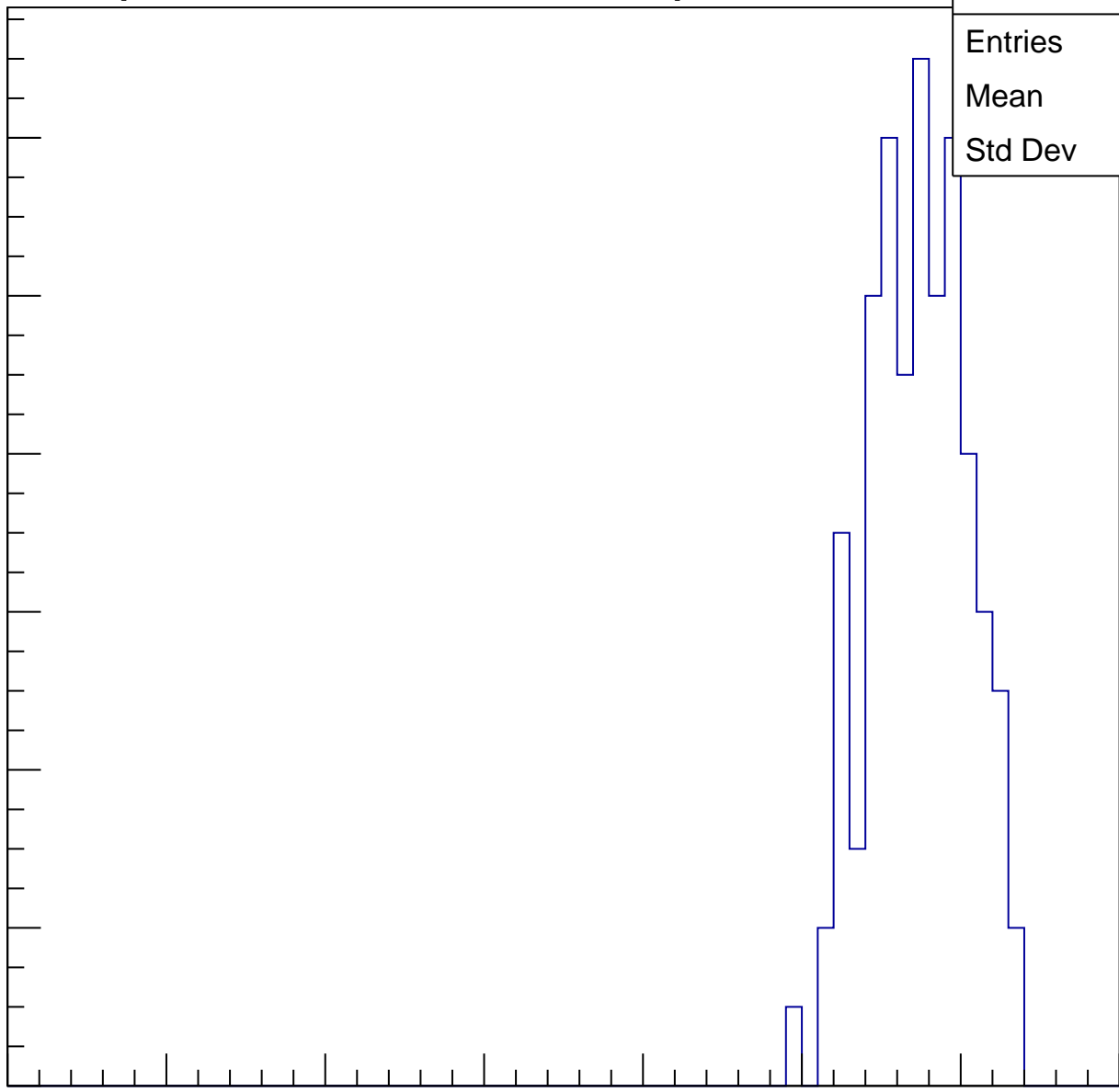
40

50

60

70

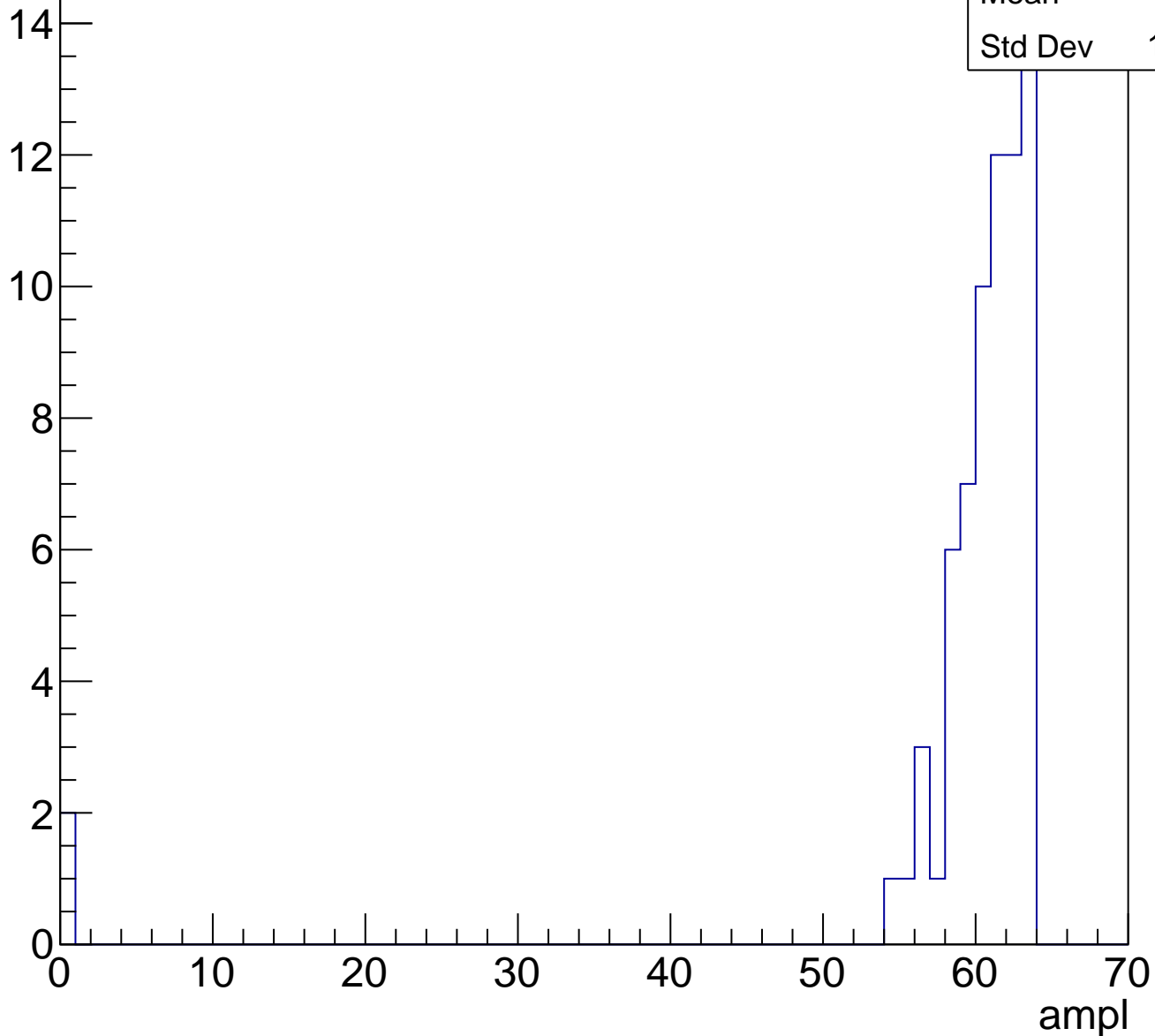
ampl



B1L001S, U26-ch76, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.8 |
| Std Dev | 10.31 |

B1L001S, U26-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62.12

Std Dev

0.7806

B1L001S, U26-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 22 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl



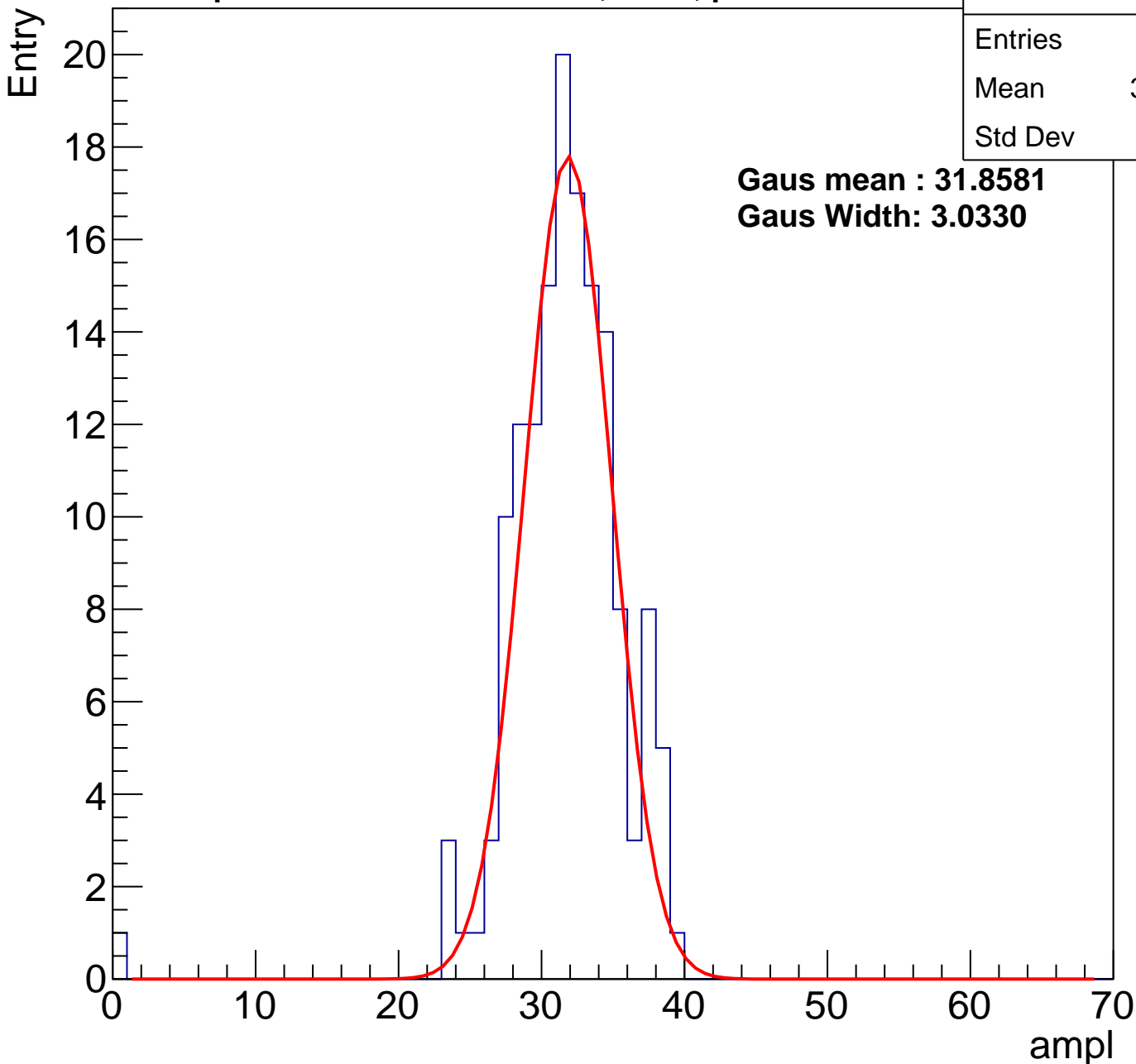
B1L001S, U26-ch77, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 149 |
| Mean | 31.19 |
| Std Dev | 4.21 |

Gaus mean : 31.8581

Gaus Width: 3.0330



B1L001S, U26-ch77, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 37.93 |
| Std Dev | 3.296 |

Gaus mean : 38.4934

Gaus Width: 3.7007

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

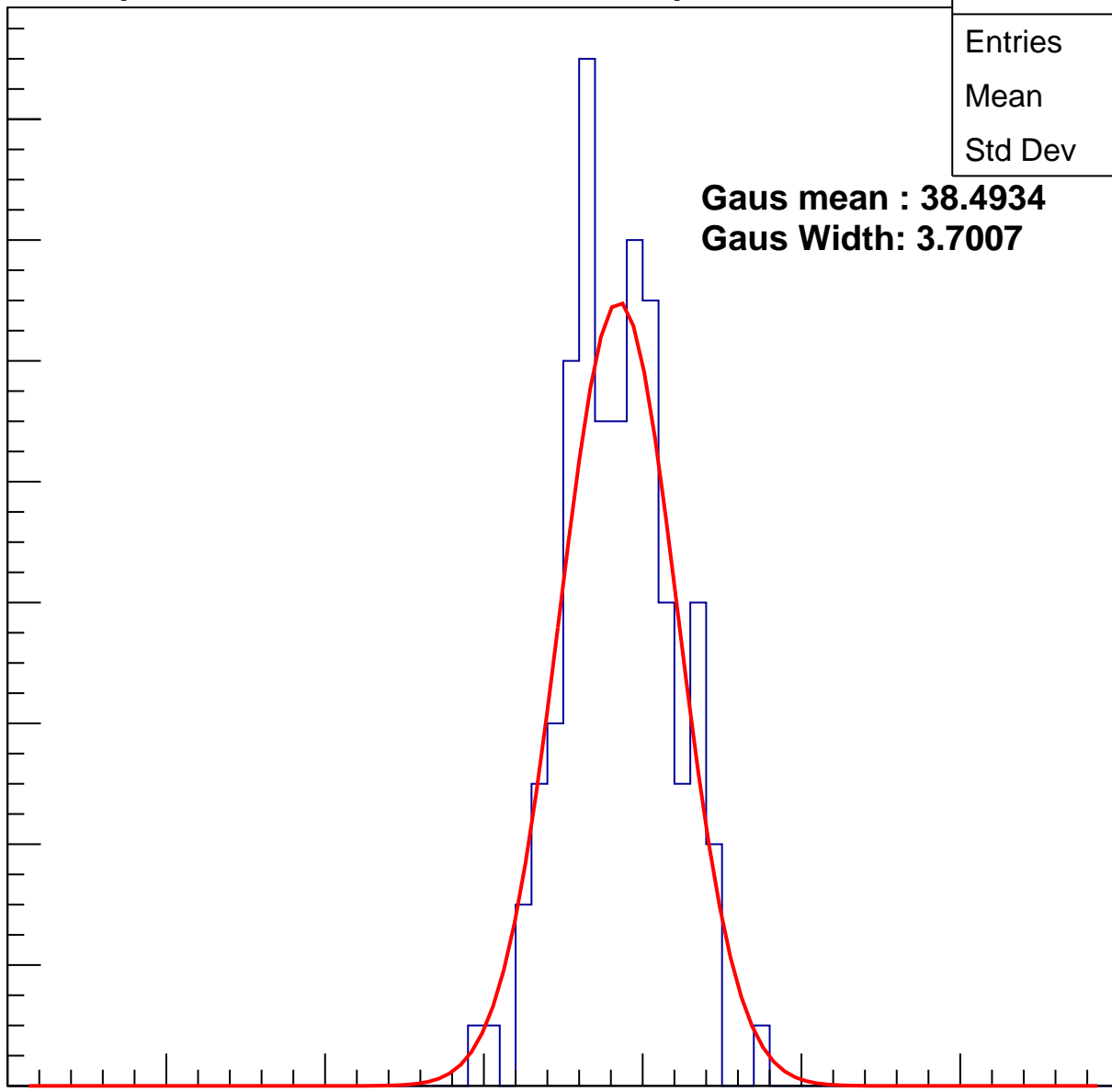
40

50

60

70

ampl



B1L001S, U26-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

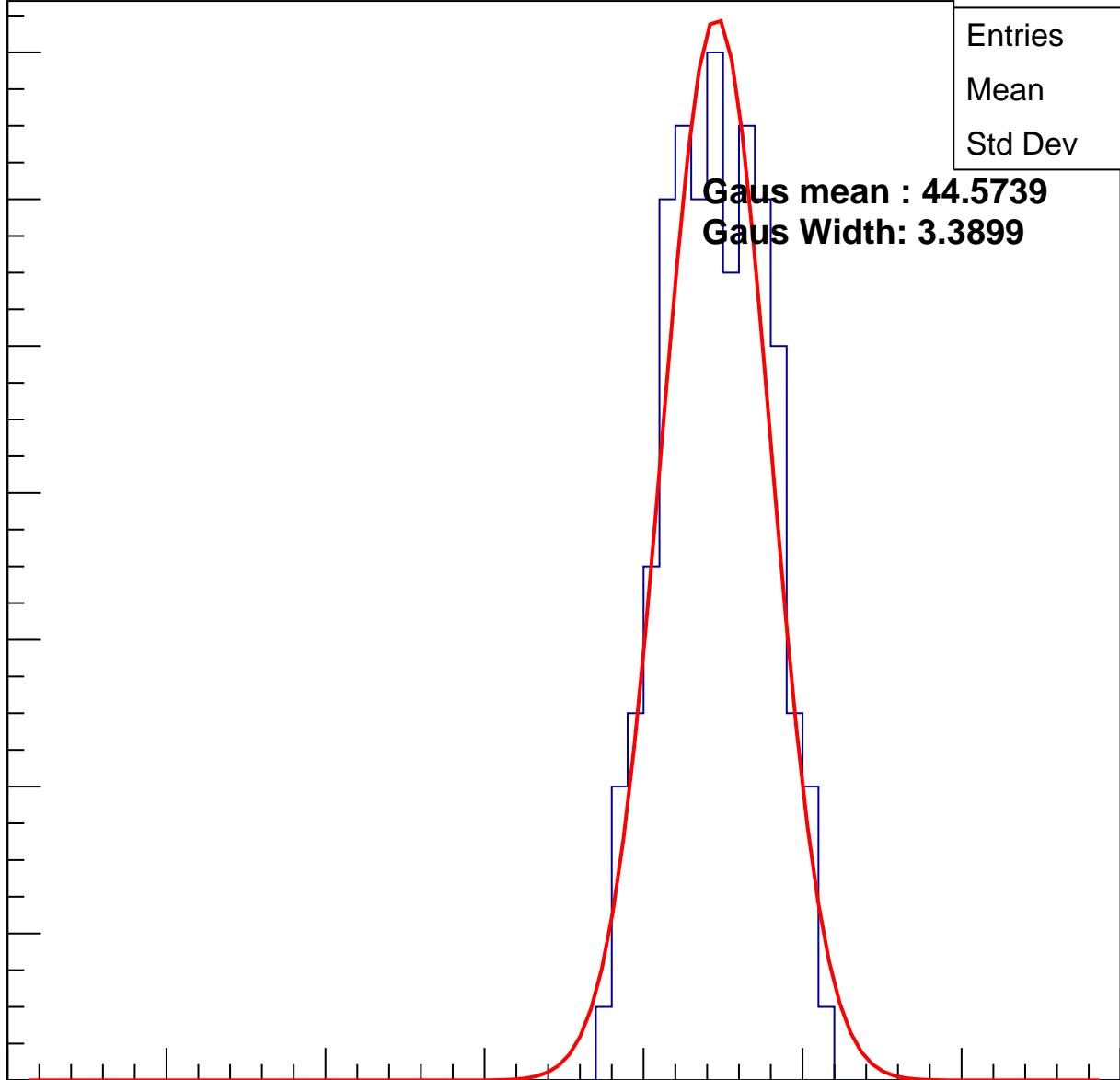
| | |
|---------|-------|
| Entries | 124 |
| Mean | 44.09 |
| Std Dev | 3.175 |

Gaus mean : 44.5739

Gaus Width: 3.3899

ampl

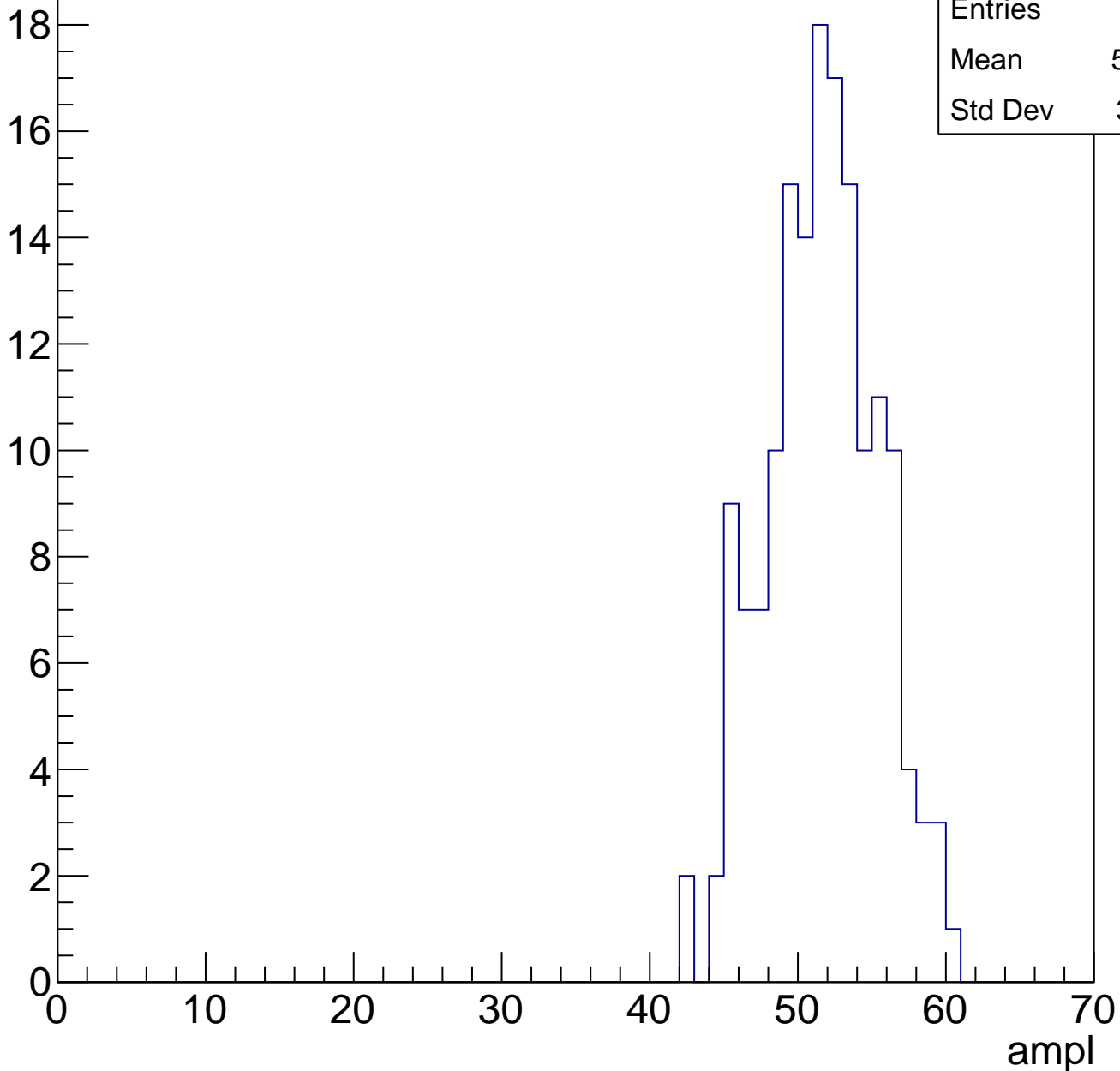
0 10 20 30 40 50 60 70



B1L001S, U26-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

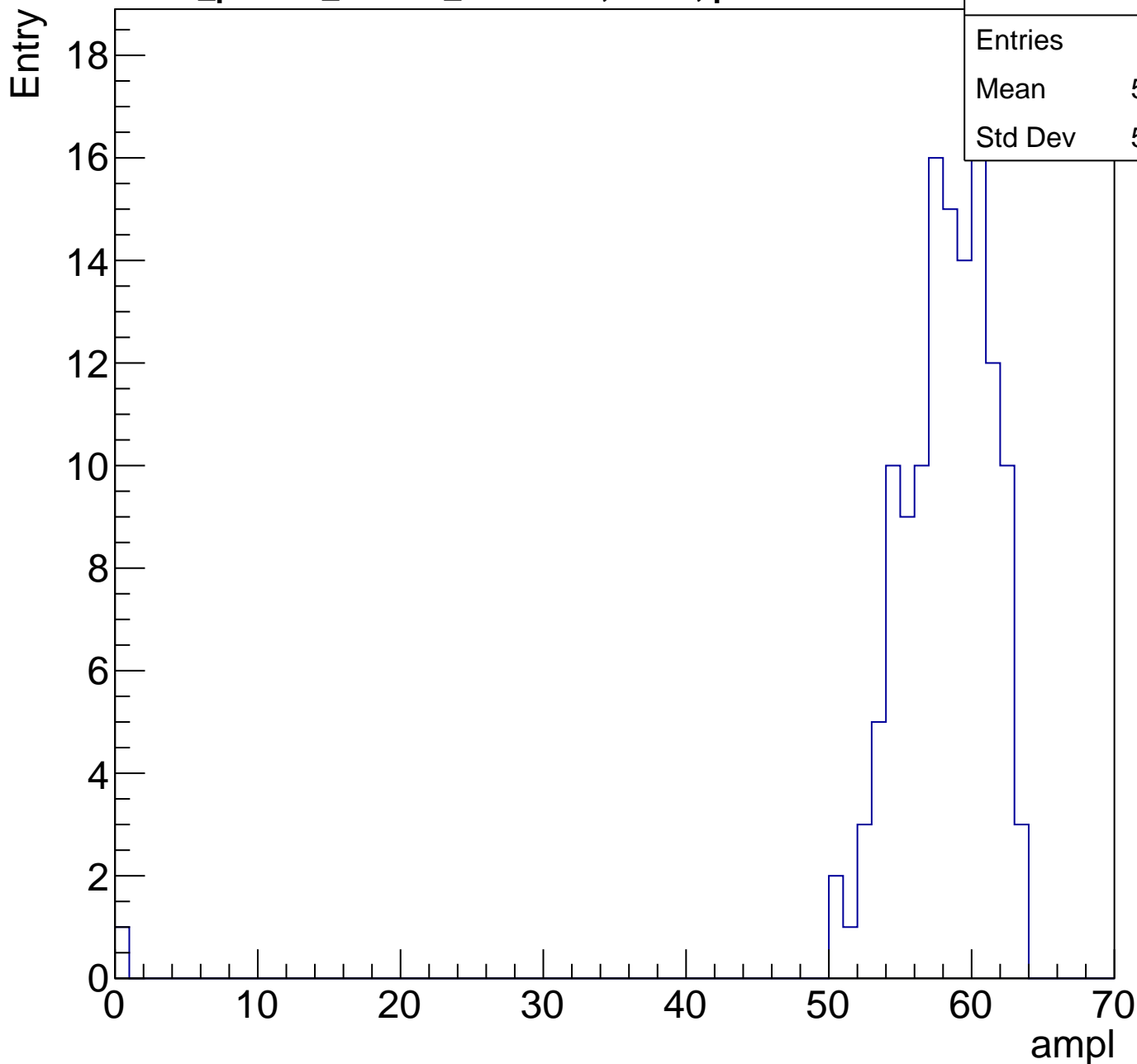
Entry



B1L001S, U26-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 57.33 |
| Std Dev | 5.868 |



B1L001S, U26-ch77, adc5

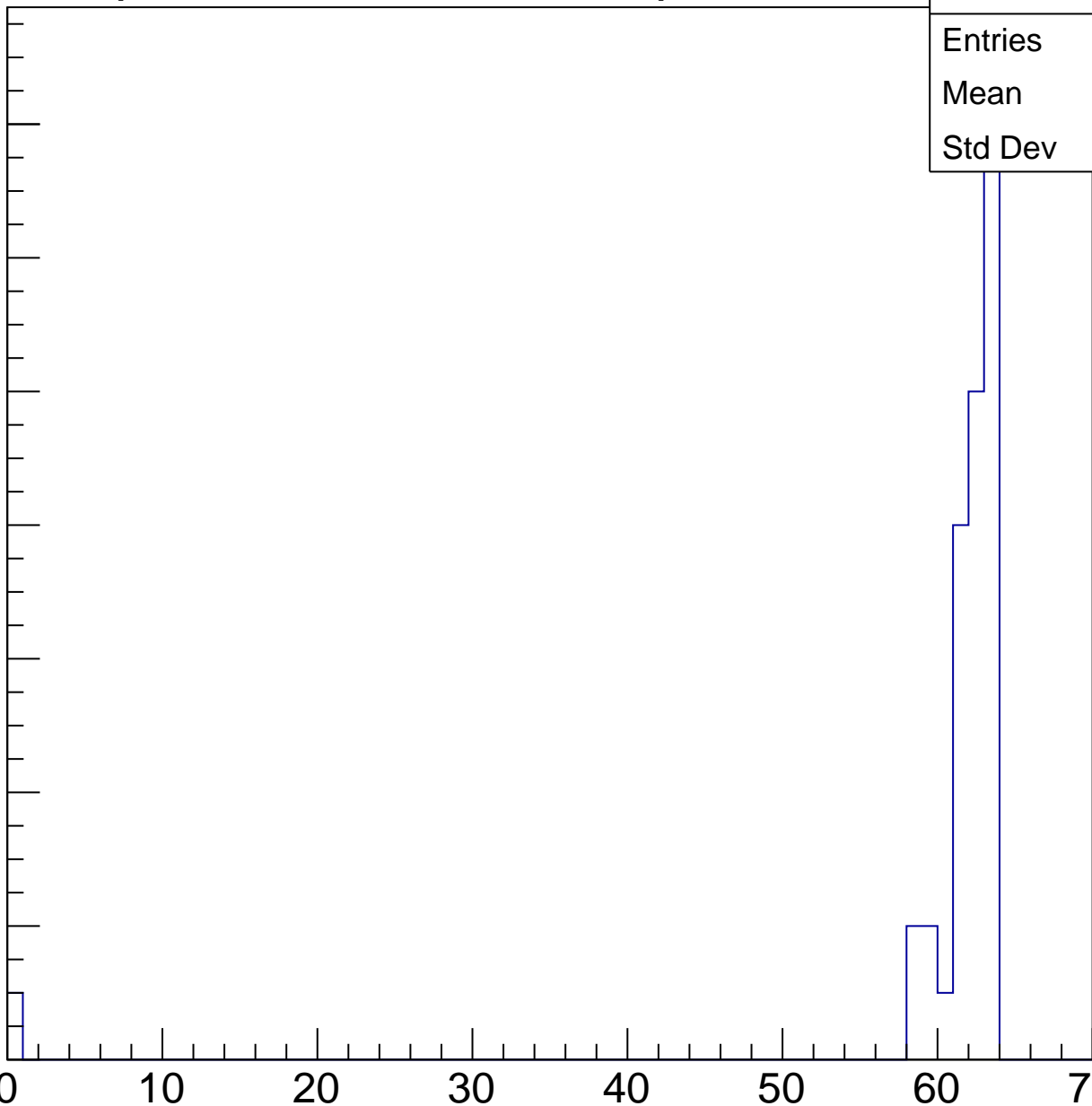
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 39 |
| Mean | 60.18 |
| Std Dev | 9.86 |

ampl



B1L001S, U26-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch78, adc0

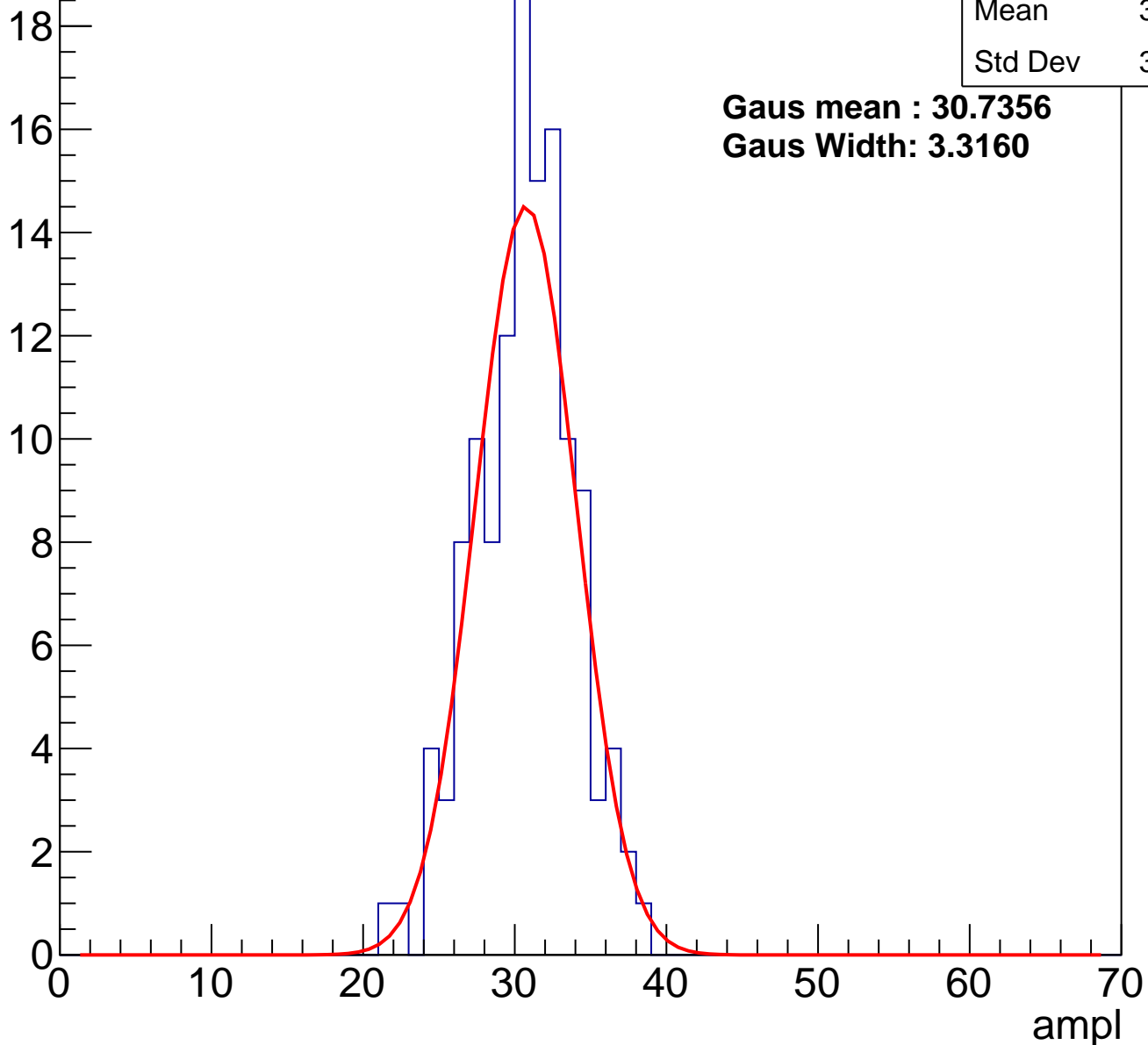
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 126 |
| Mean | 30.22 |
| Std Dev | 3.229 |

Gaus mean : 30.7356

Gaus Width: 3.3160

Entry



B1L001S, U26-ch78, adc1

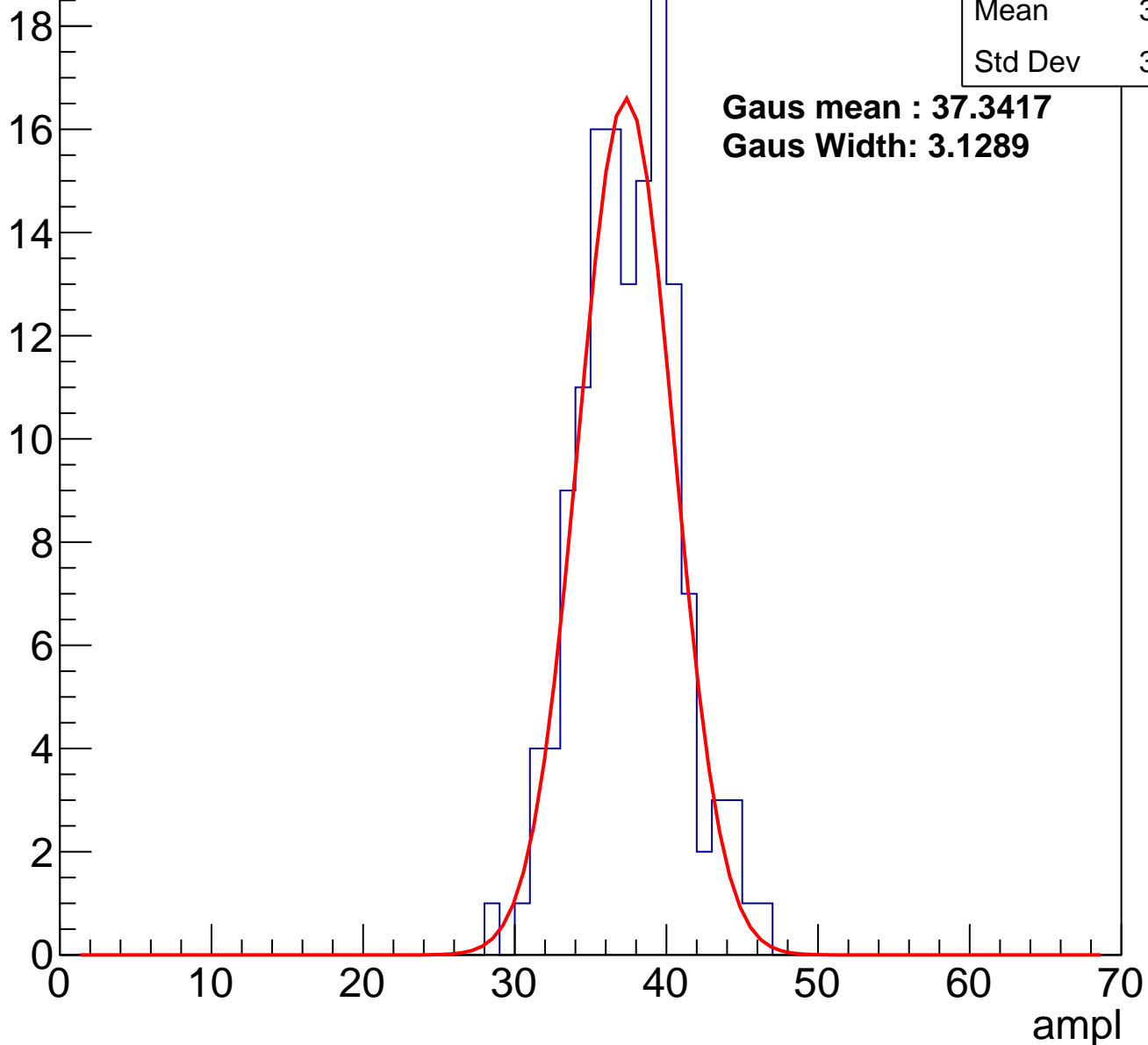
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 139 |
| Mean | 37.06 |
| Std Dev | 3.248 |

Gaus mean : 37.3417

Gaus Width: 3.1289

Entry



B1L001S, U26-ch78, adc2

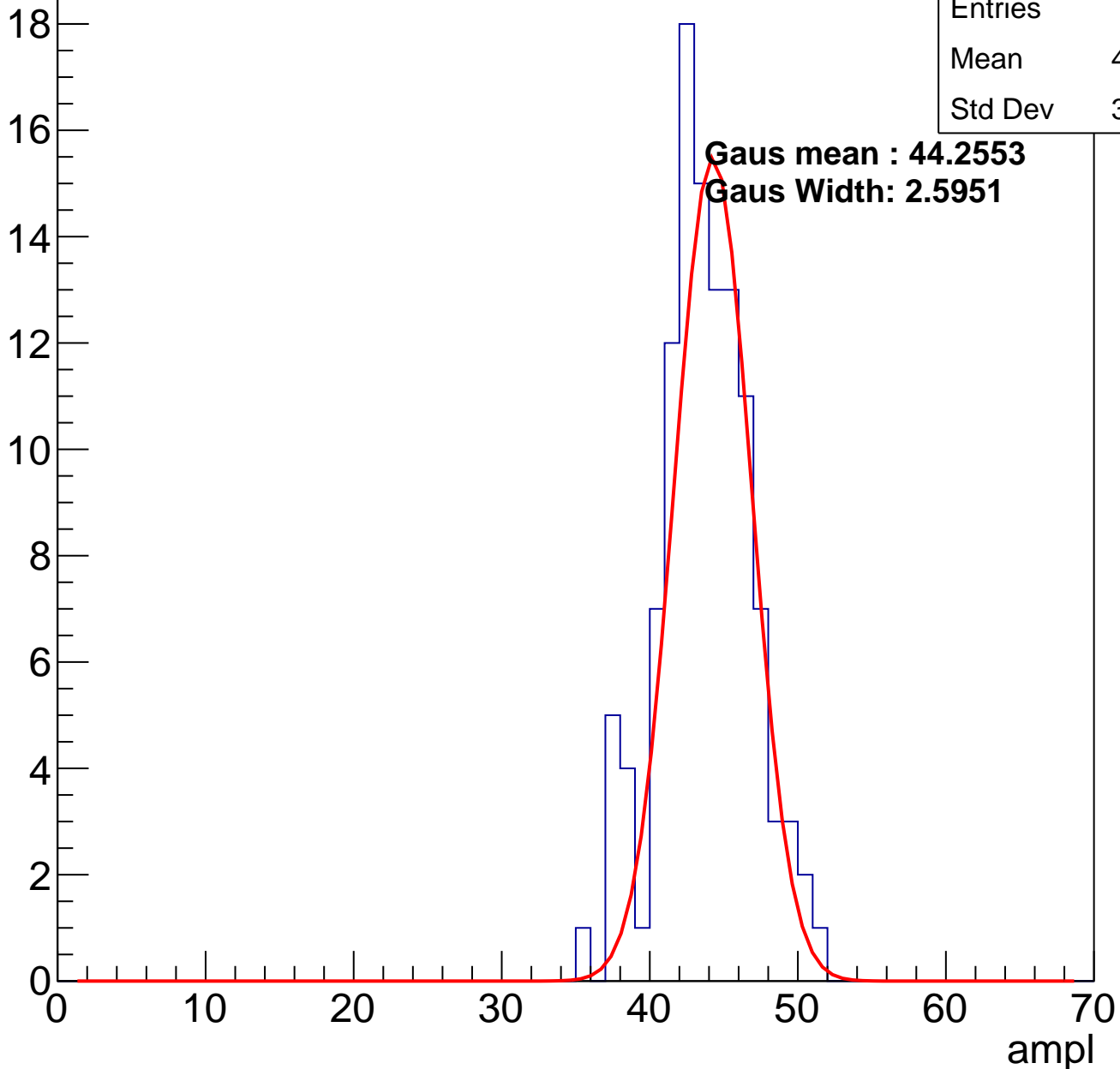
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 43.26 |
| Std Dev | 3.083 |

Gaus mean : 44.2553

Gaus Width: 2.5951

Entry



B1L001S, U26-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

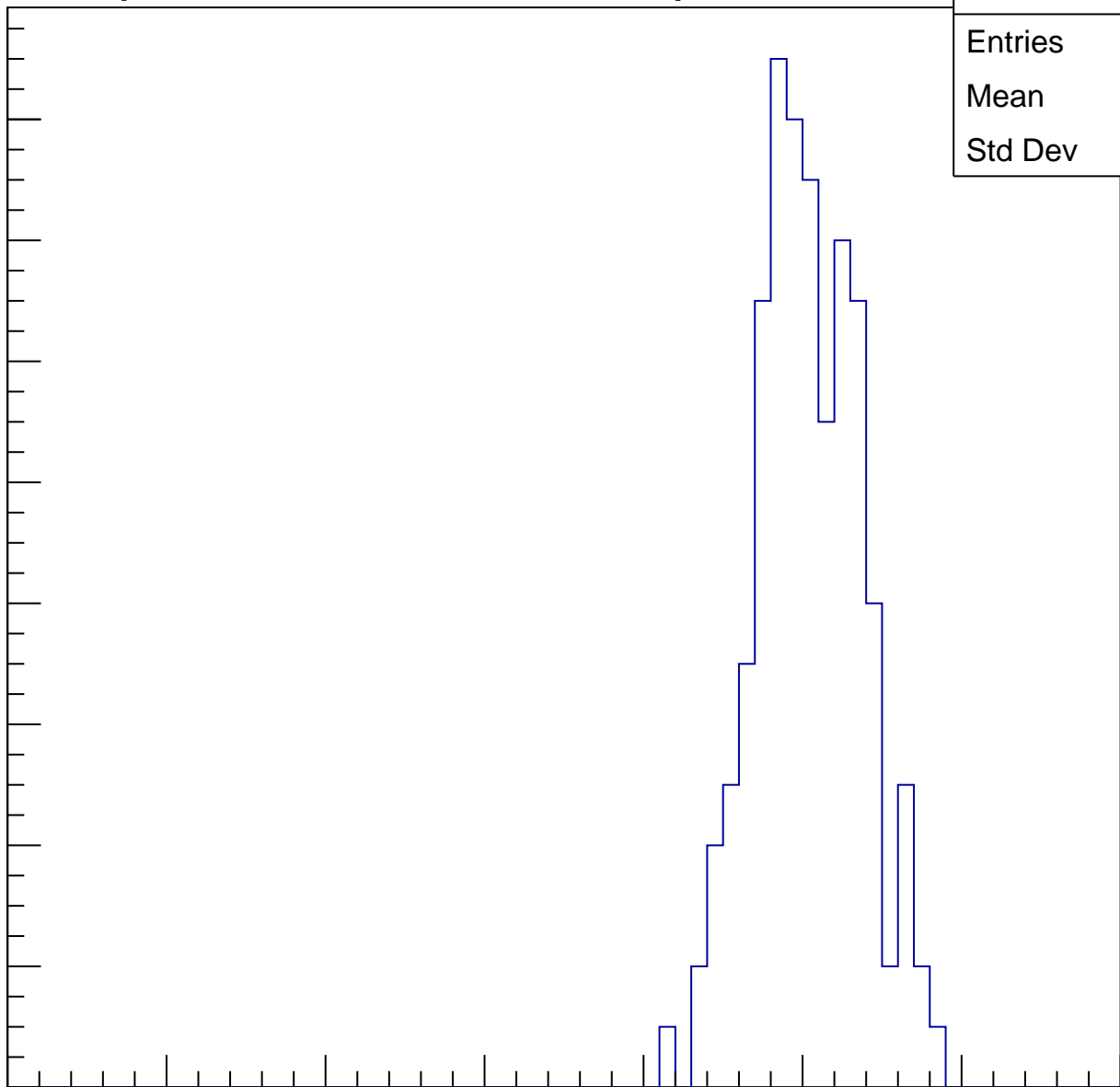
| | |
|---------|-------|
| Entries | 136 |
| Mean | 49.88 |
| Std Dev | 3.28 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

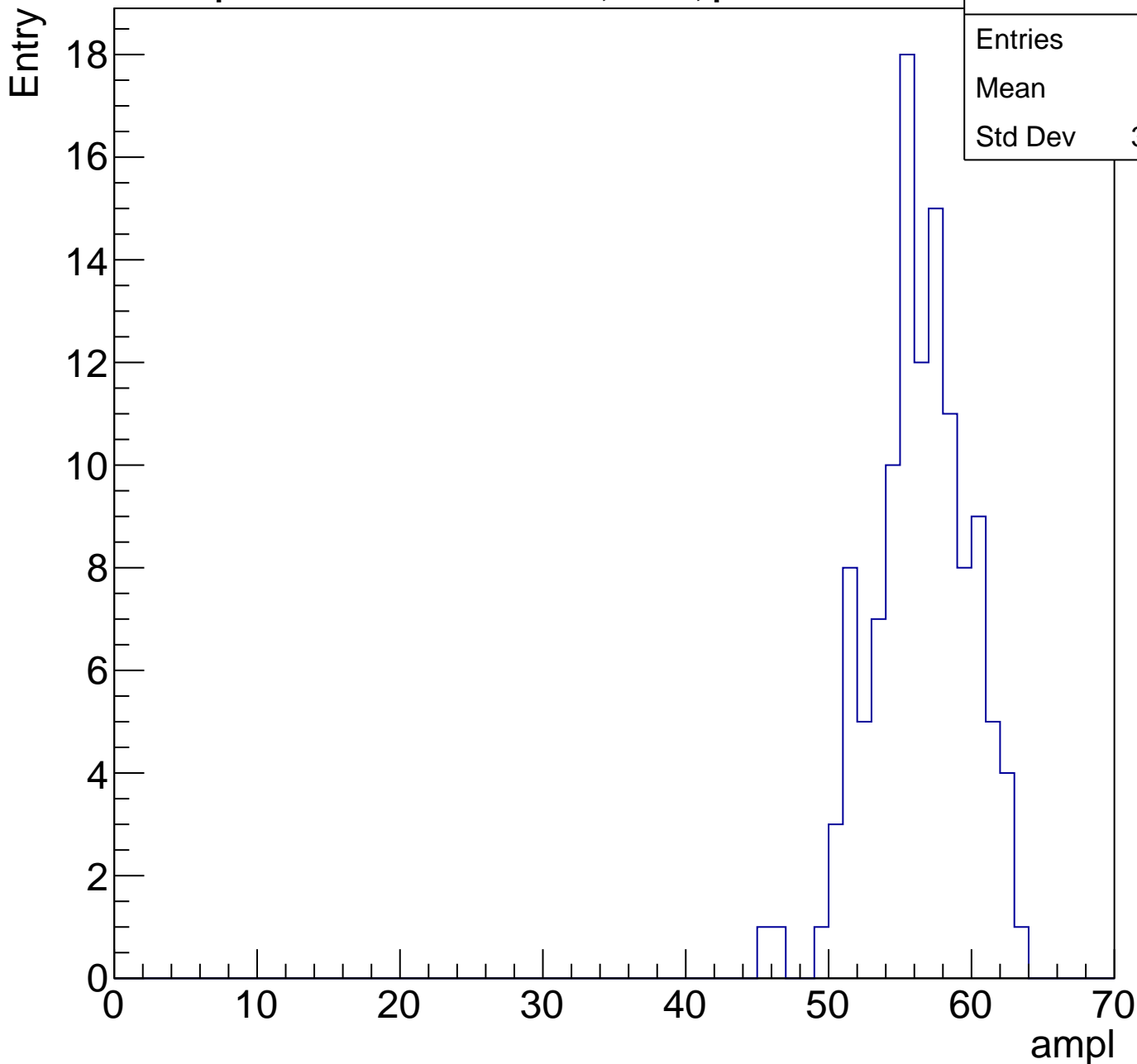
ampl



B1L001S, U26-ch78, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 55.9 |
| Std Dev | 3.394 |



B1L001S, U26-ch78, adc5

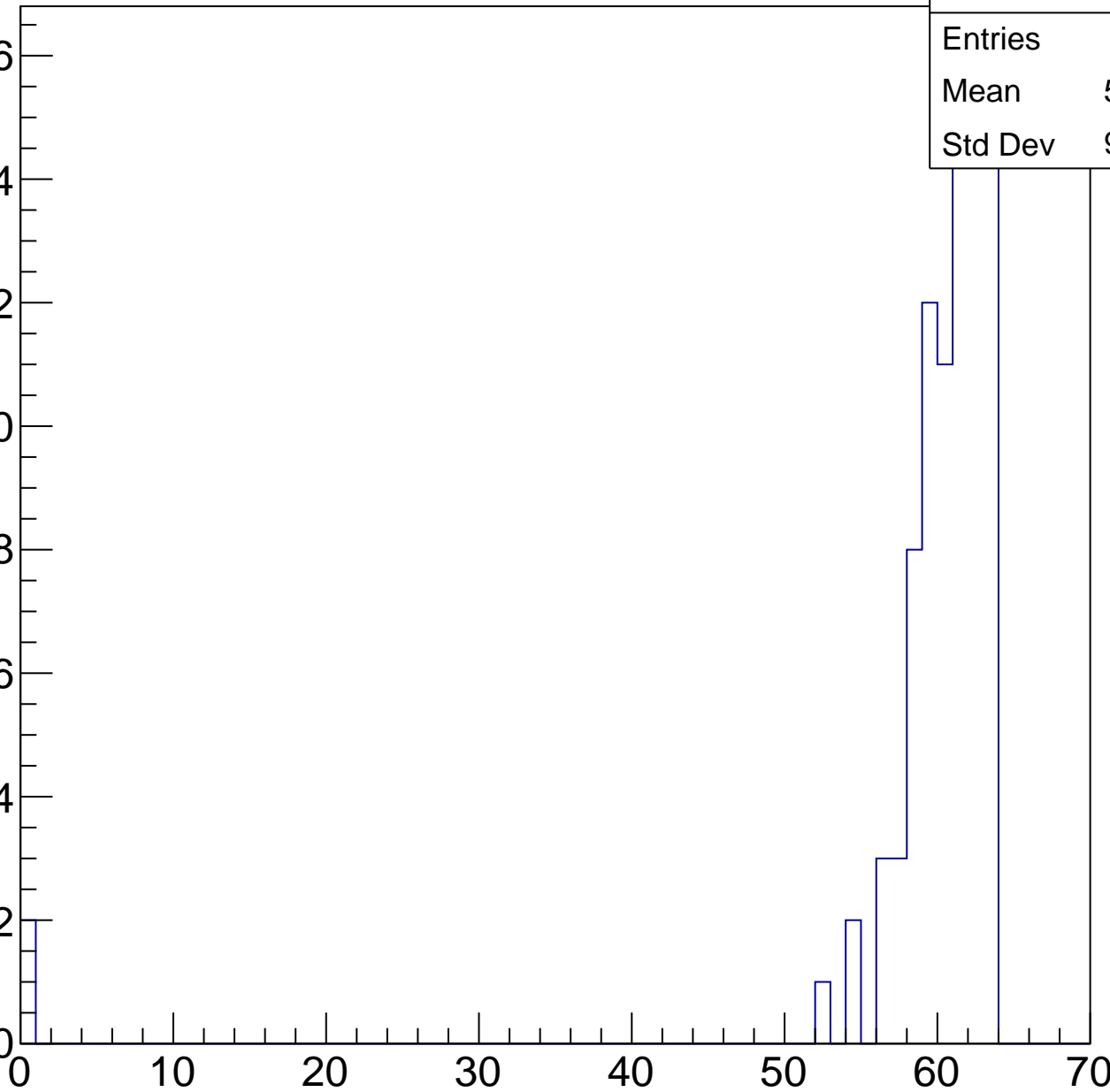
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 58.94 |
| Std Dev | 9.228 |

ampl



B1L001S, U26-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



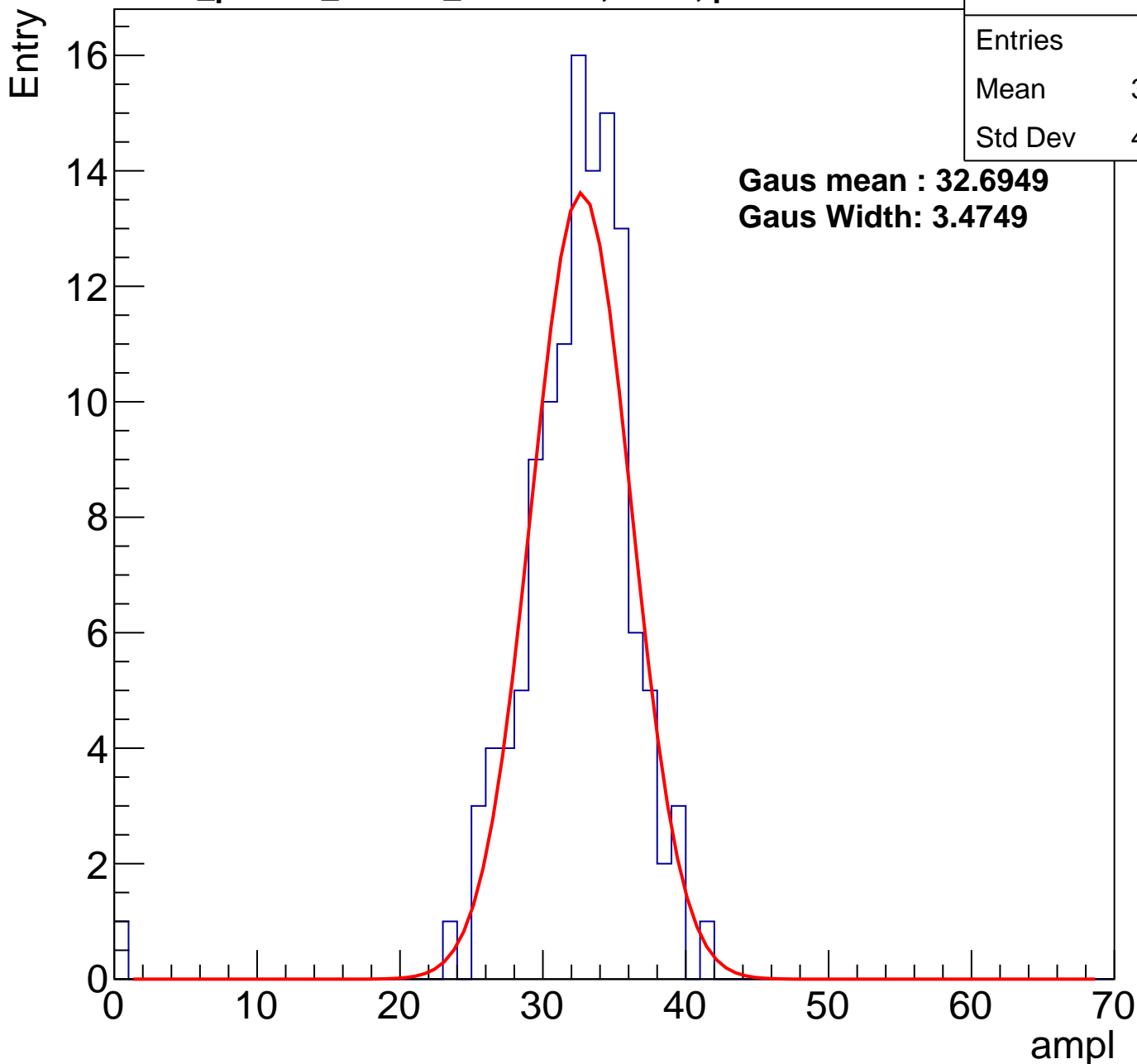
B1L001S, U26-ch79, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 31.92 |
| Std Dev | 4.435 |

Gaus mean : 32.6949

Gaus Width: 3.4749



B1L001S, U26-ch79, adc1

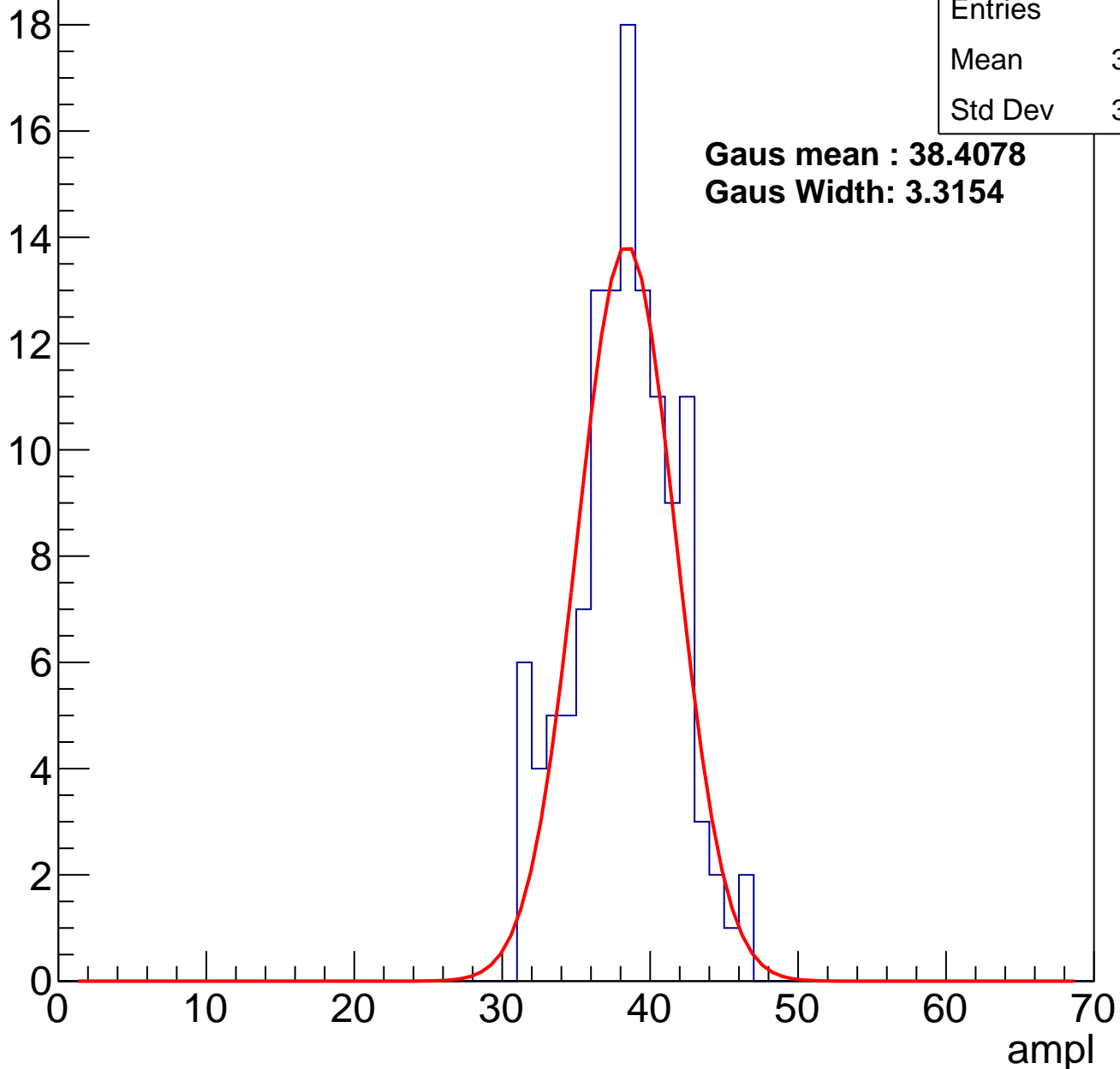
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 37.88 |
| Std Dev | 3.382 |

Gaus mean : 38.4078

Gaus Width: 3.3154

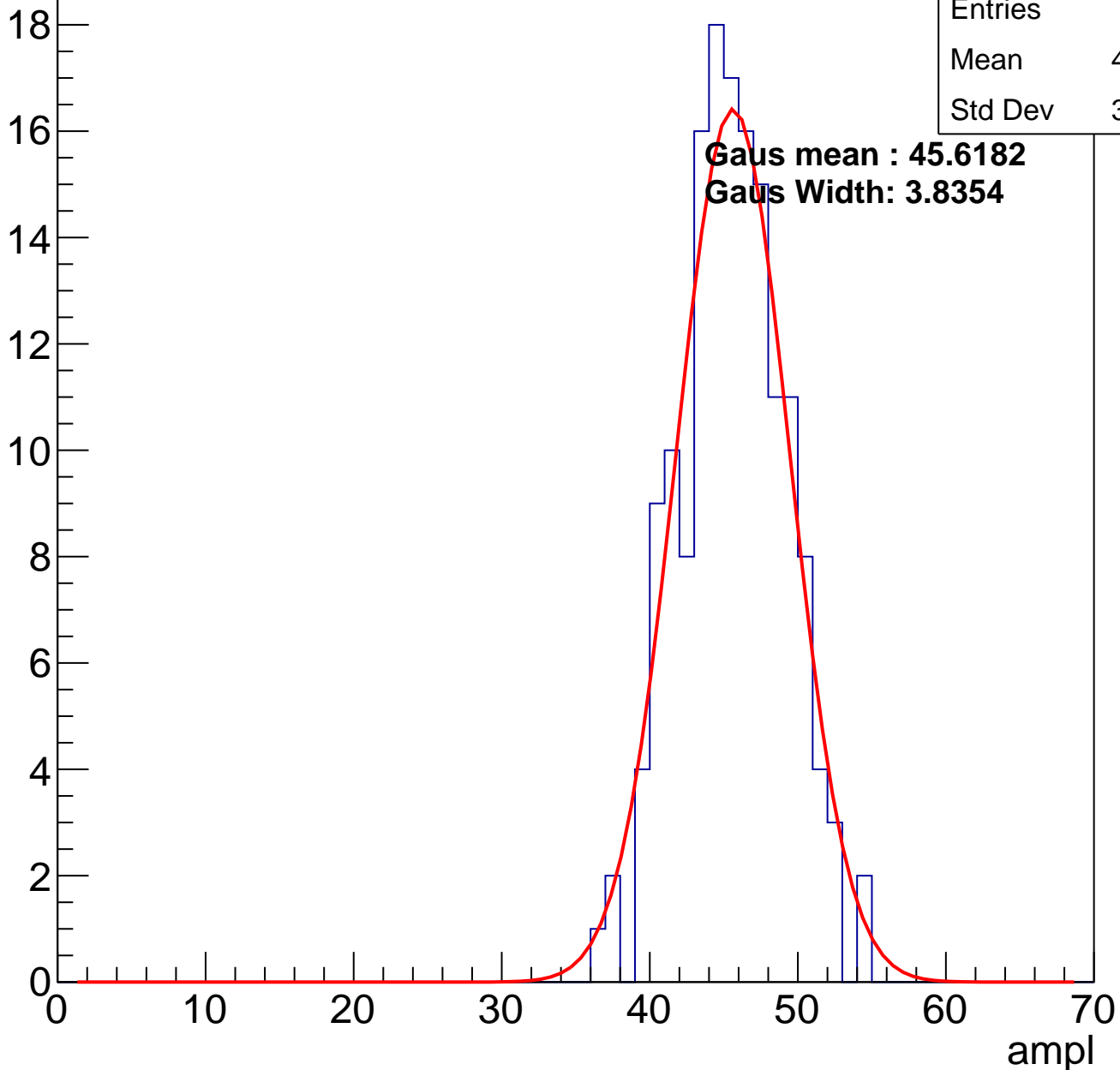
Entry



B1L001S, U26-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

52.27

Std Dev

3.527

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 57.61 |
| Std Dev | 6.104 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

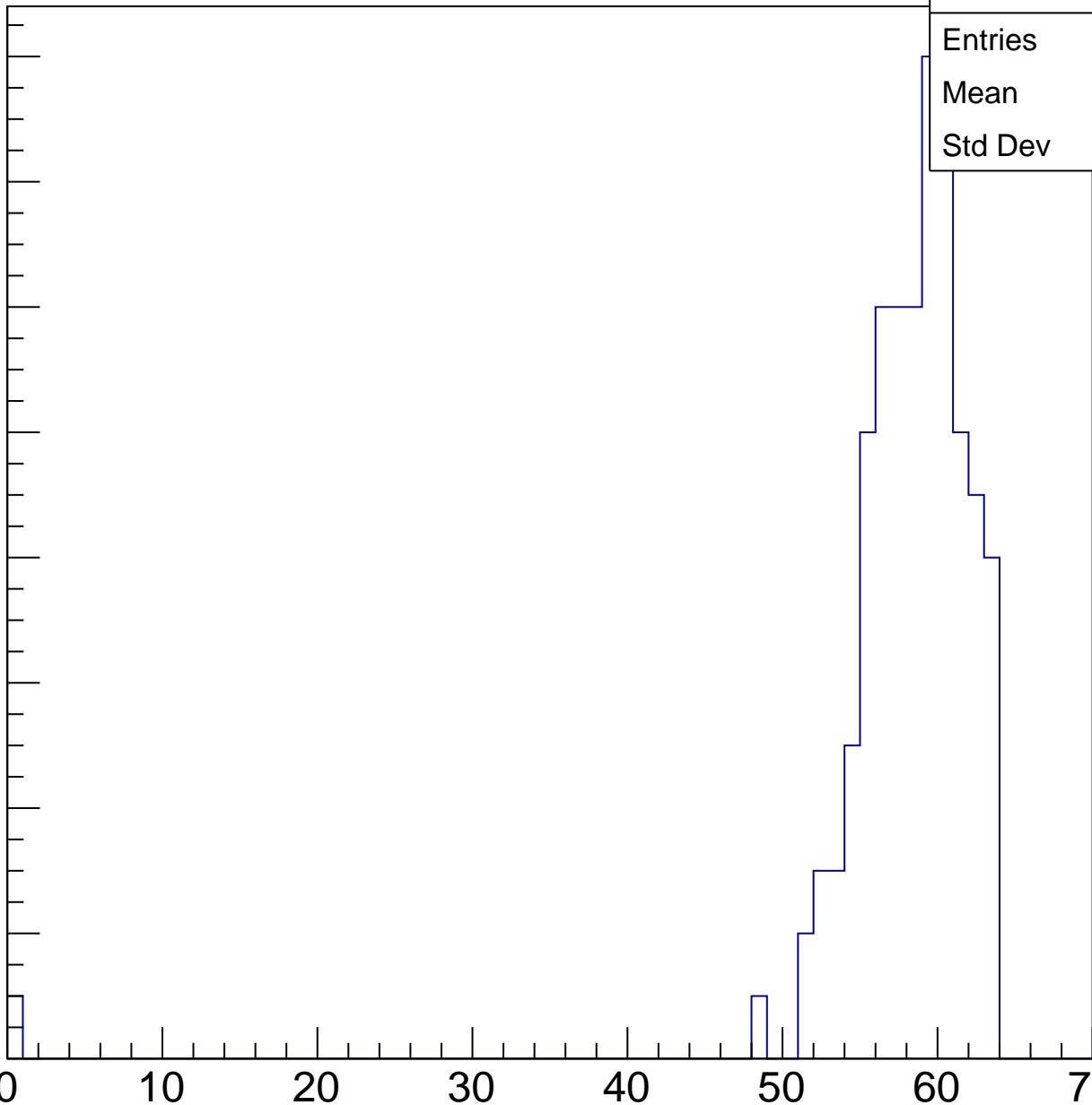
40

50

60

70

ampl



B1L001S, U26-ch79, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

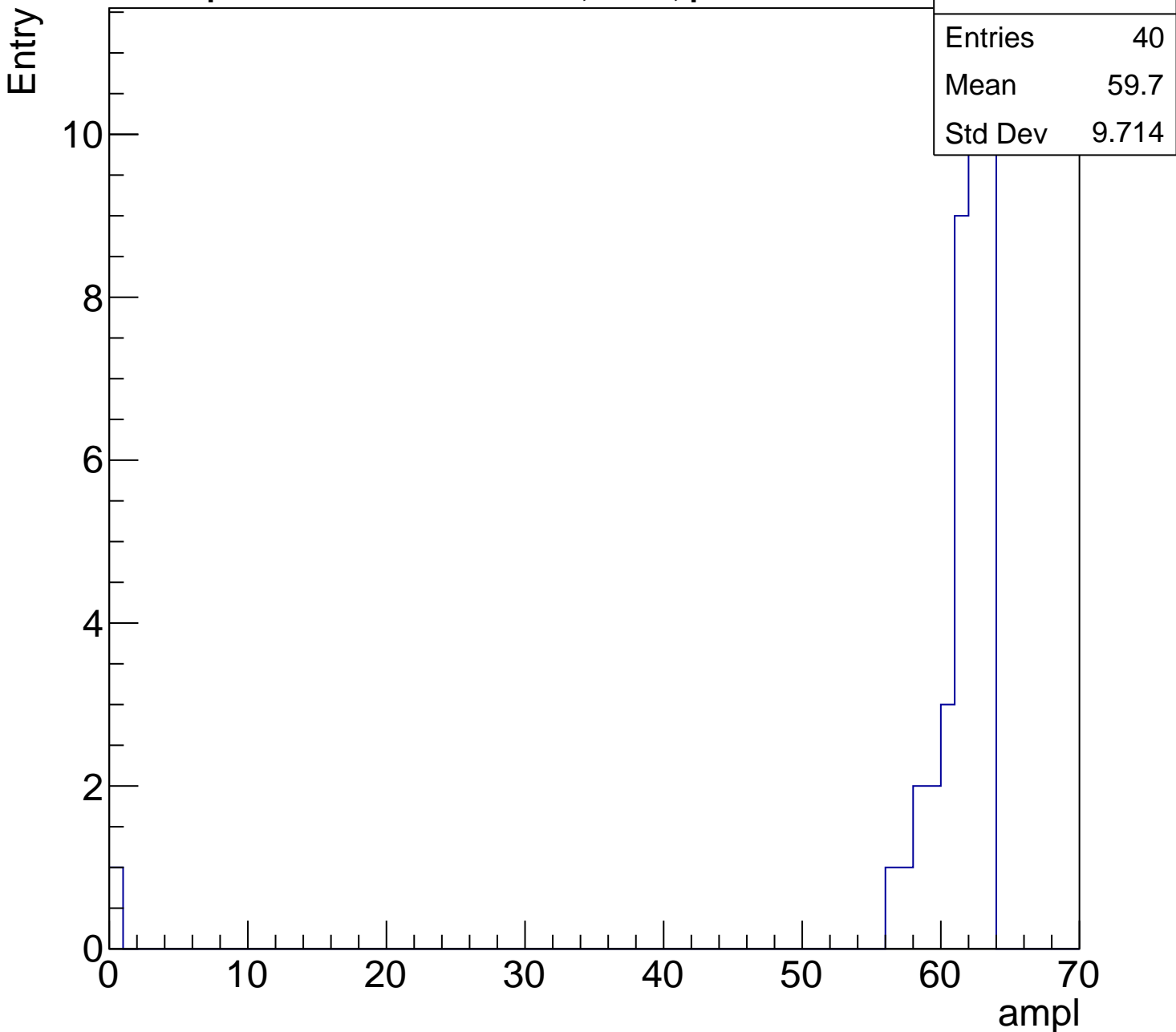
2

0

| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.7 |
| Std Dev | 9.714 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

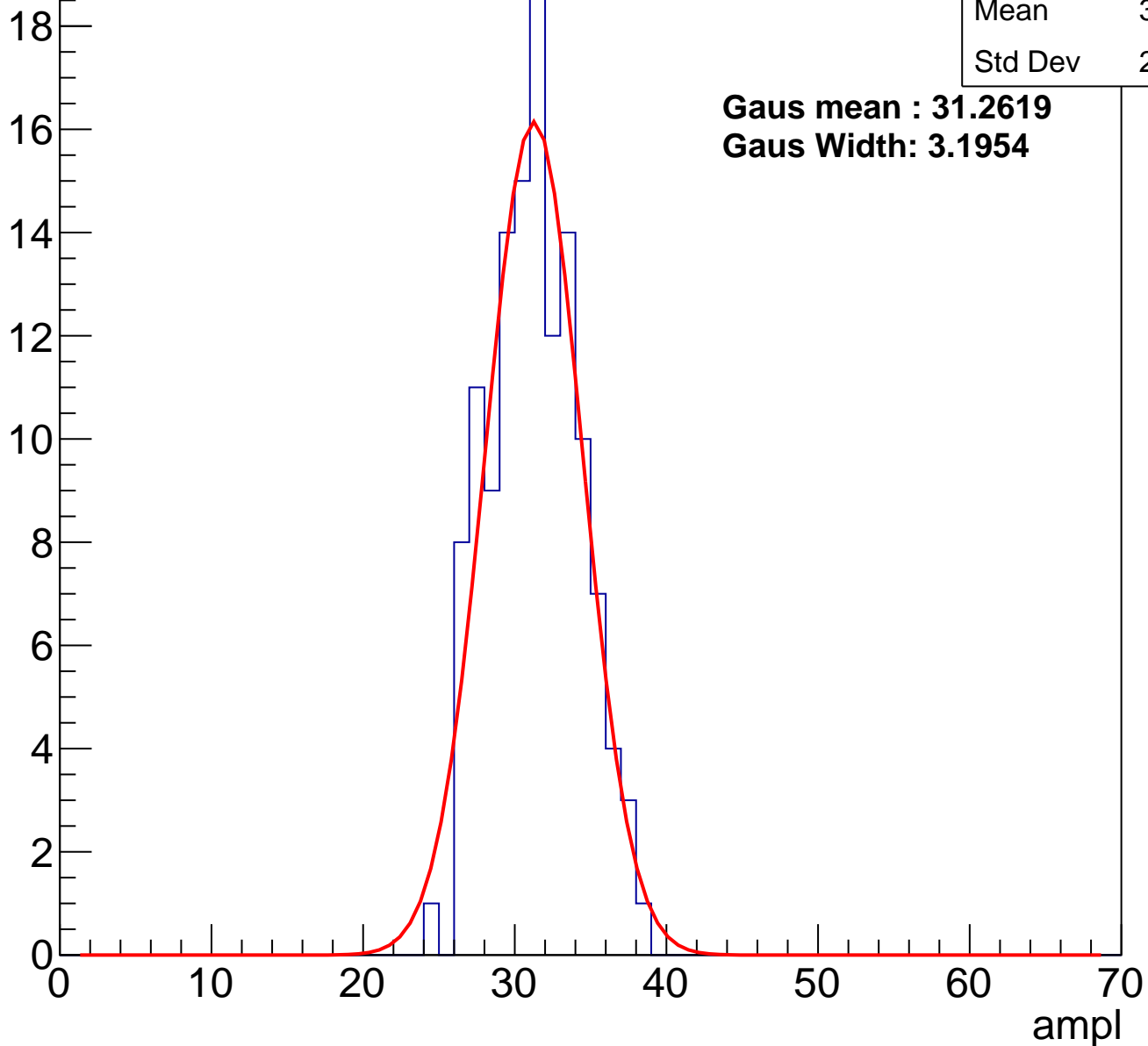


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

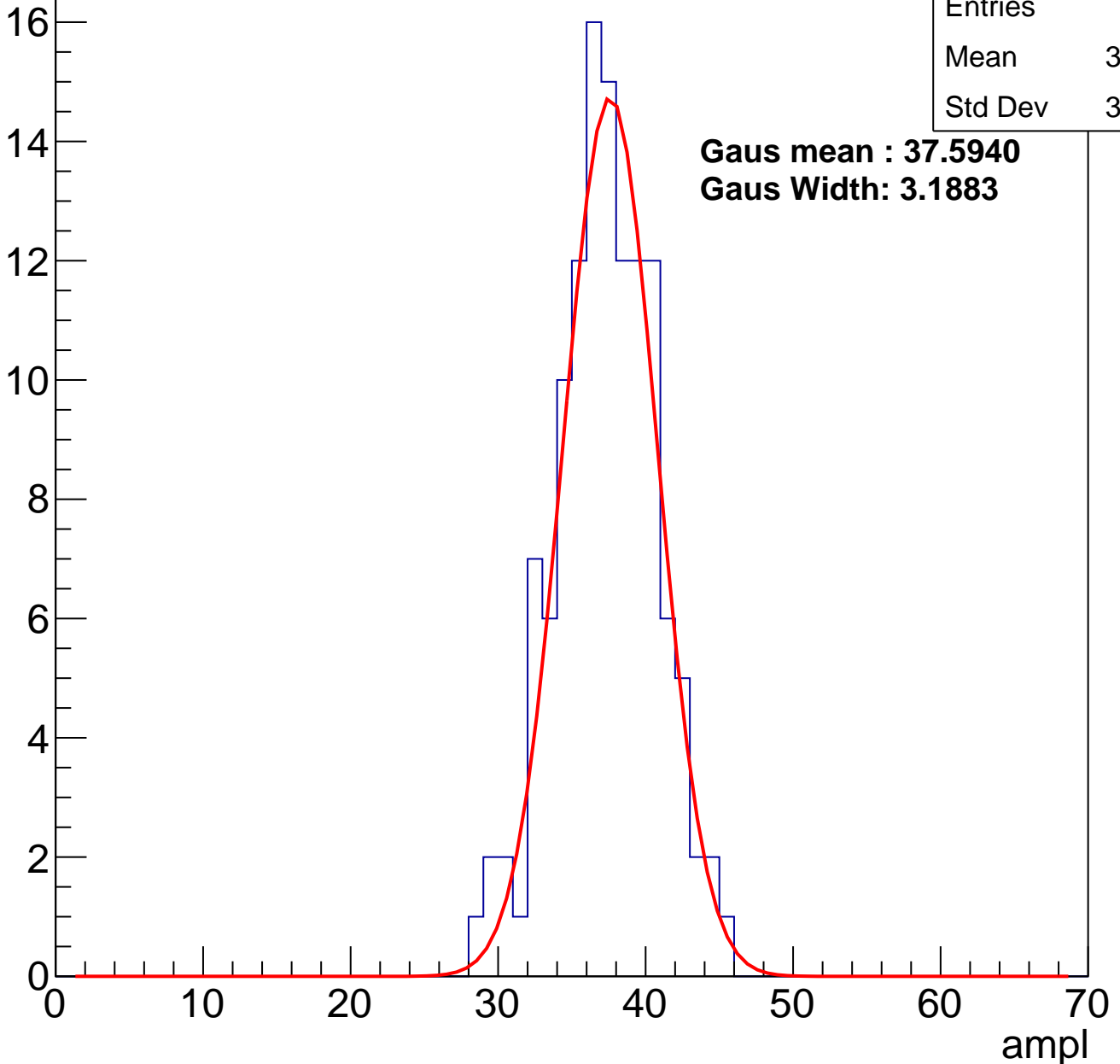
Entry



B1L001S, U26-ch80, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

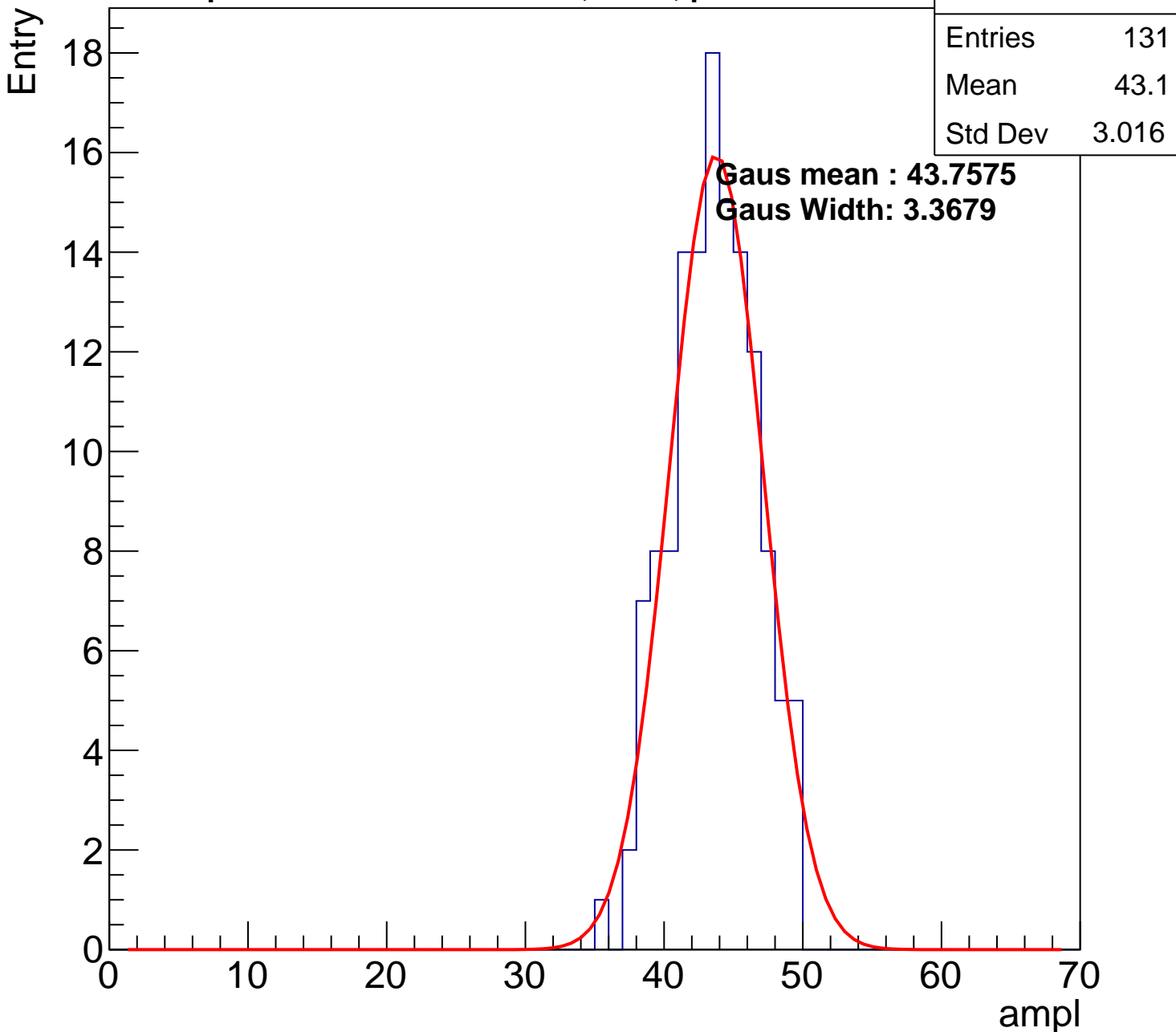
Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 131 |
| Mean | 43.1 |
| Std Dev | 3.016 |

Gaus mean : 43.7575
Gaus Width: 3.3679

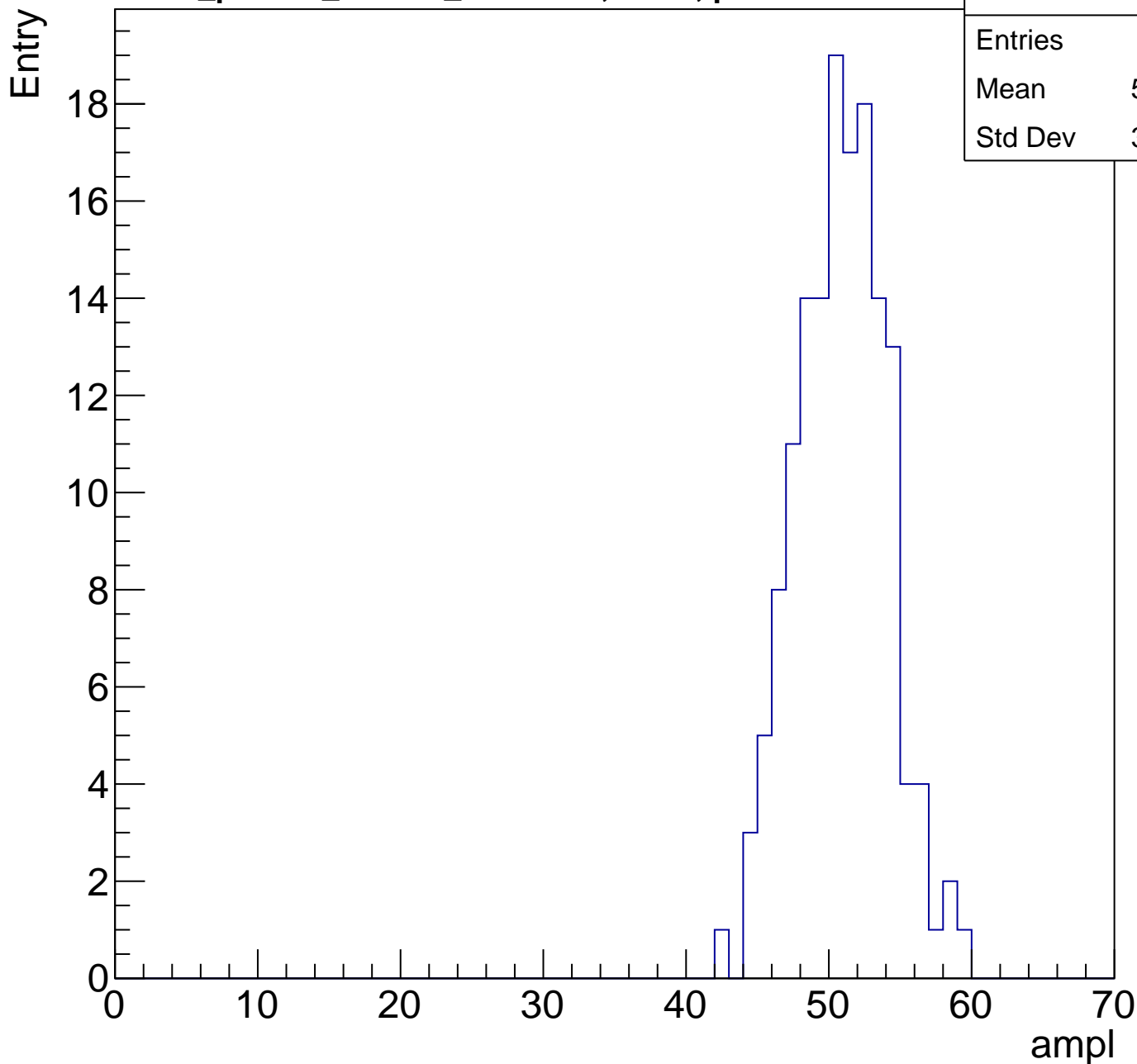
0 10 20 30 40 50 60 70
ampl



B1L001S, U26-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

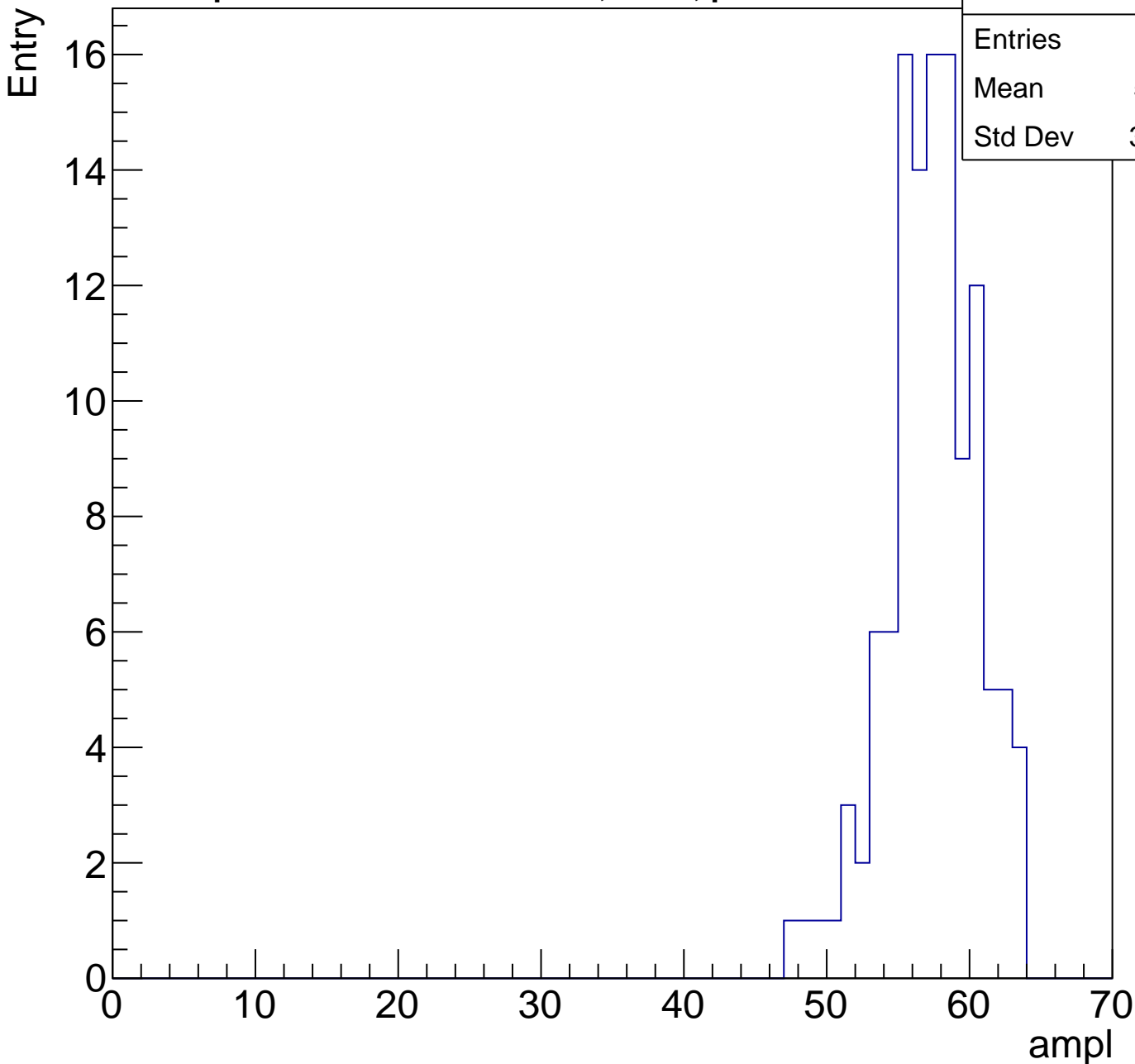
| | |
|---------|-------|
| Entries | 149 |
| Mean | 50.44 |
| Std Dev | 3.163 |



B1L001S, U26-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 56.91 |
| Std Dev | 3.173 |



B1L001S, U26-ch80, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

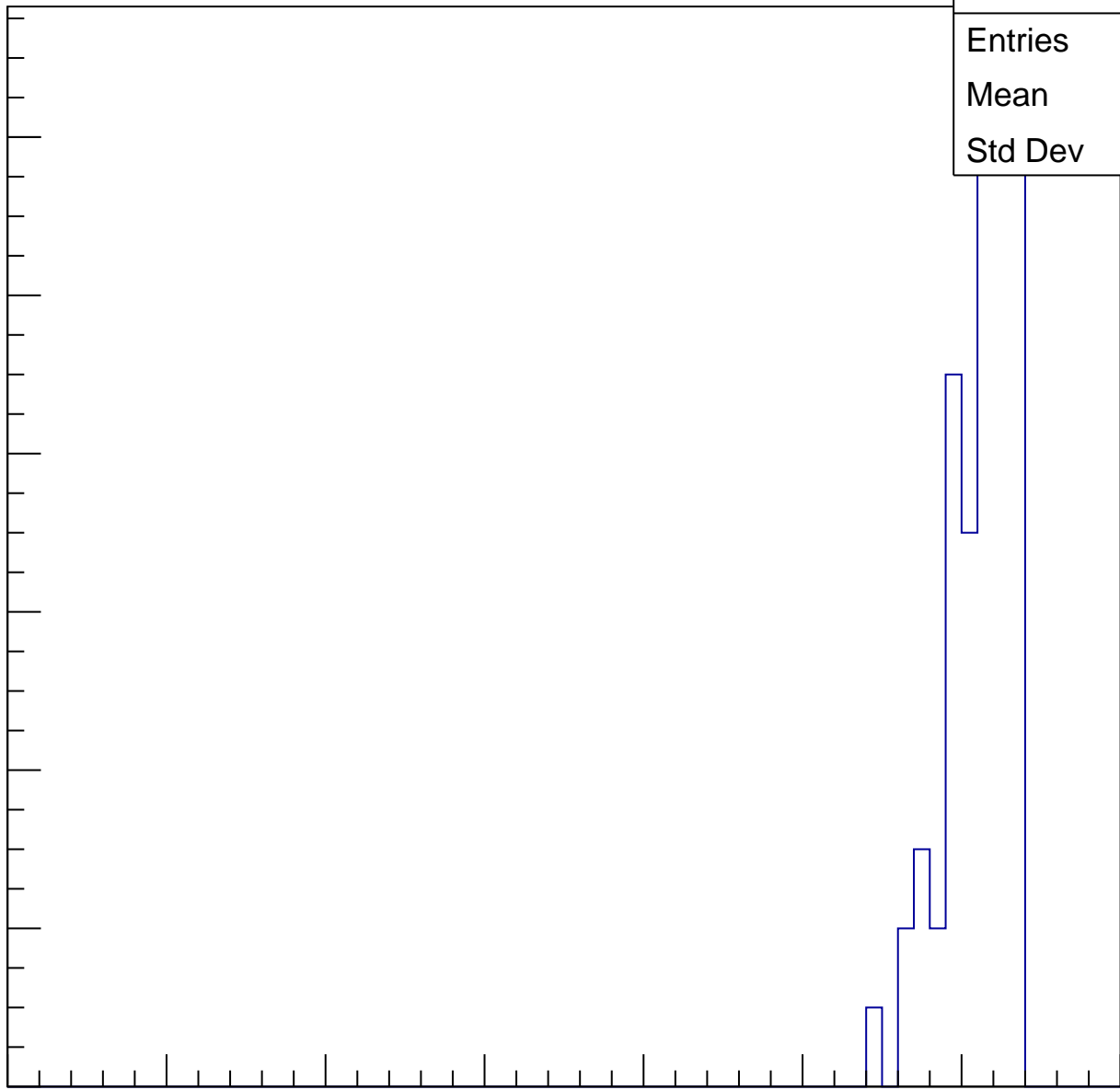
2

0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 60.63 |
| Std Dev | 2.05 |

ampl

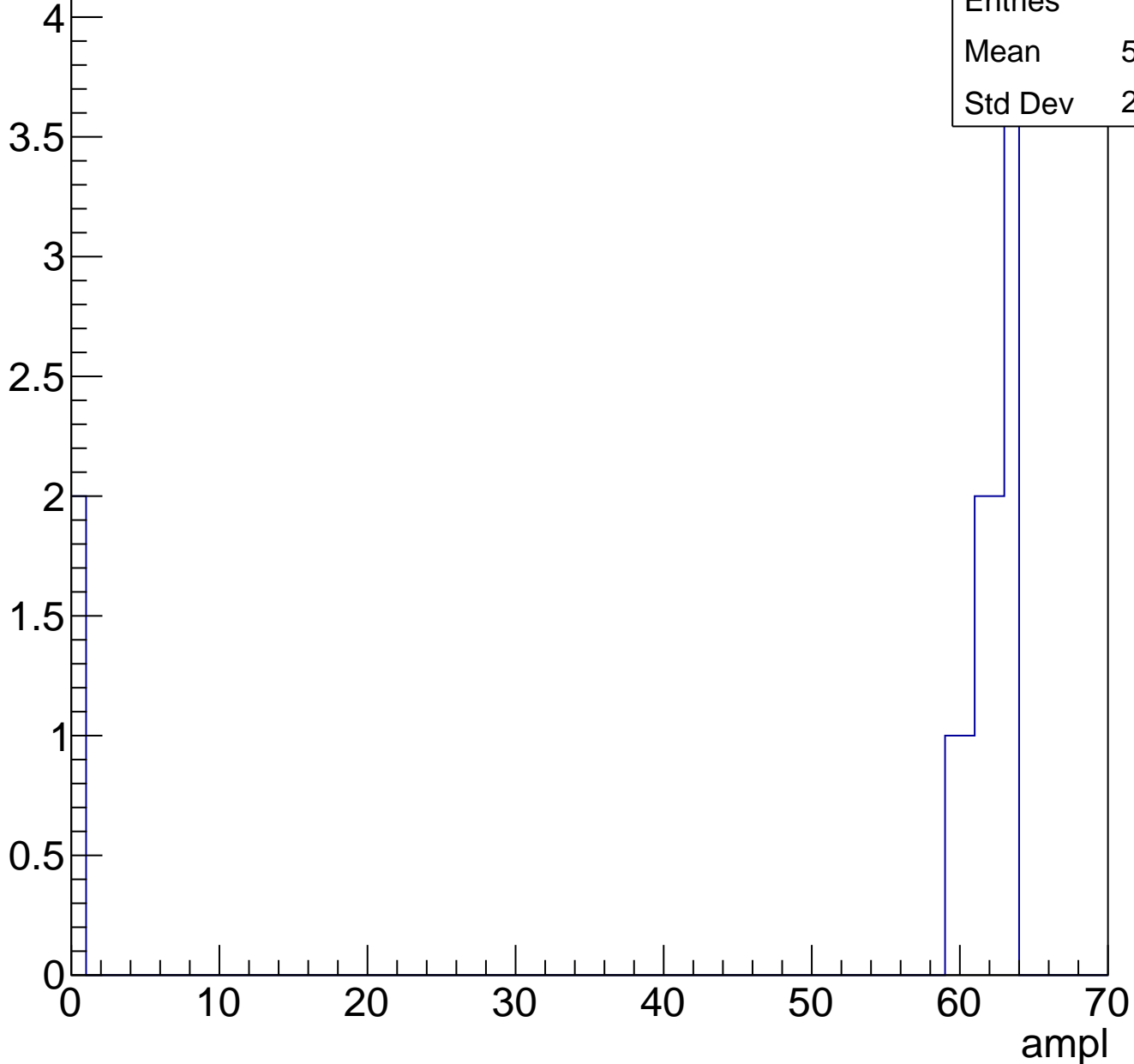
0 10 20 30 40 50 60 70



B1L001S, U26-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

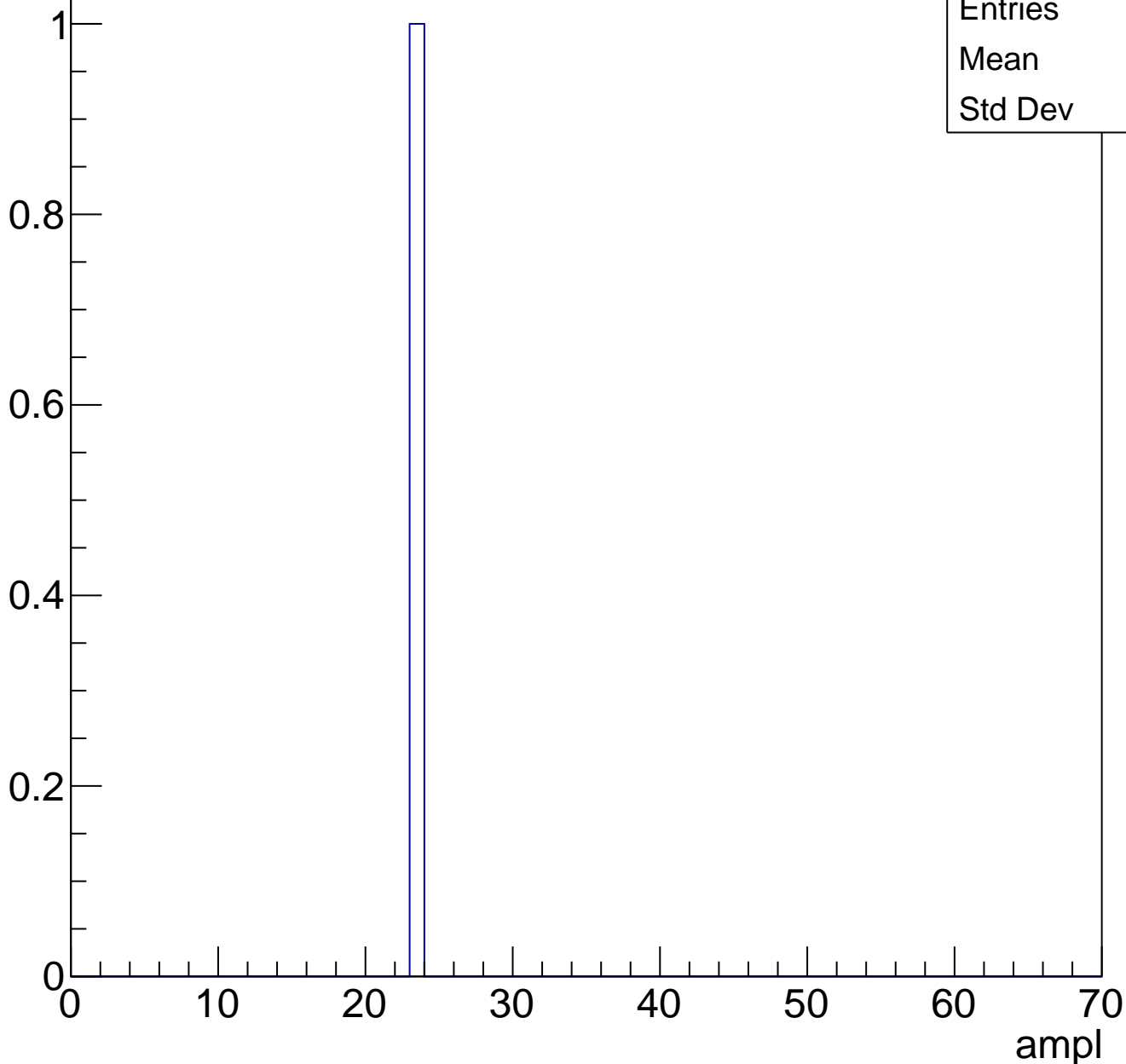
Entry



B1L001S, U26-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch81, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 29.62 |
| Std Dev | 3.358 |

Gaus mean : 30.0260

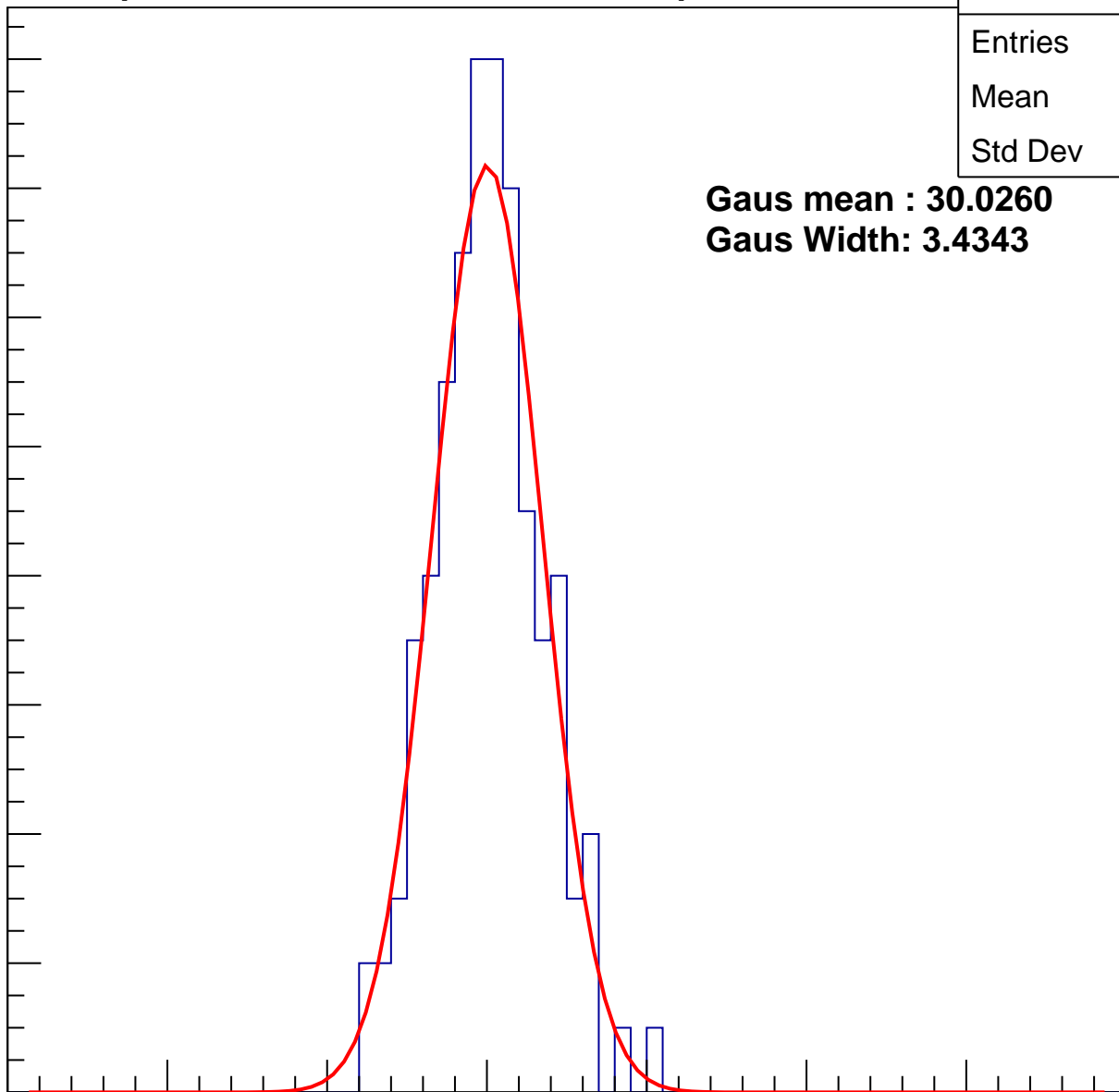
Gaus Width: 3.4343

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch81, adc1

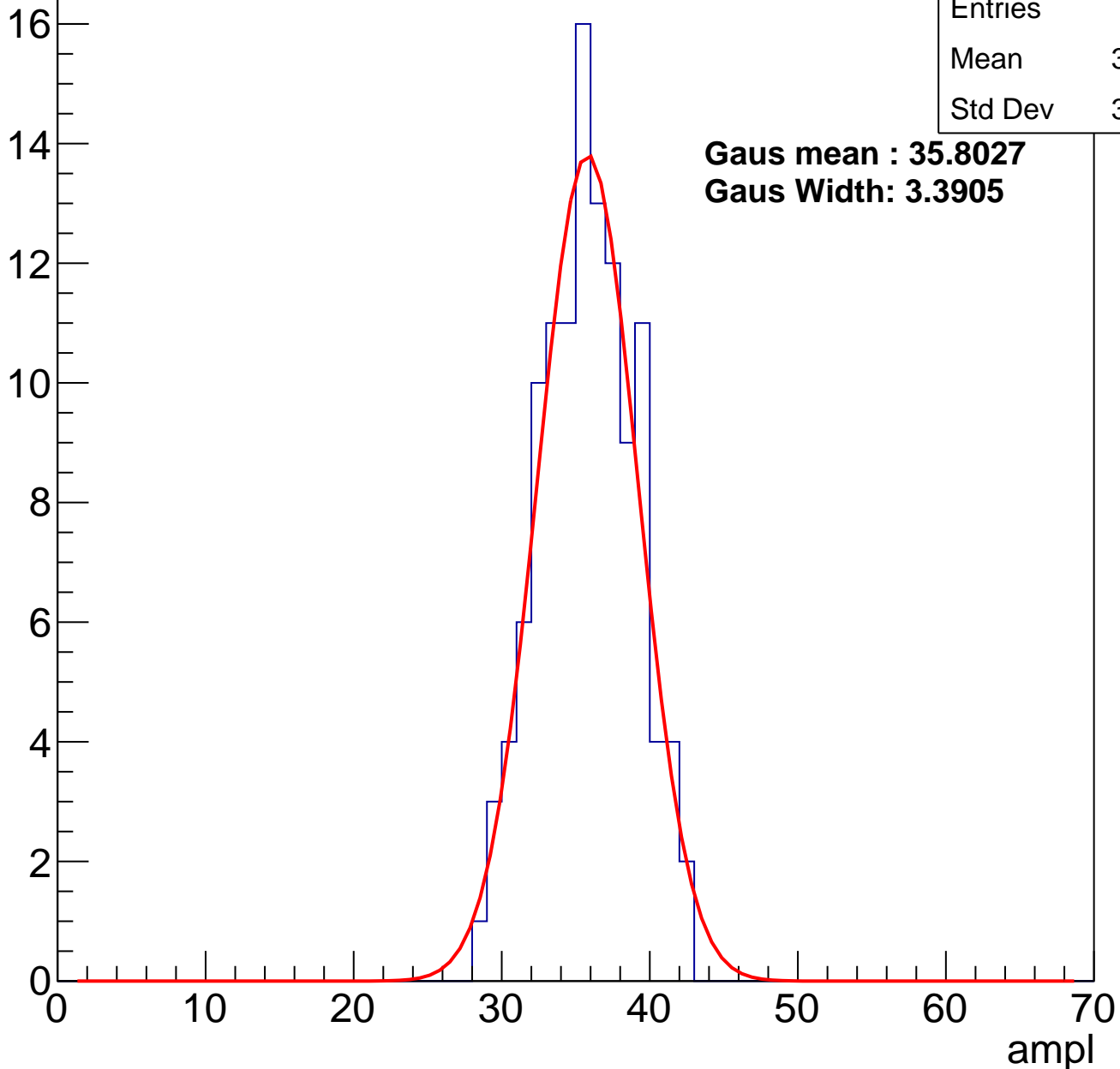
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 35.29 |
| Std Dev | 3.133 |

Gaus mean : 35.8027

Gaus Width: 3.3905

Entry



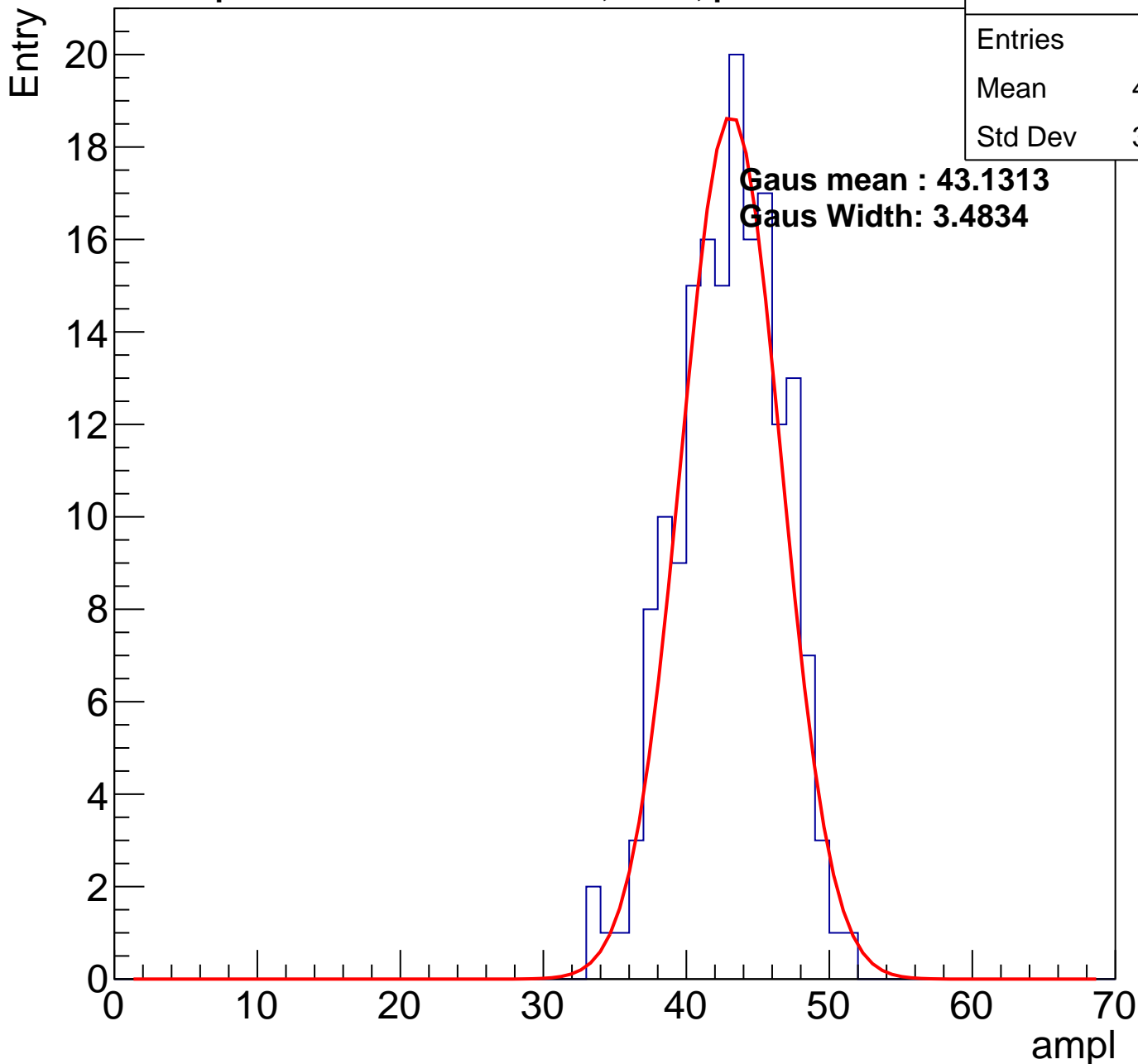
B1L001S, U26-ch81, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 170 |
| Mean | 42.54 |
| Std Dev | 3.555 |

Gaus mean : 43.1313

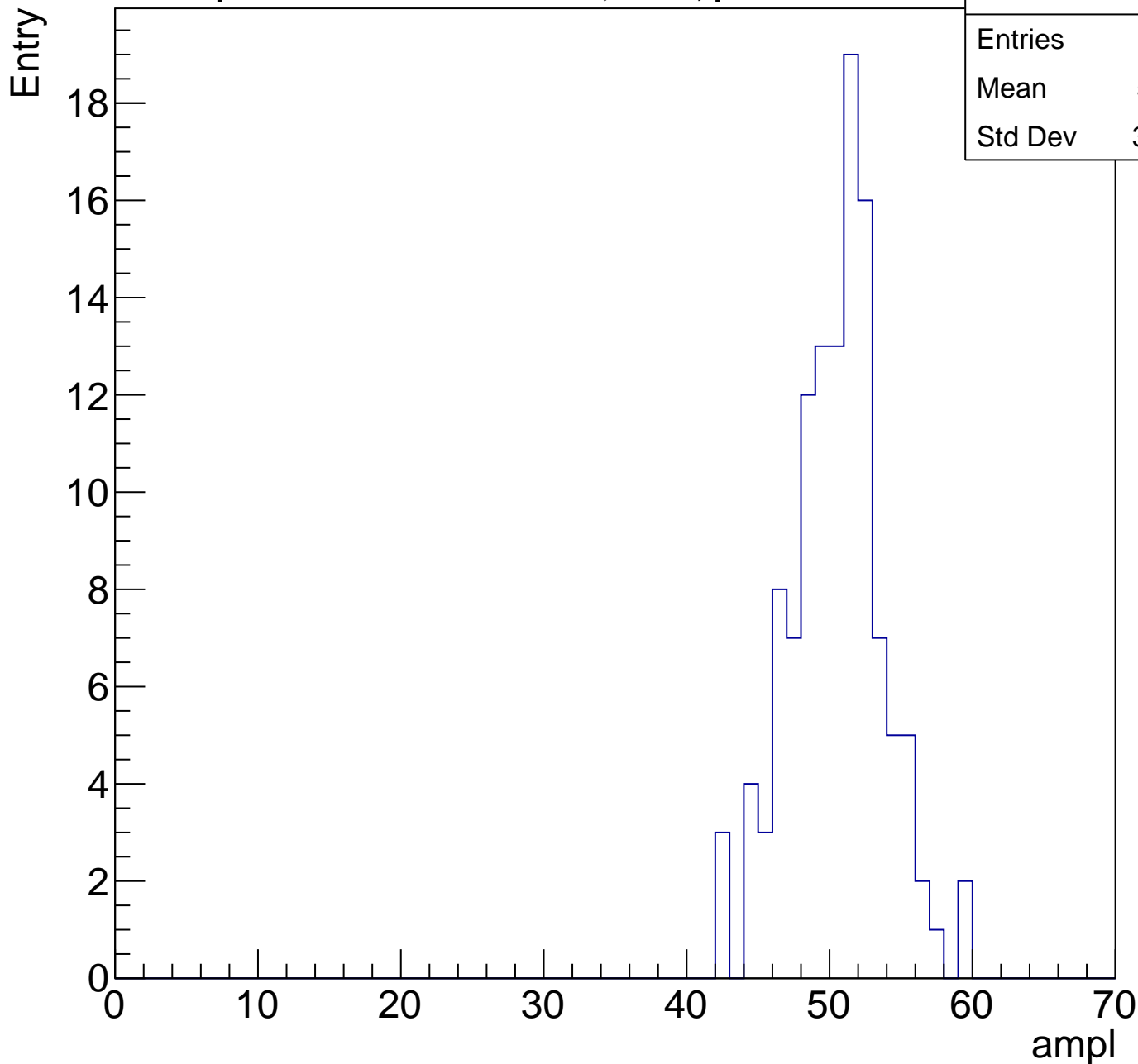
Gaus Width: 3.4834



B1L001S, U26-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 50.01 |
| Std Dev | 3.288 |



B1L001S, U26-ch81, adc4

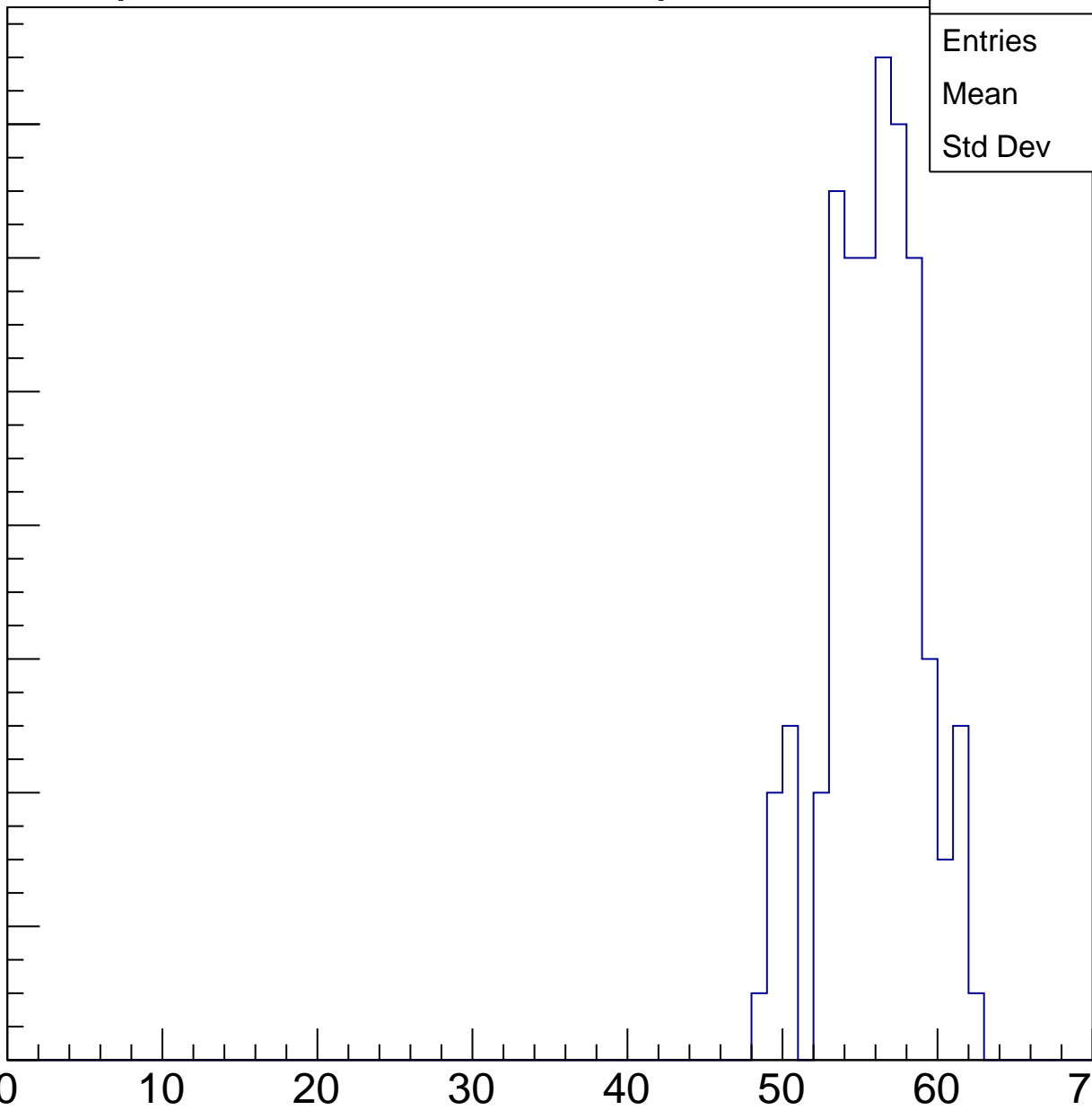
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 107 |
| Mean | 55.46 |
| Std Dev | 3.018 |

ampl



B1L001S, U26-ch81, adc5

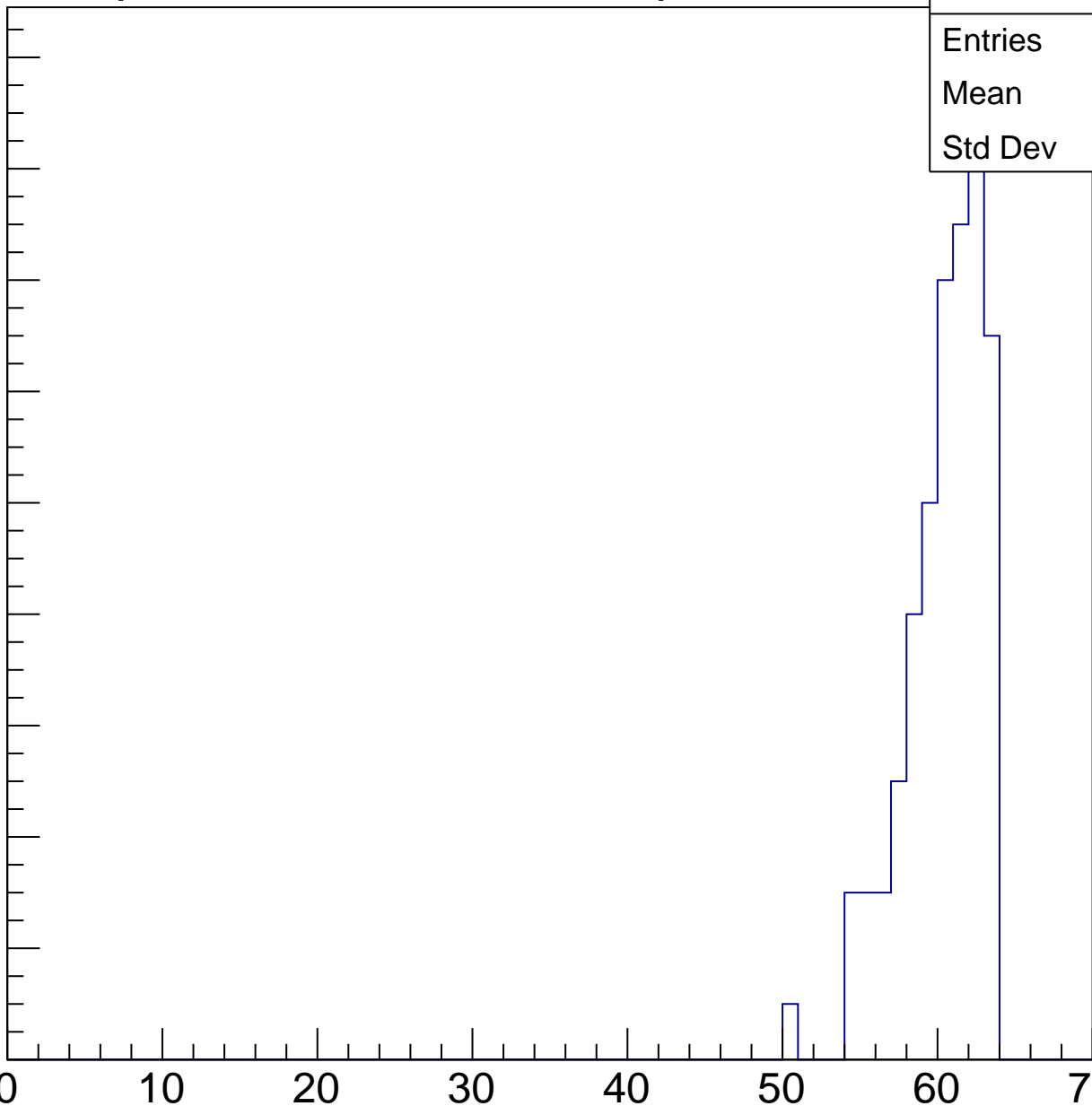
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 93 |
| Mean | 59.94 |
| Std Dev | 2.585 |

ampl

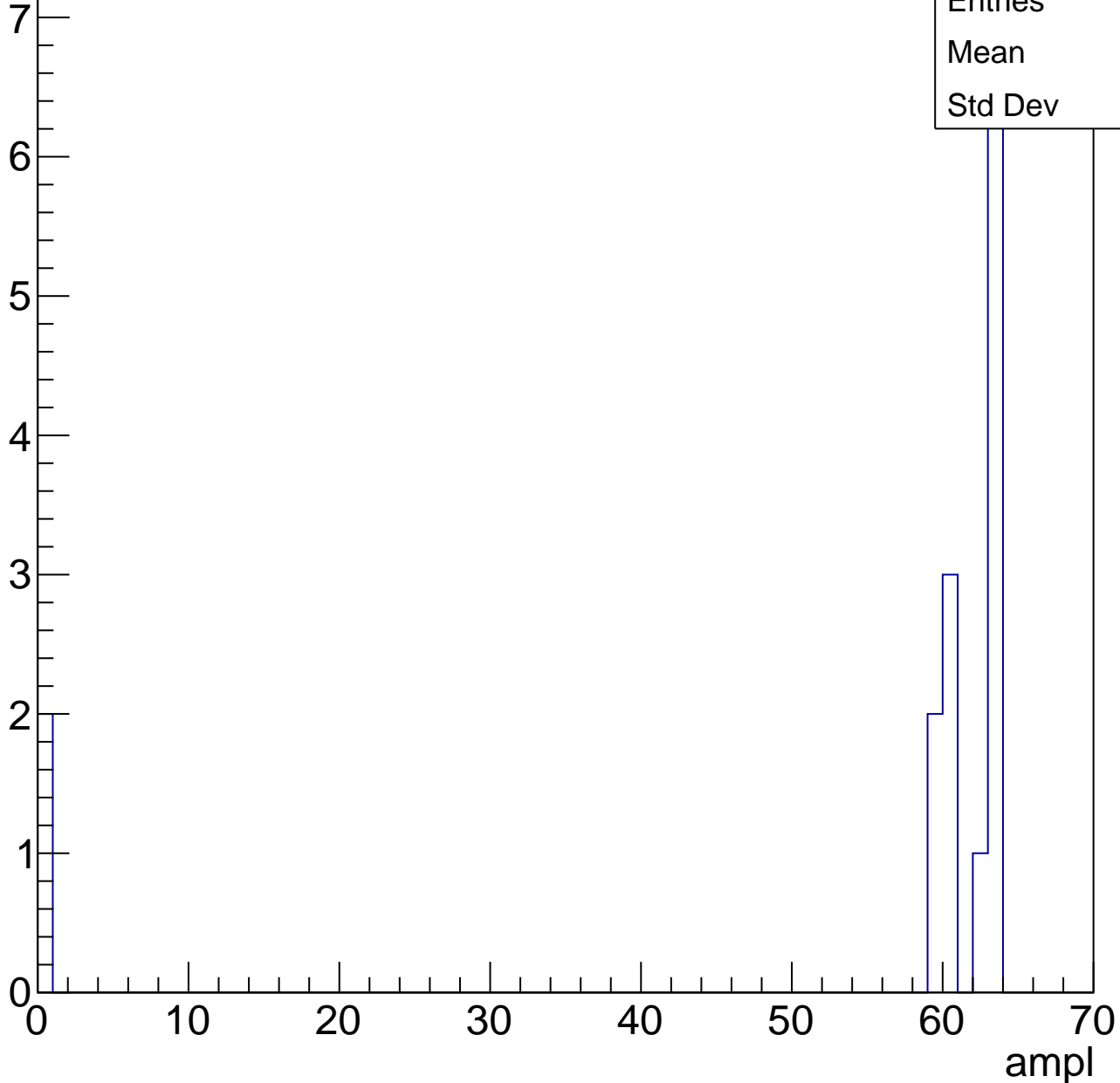


B1L001S, U26-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

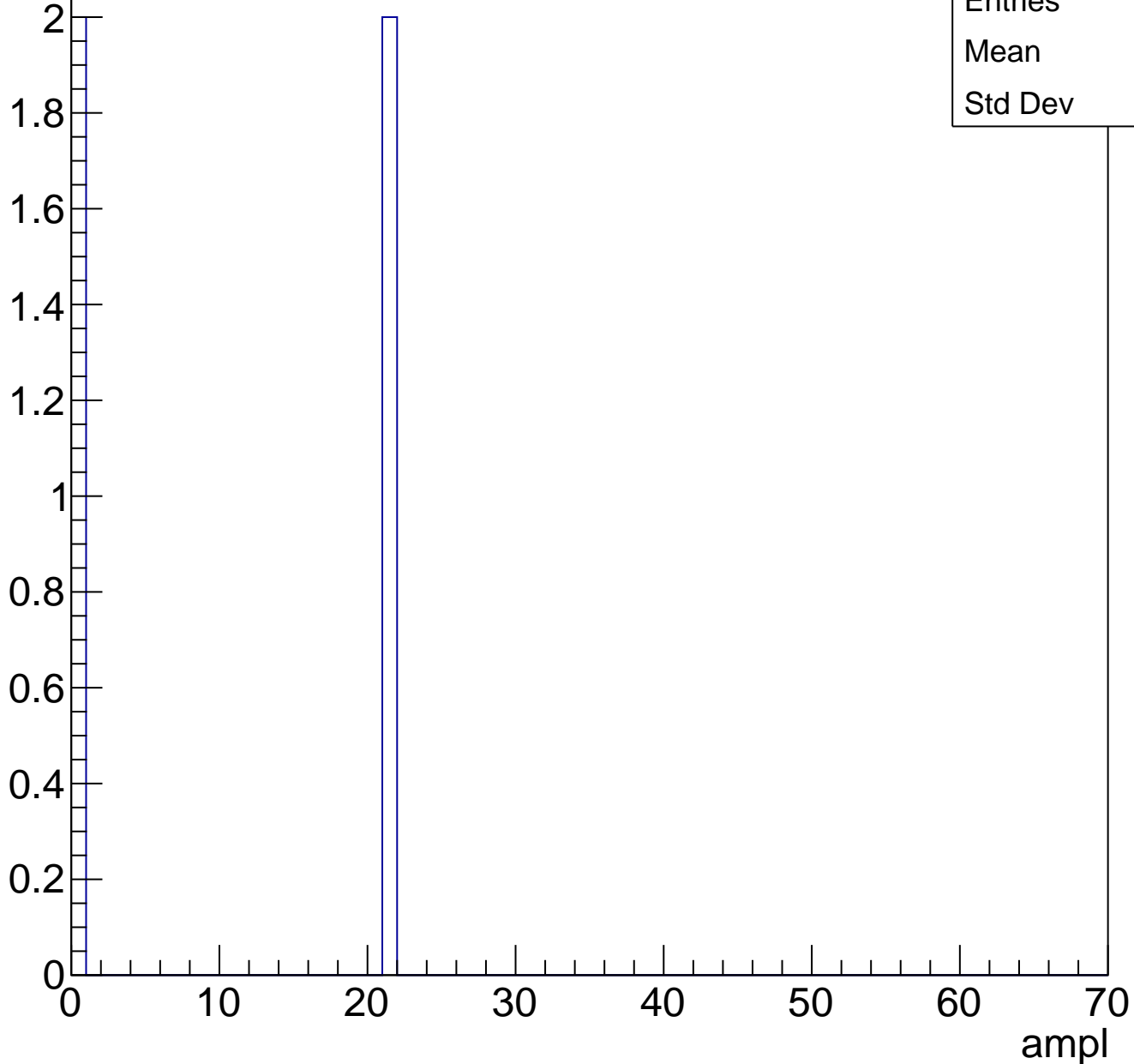
| | |
|---------|------|
| Entries | 15 |
| Mean | 53.4 |
| Std Dev | 21 |



B1L001S, U26-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch82, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 29.07 |
| Std Dev | 4.174 |

Gaus mean : 29.6837

Gaus Width: 3.0687

Entry

14

12

10

8

6

4

2

0

0

10

20

30

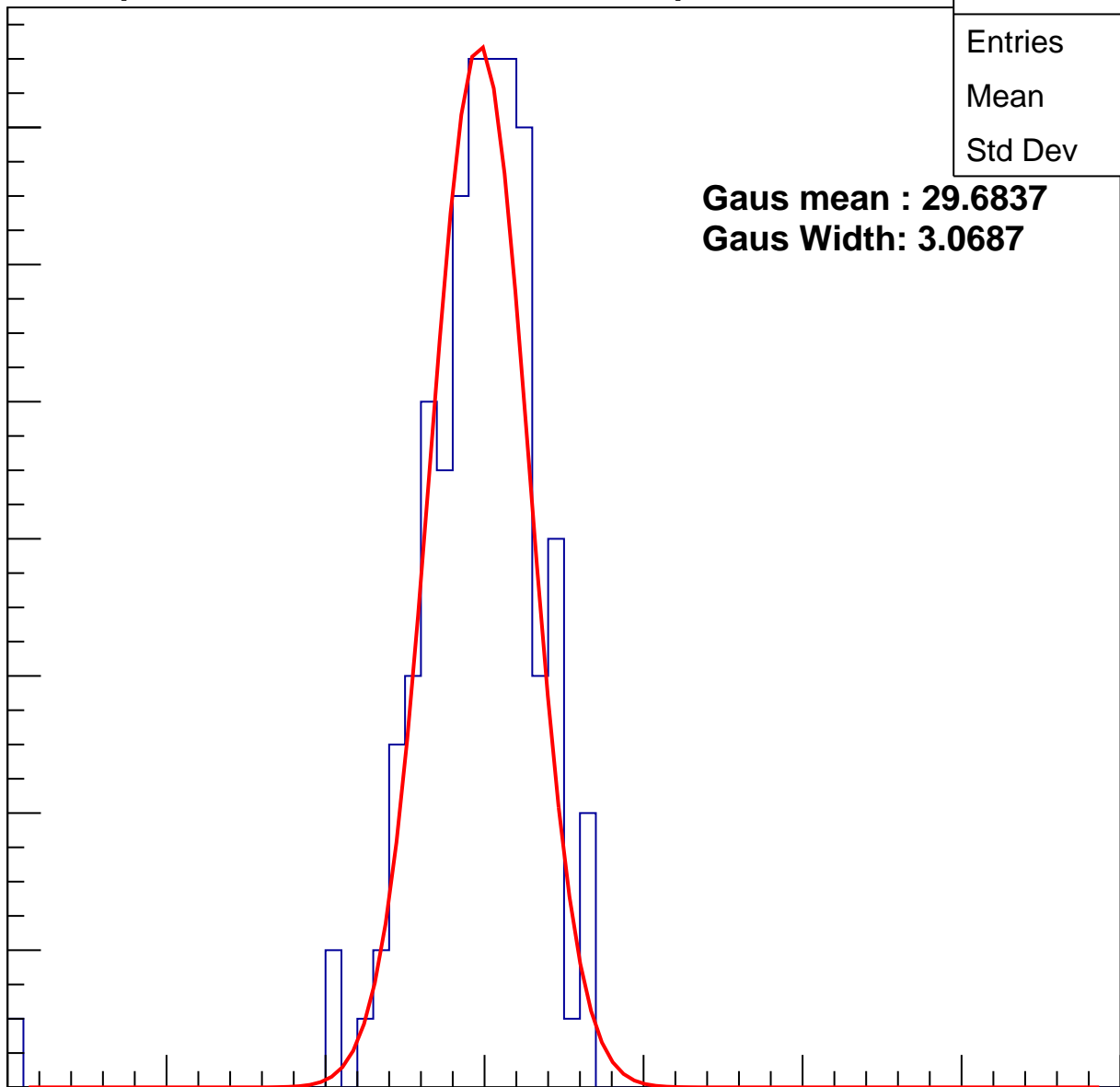
40

50

60

70

ampl



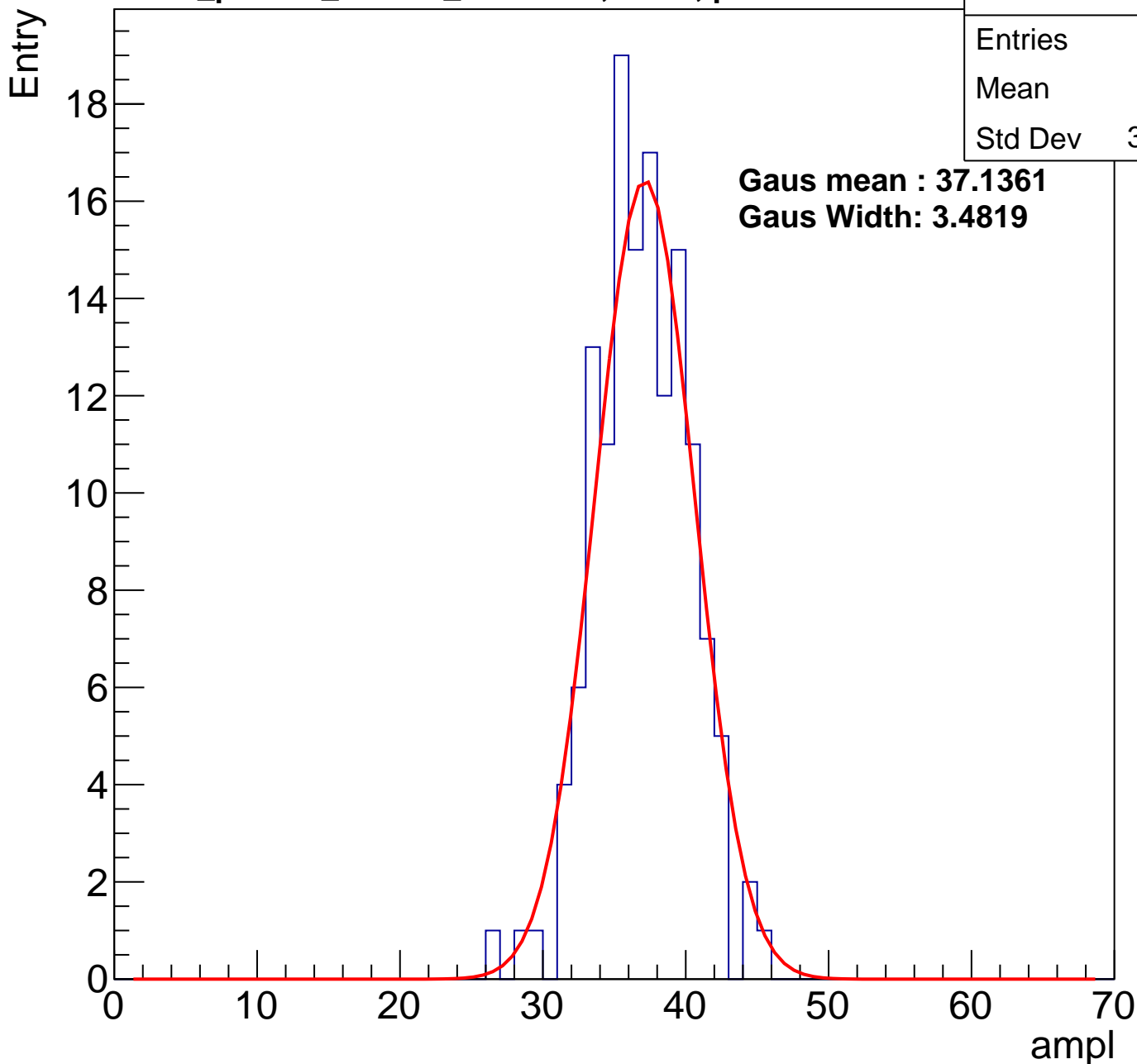
B1L001S, U26-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 36.5 |
| Std Dev | 3.254 |

Gaus mean : 37.1361

Gaus Width: 3.4819



B1L001S, U26-ch82, adc2

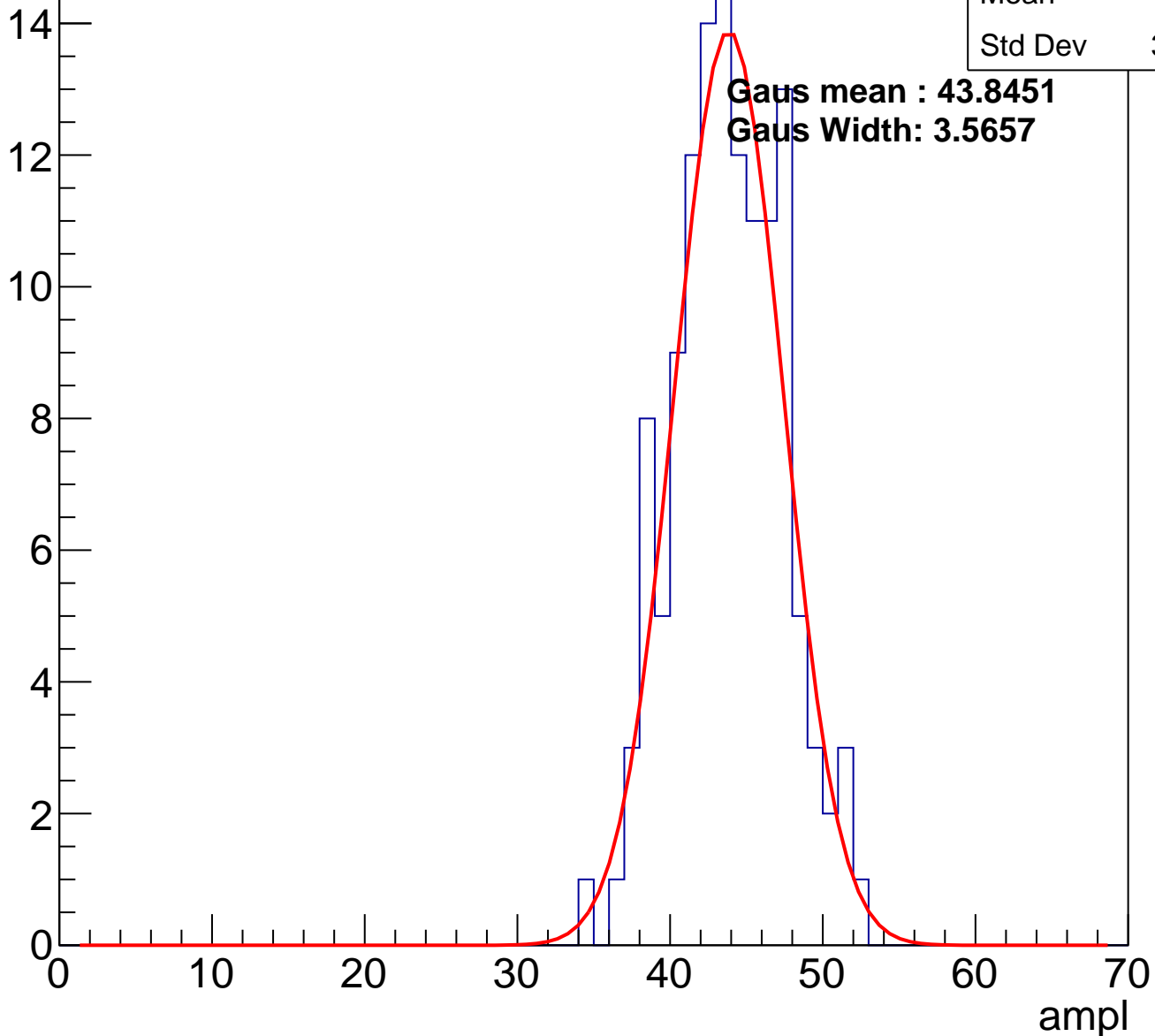
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 43.39 |
| Std Dev | 3.531 |

Gaus mean : 43.8451

Gaus Width: 3.5657

Entry



B1L001S, U26-ch82, adc3

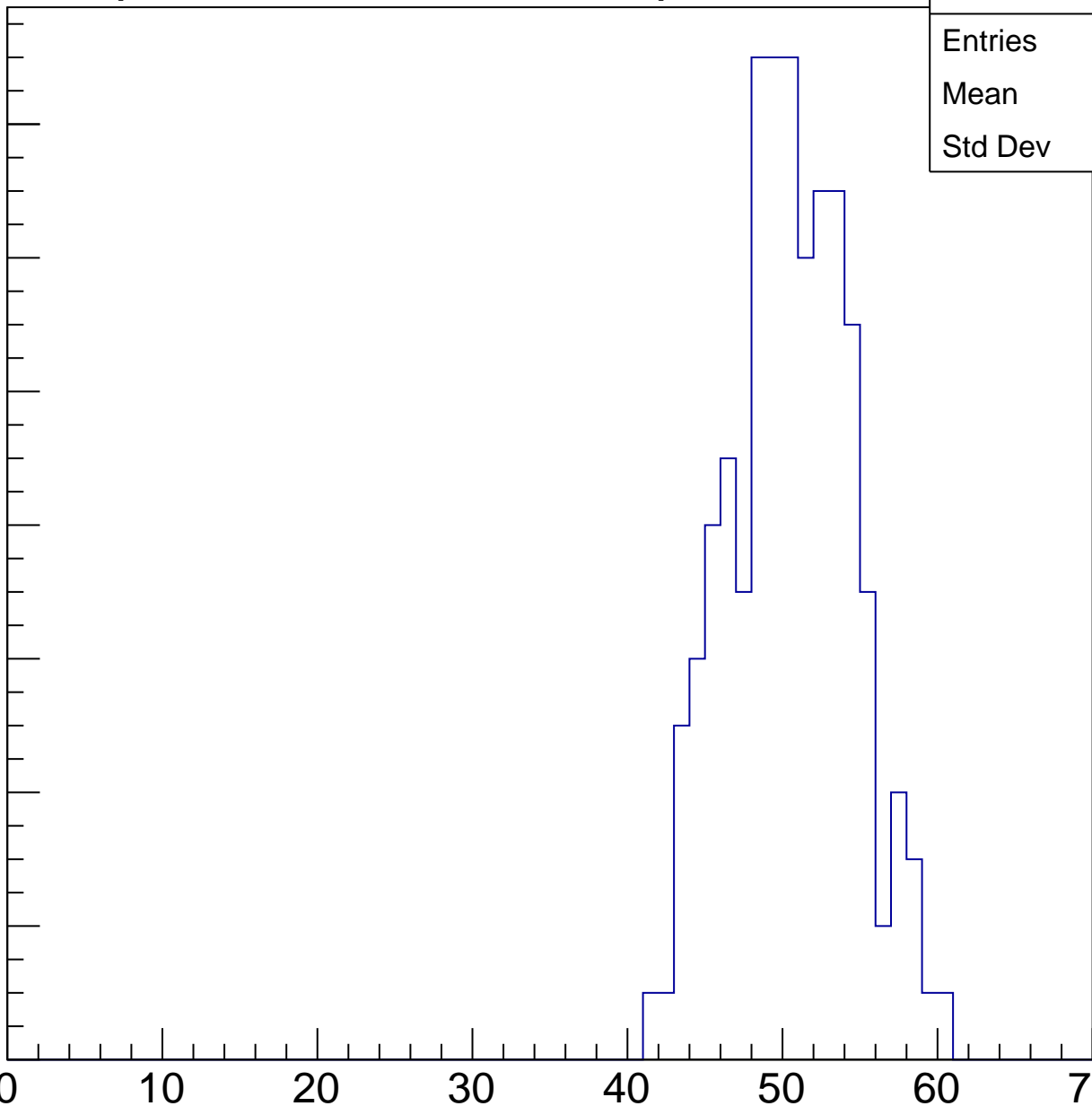
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 149 |
| Mean | 50.06 |
| Std Dev | 3.895 |

ampl



B1L001S, U26-ch82, adc4

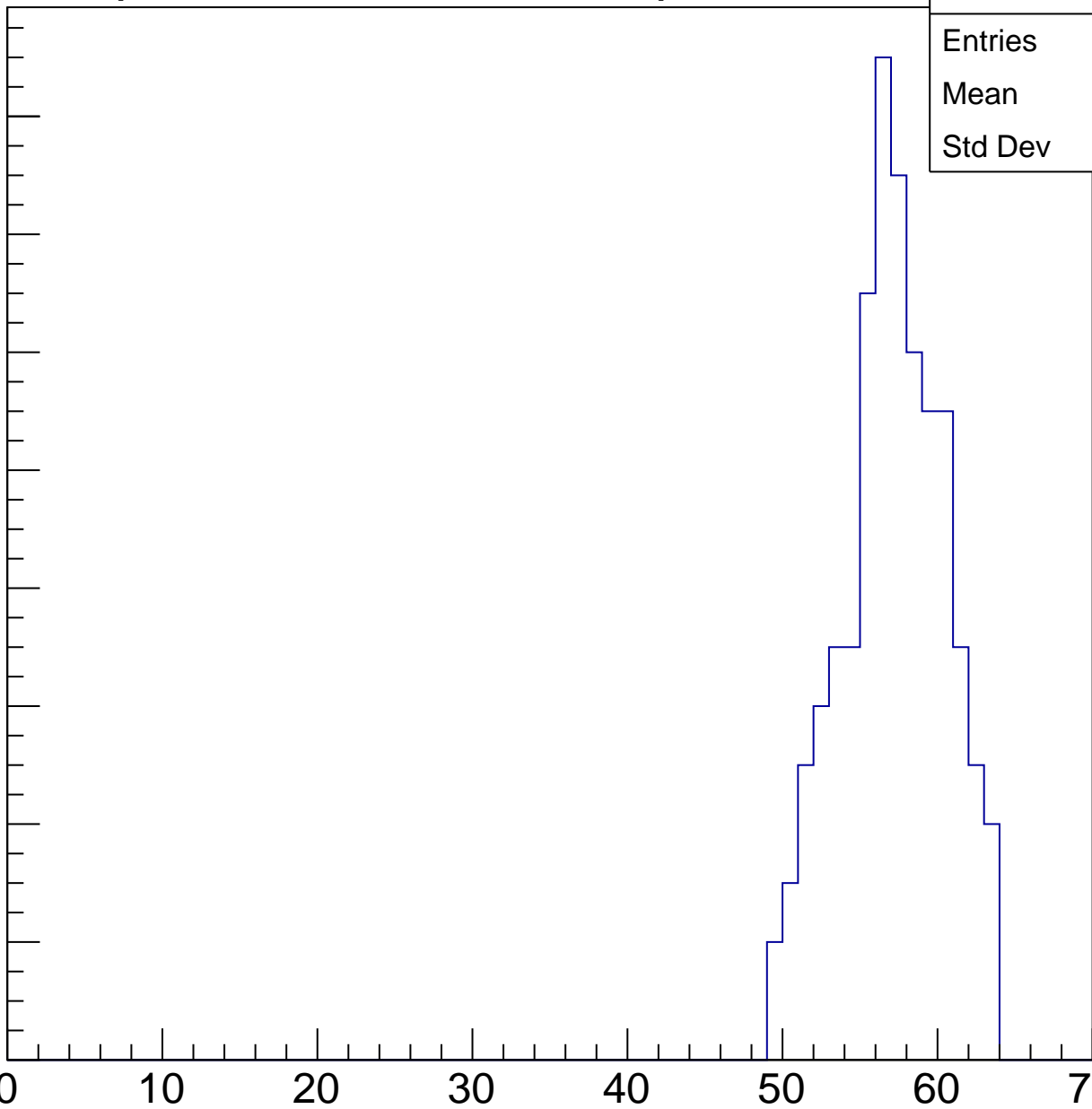
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 125 |
| Mean | 56.64 |
| Std Dev | 3.309 |

ampl



B1L001S, U26-ch82, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

| | |
|---------|-------|
| Entries | 63 |
| Mean | 60.6 |
| Std Dev | 2.082 |

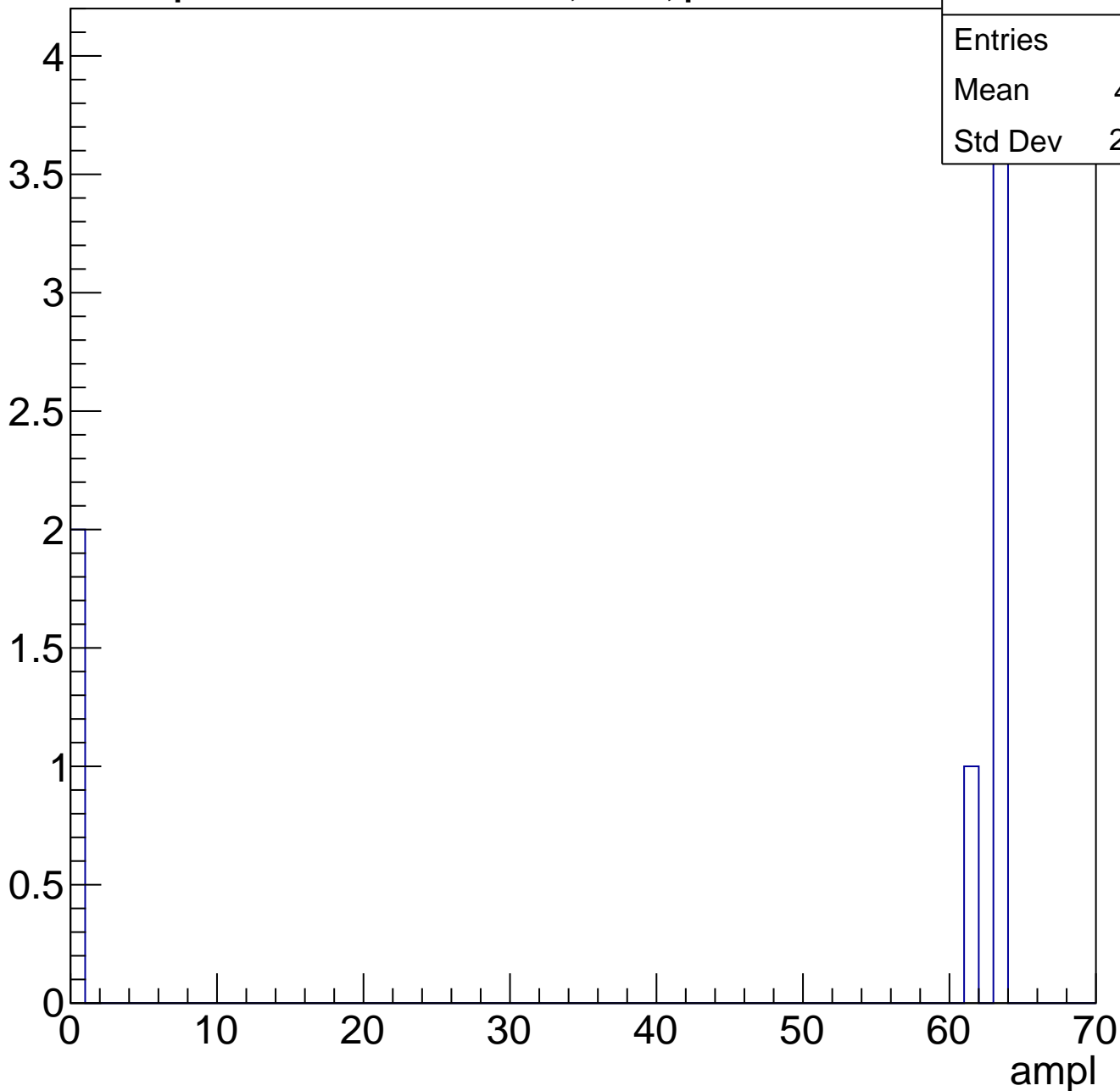
ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B1L001S, U26-ch83, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 30.99 |
| Std Dev | 3.391 |

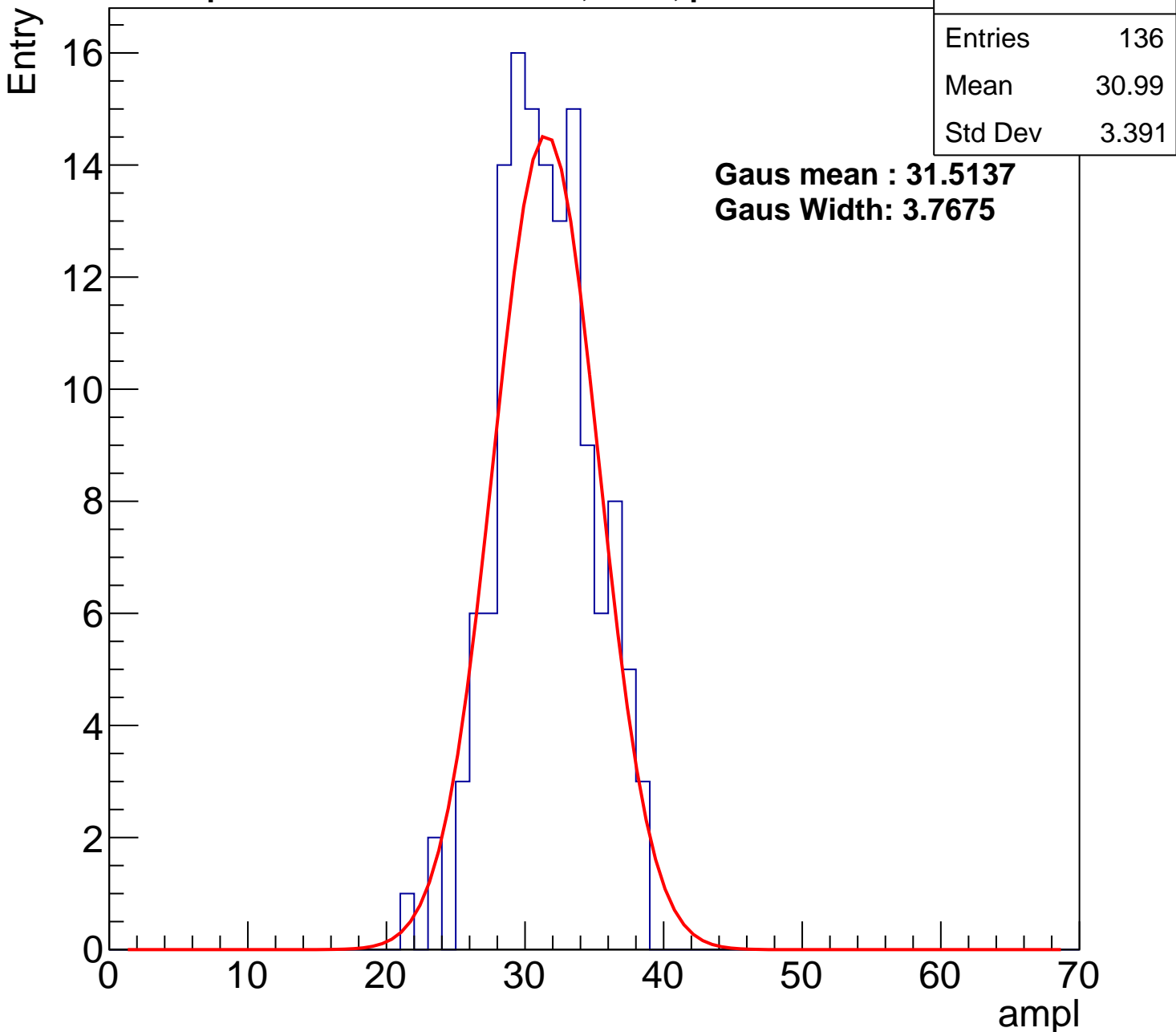
Gaus mean : 31.5137
Gaus Width: 3.7675

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



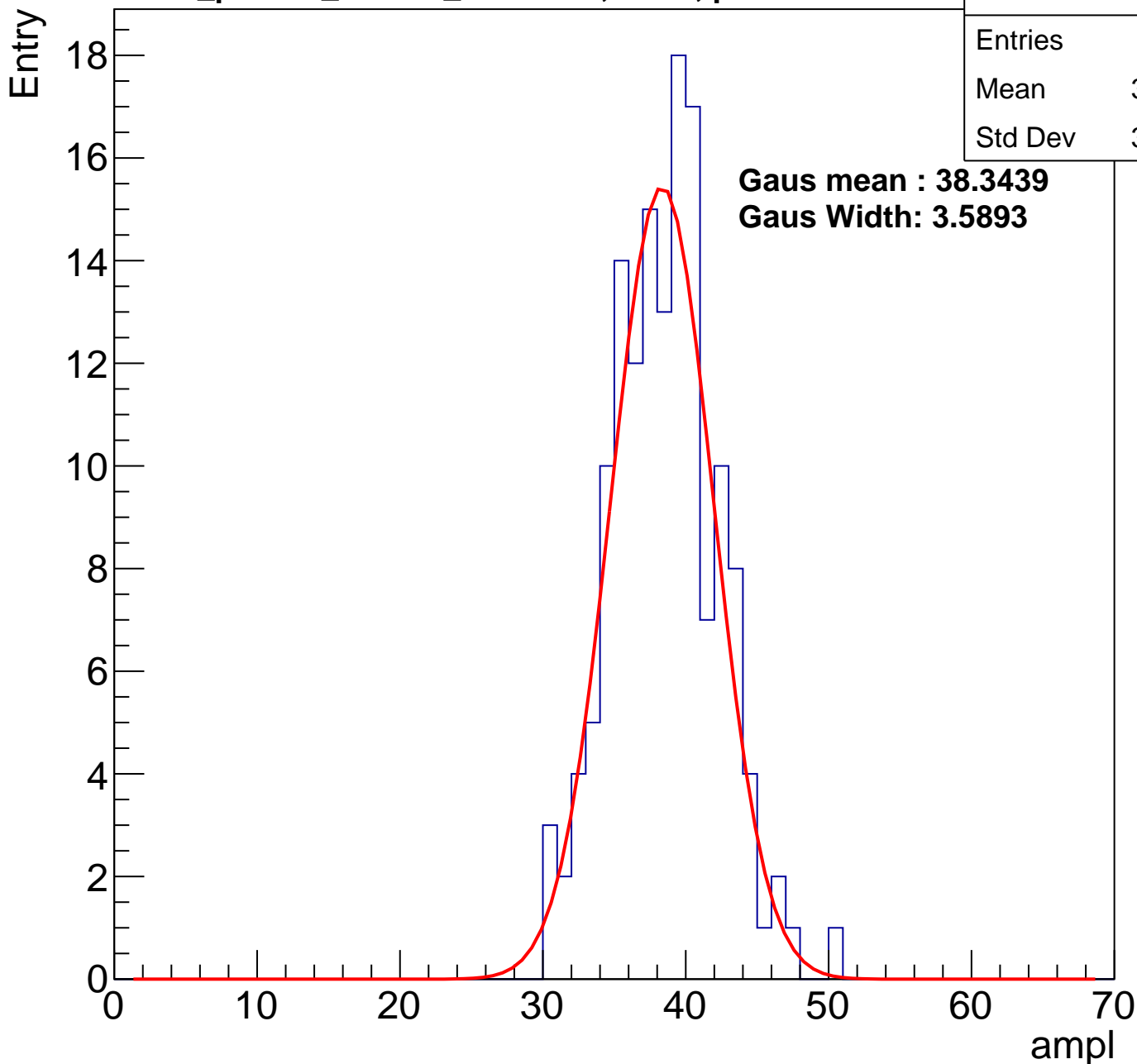
B1L001S, U26-ch83, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 147 |
| Mean | 38.09 |
| Std Dev | 3.653 |

Gaus mean : 38.3439

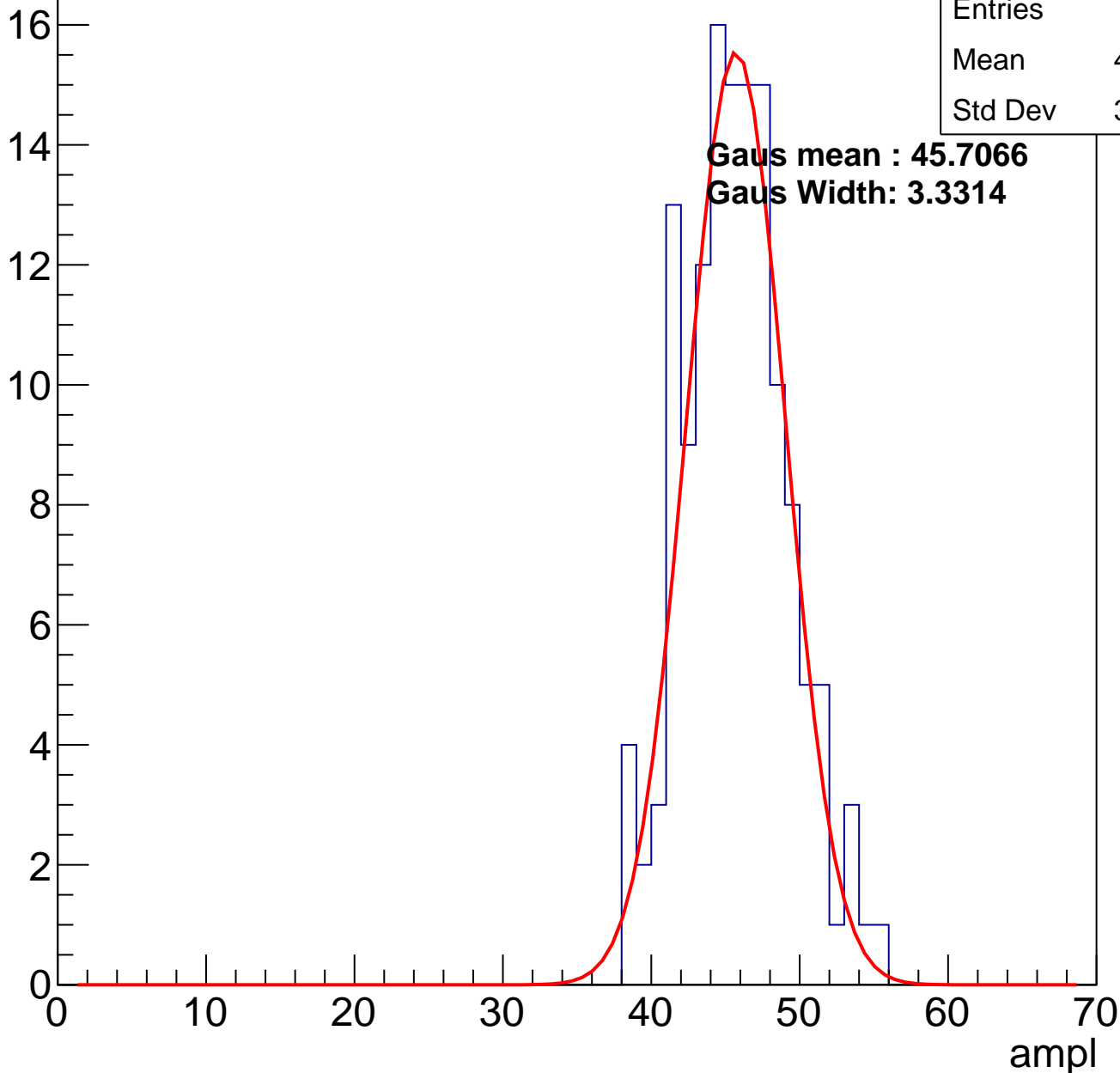
Gaus Width: 3.5893



B1L001S, U26-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

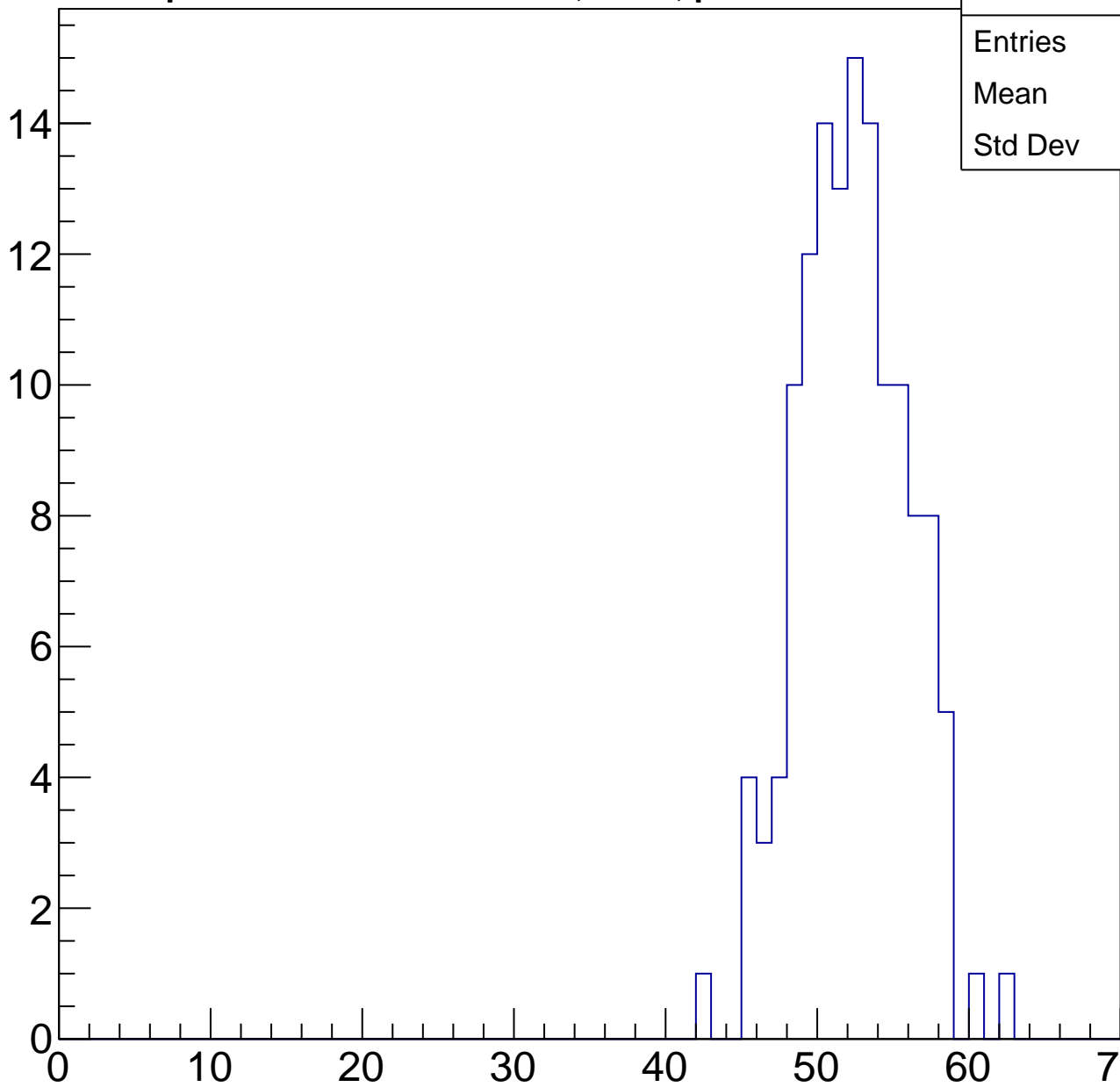
| | |
|---------|-------|
| Entries | 133 |
| Mean | 51.93 |
| Std Dev | 3.529 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch83, adc4

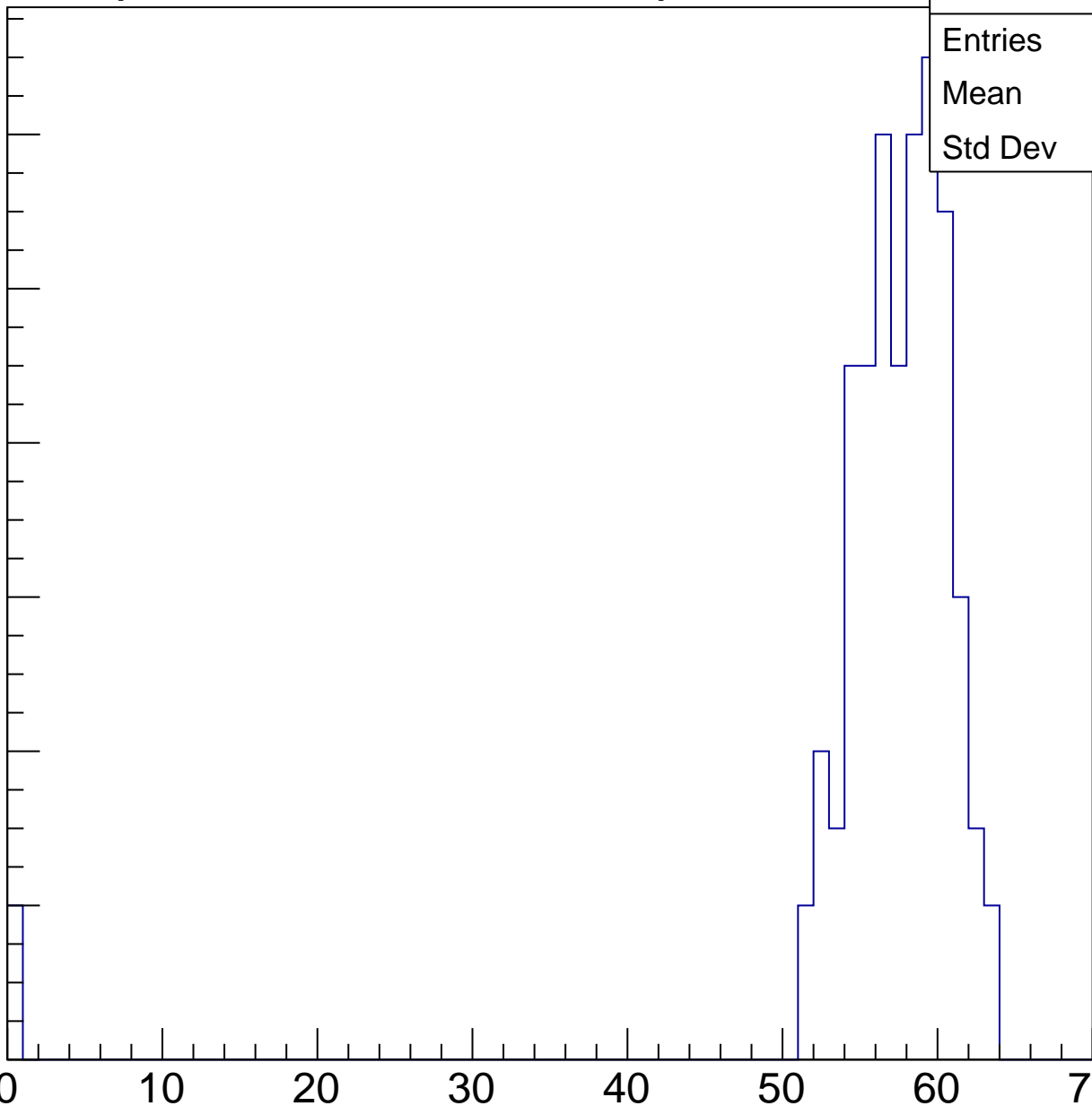
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 97 |
| Mean | 56.04 |
| Std Dev | 8.594 |

ampl



B1L001S, U26-ch83, adc5

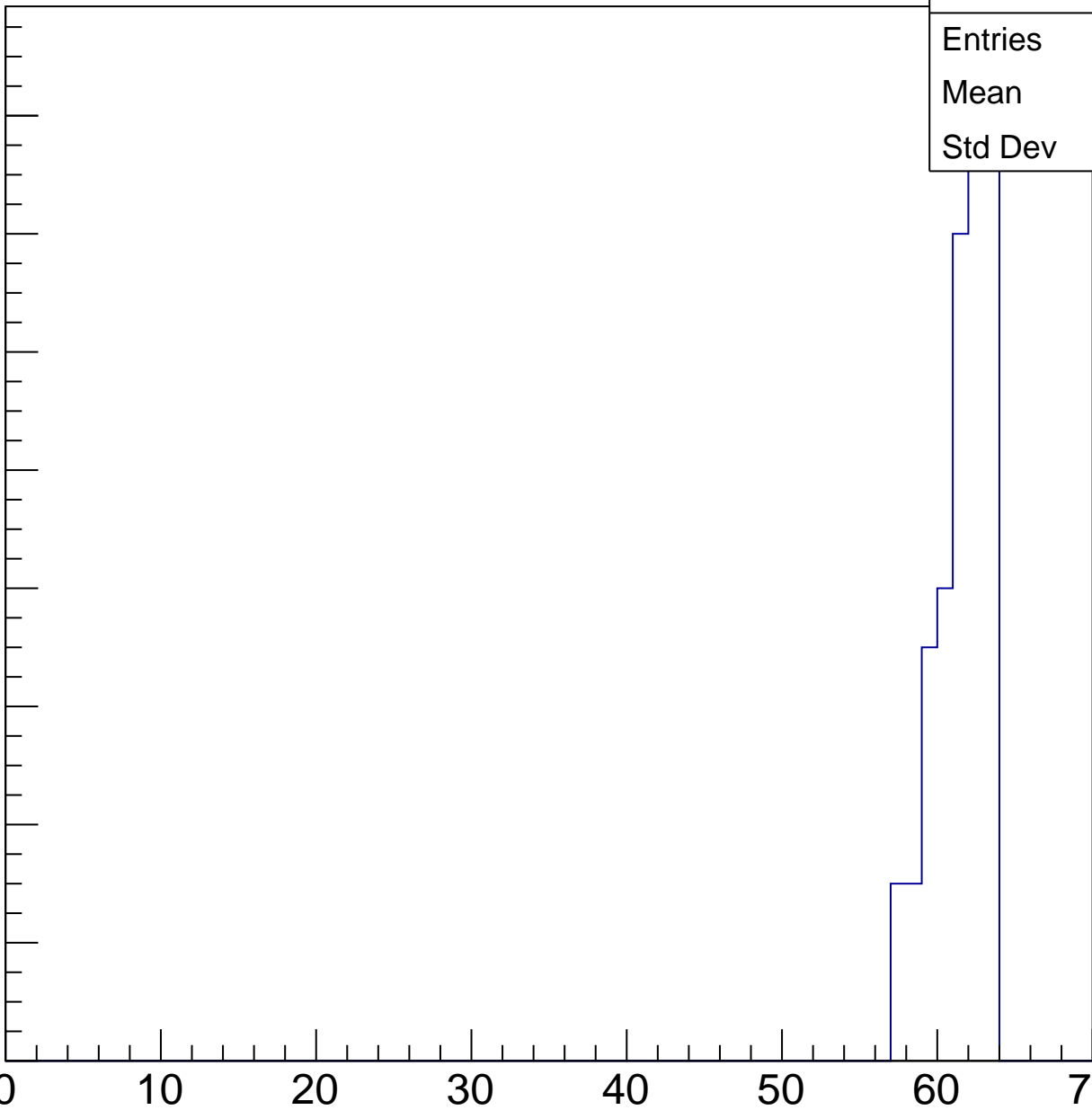
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 68 |
| Mean | 61.1 |
| Std Dev | 1.69 |

ampl



B1L001S, U26-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

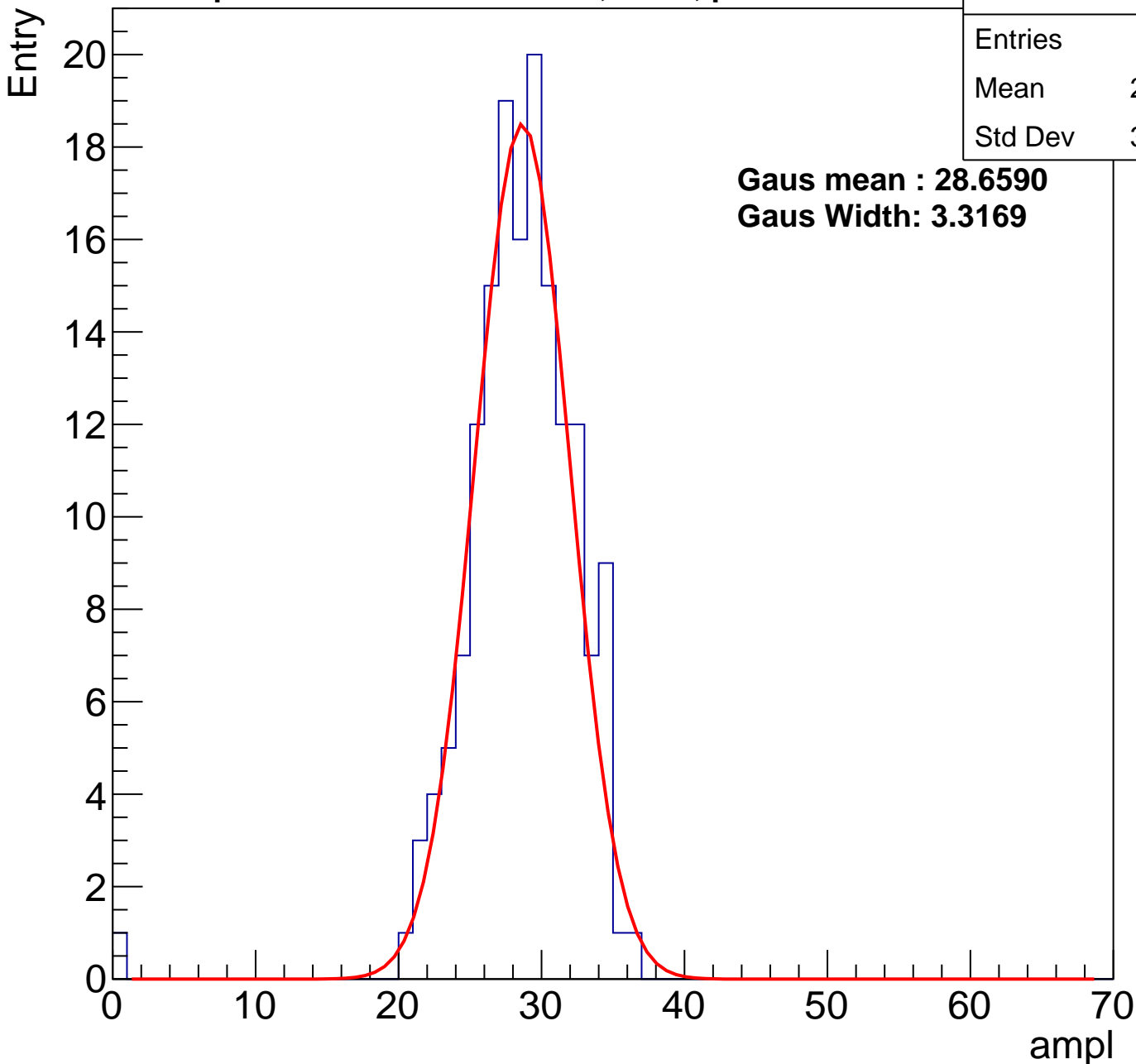
B1L001S, U26-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 160 |
| Mean | 28.12 |
| Std Dev | 3.994 |

Gaus mean : 28.6590

Gaus Width: 3.3169



B1L001S, U26-ch84, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 35.72 |
| Std Dev | 3.366 |

Gaus mean : 36.3158

Gaus Width: 3.6220

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch84, adc2

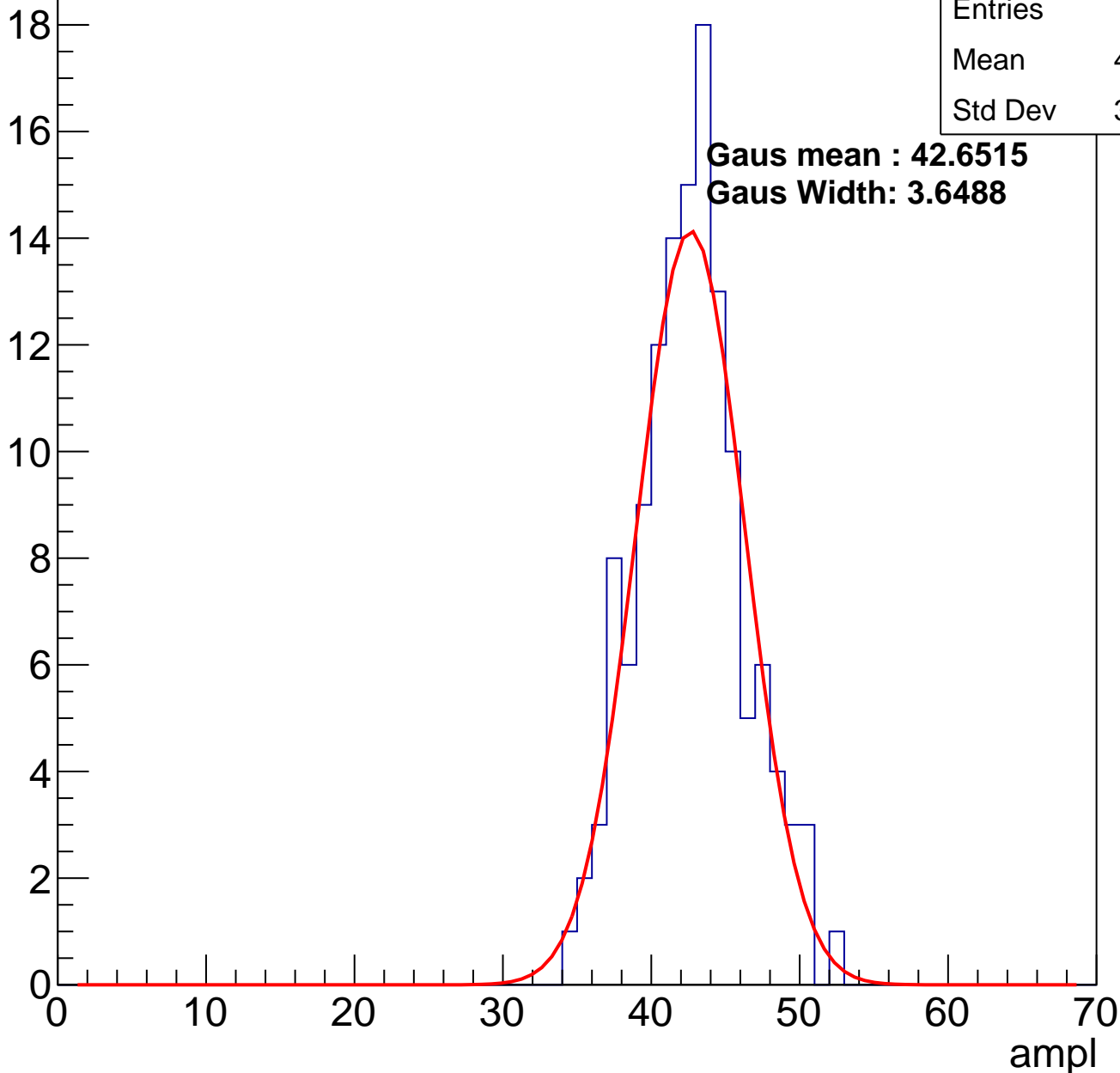
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 42.26 |
| Std Dev | 3.547 |

Gaus mean : 42.6515

Gaus Width: 3.6488

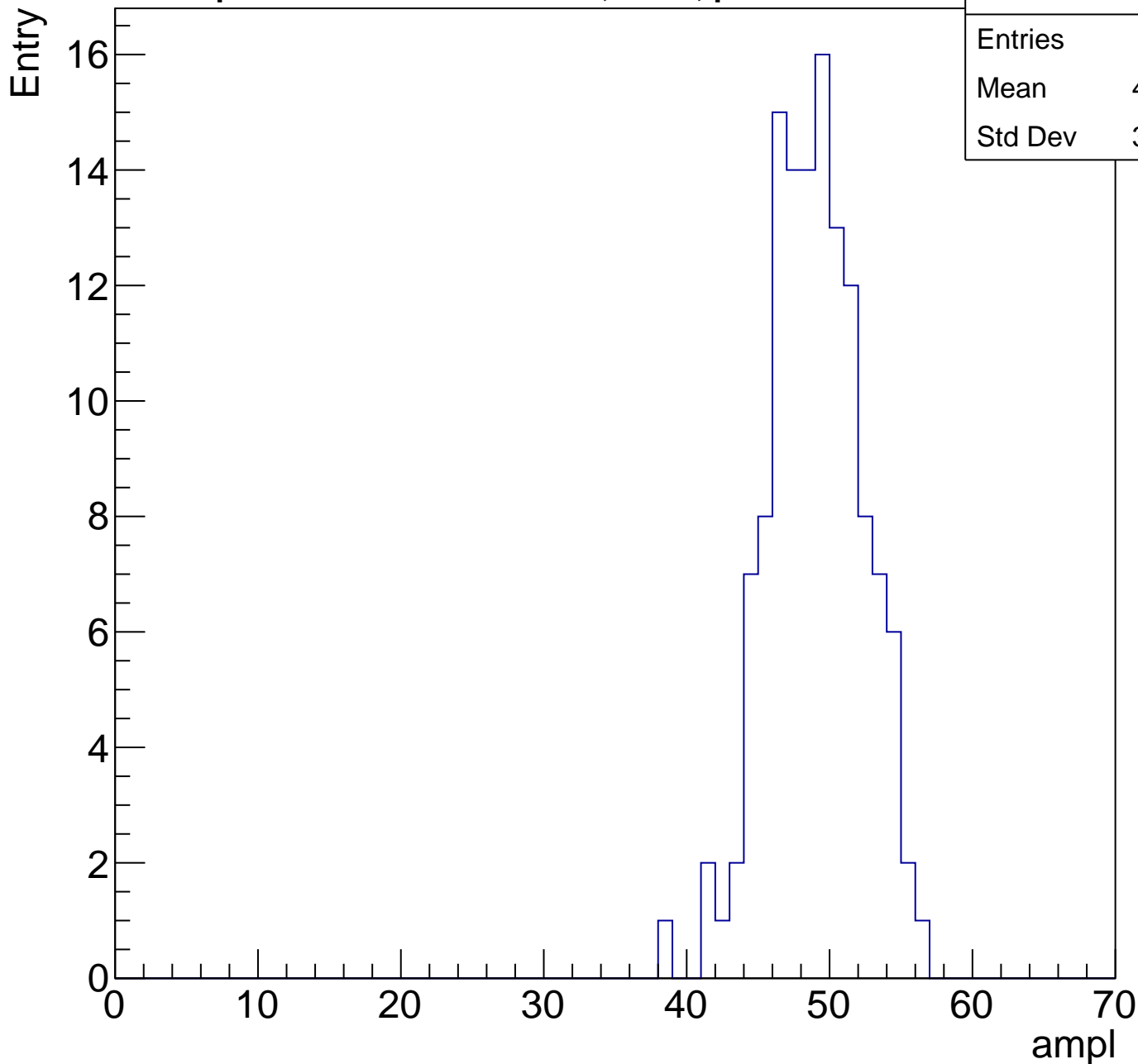
Entry



B1L001S, U26-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

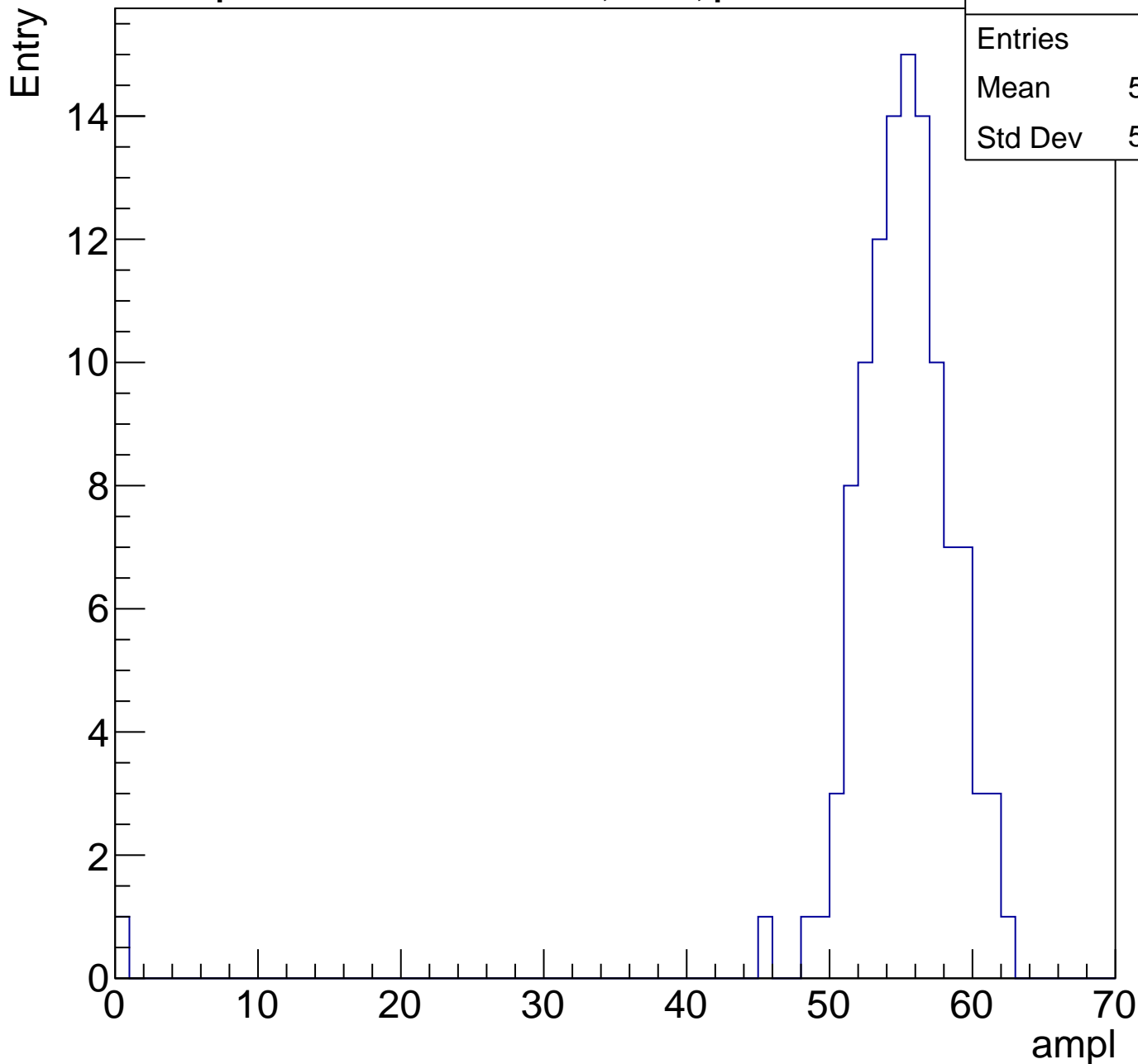
| | |
|---------|-------|
| Entries | 129 |
| Mean | 48.52 |
| Std Dev | 3.252 |



B1L001S, U26-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 111 |
| Mean | 54.37 |
| Std Dev | 5.989 |



B1L001S, U26-ch84, adc5

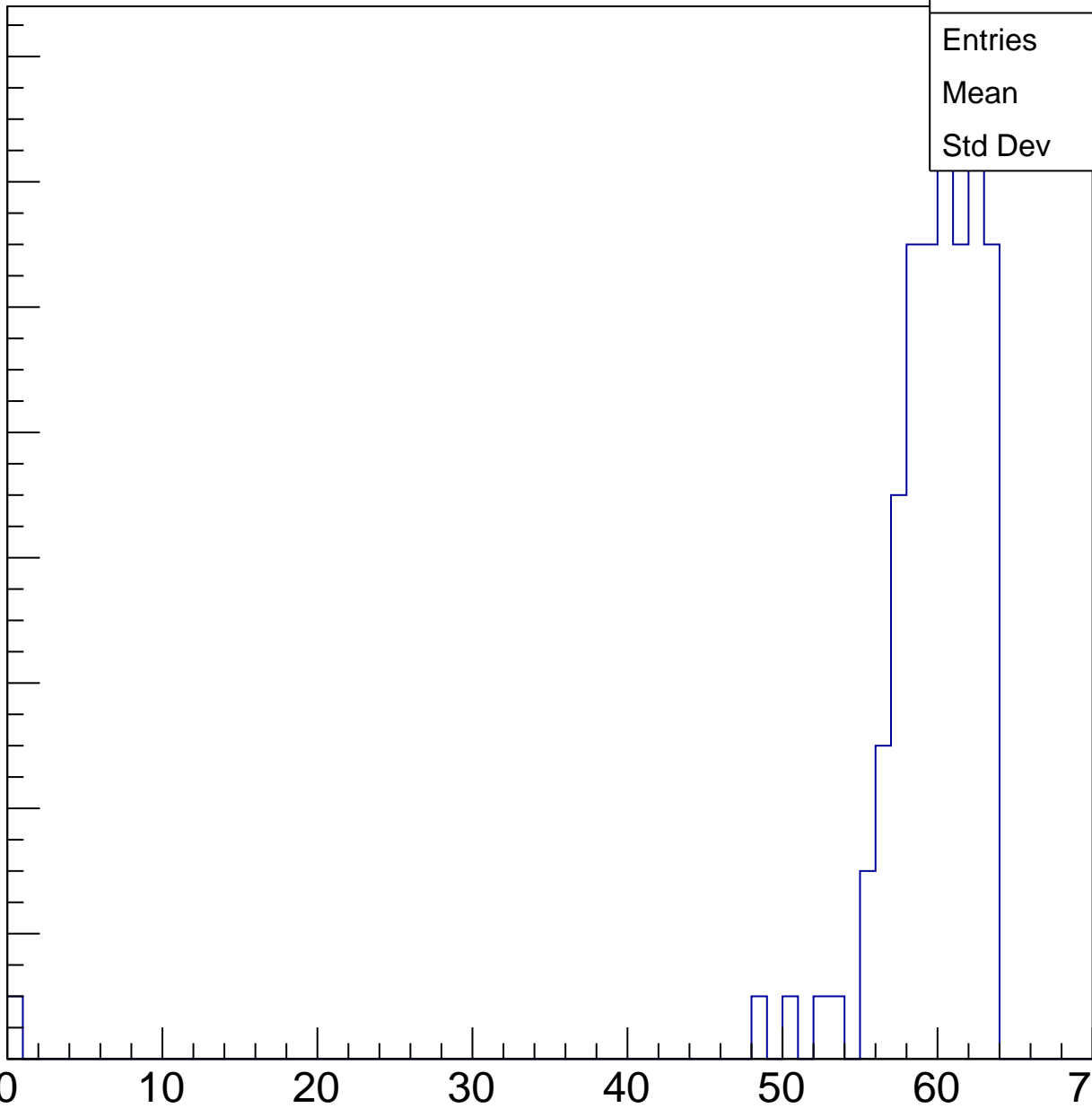
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 105 |
| Mean | 58.91 |
| Std Dev | 6.42 |

ampl

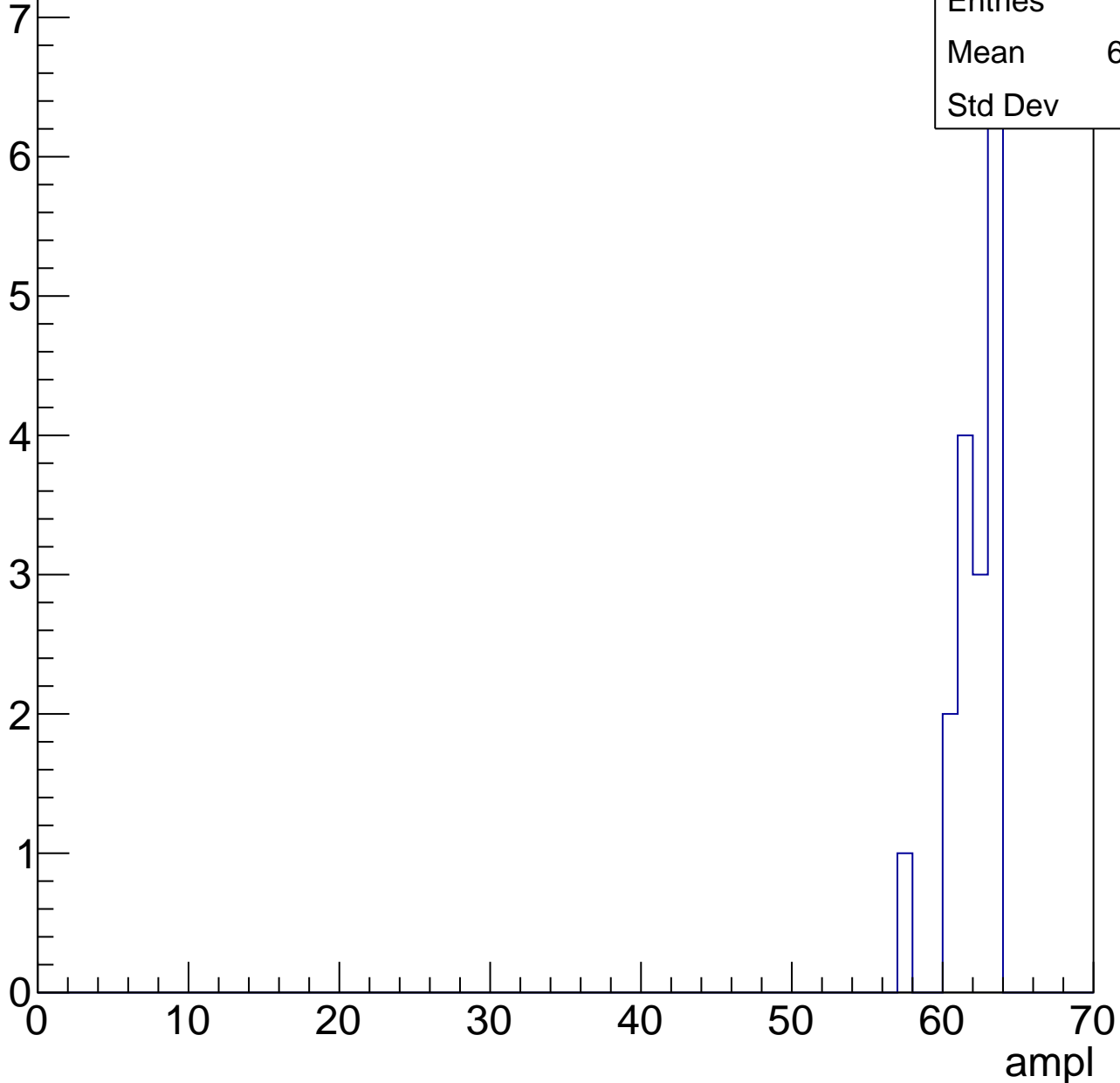


B1L001S, U26-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.65 |
| Std Dev | 1.57 |



B1L001S, U26-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch85, adc0

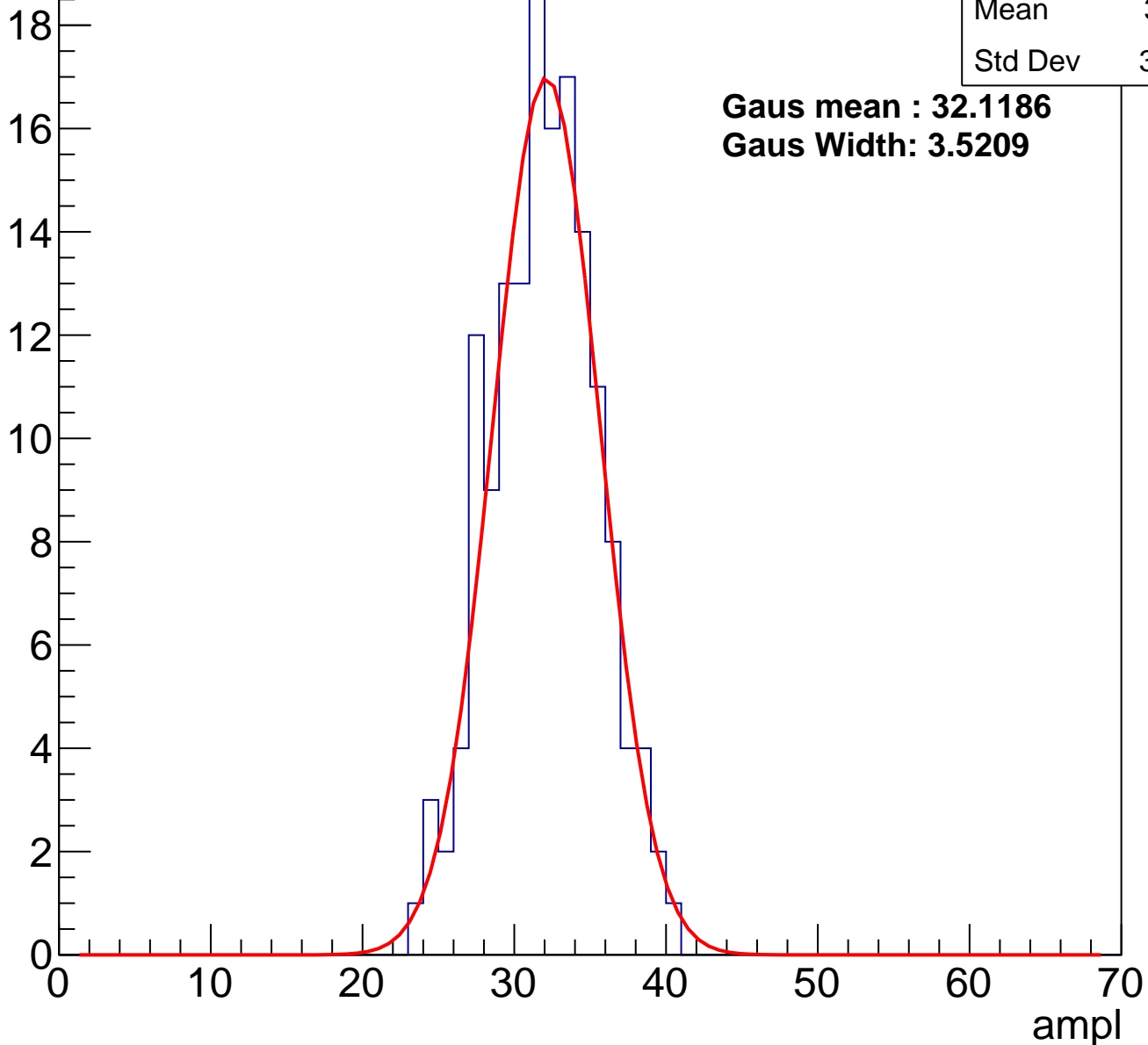
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 31.51 |
| Std Dev | 3.445 |

Gaus mean : 32.1186

Gaus Width: 3.5209

Entry

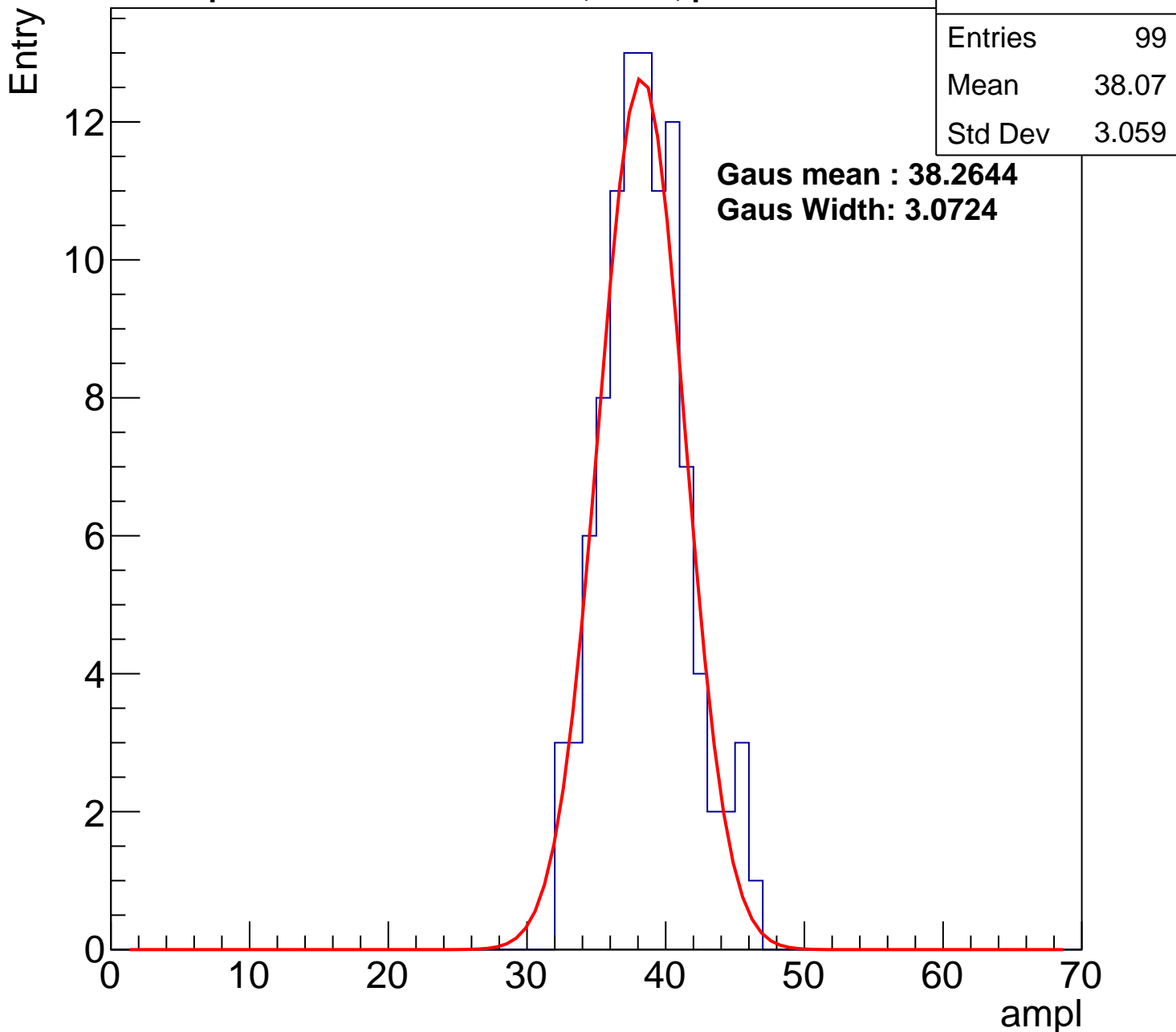


B1L001S, U26-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 99 |
| Mean | 38.07 |
| Std Dev | 3.059 |

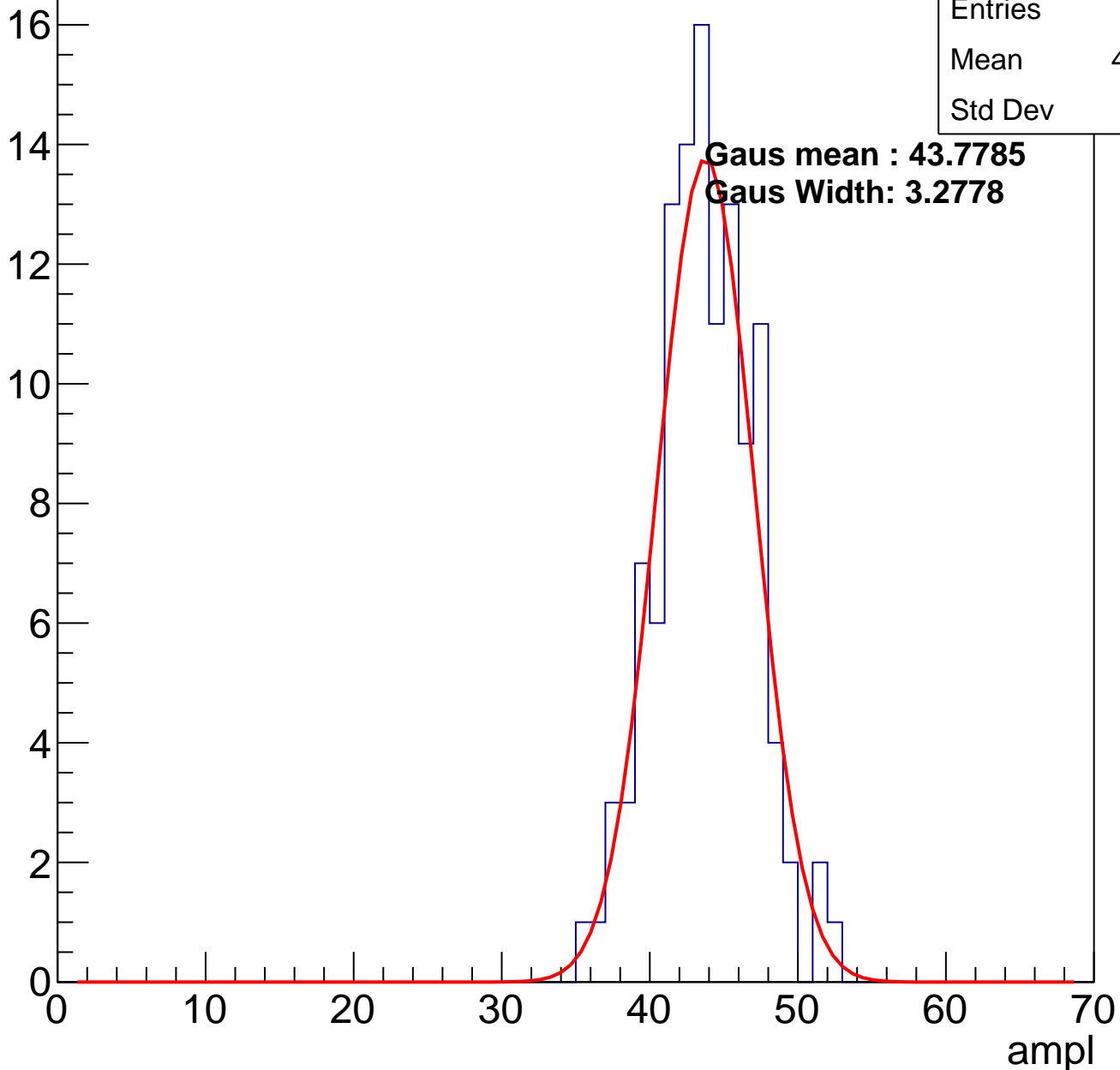
Gaus mean : 38.2644
Gaus Width: 3.0724



B1L001S, U26-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

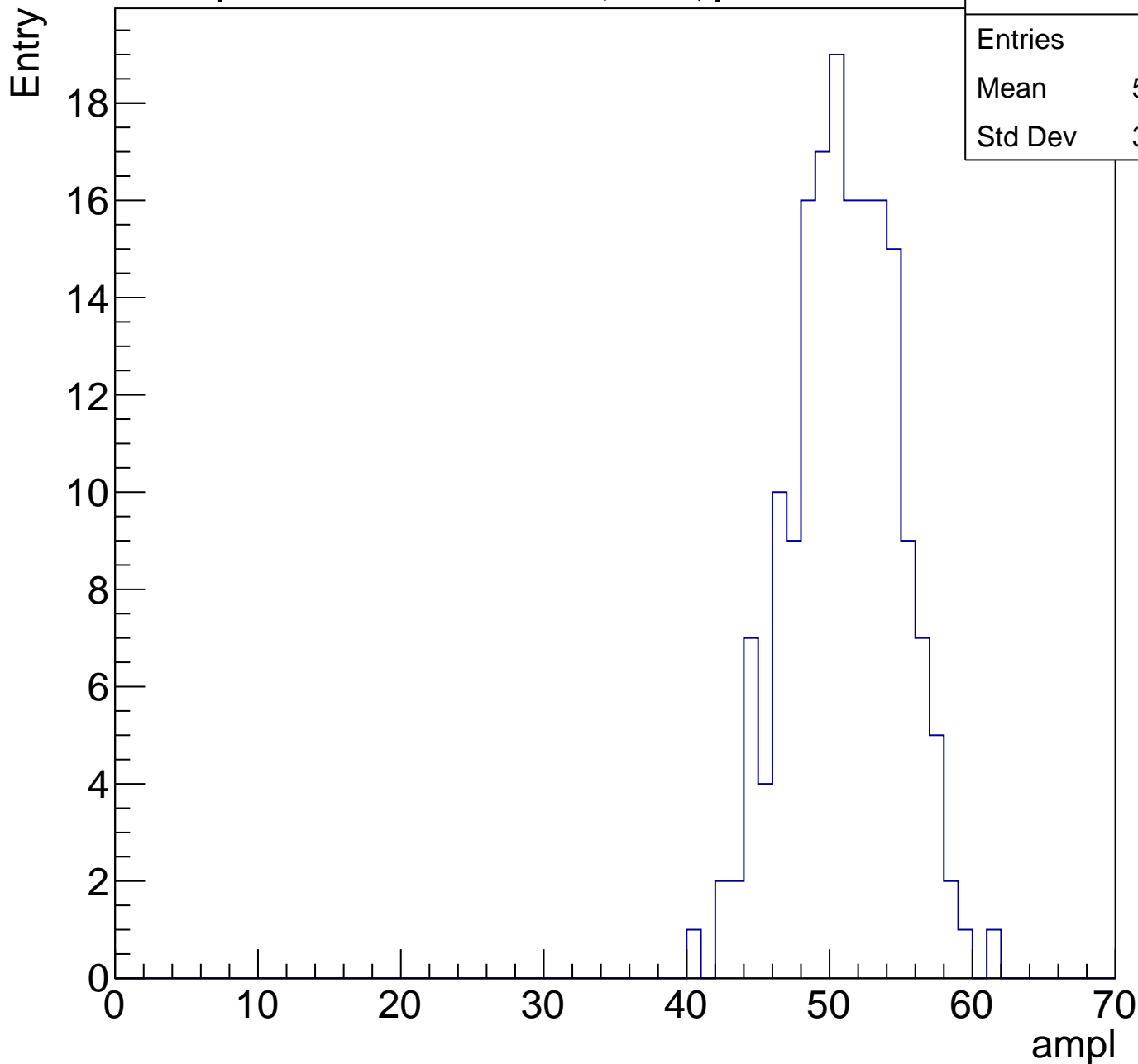
Entry



B1L001S, U26-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 175 |
| Mean | 50.55 |
| Std Dev | 3.752 |



B1L001S, U26-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 93 |
| Mean | 57.09 |
| Std Dev | 2.91 |

Entry

12

10

8

6

4

2

0

0

10

20

30

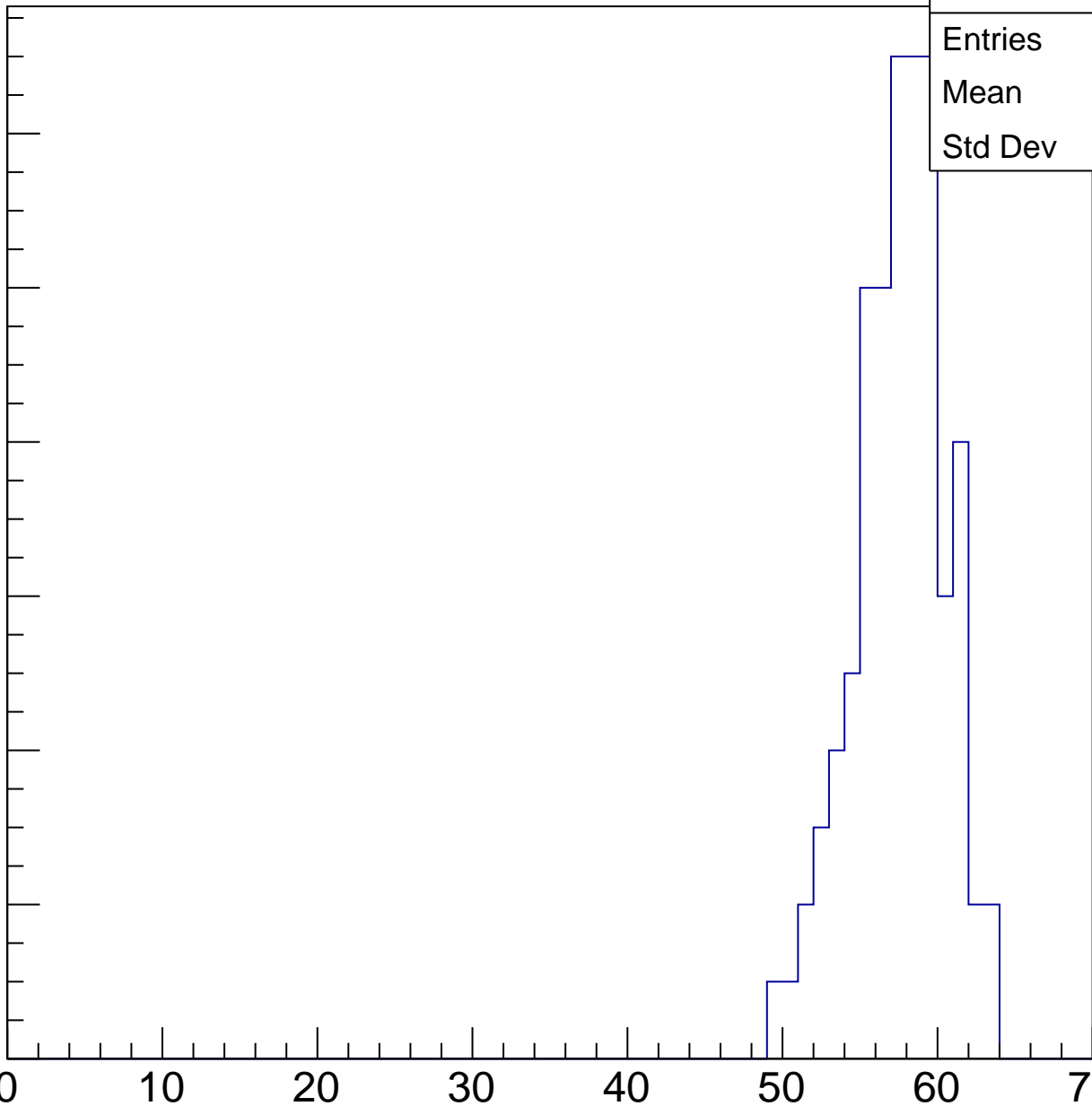
40

50

60

70

ampl



B1L001S, U26-ch85, adc5

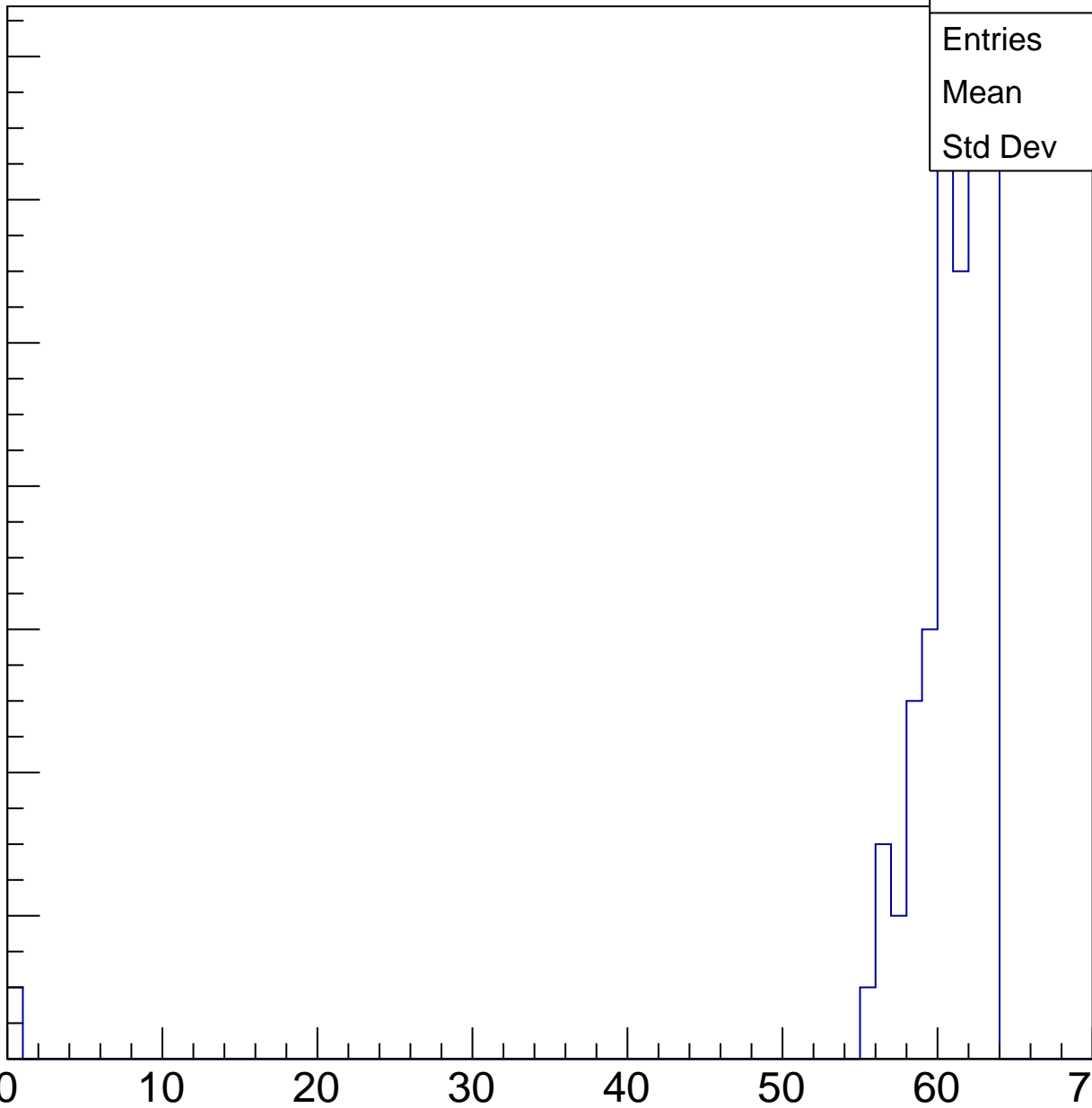
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 69 |
| Mean | 59.67 |
| Std Dev | 7.508 |

ampl

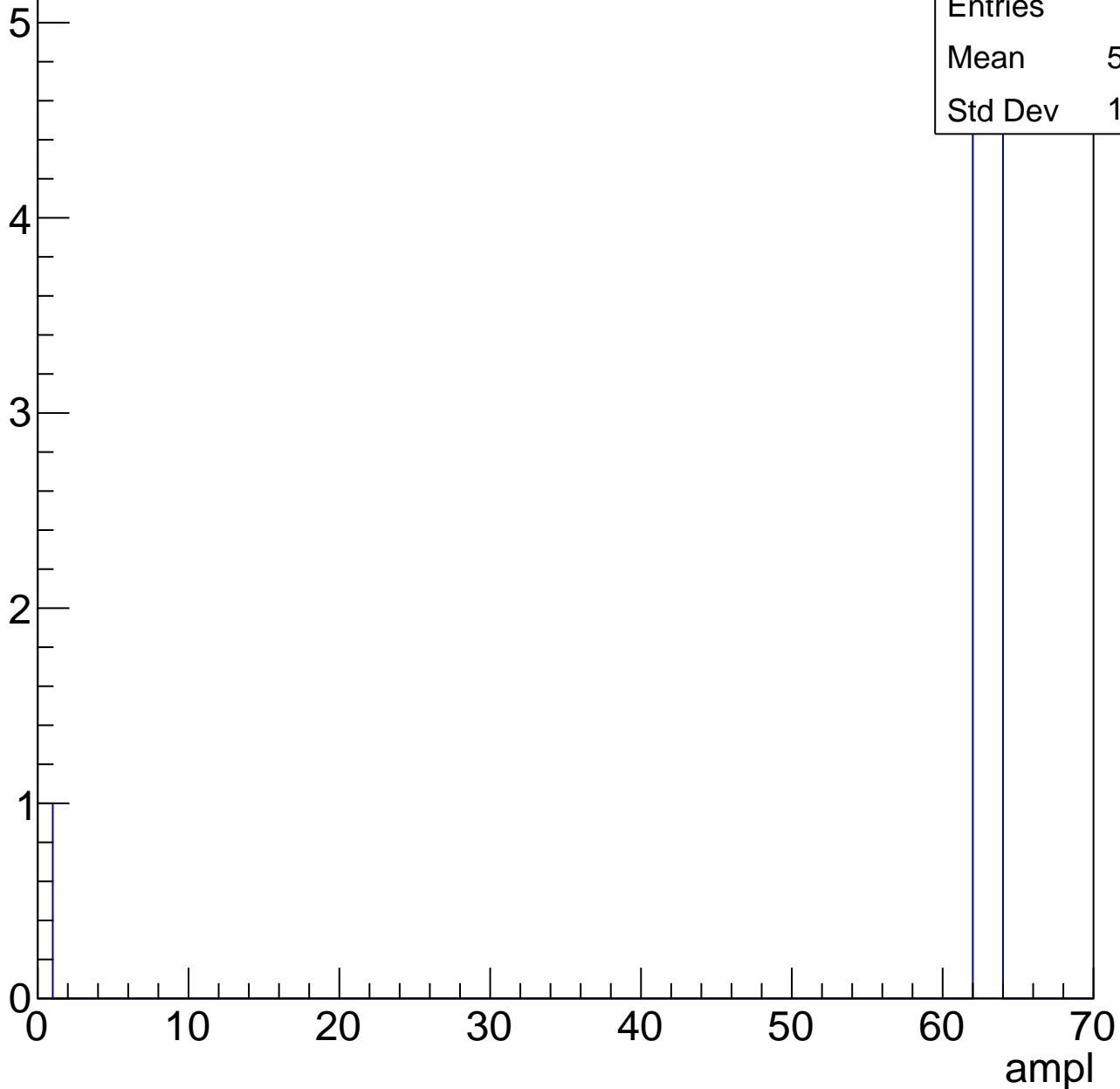


B1L001S, U26-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.82 |
| Std Dev | 17.97 |



B1L001S, U26-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 28.64 |
| Std Dev | 3.151 |

Gaus mean : 29.1676

Gaus Width: 3.3510

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

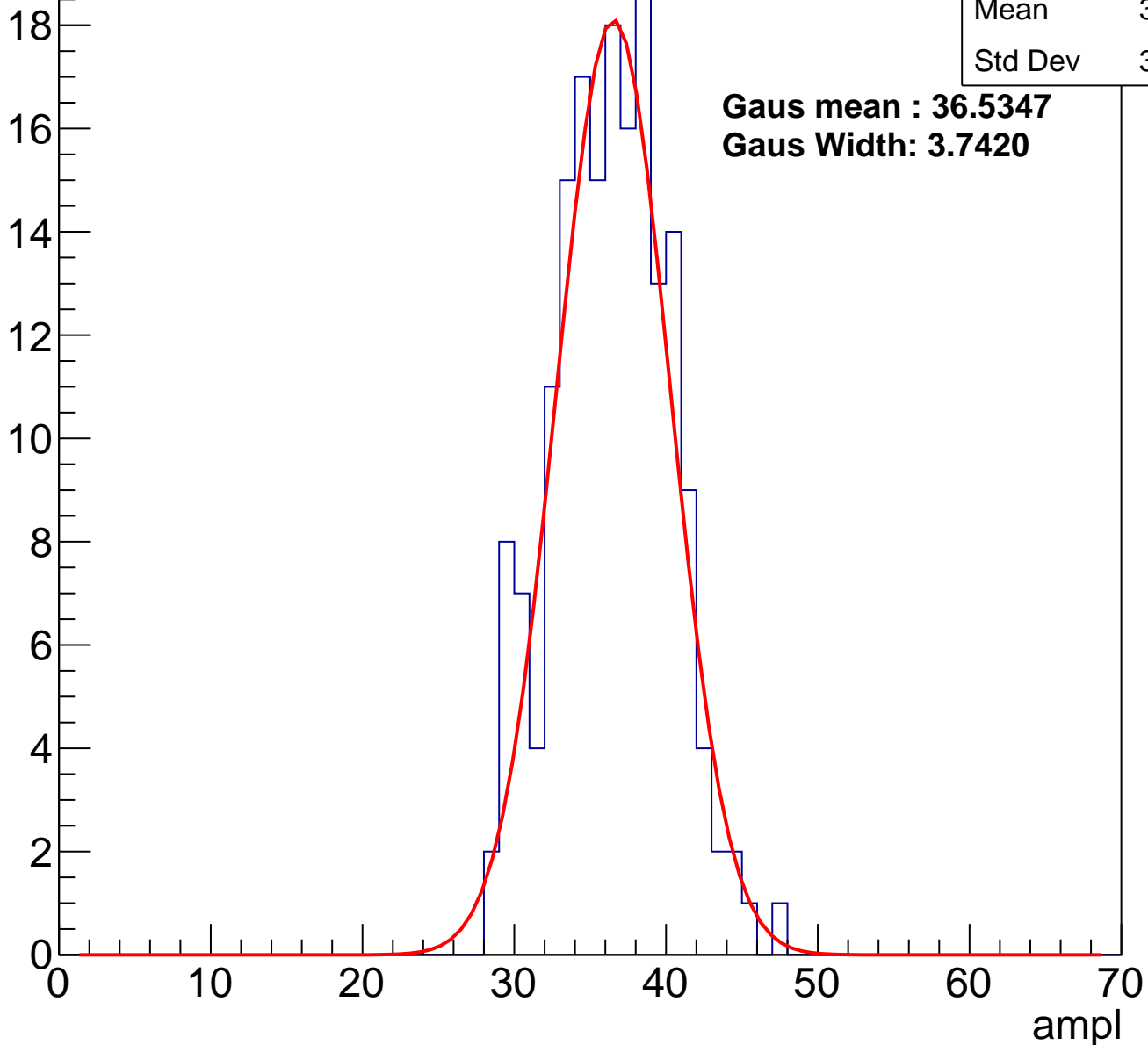
B1L001S, U26-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 178 |
| Mean | 35.98 |
| Std Dev | 3.745 |

Gaus mean : 36.5347
Gaus Width: 3.7420

Entry



B1L001S, U26-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

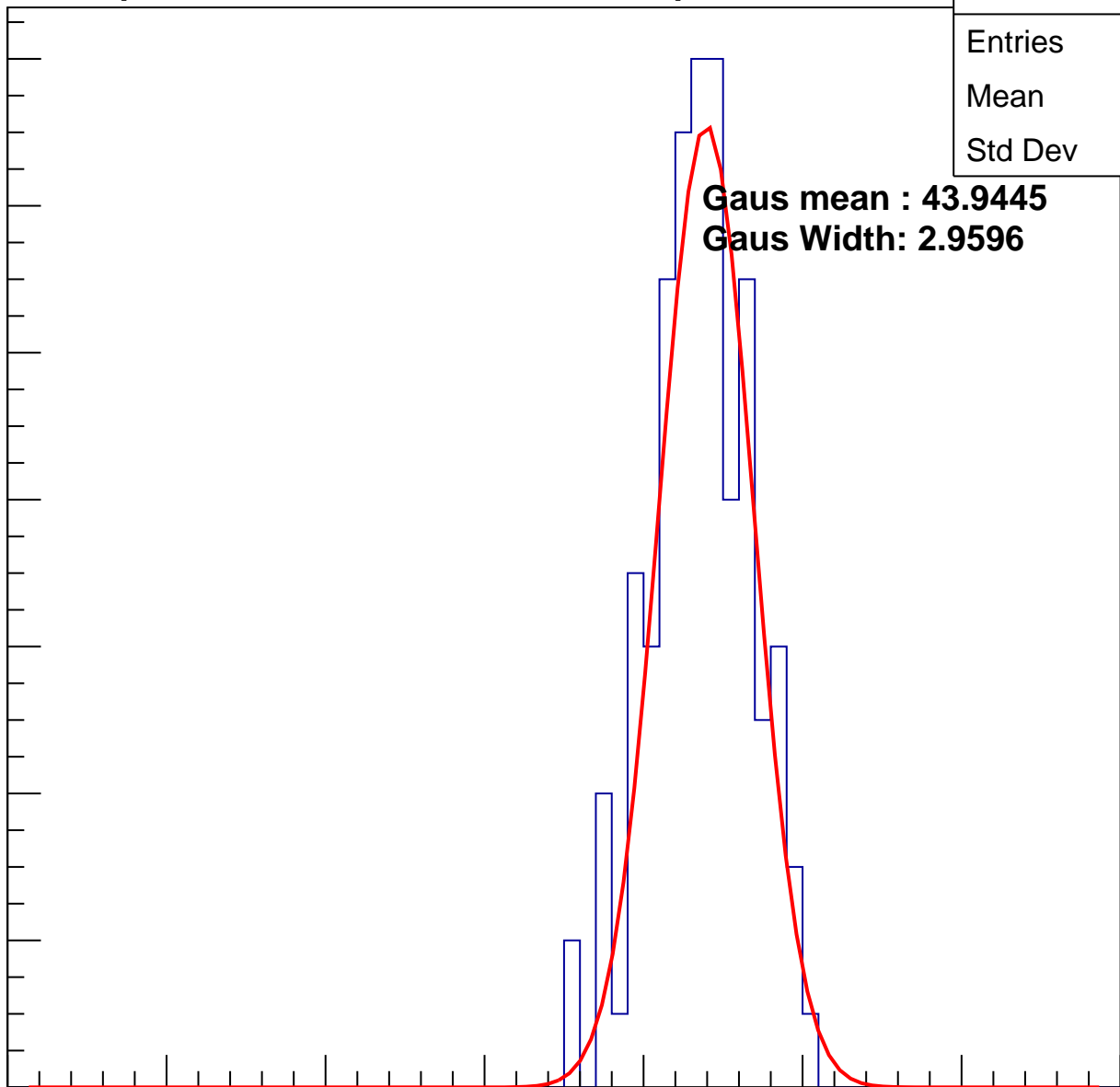
| | |
|---------|-------|
| Entries | 106 |
| Mean | 43.11 |
| Std Dev | 3.148 |

Gaus mean : 43.9445

Gaus Width: 2.9596

0 10 20 30 40 50 60 70

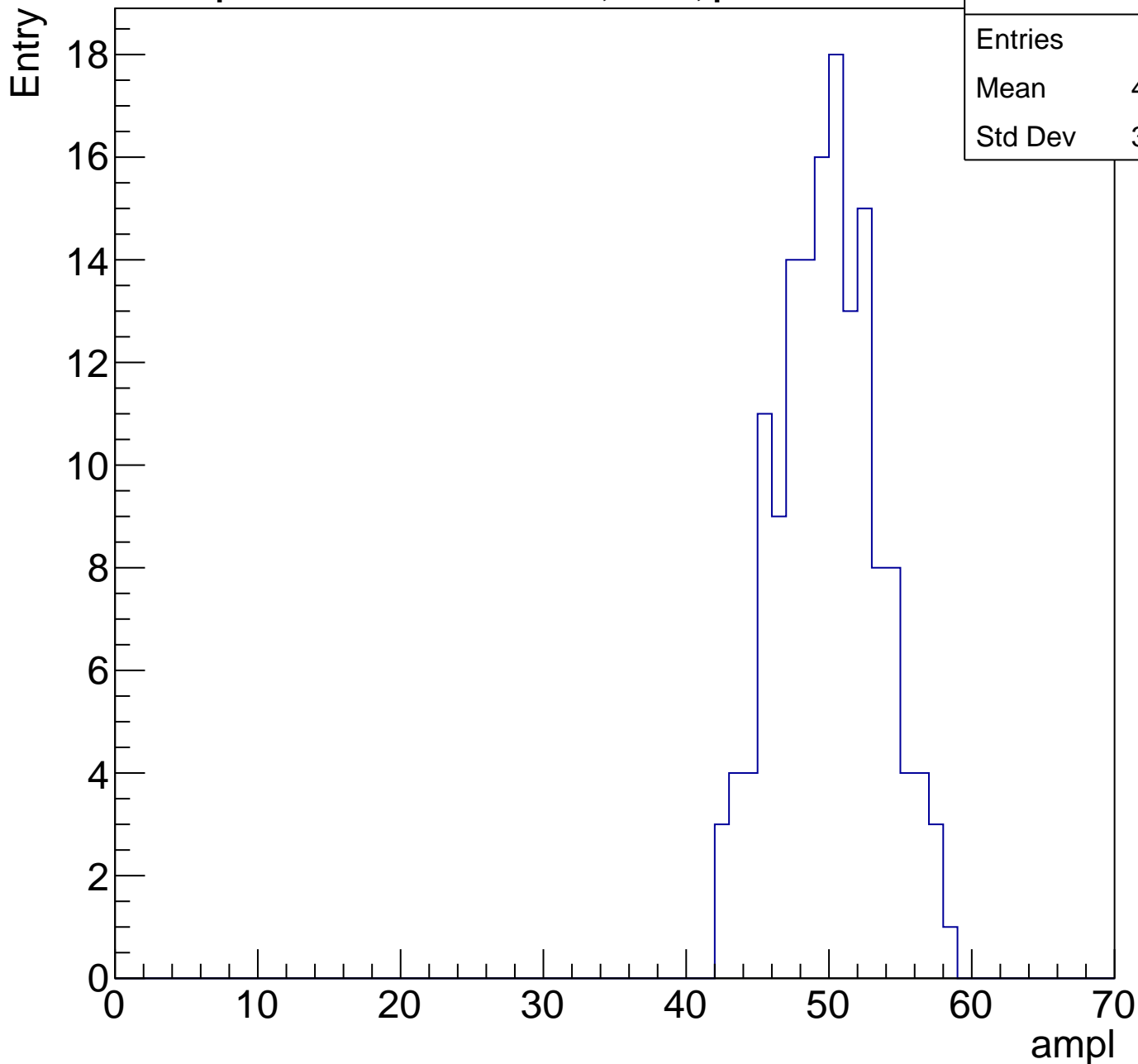
ampl



B1L001S, U26-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

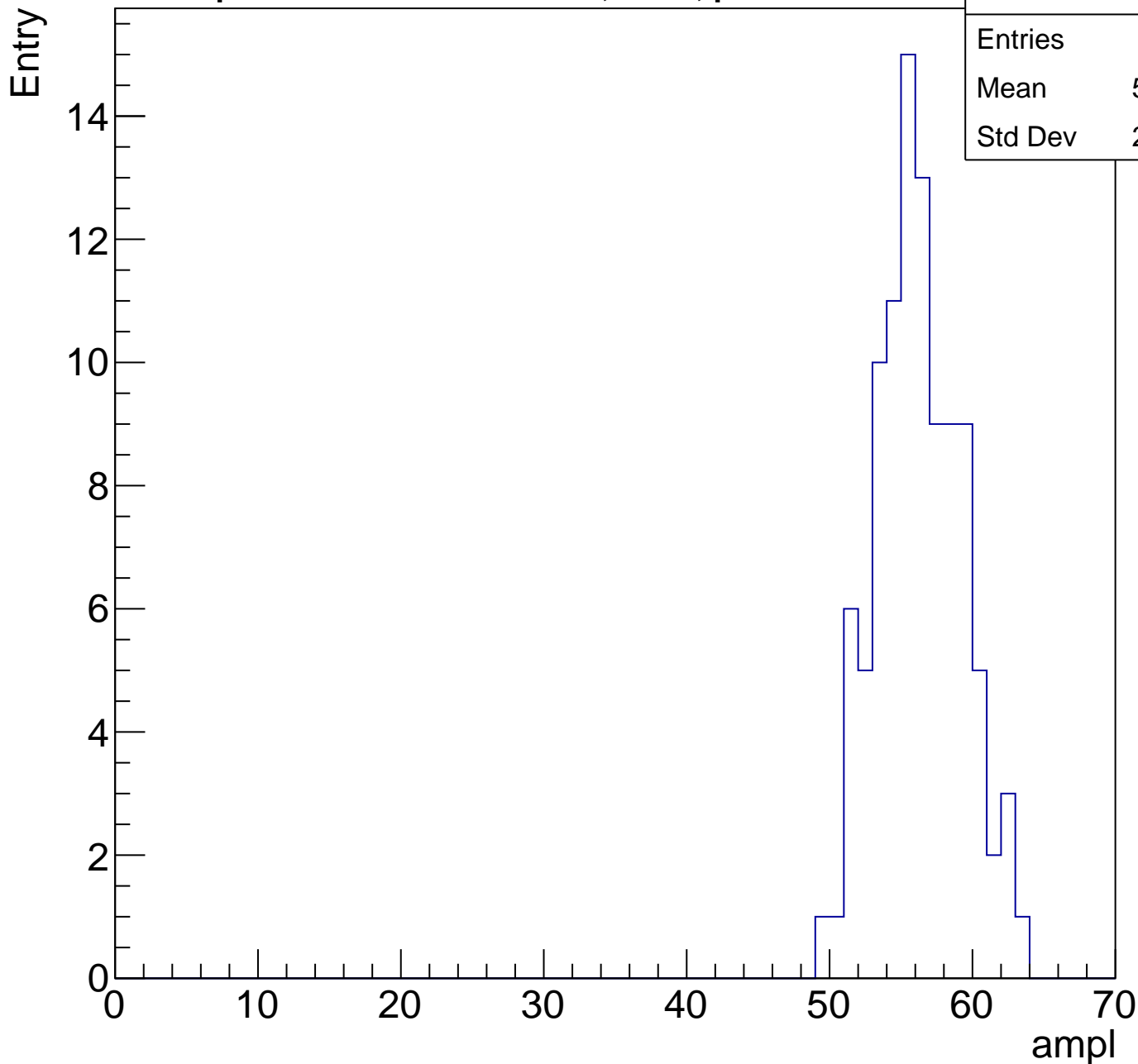
| | |
|---------|-------|
| Entries | 149 |
| Mean | 49.46 |
| Std Dev | 3.507 |



B1L001S, U26-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 100 |
| Mean | 55.79 |
| Std Dev | 2.954 |



B1L001S, U26-ch86, adc5

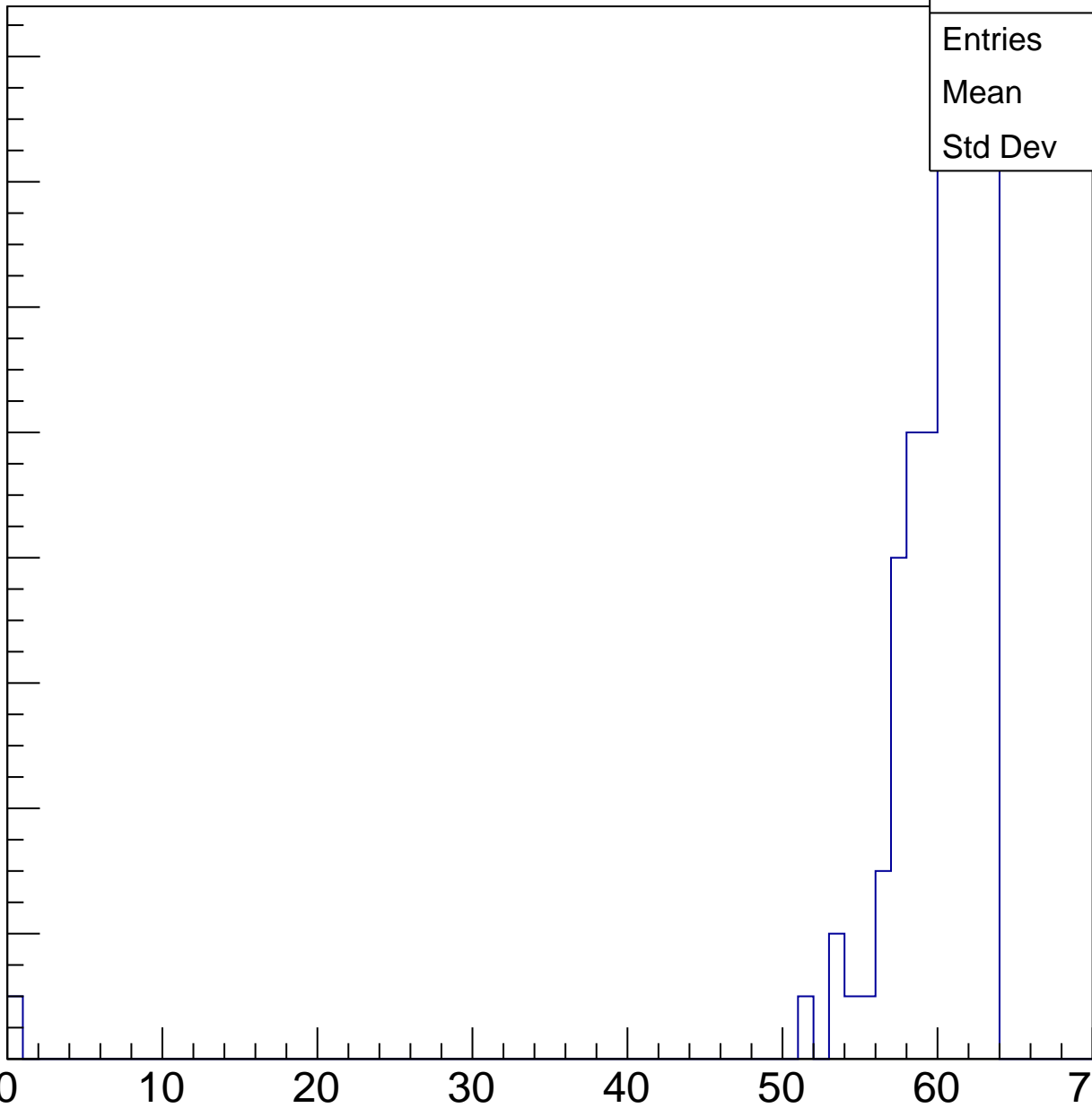
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 59.3 |
| Std Dev | 6.525 |

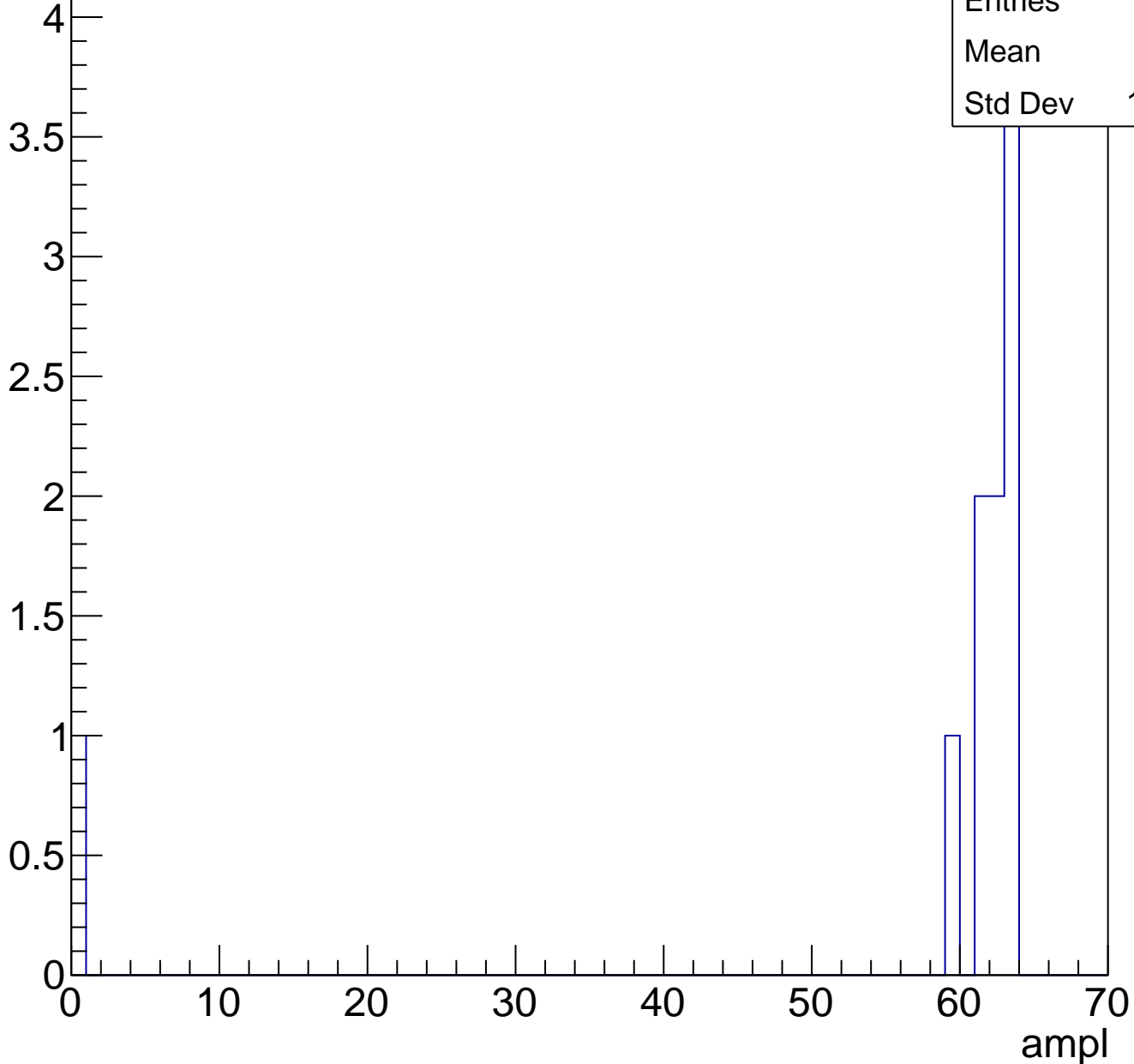
ampl



B1L001S, U26-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl

B1L001S, U26-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 29.24 |
| Std Dev | 4.313 |

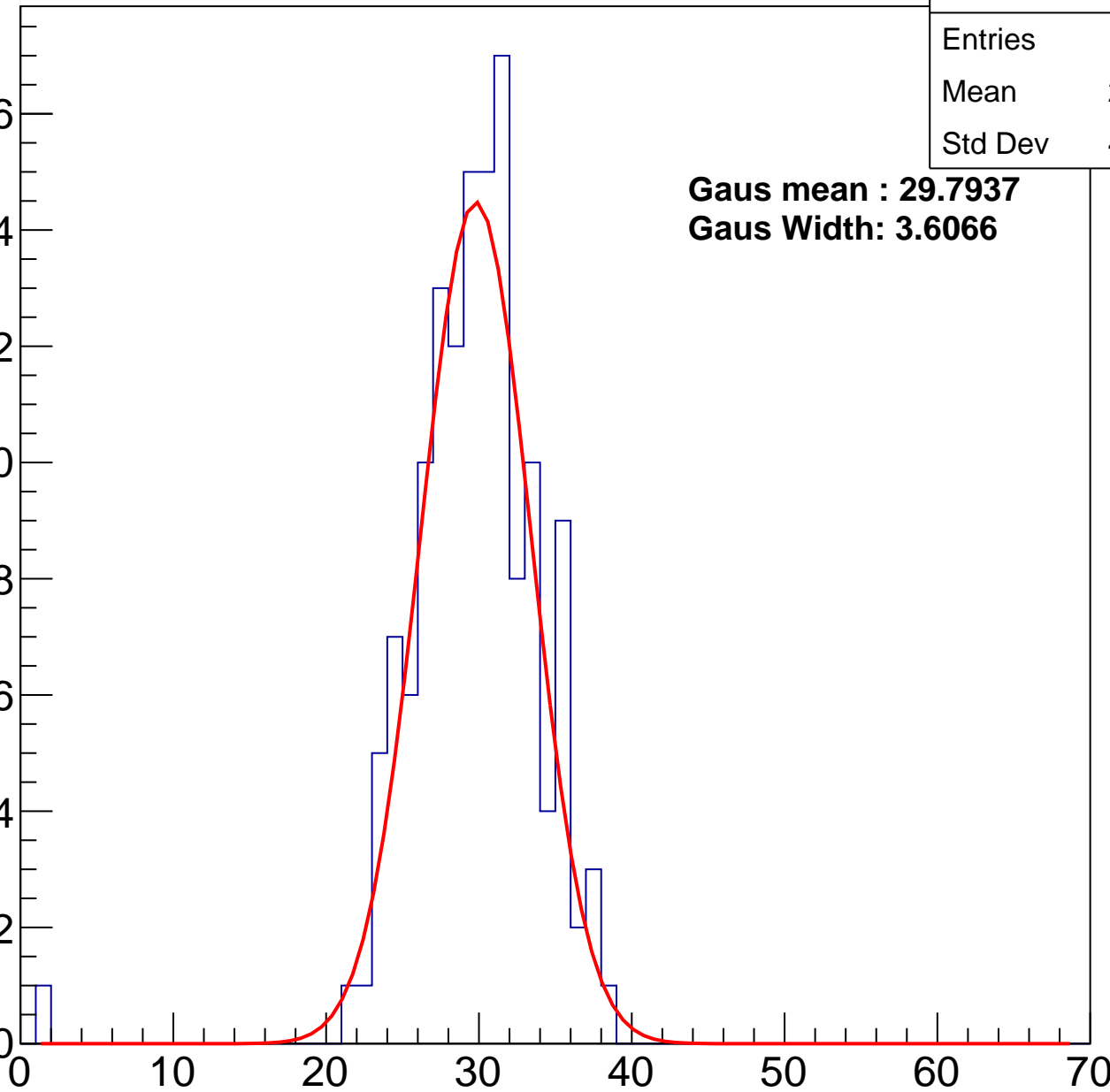
Gaus mean : 29.7937

Gaus Width: 3.6066

Entry

16
14
12
10
8
6
4
2
0

ampl



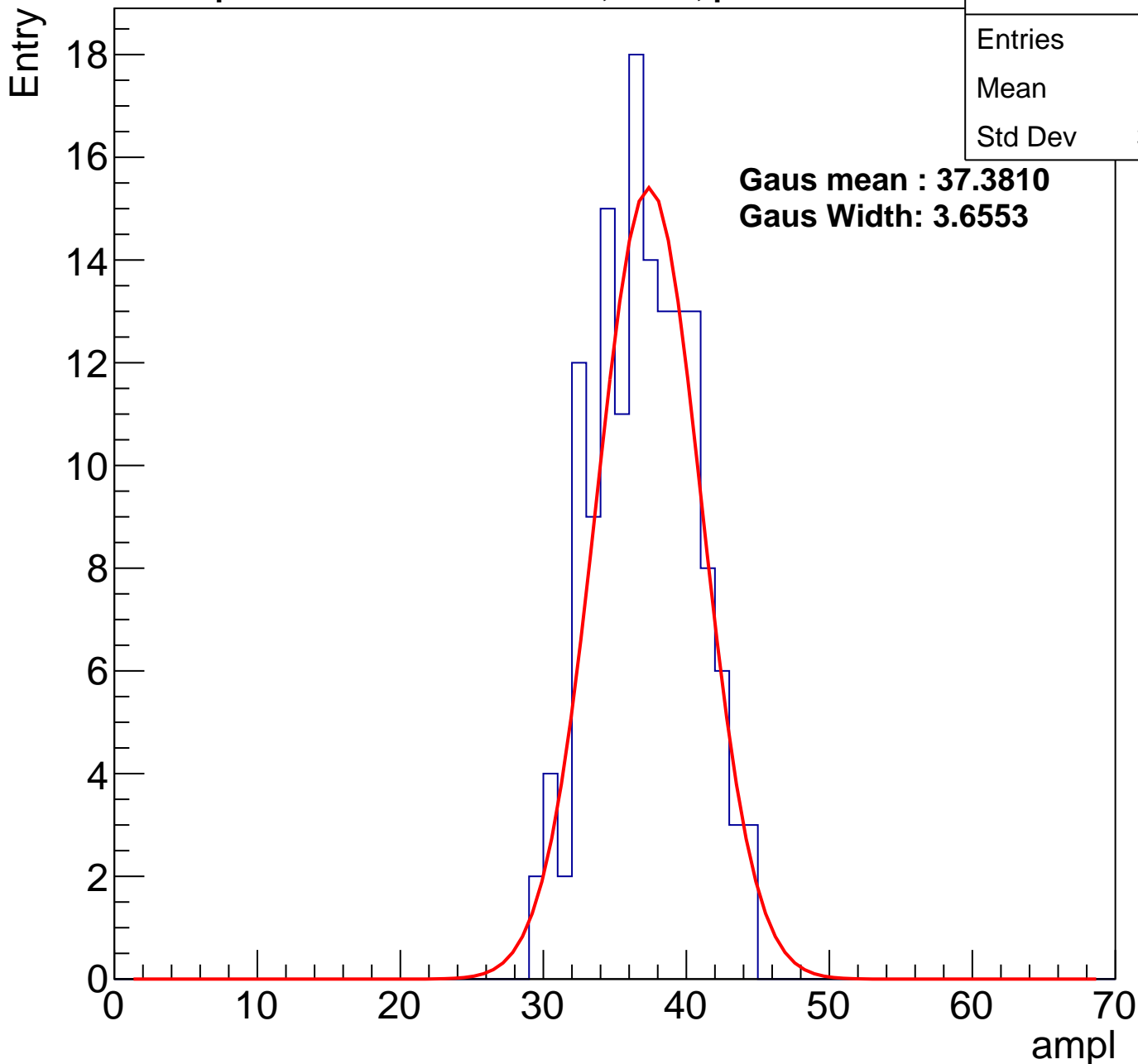
B1L001S, U26-ch87, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 36.6 |
| Std Dev | 3.441 |

Gaus mean : 37.3810

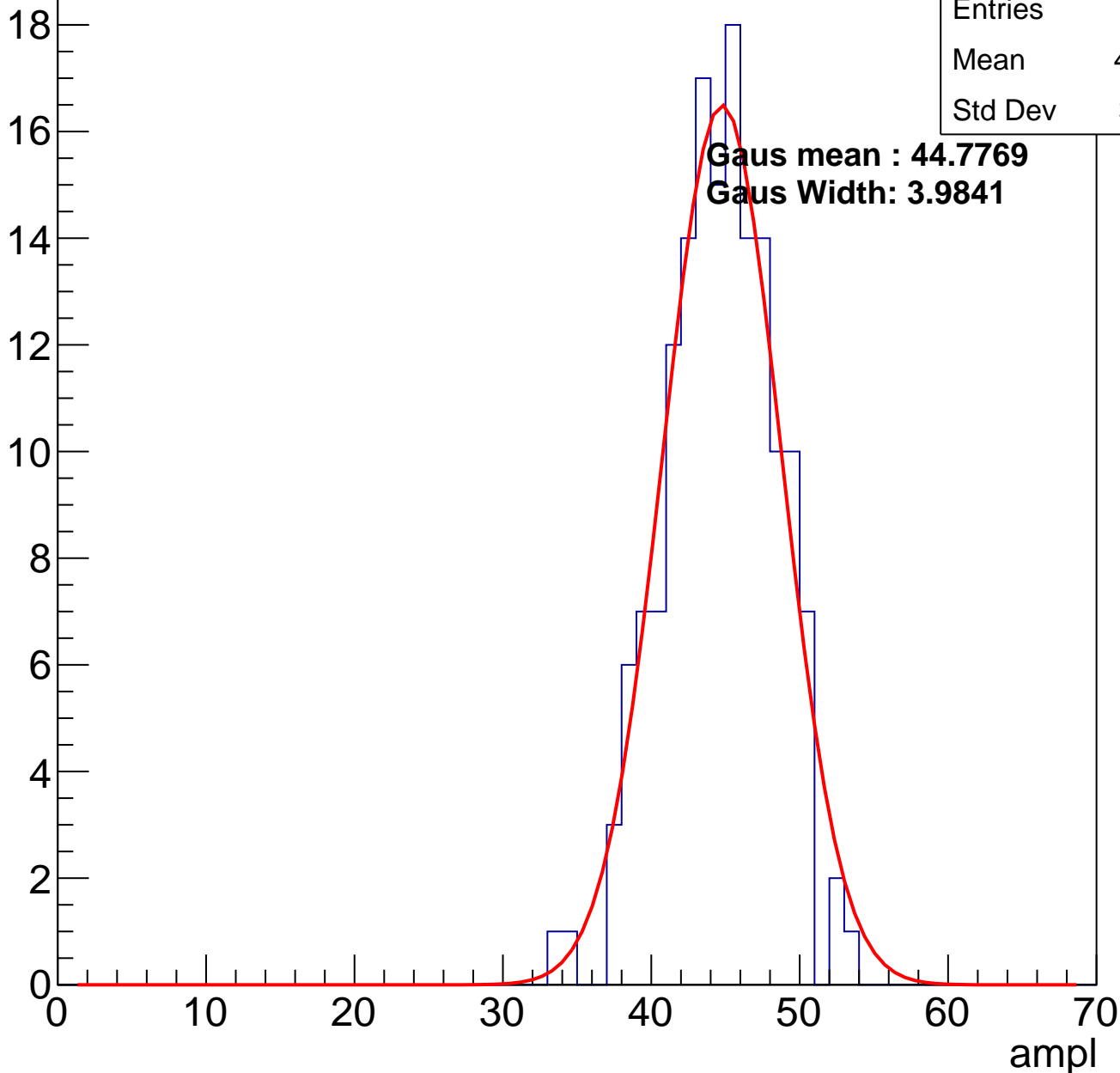
Gaus Width: 3.6553



B1L001S, U26-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

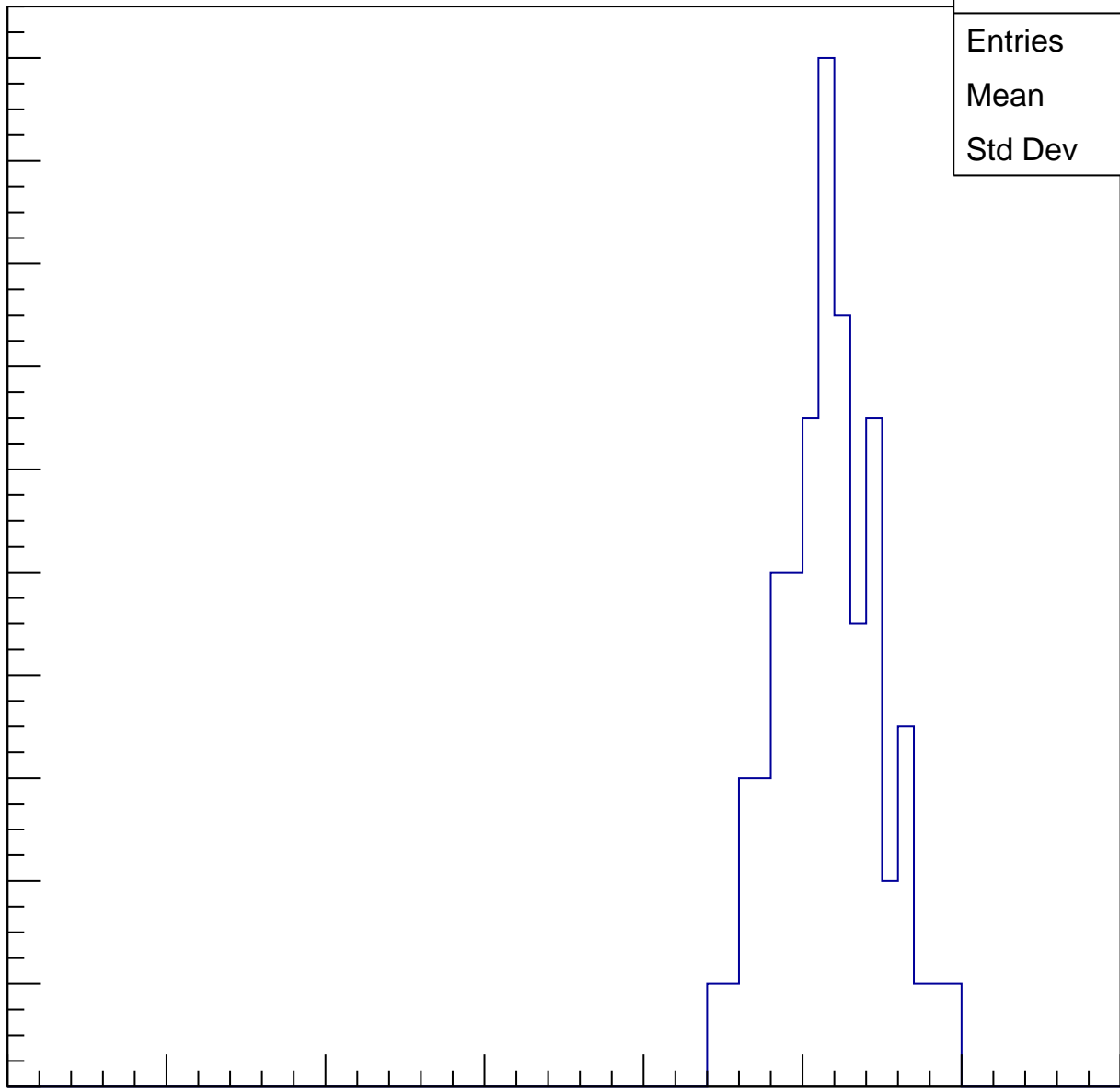
Entry

20
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 123 |
| Mean | 51.18 |
| Std Dev | 3.201 |

ampl

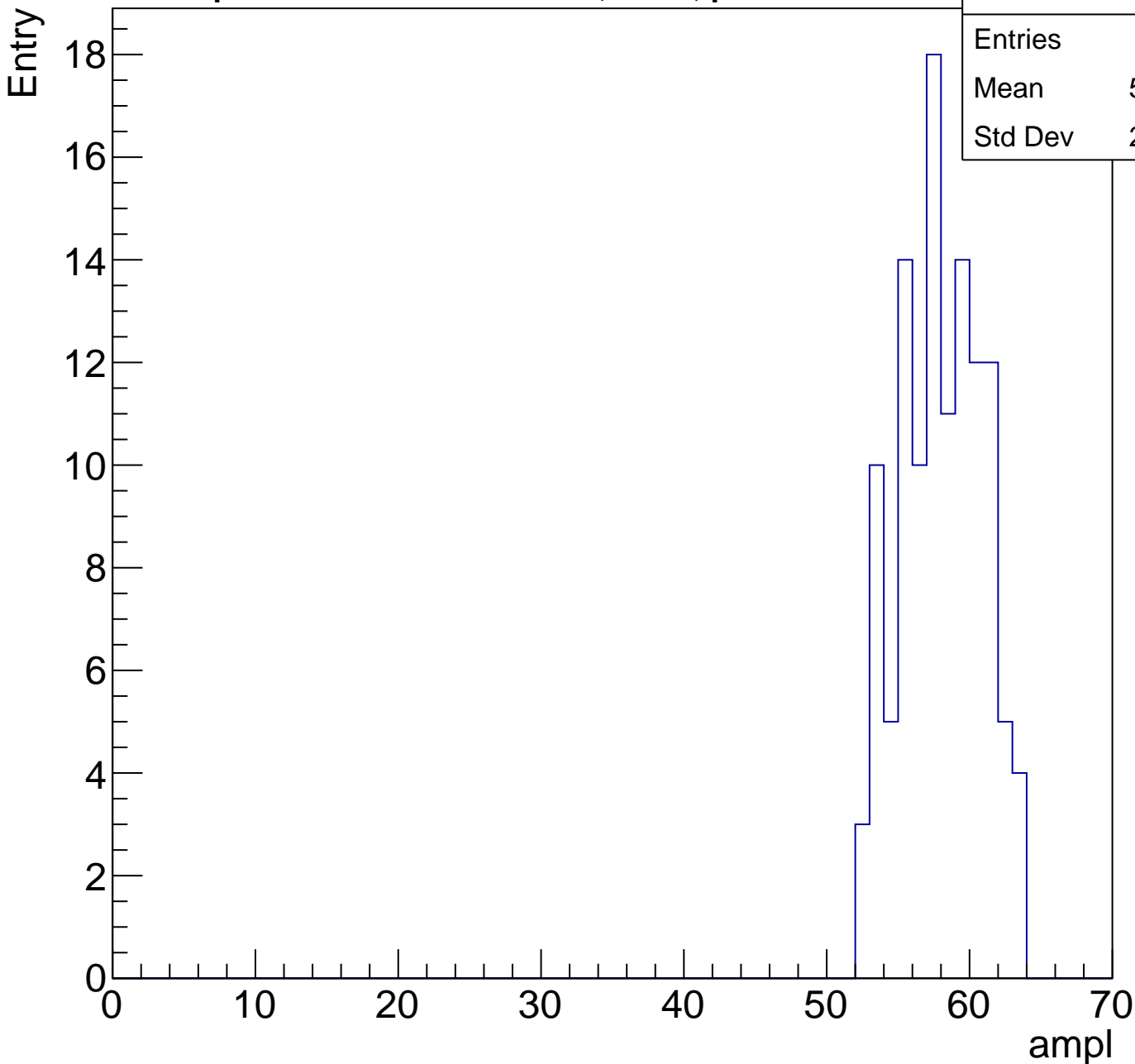
0 10 20 30 40 50 60 70



B1L001S, U26-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 57.54 |
| Std Dev | 2.833 |



B1L001S, U26-ch87, adc5

calib_packv5_042523_0143.root, FC#2, port C2

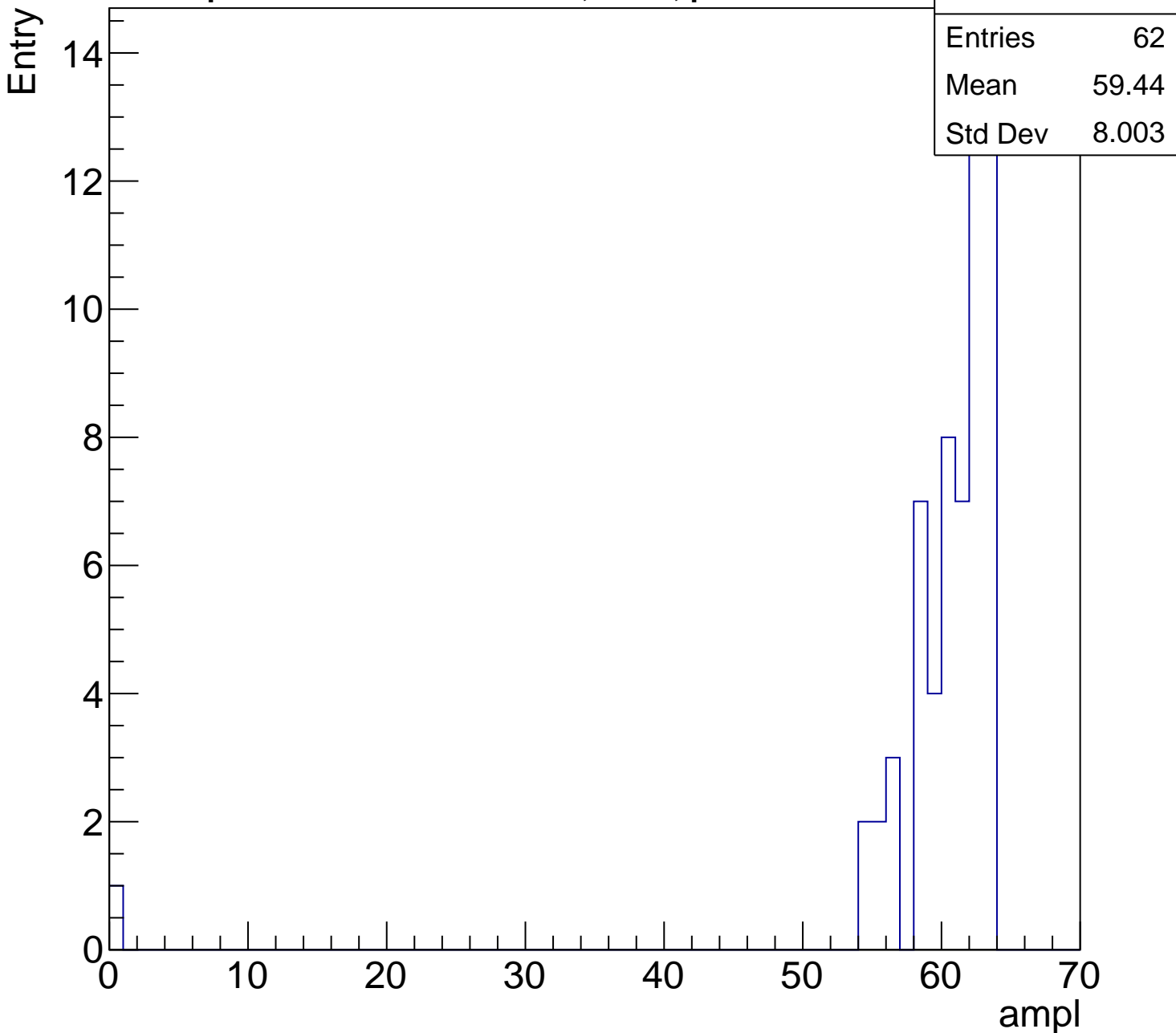
Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.44 |
| Std Dev | 8.003 |

ampl

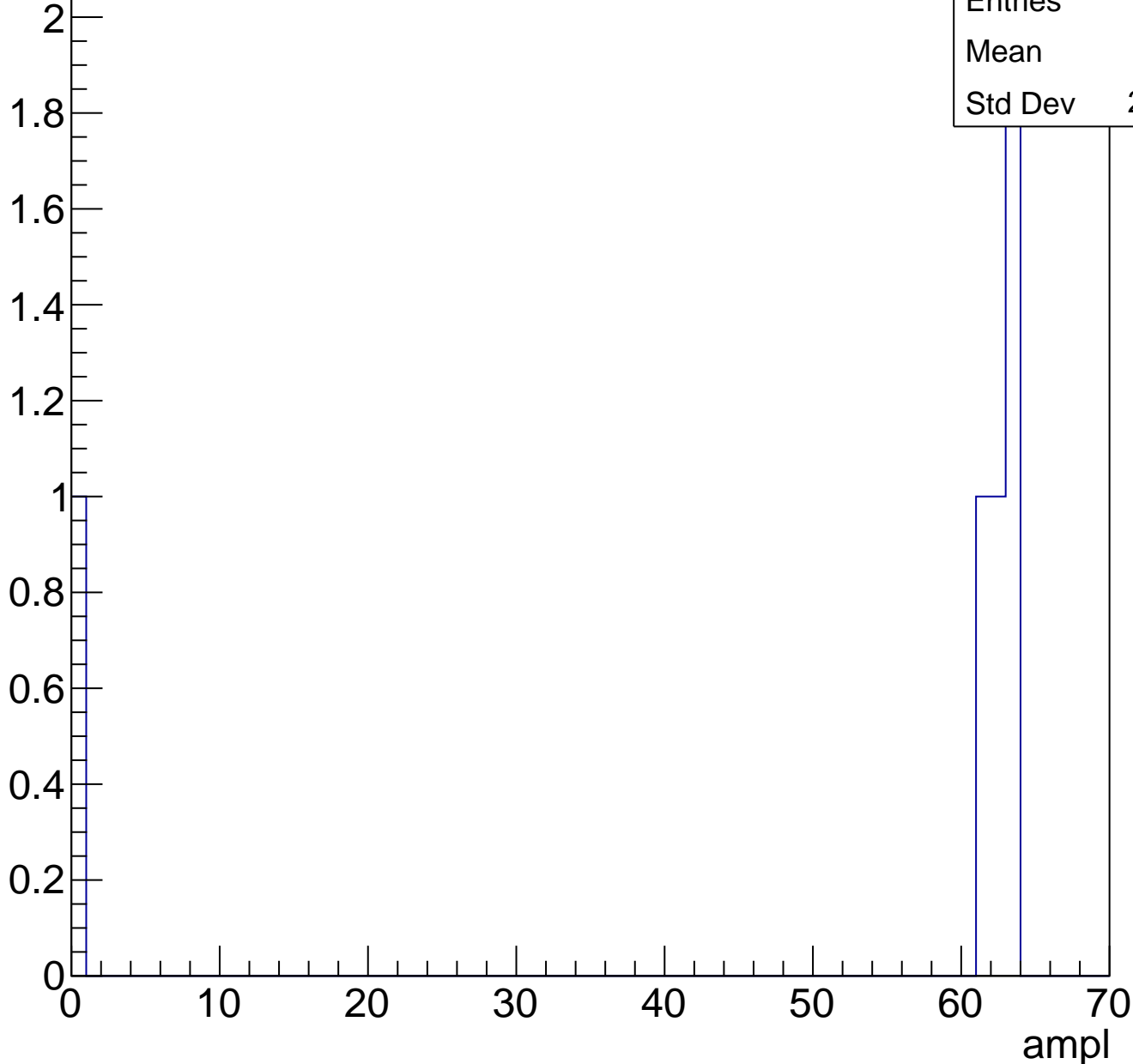
0 10 20 30 40 50 60 70



B1L001S, U26-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

B1L001S, U26-ch88, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 29.88 |
| Std Dev | 4.203 |

Gaus mean : 30.7282

Gaus Width: 3.4494

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

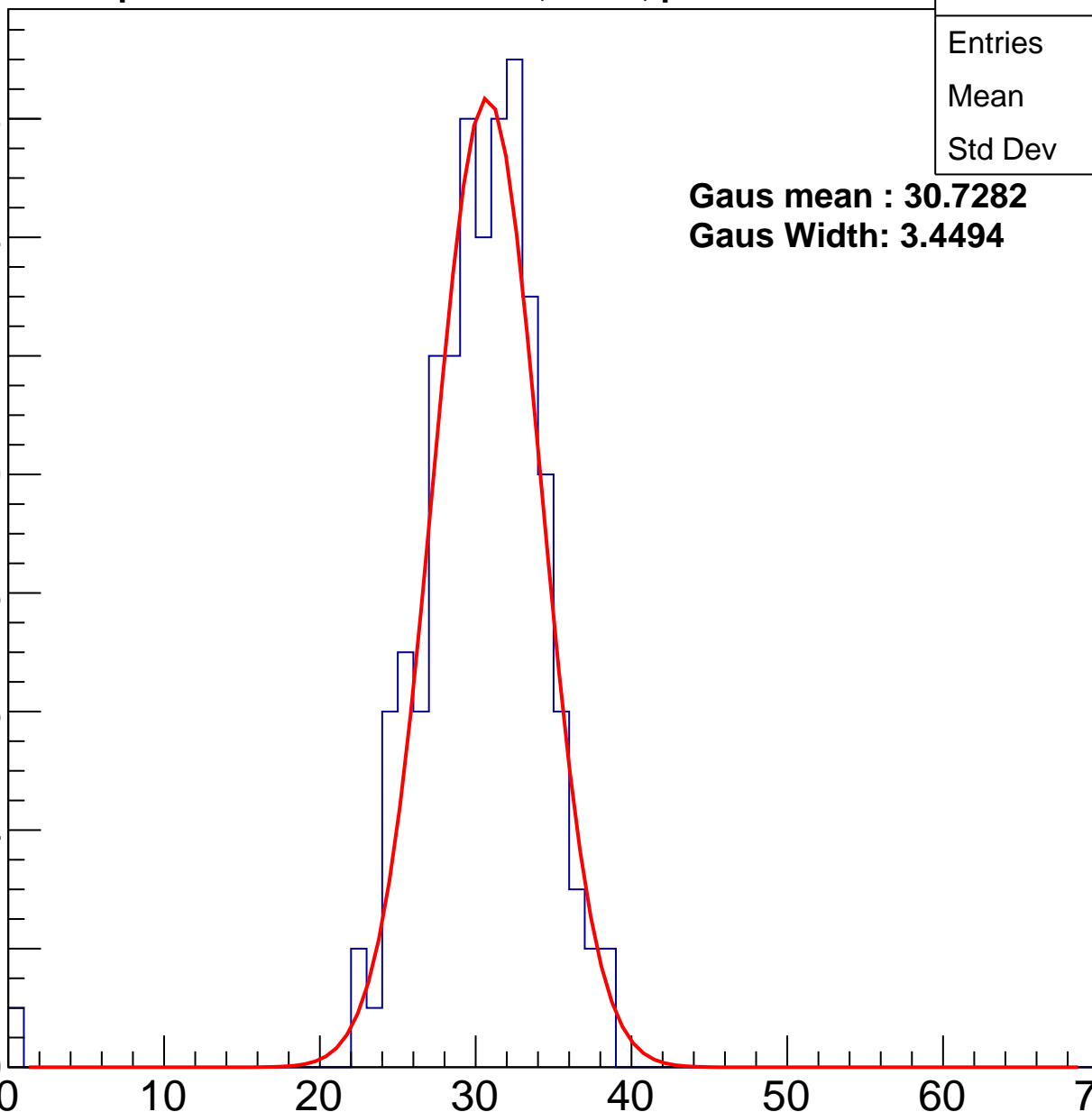
40

50

60

70

ampl



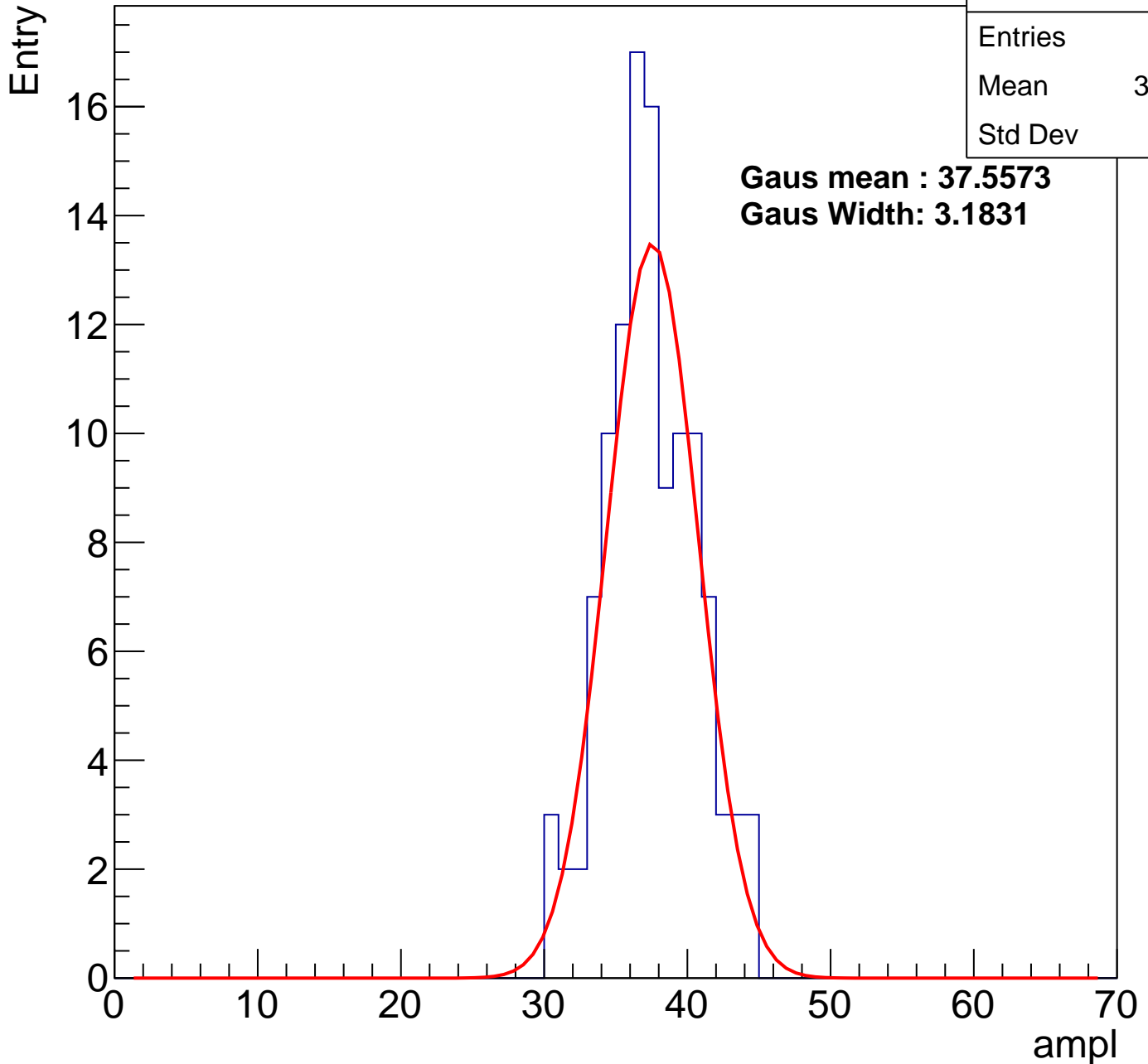
B1L001S, U26-ch88, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 114 |
| Mean | 36.99 |
| Std Dev | 3.13 |

Gaus mean : 37.5573

Gaus Width: 3.1831



B1L001S, U26-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 42.55 |
| Std Dev | 3.141 |

Gaus mean : 43.0565

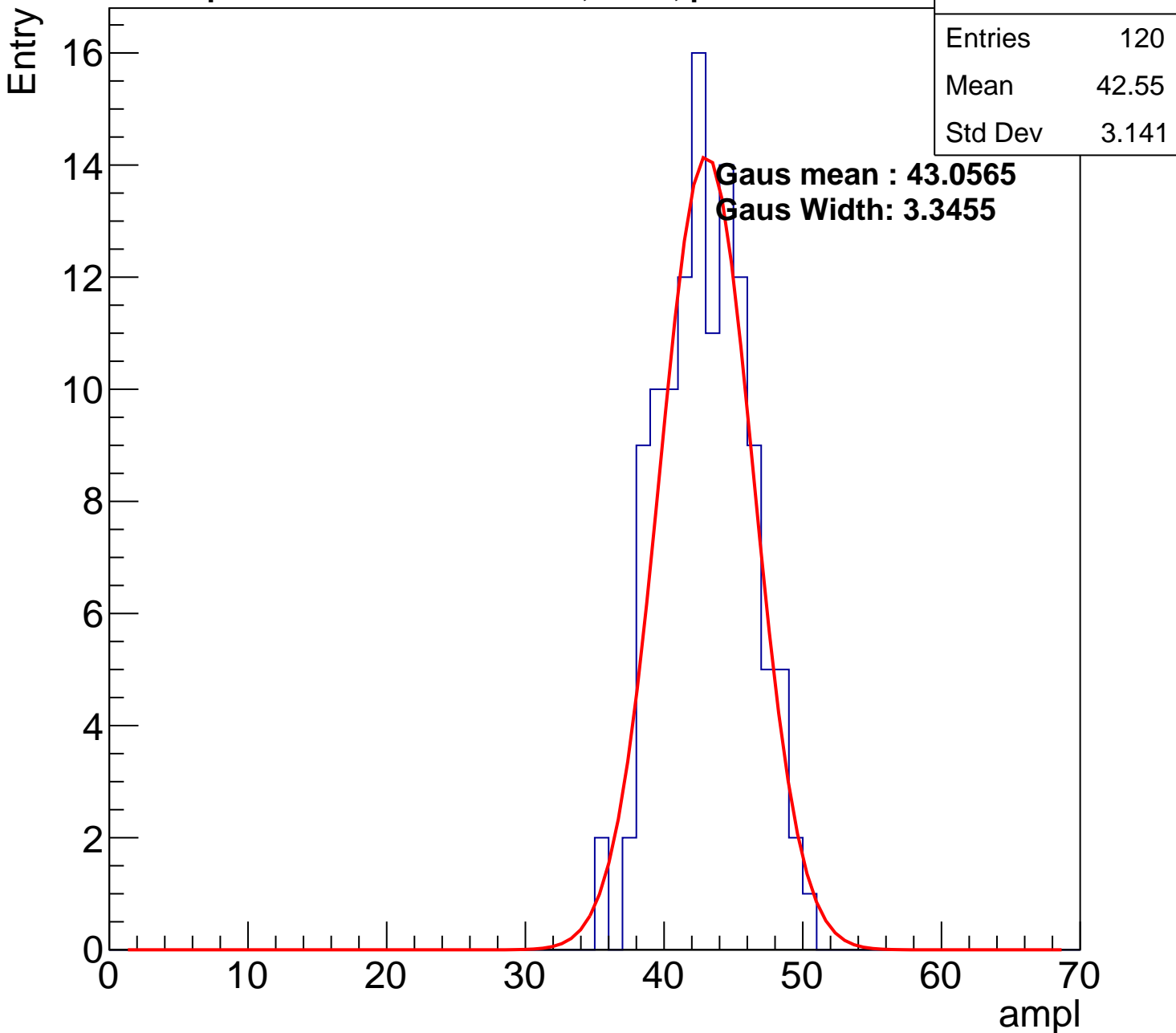
Gaus Width: 3.3455

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

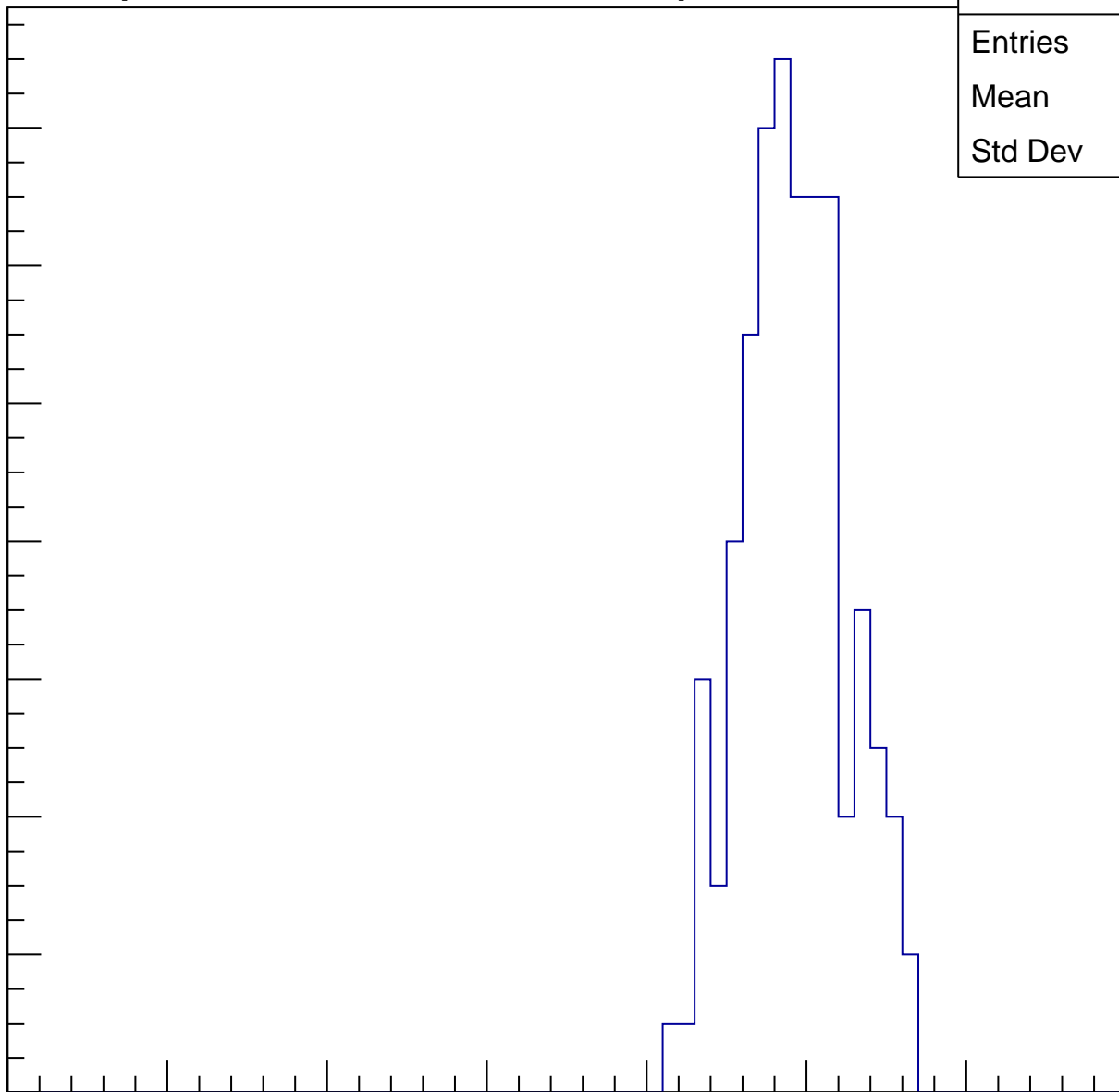
| | |
|---------|-------|
| Entries | 120 |
| Mean | 48.73 |
| Std Dev | 3.258 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch88, adc4

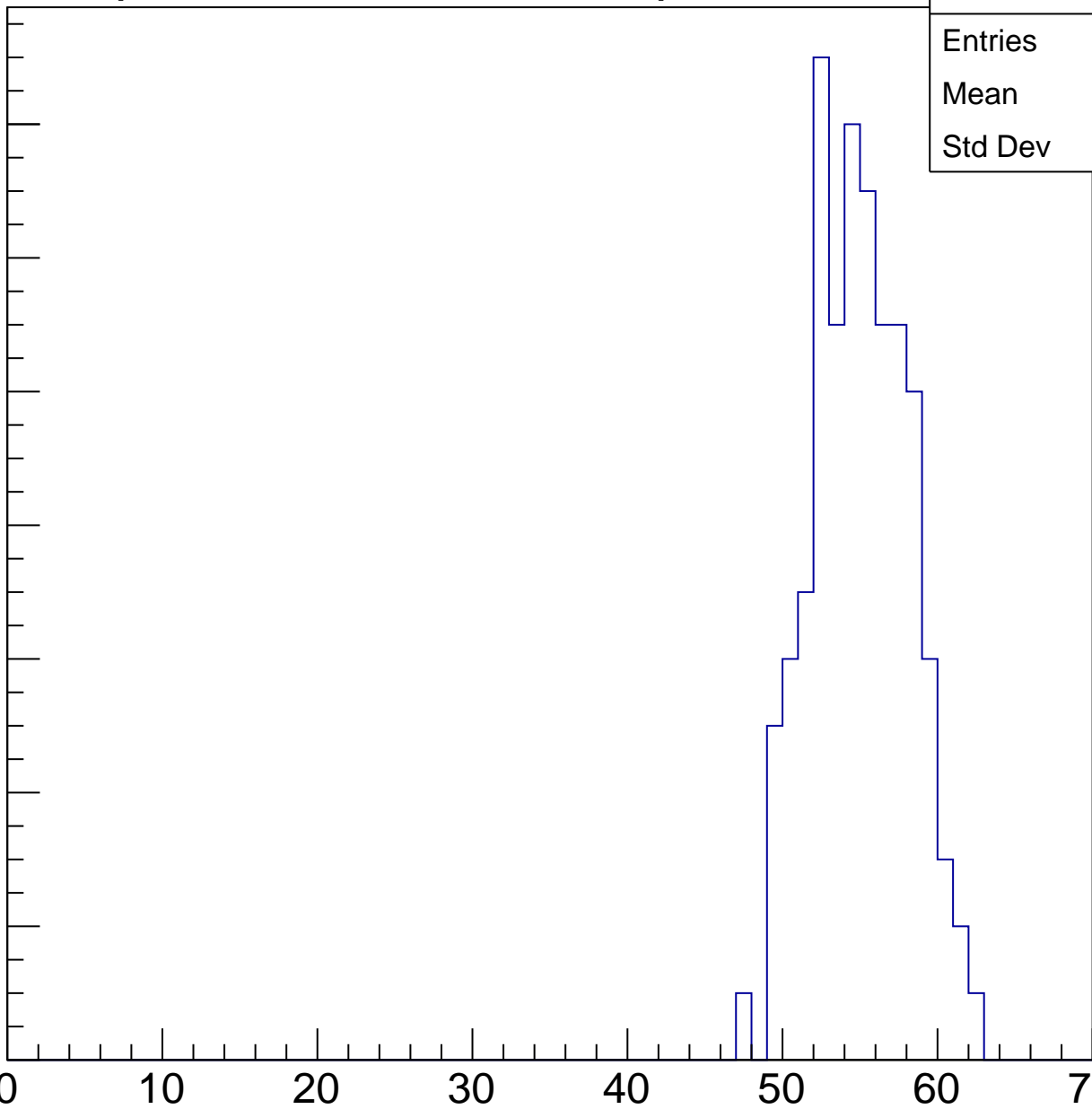
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 116 |
| Mean | 54.52 |
| Std Dev | 3.092 |

ampl



B1L001S, U26-ch88, adc5

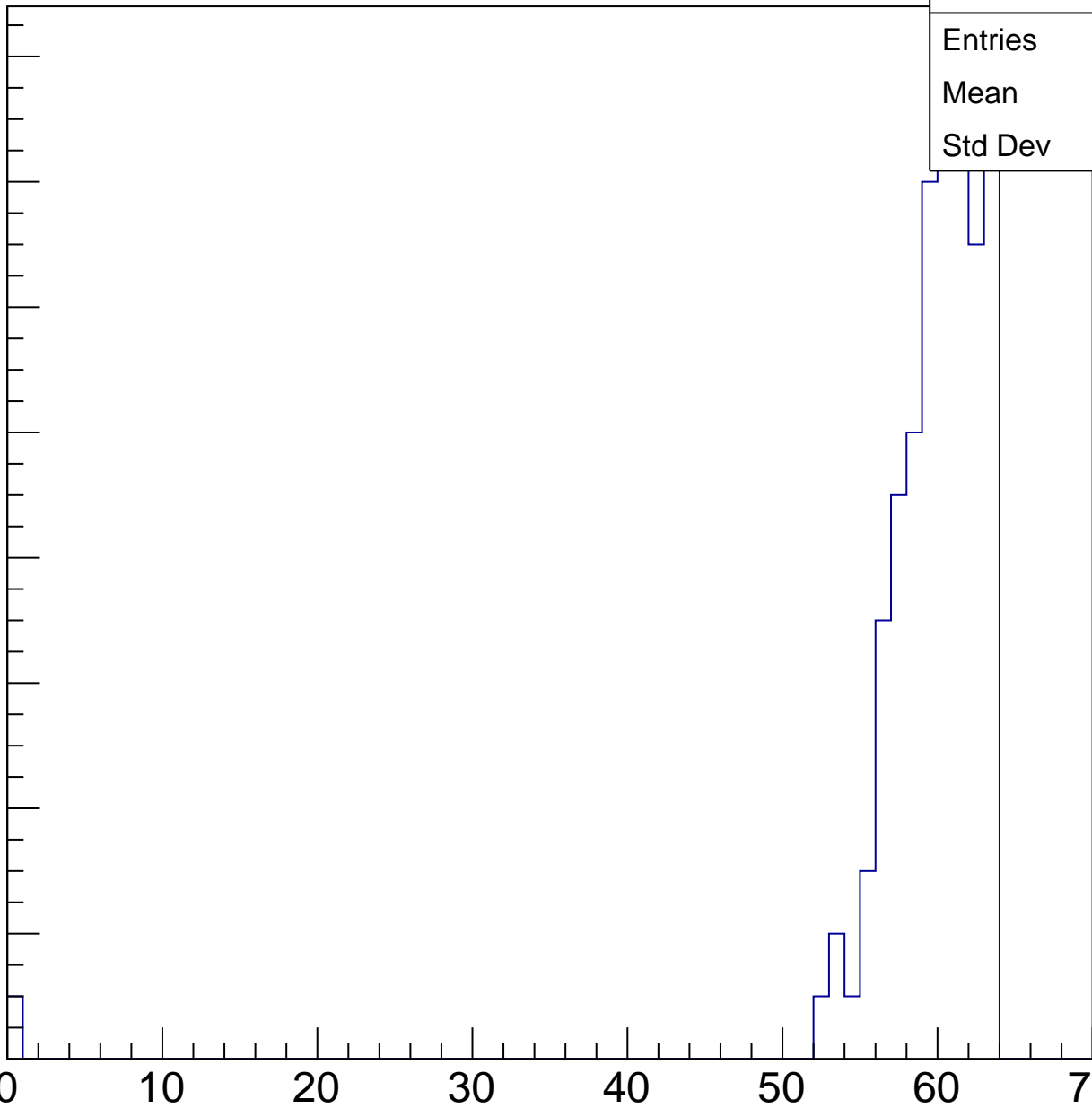
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 107 |
| Mean | 59.01 |
| Std Dev | 6.278 |

ampl

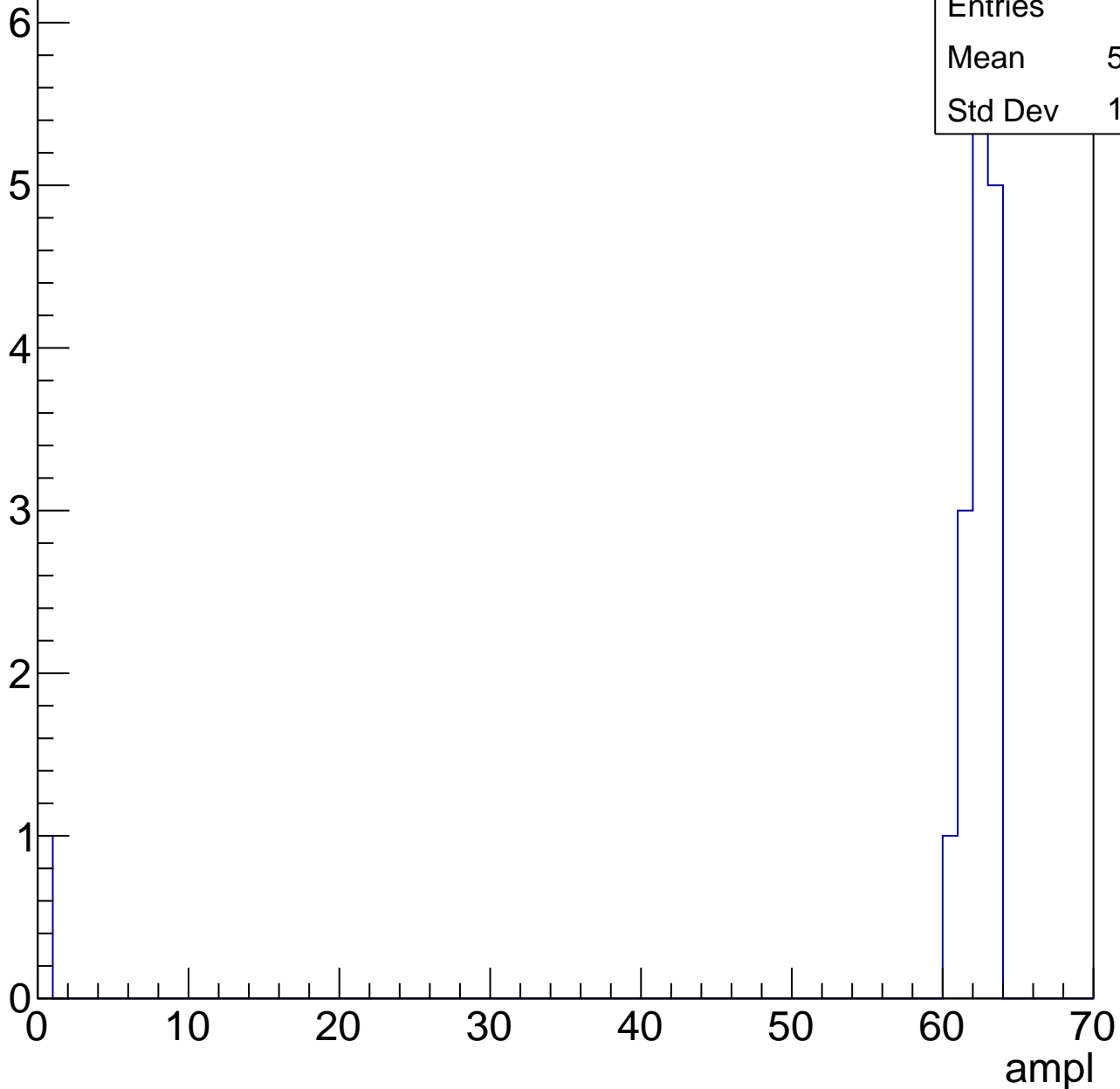


B1L001S, U26-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

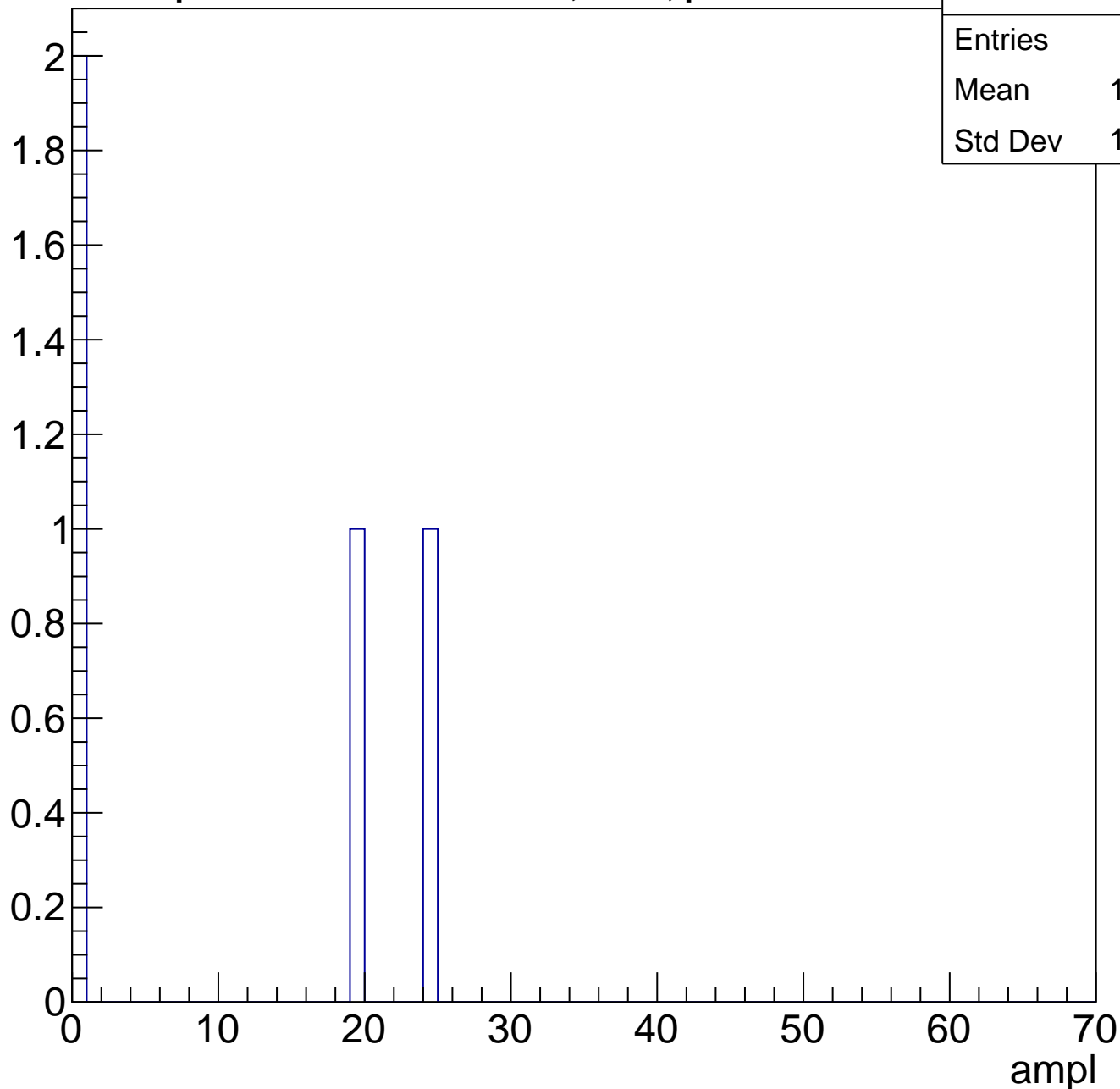
| | |
|---------|-------|
| Entries | 16 |
| Mean | 58.12 |
| Std Dev | 15.03 |



B1L001S, U26-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 10.75 |
| Std Dev | 10.89 |

B1L001S, U26-ch89, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

164

Mean

28.74

Std Dev

3.652

Gaus mean : 29.1513

Gaus Width: 3.8971

0

10

20

30

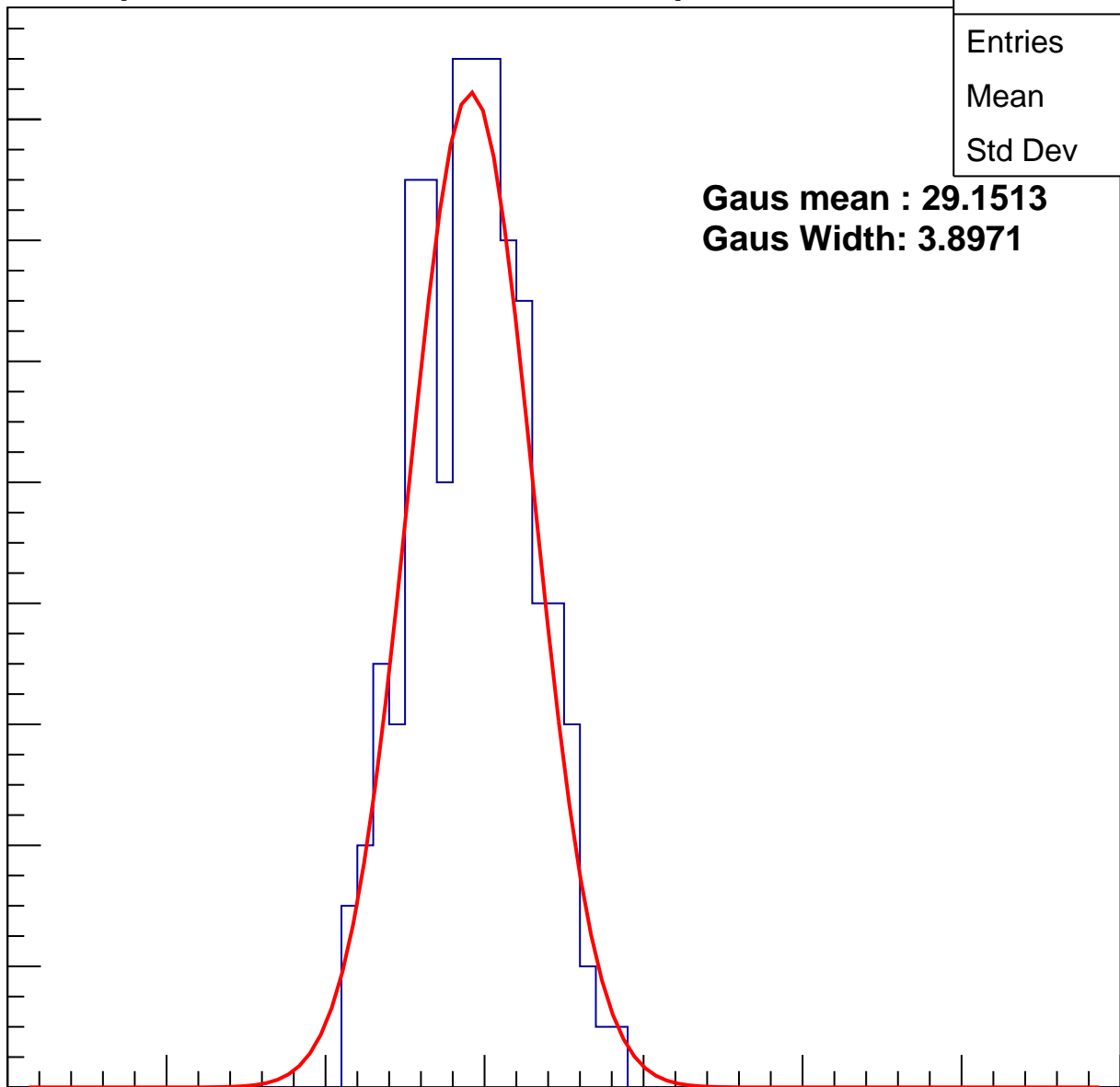
40

50

60

70

ampl



B1L001S, U26-ch89, adc1

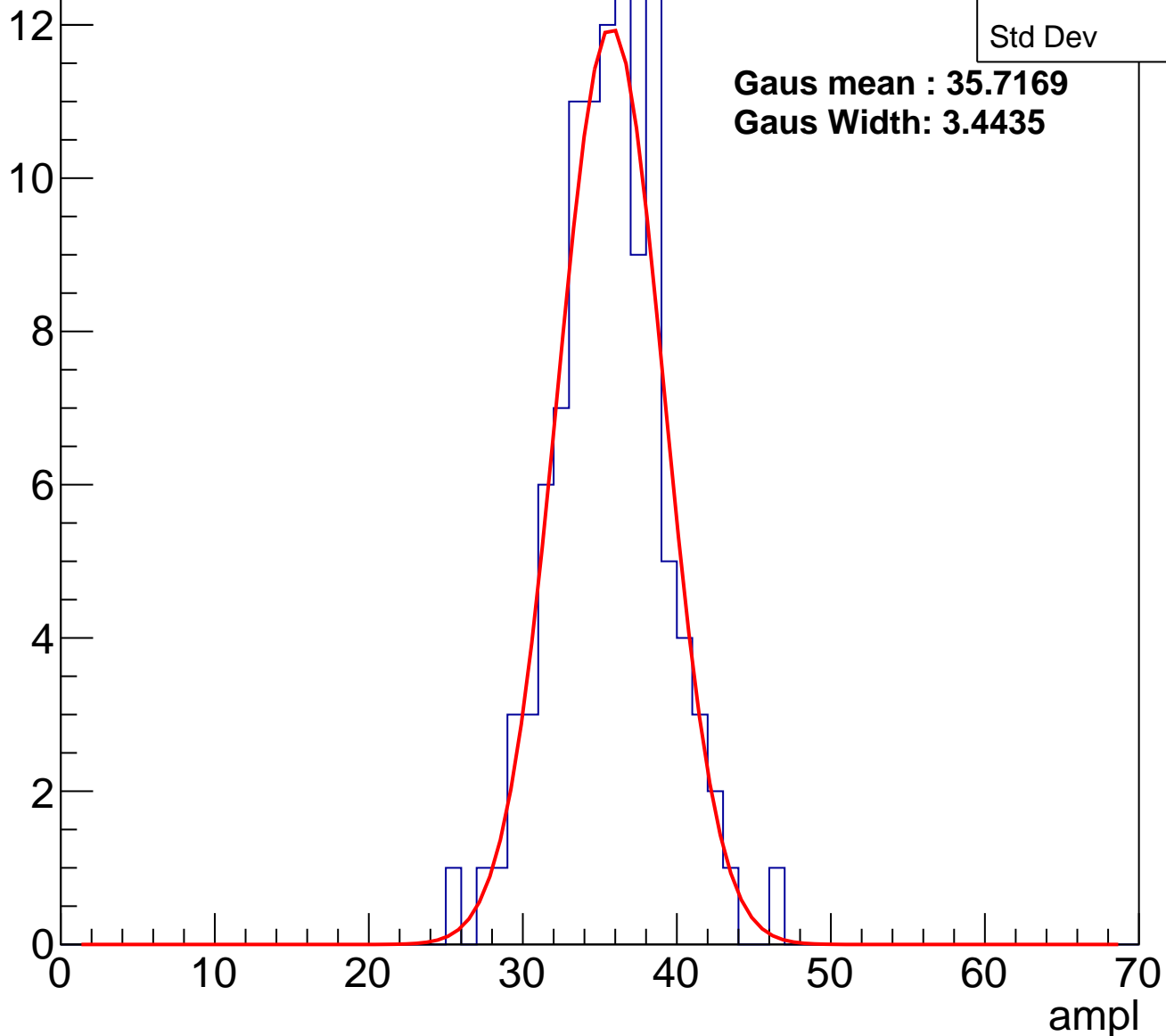
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 107 |
| Mean | 35.23 |
| Std Dev | 3.54 |

Gaus mean : 35.7169

Gaus Width: 3.4435

Entry



B1L001S, U26-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 41.87 |
| Std Dev | 3.393 |

Gaus mean : 42.4829

Gaus Width: 3.5784

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

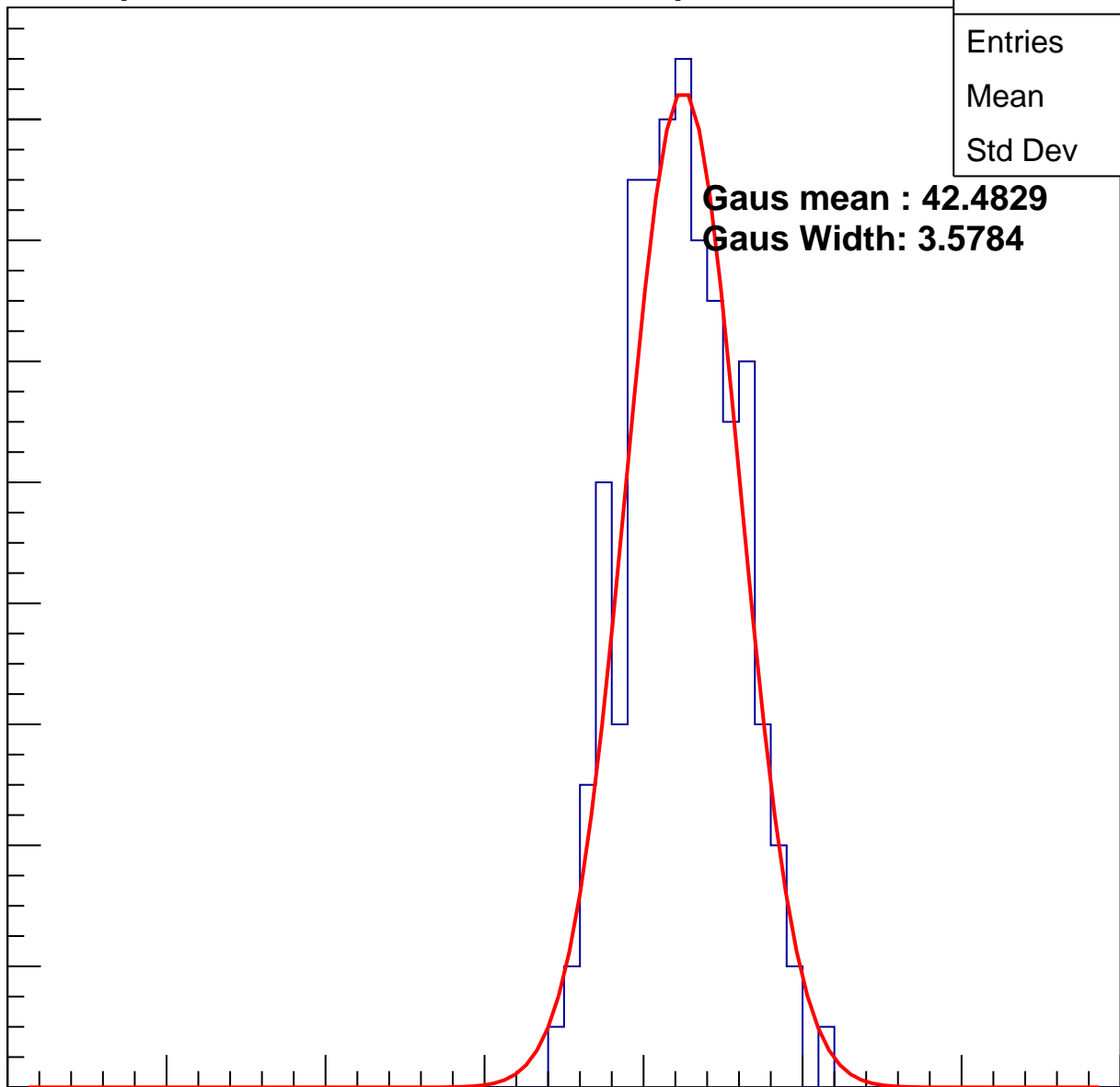
40

50

60

70

ampl



B1L001S, U26-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 48.43 |
| Std Dev | 3.095 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

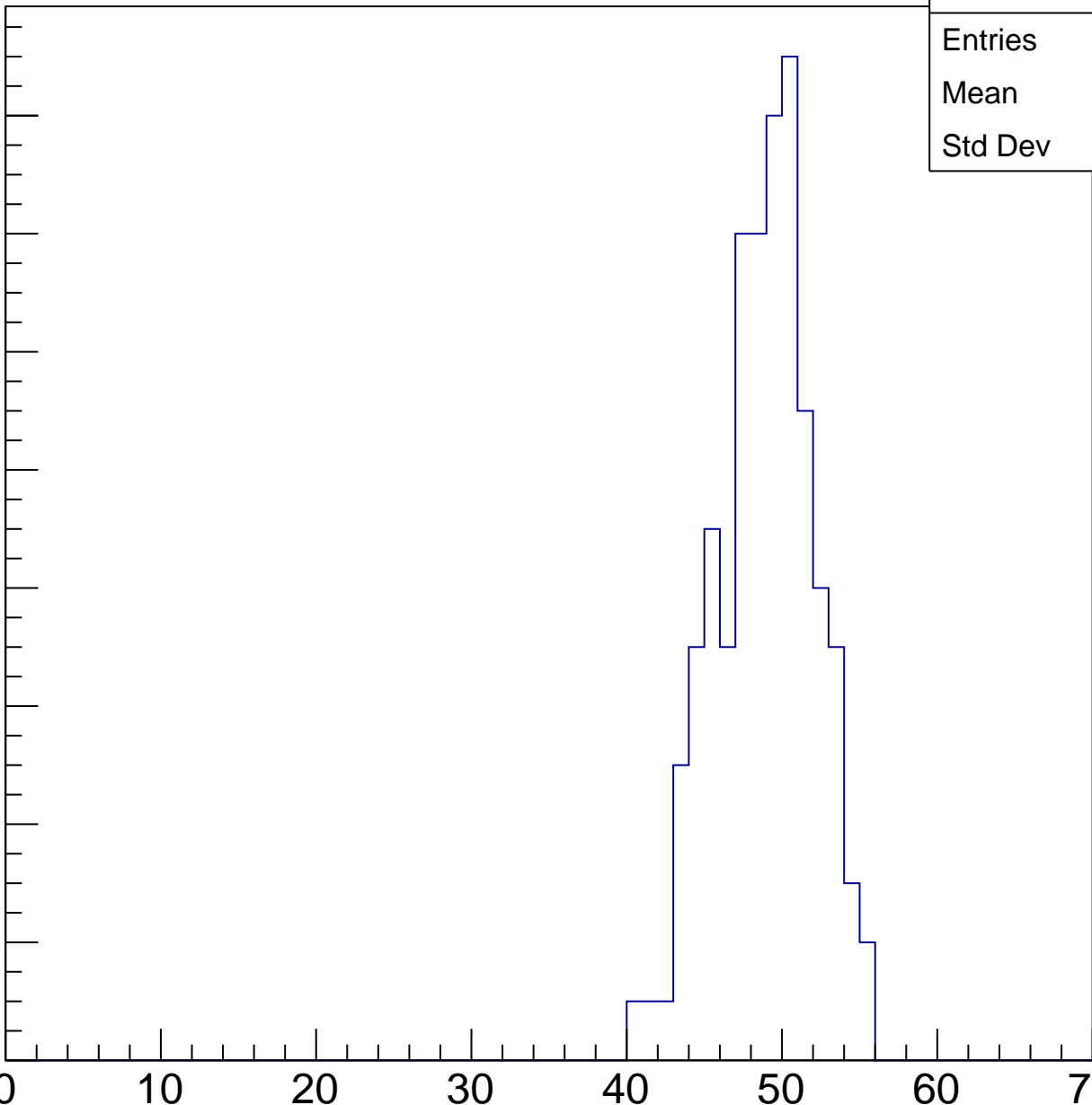
30

40

50

60

ampl



B1L001S, U26-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

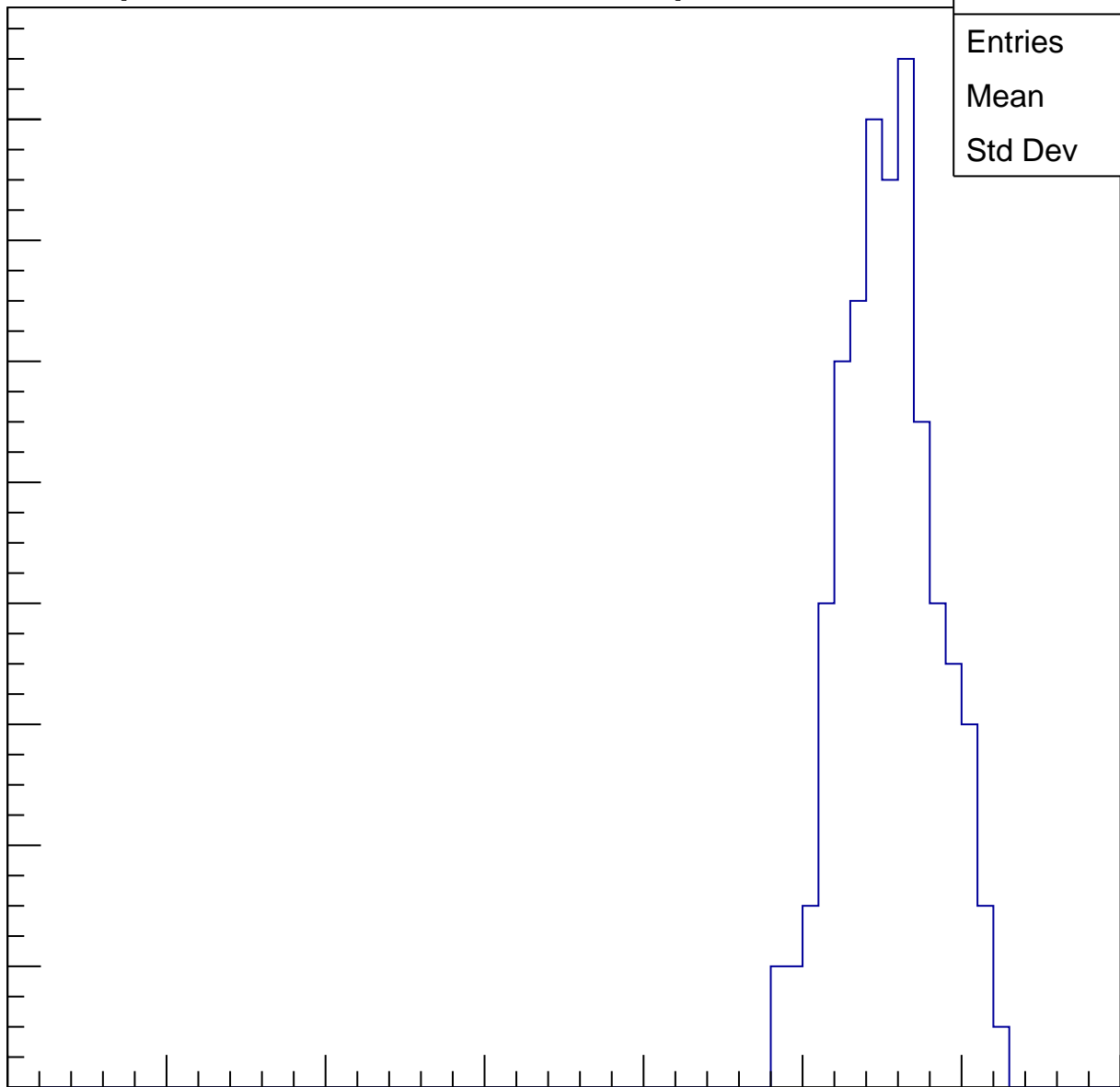
| | |
|---------|-------|
| Entries | 124 |
| Mean | 54.96 |
| Std Dev | 2.979 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch89, adc5

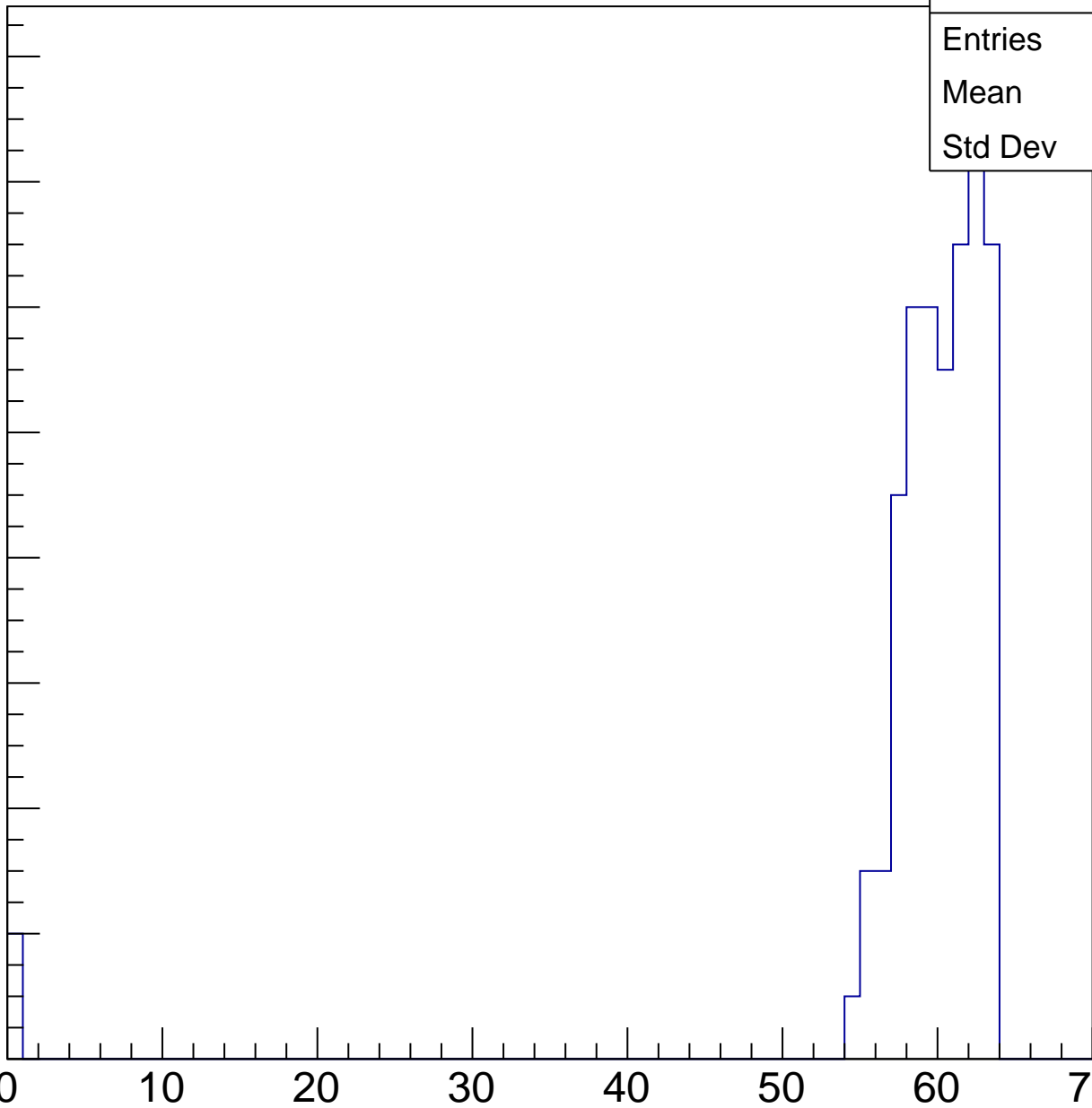
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 95 |
| Mean | 58.61 |
| Std Dev | 8.892 |

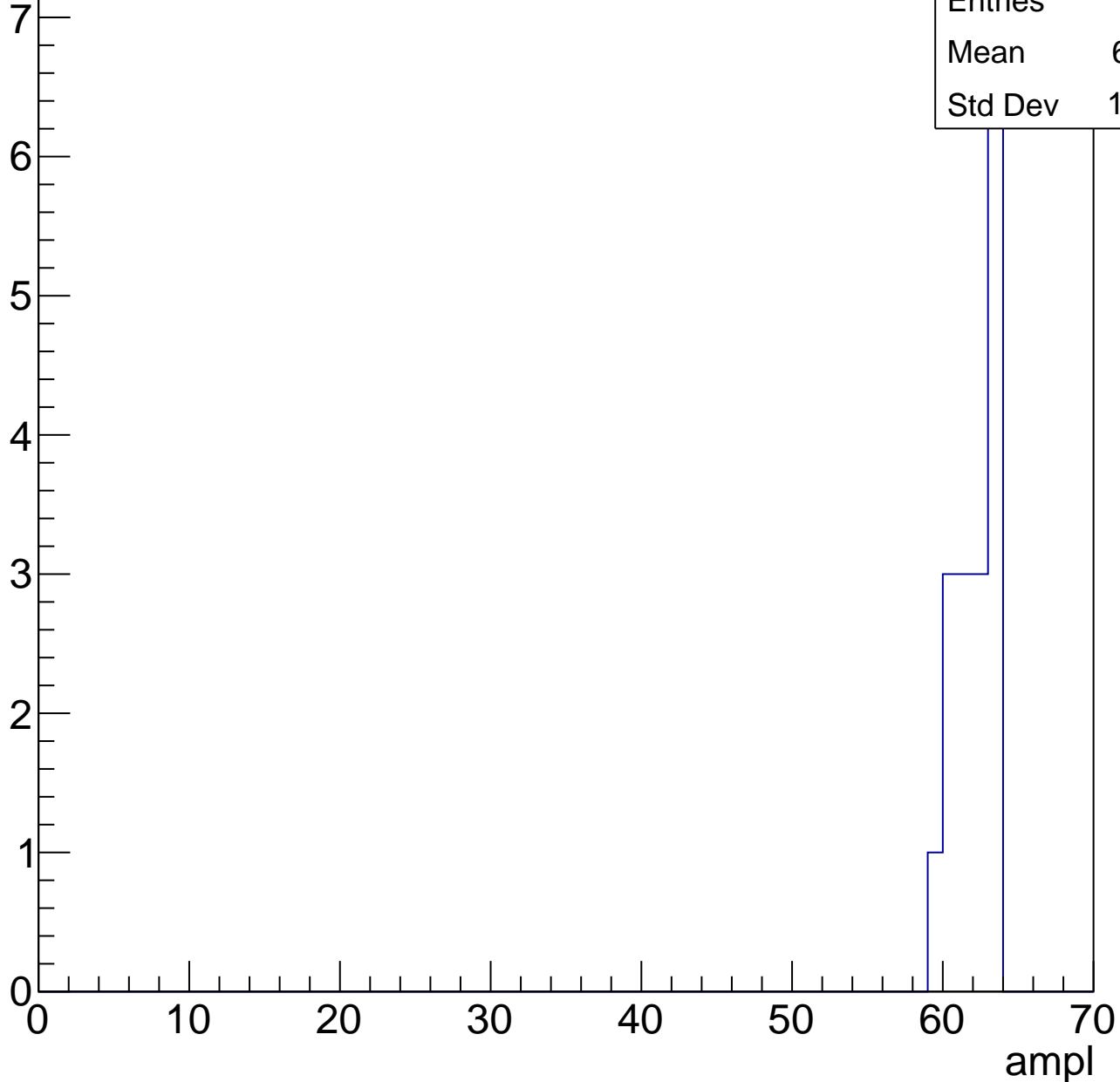
ampl



B1L001S, U26-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

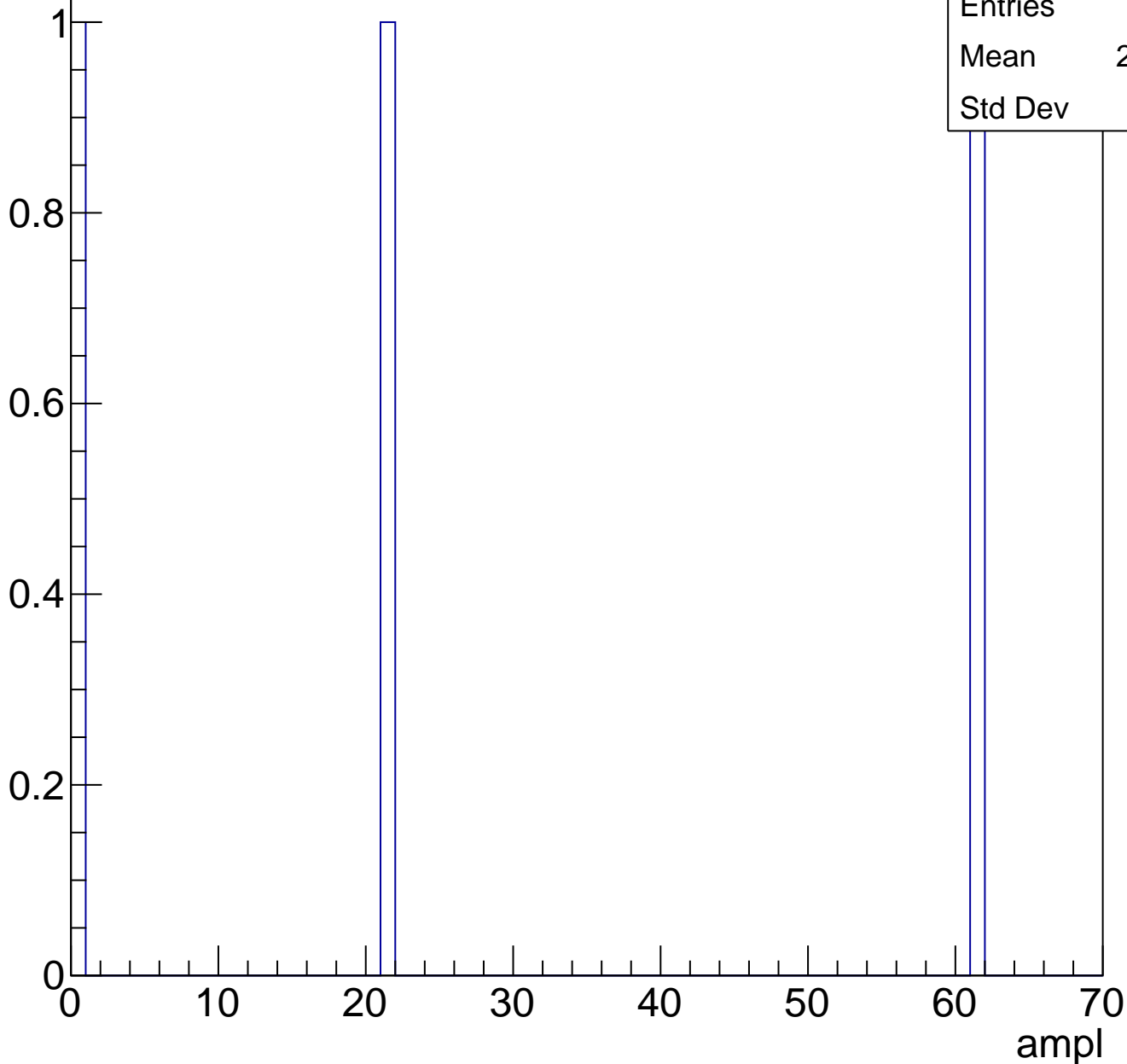


| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.71 |
| Std Dev | 1.318 |

B1L001S, U26-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch90, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 28.88 |
| Std Dev | 4.184 |

Gaus mean : 29.5164

Gaus Width: 3.2343

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

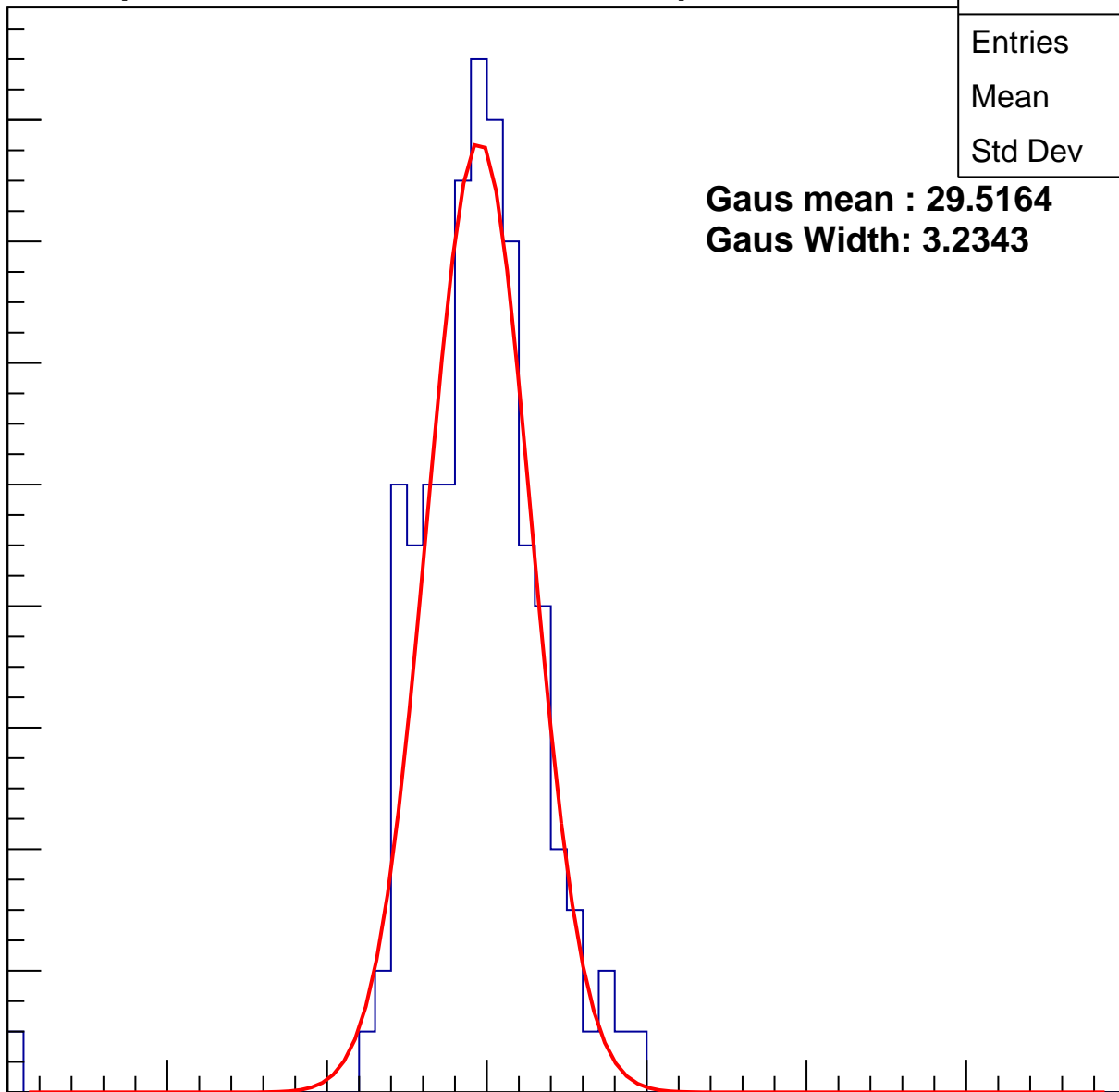
40

50

60

70

ampl



B1L001S, U26-ch90, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 36.01 |
| Std Dev | 3.418 |

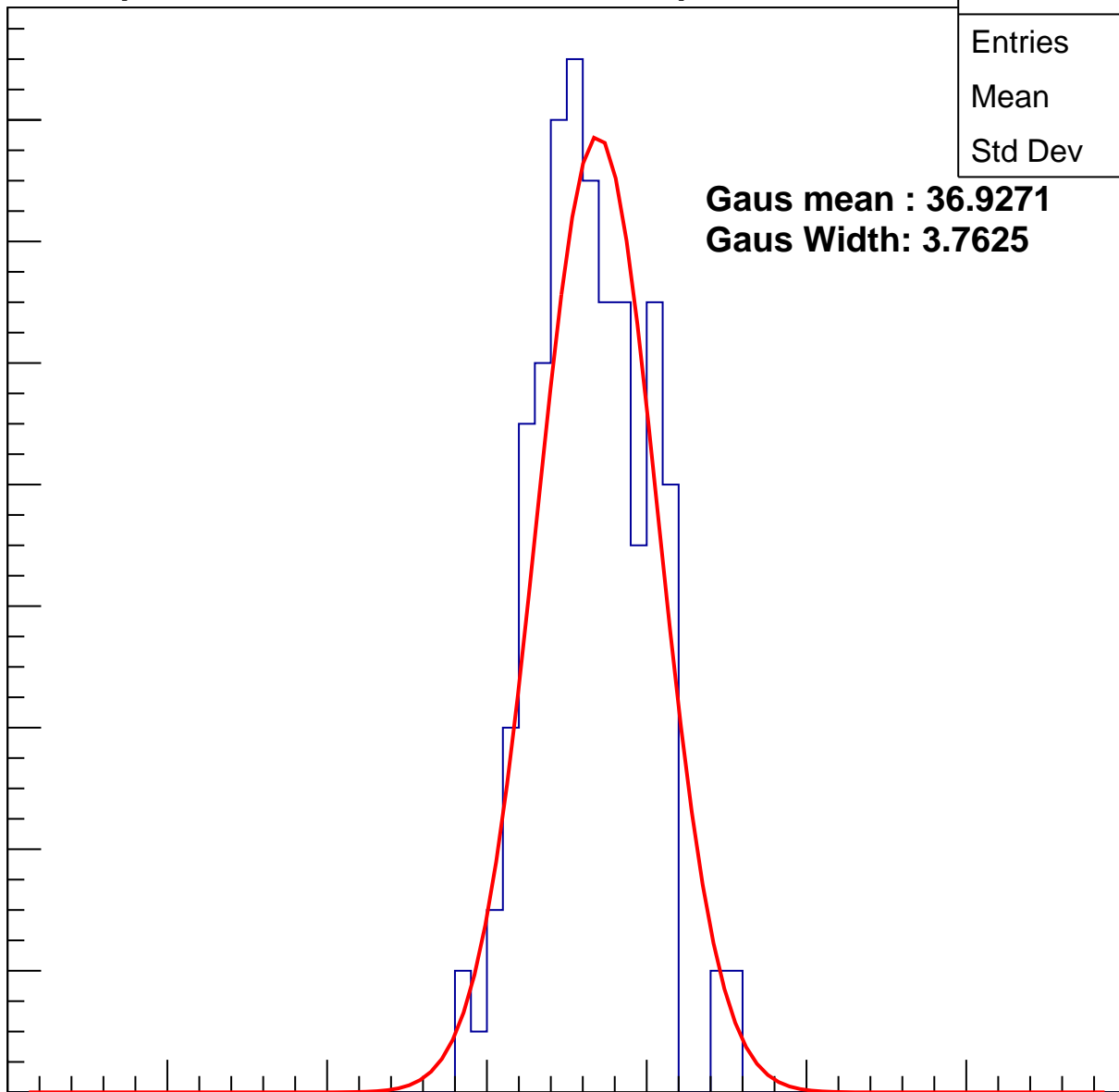
Gaus mean : 36.9271
Gaus Width: 3.7625

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 113 |
| Mean | 42.47 |
| Std Dev | 3.272 |

Gaus mean : 43.2020

Gaus Width: 4.0837

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch90, adc3

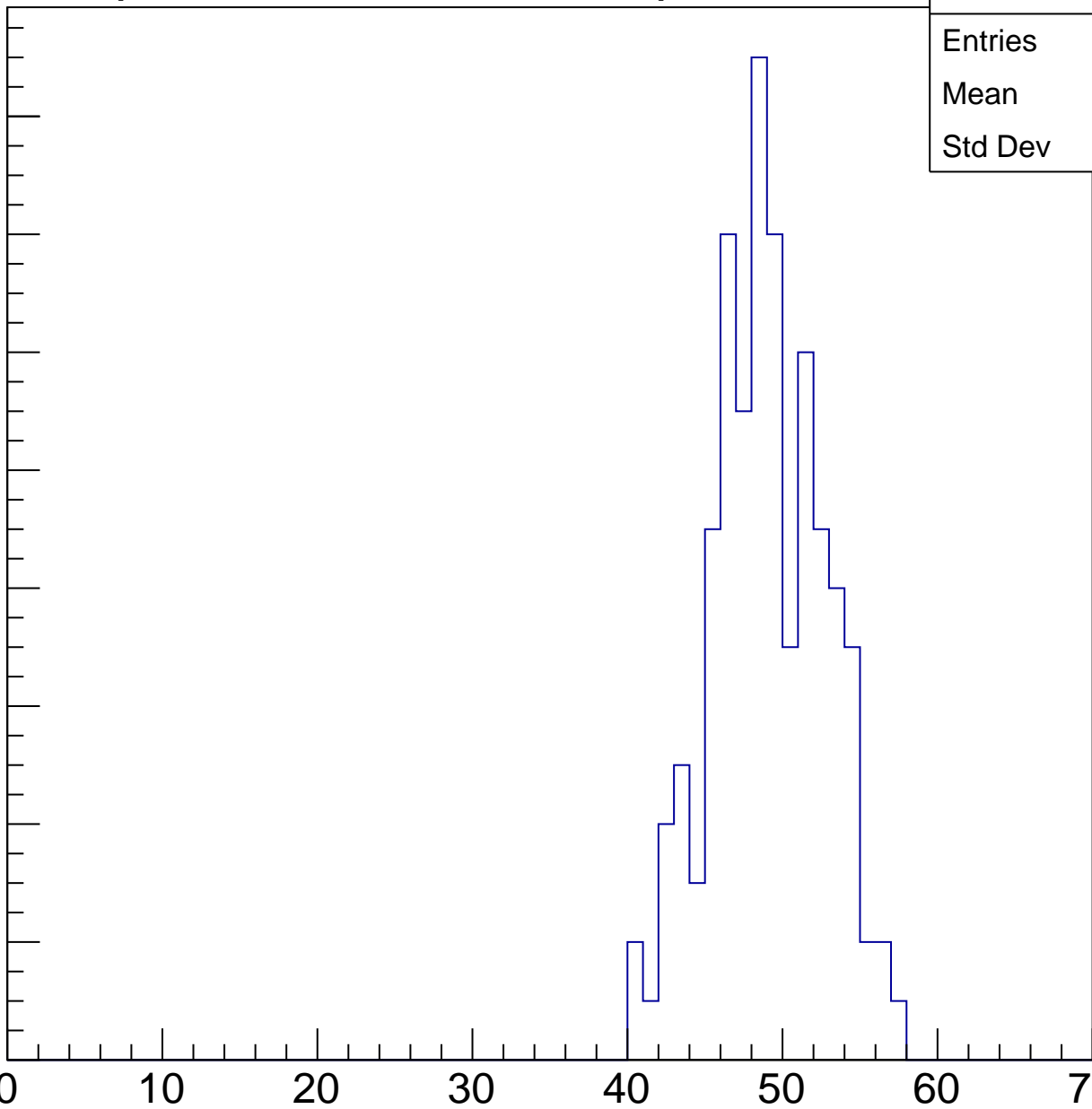
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 128 |
| Mean | 48.55 |
| Std Dev | 3.596 |

ampl

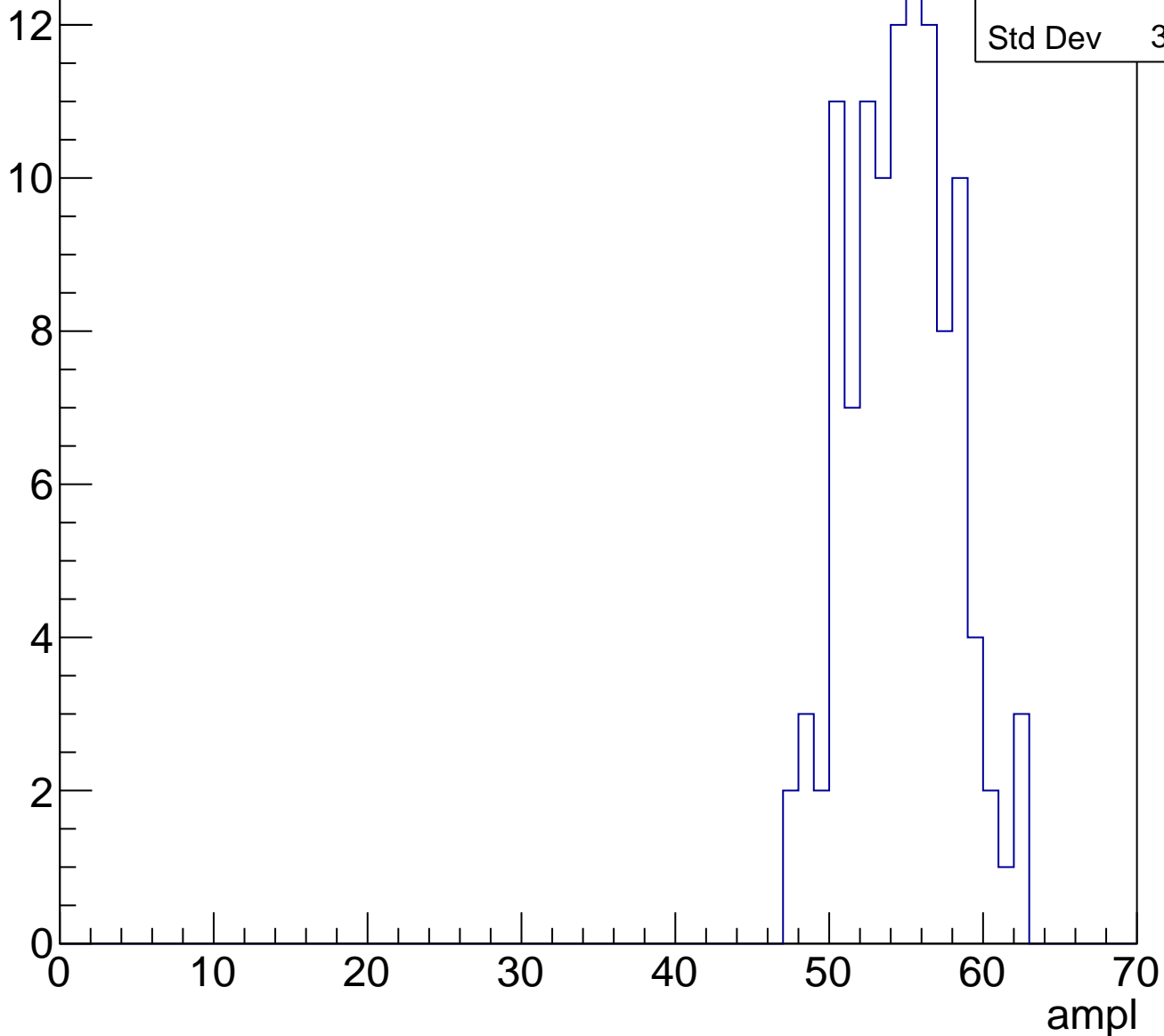


B1L001S, U26-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 111 |
| Mean | 54.23 |
| Std Dev | 3.355 |

Entry



B1L001S, U26-ch90, adc5

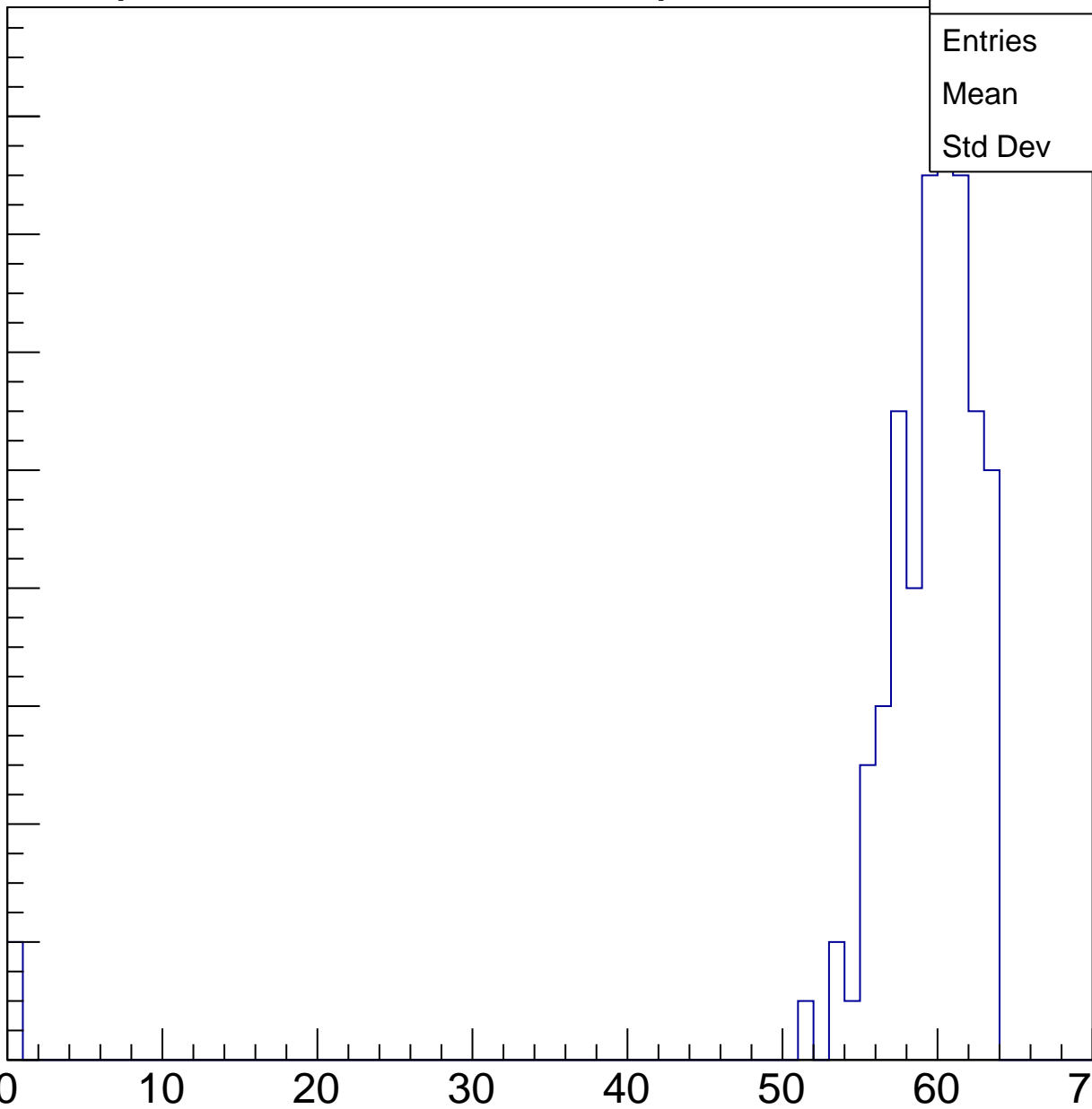
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 104 |
| Mean | 58.12 |
| Std Dev | 8.529 |

ampl



B1L001S, U26-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 24 |
| Mean | 61.71 |
| Std Dev | 1.541 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L001S, U26-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B1L001S, U26-ch91, adc0

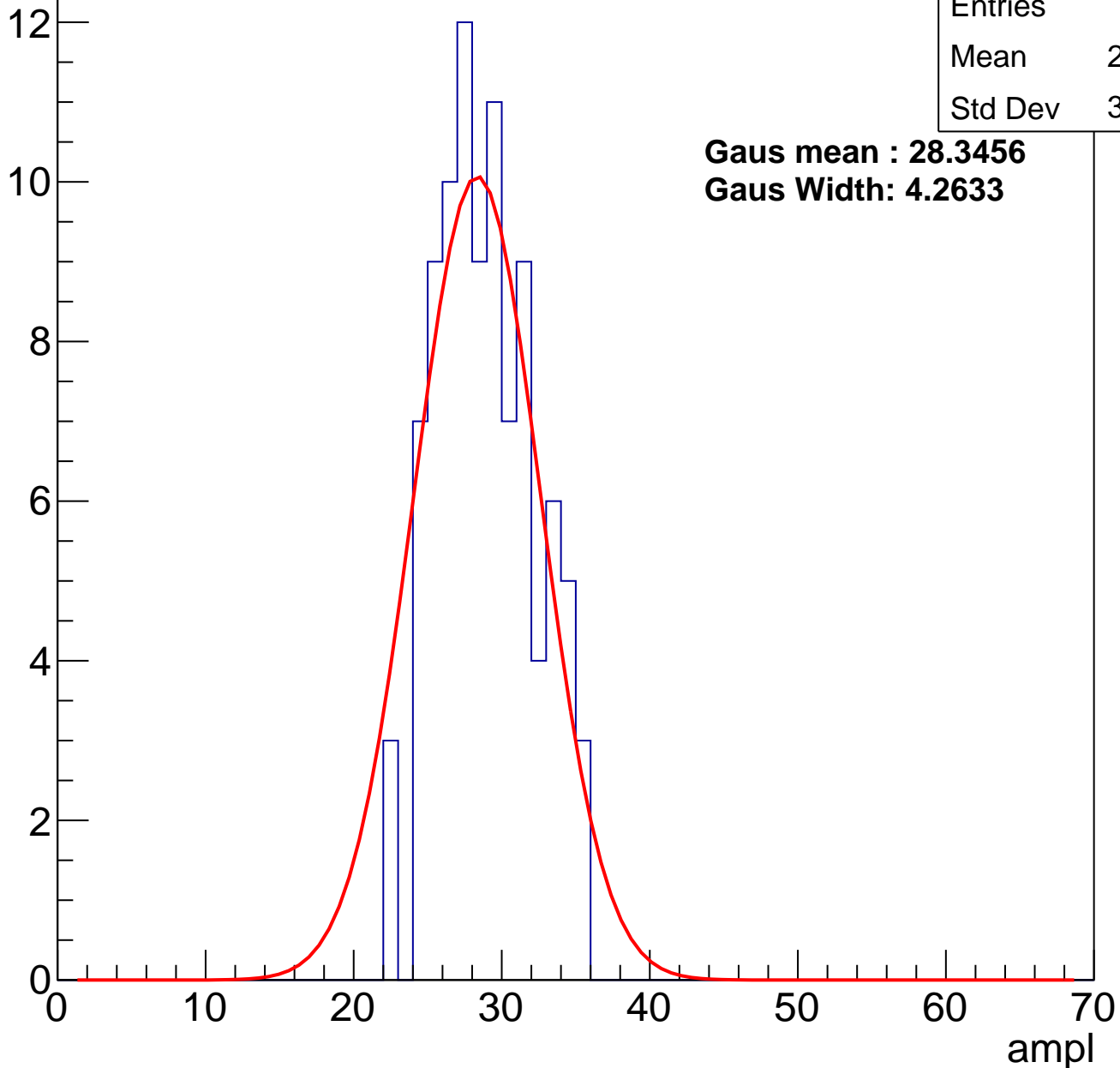
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 95 |
| Mean | 28.46 |
| Std Dev | 3.234 |

Gaus mean : 28.3456

Gaus Width: 4.2633

Entry

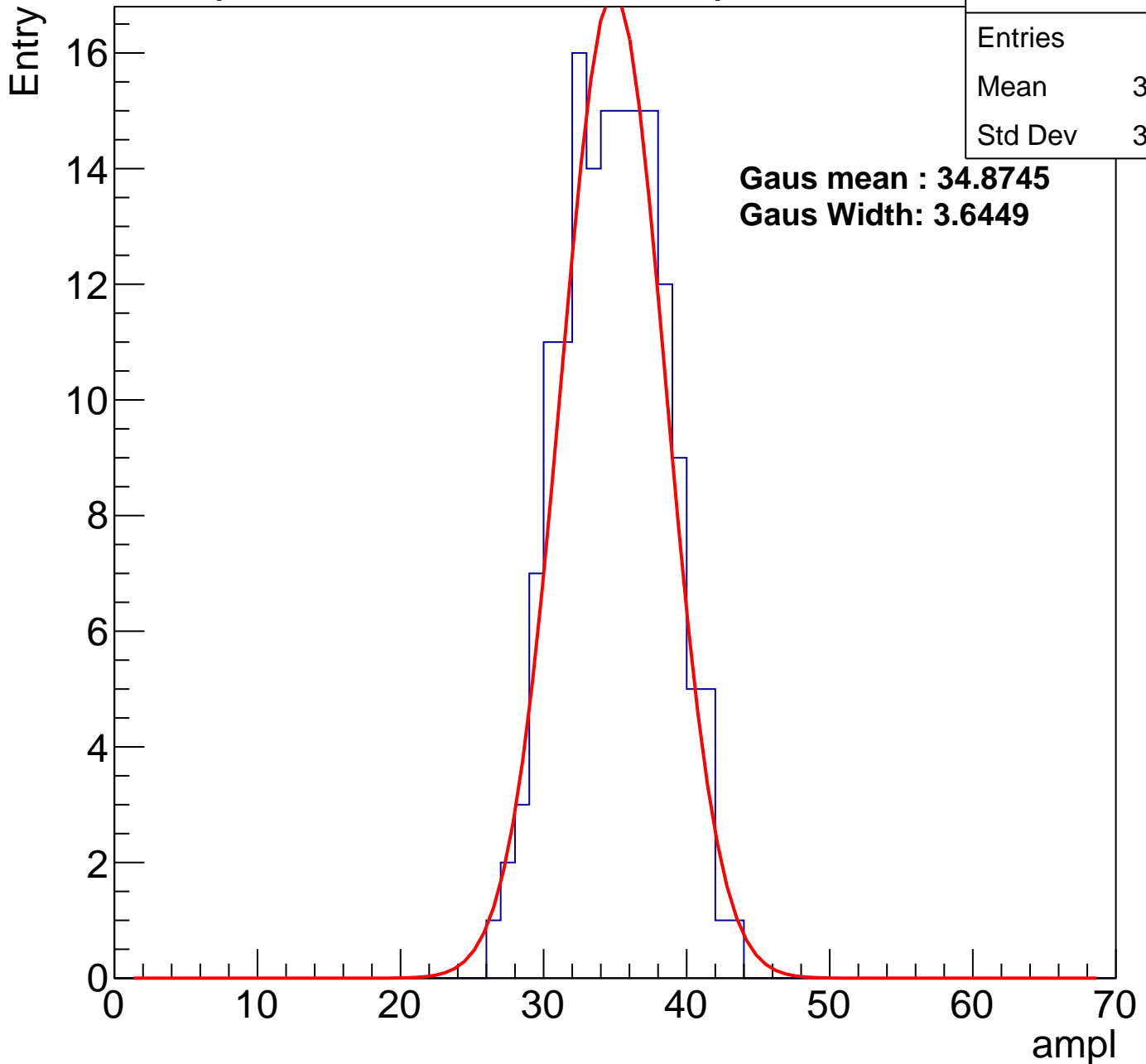


B1L001S, U26-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 34.42 |
| Std Dev | 3.528 |

Gaus mean : 34.8745
Gaus Width: 3.6449



B1L001S, U26-ch91, adc2

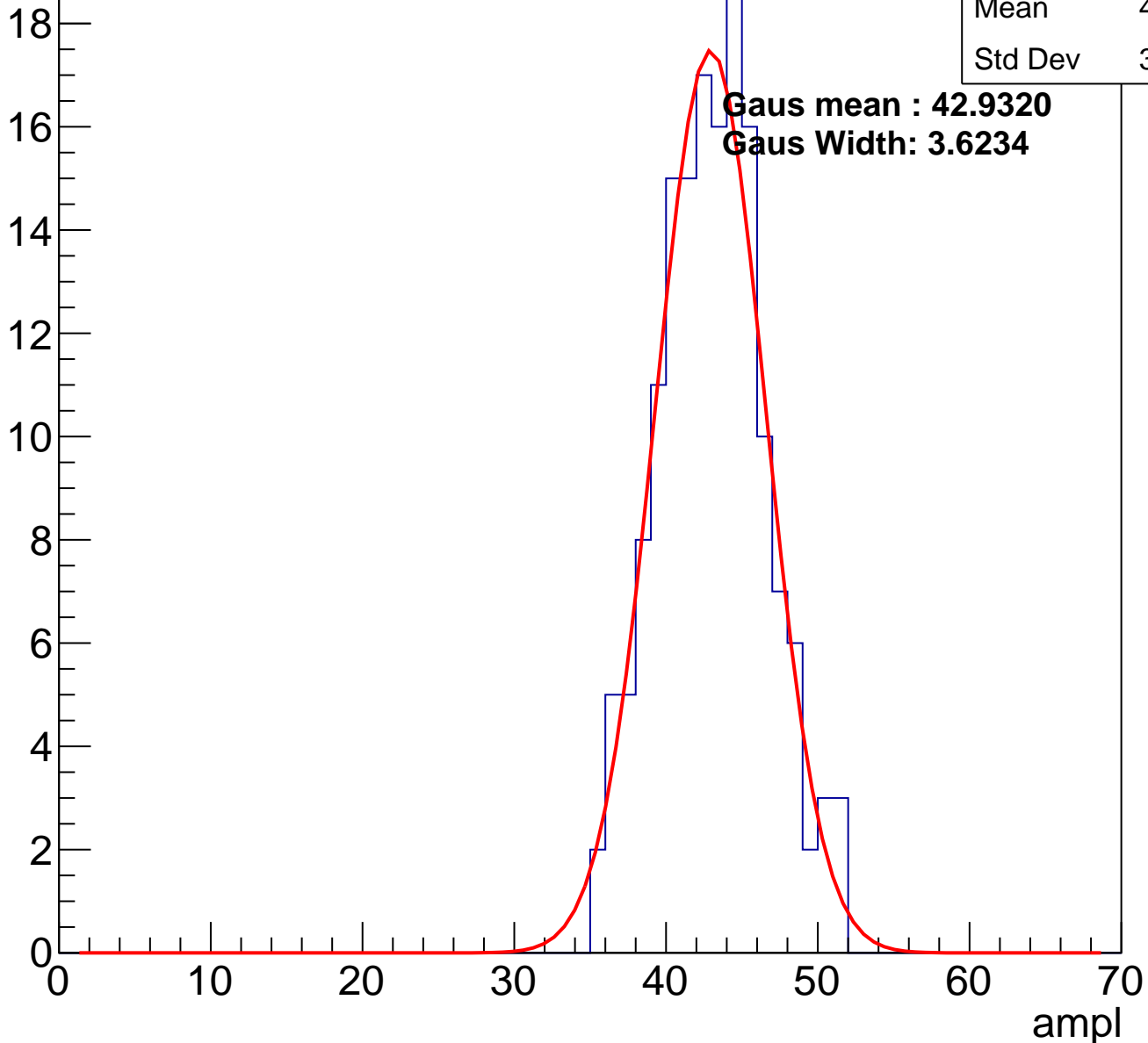
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 160 |
| Mean | 42.62 |
| Std Dev | 3.503 |

Gaus mean : 42.9320

Gaus Width: 3.6234

Entry



B1L001S, U26-ch91, adc3

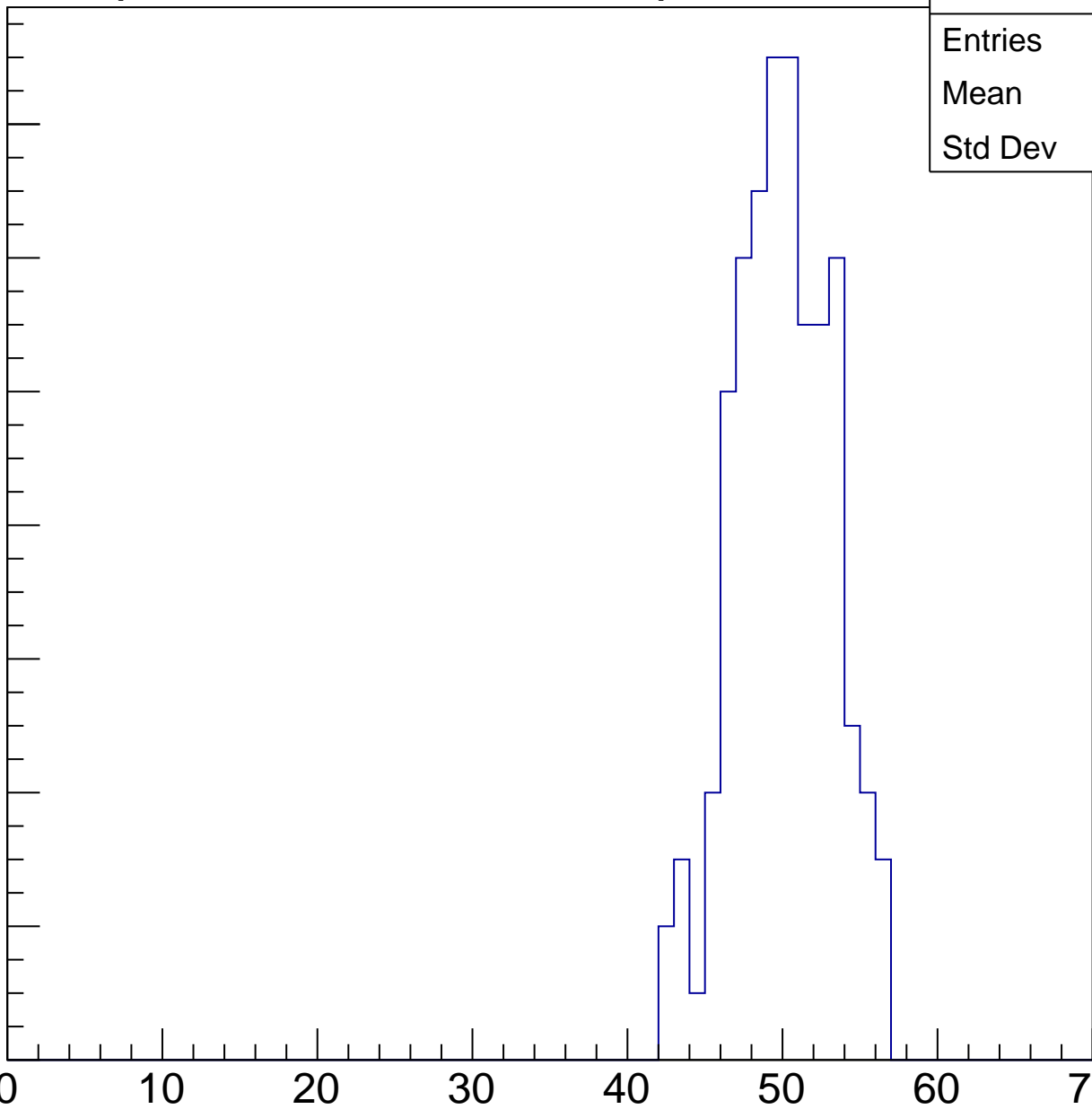
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 121 |
| Mean | 49.56 |
| Std Dev | 3.115 |

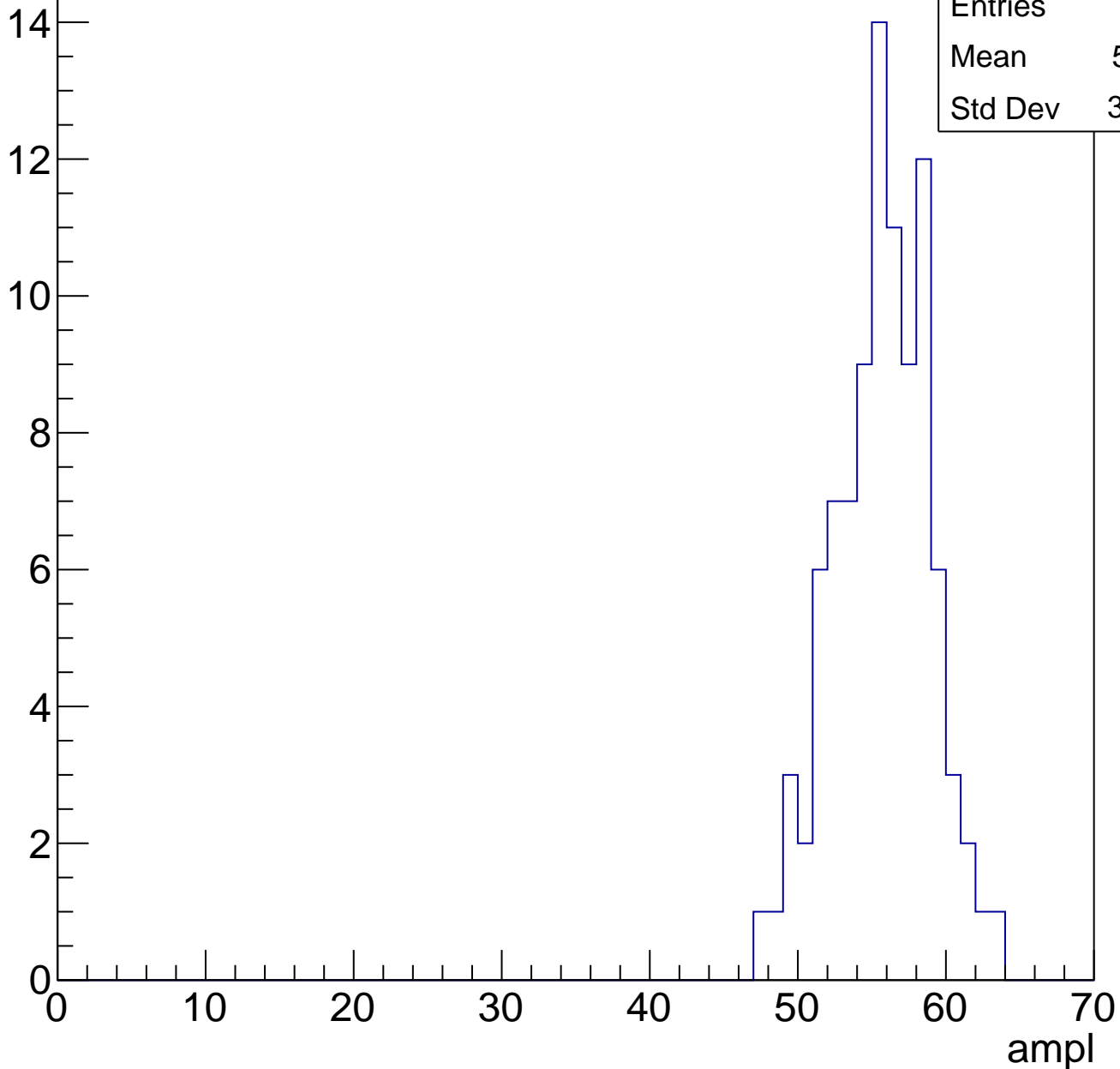
ampl



B1L001S, U26-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch91, adc5

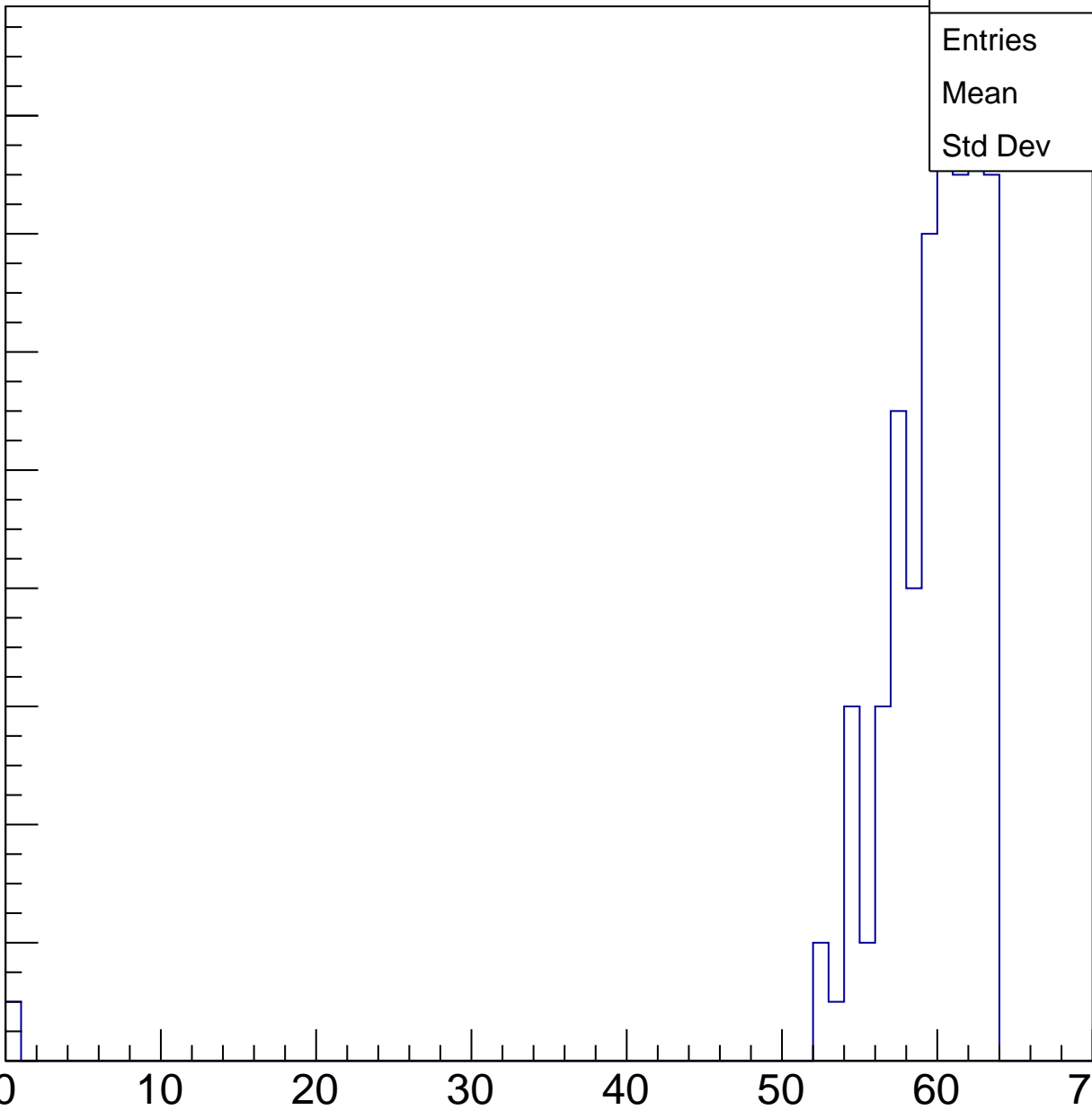
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 115 |
| Mean | 58.94 |
| Std Dev | 6.166 |

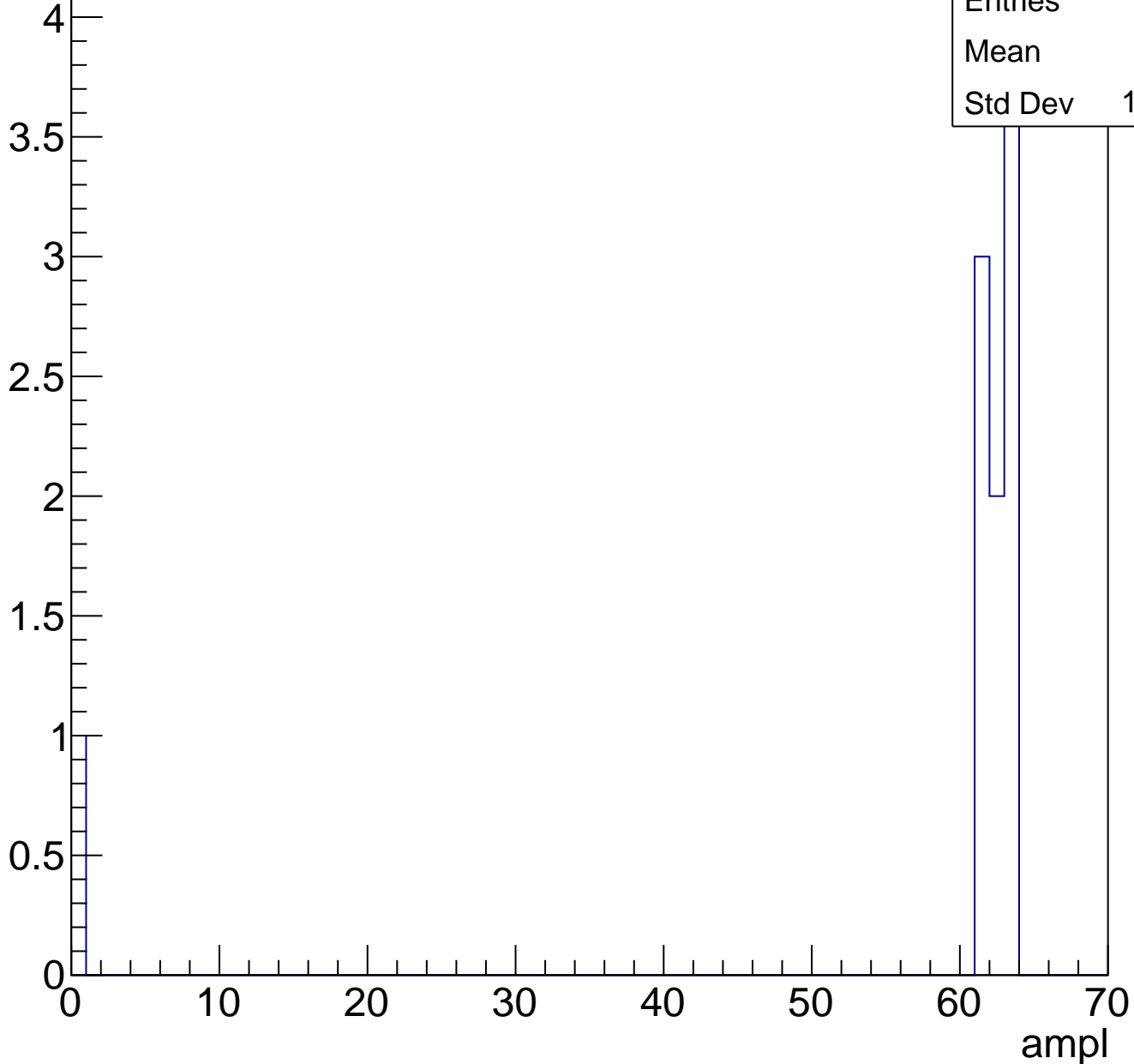
ampl



B1L001S, U26-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

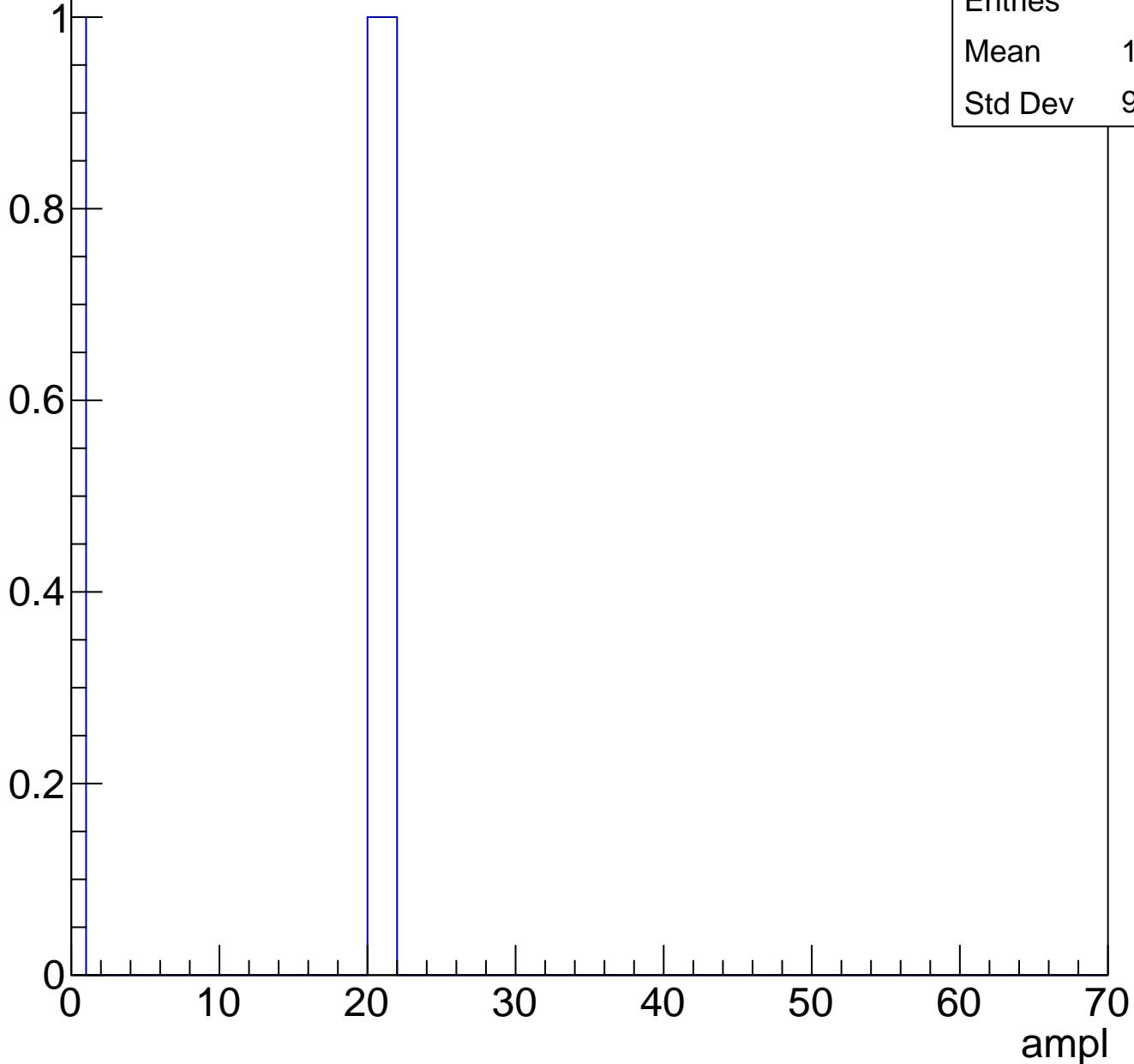
Entry



B1L001S, U26-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 13.67 |
| Std Dev | 9.672 |

B1L001S, U26-ch92, adc0

calib_packv5_042523_0143.root, FC#2, port C2

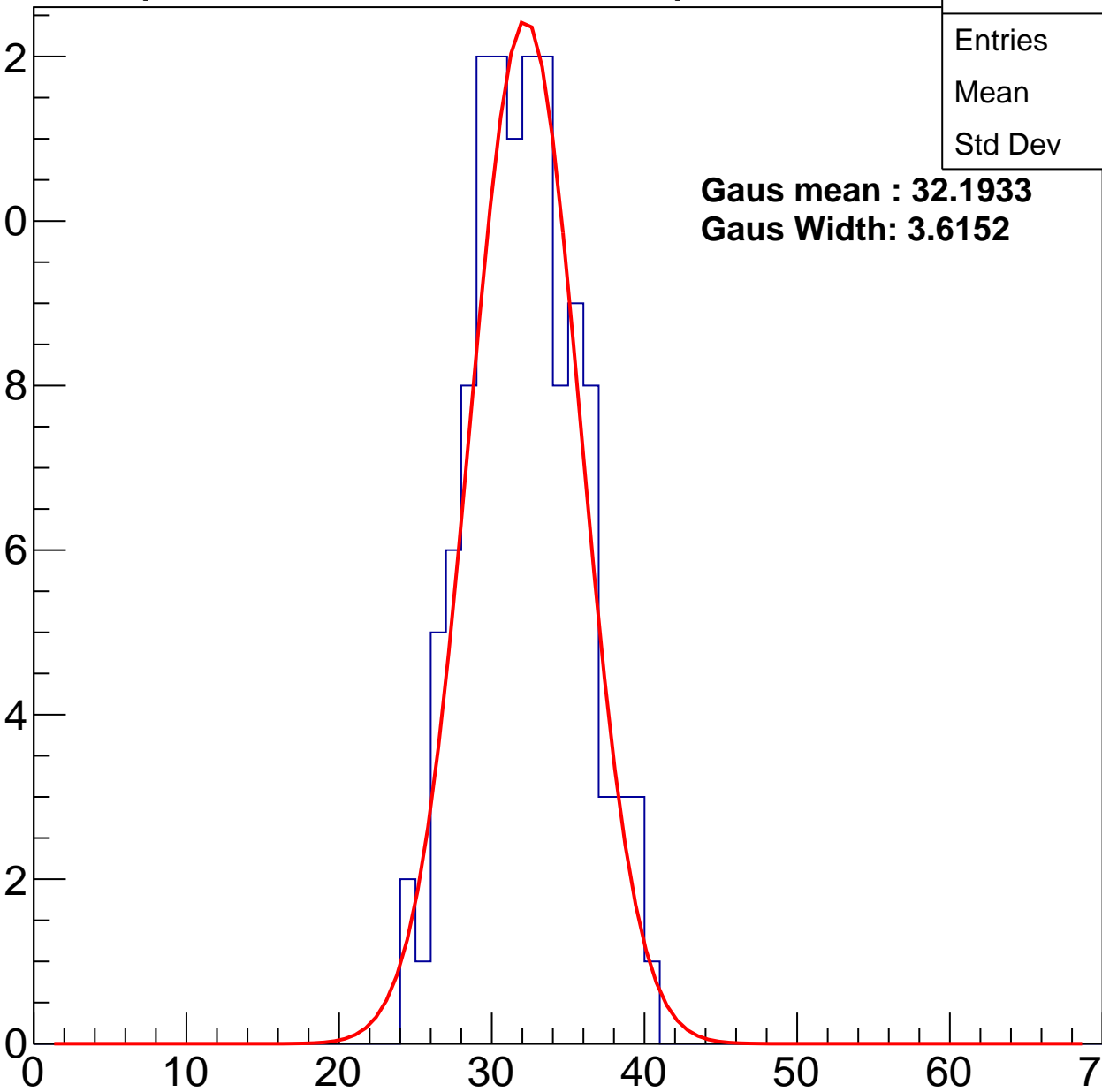
Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 116 |
| Mean | 31.68 |
| Std Dev | 3.52 |

Gaus mean : 32.1933
Gaus Width: 3.6152

ampl



B1L001S, U26-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 37.14 |
| Std Dev | 3.685 |

Gaus mean : 37.7930

Gaus Width: 3.6193

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

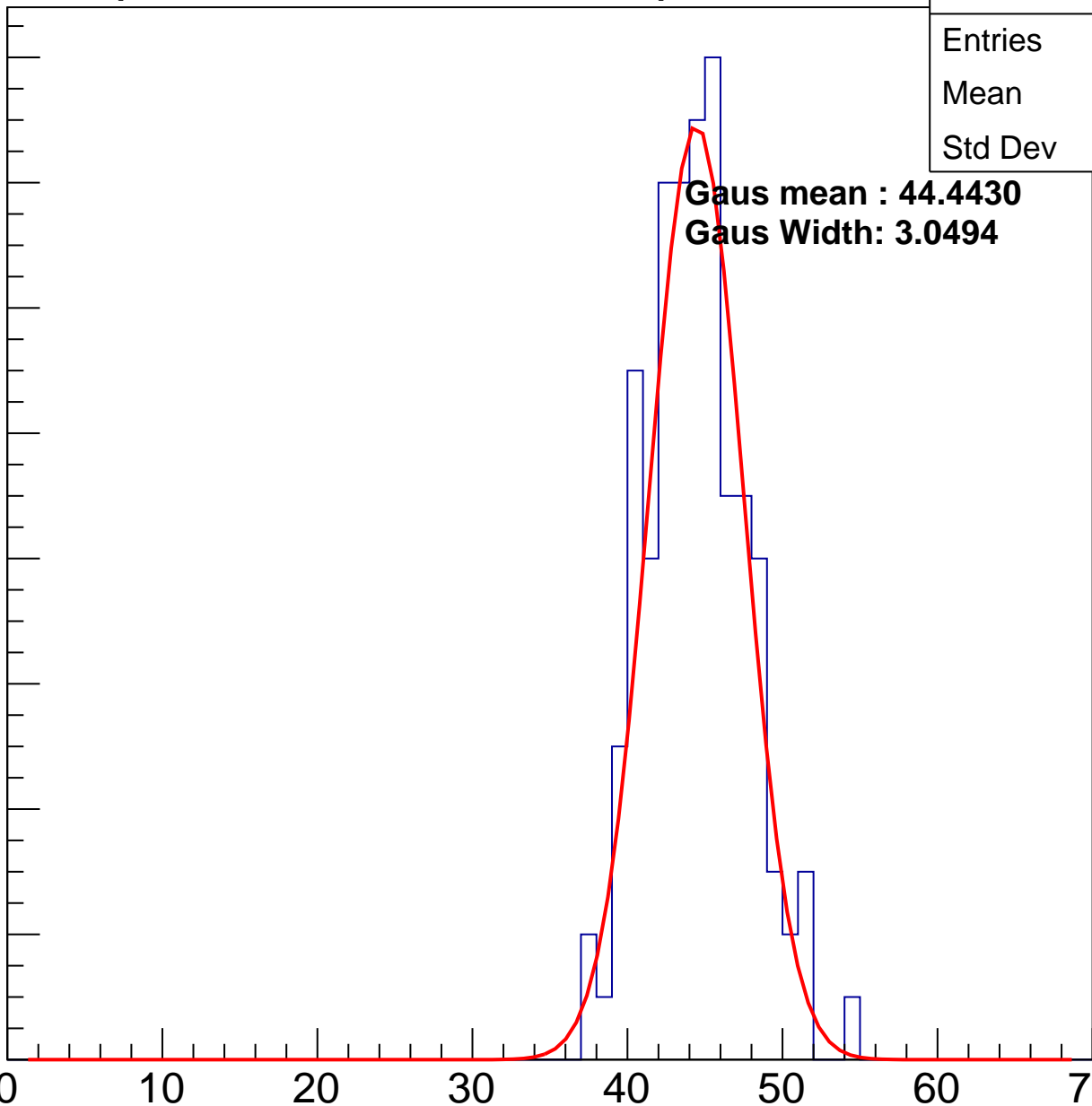
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 121 |
| Mean | 43.97 |
| Std Dev | 3.191 |

Gaus mean : 44.4430

Gaus Width: 3.0494

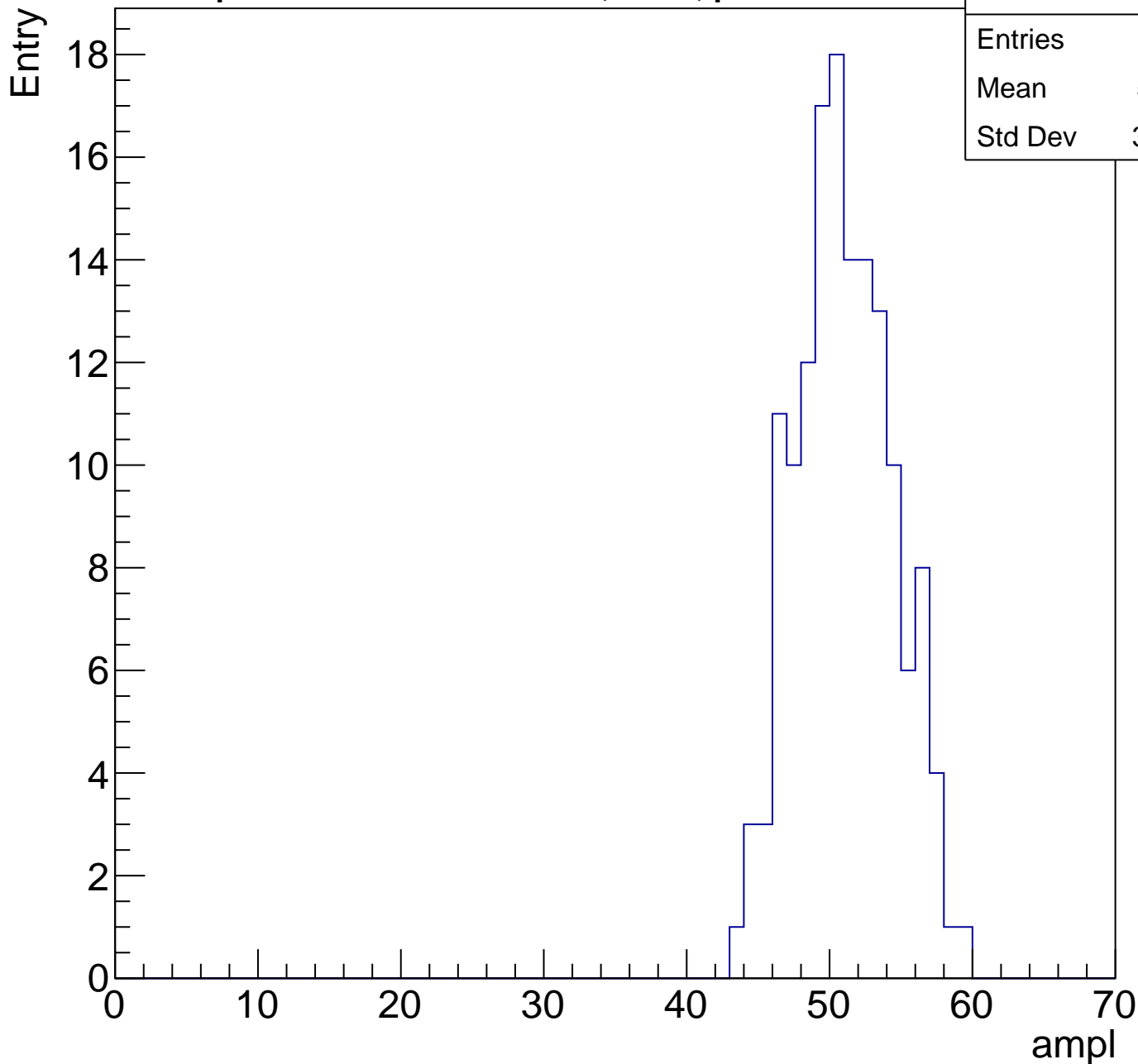
0 10 20 30 40 50 60 70
ampl



B1L001S, U26-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 50.61 |
| Std Dev | 3.346 |



B1L001S, U26-ch92, adc4

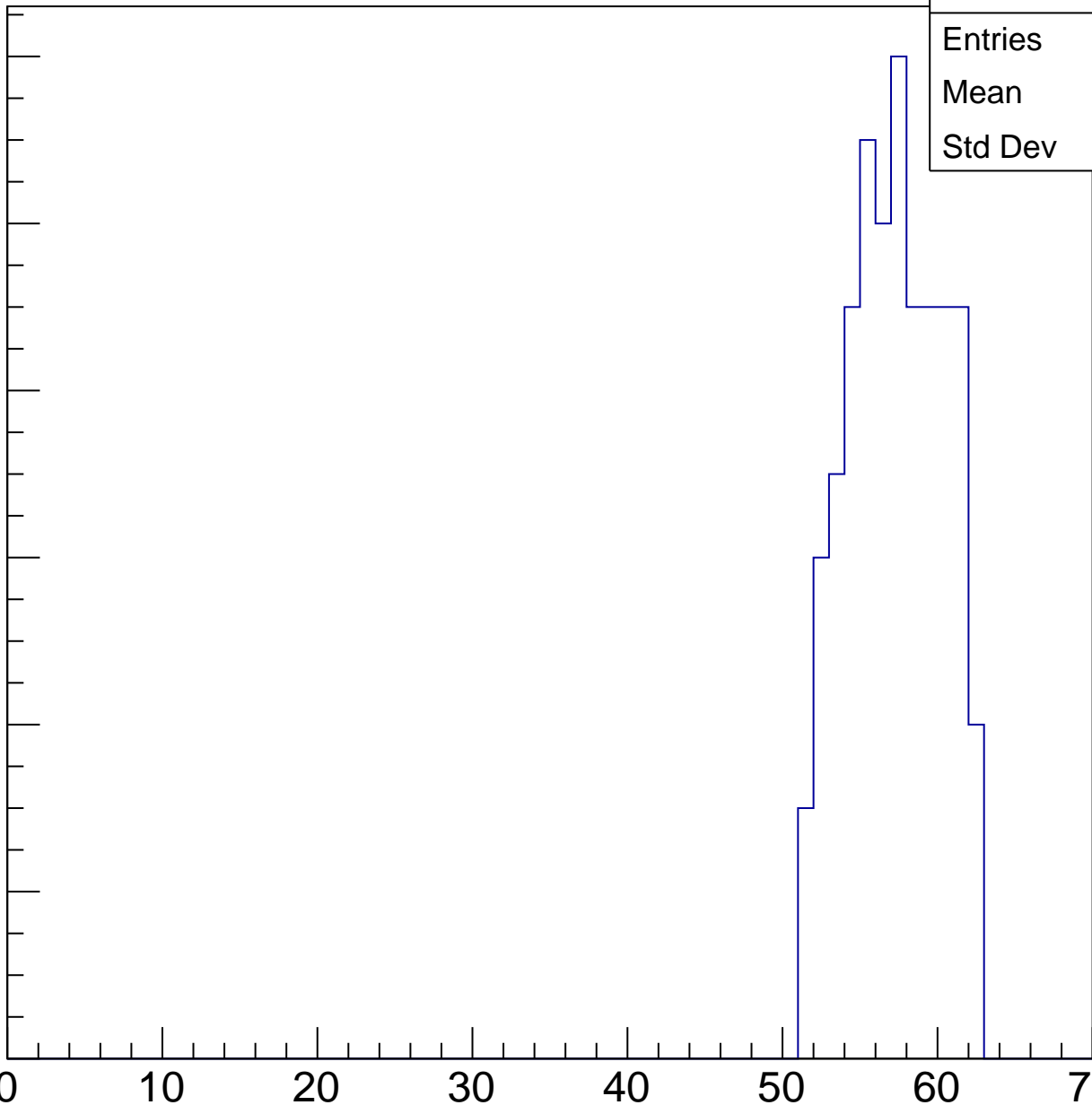
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 56.74 |
| Std Dev | 2.977 |

ampl



B1L001S, U26-ch92, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

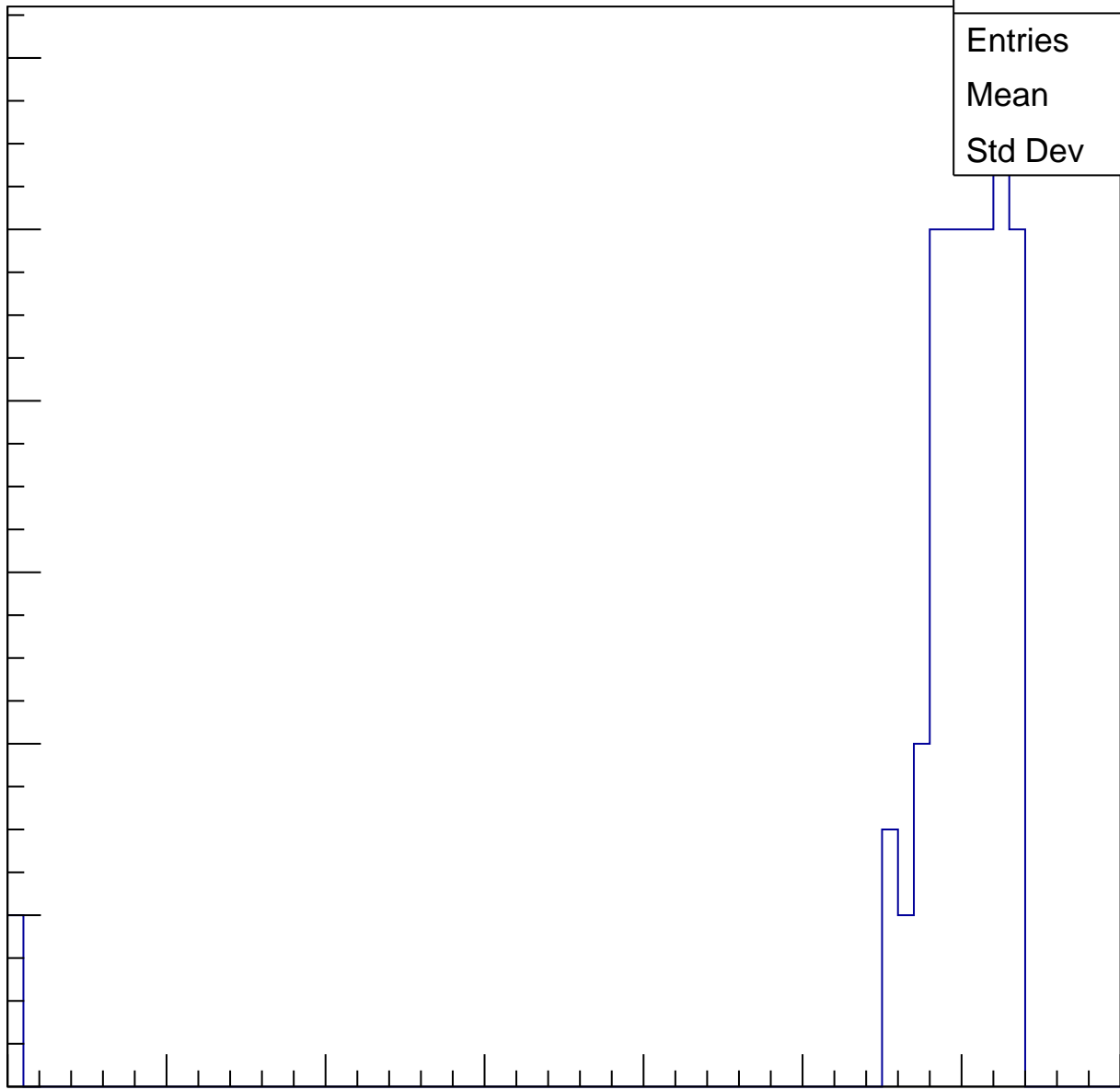
2

0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 58.34 |
| Std Dev | 10.03 |

ampl

0 10 20 30 40 50 60 70

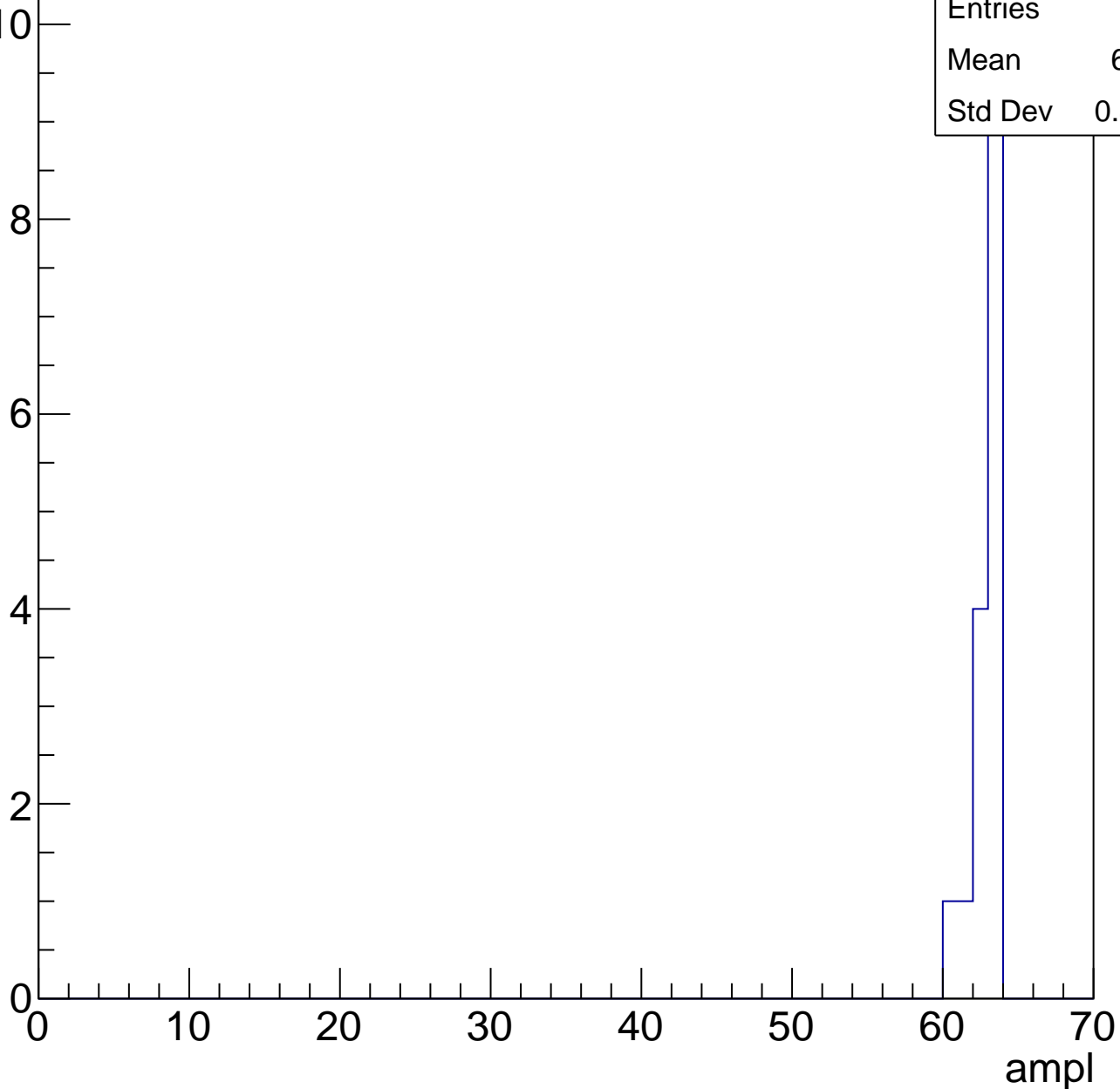


B1L001S, U26-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|--------|
| Entries | 16 |
| Mean | 62.44 |
| Std Dev | 0.8638 |



B1L001S, U26-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 8 |
| Std Dev | 11.31 |

B1L001S, U26-ch93, adc0

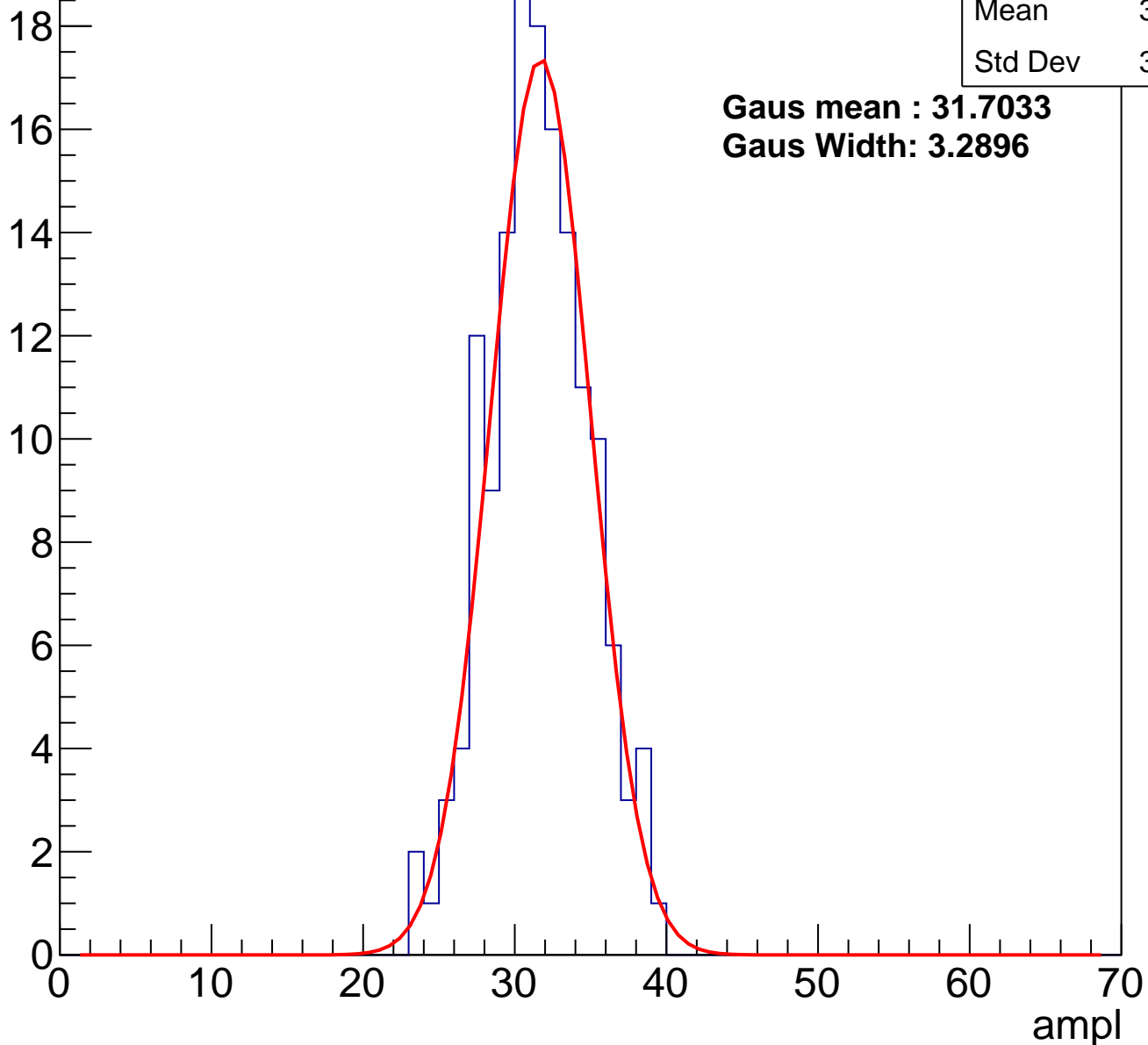
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 147 |
| Mean | 31.12 |
| Std Dev | 3.278 |

Gaus mean : 31.7033

Gaus Width: 3.2896

Entry



B1L001S, U26-ch93, adc1

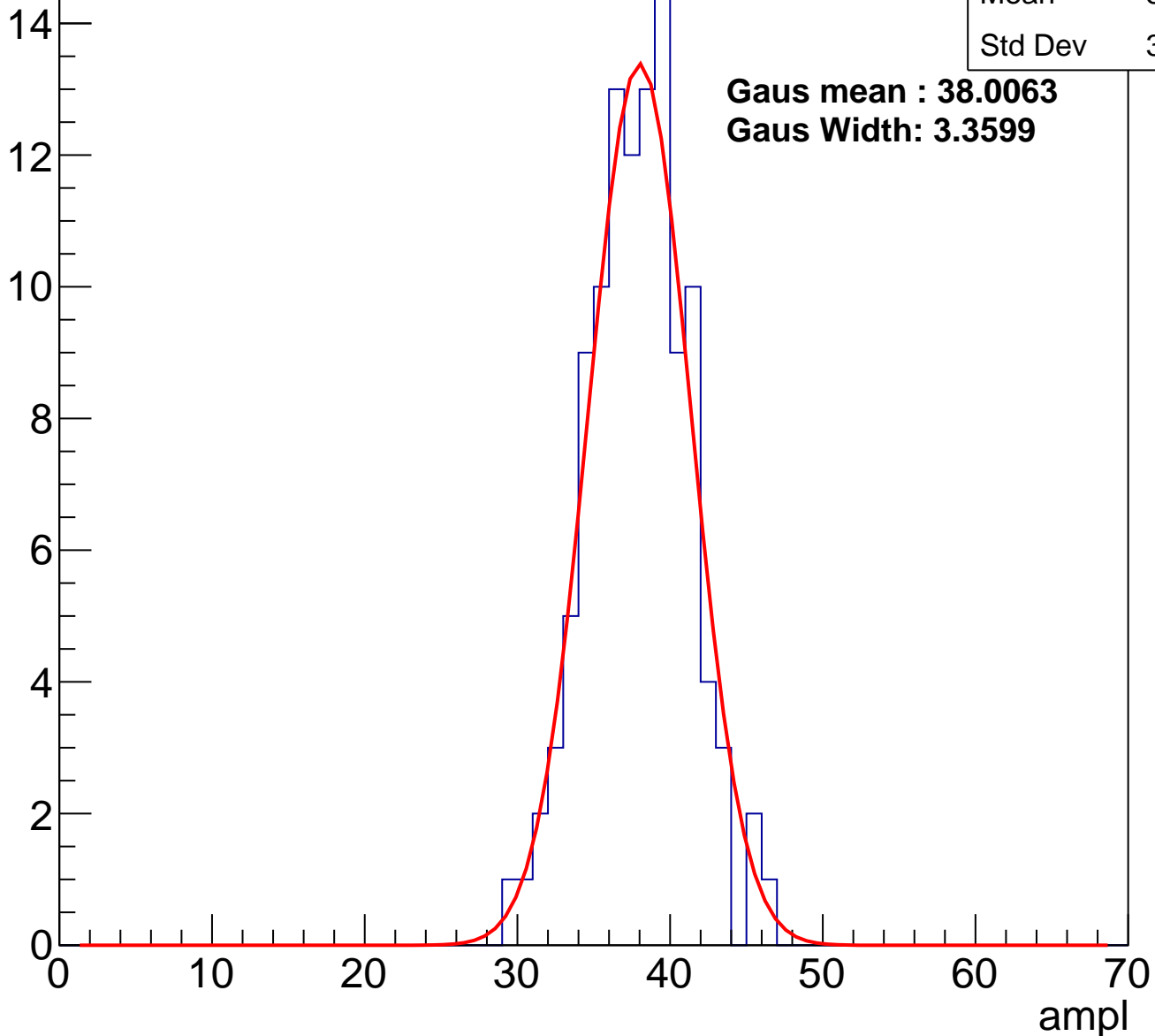
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 113 |
| Mean | 37.45 |
| Std Dev | 3.234 |

Gaus mean : 38.0063

Gaus Width: 3.3599

Entry



B1L001S, U26-ch93, adc2

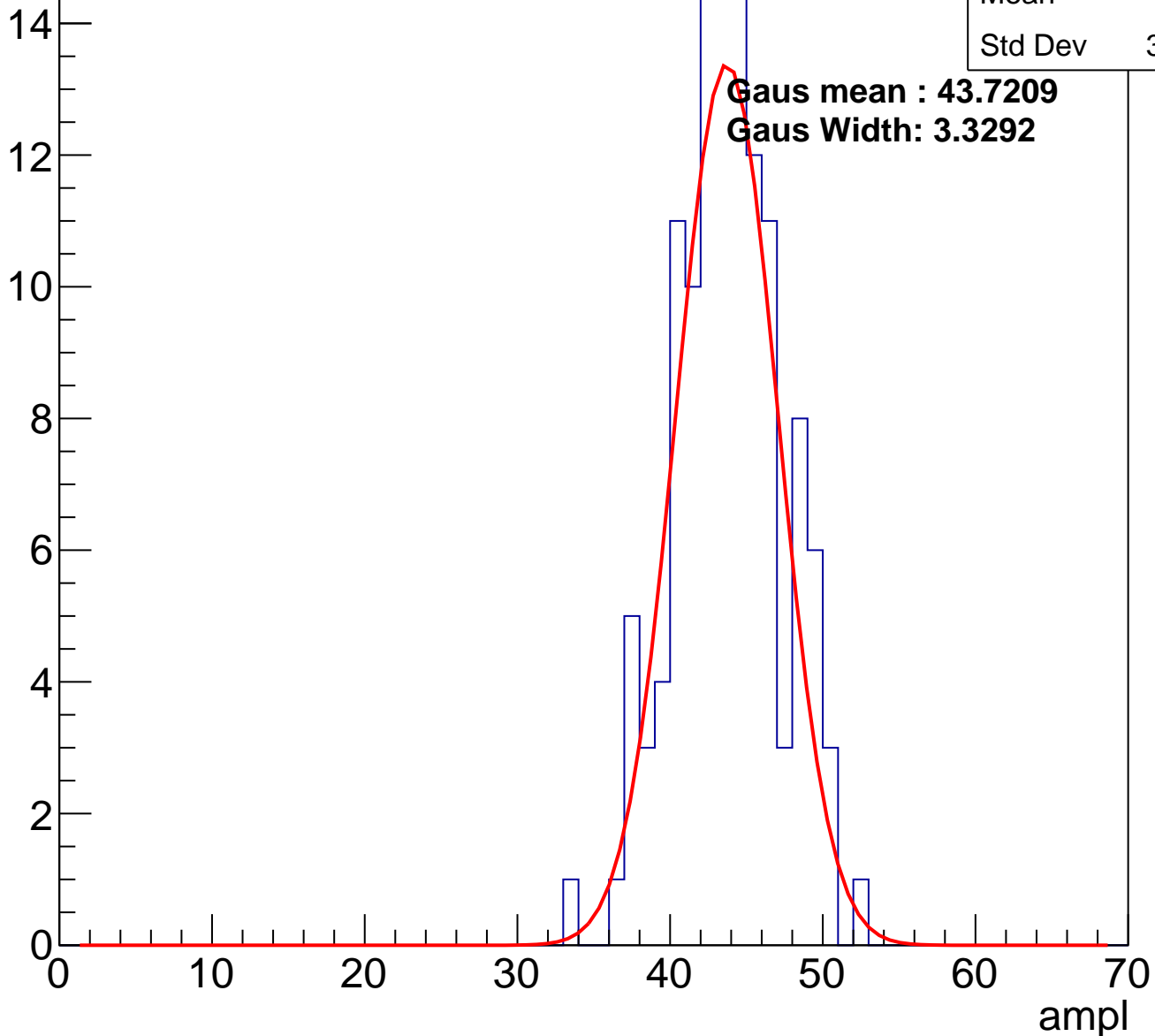
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 43.35 |
| Std Dev | 3.432 |

Gaus mean : 43.7209

Gaus Width: 3.3292

Entry



B1L001S, U26-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 50.25 |
| Std Dev | 3.513 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

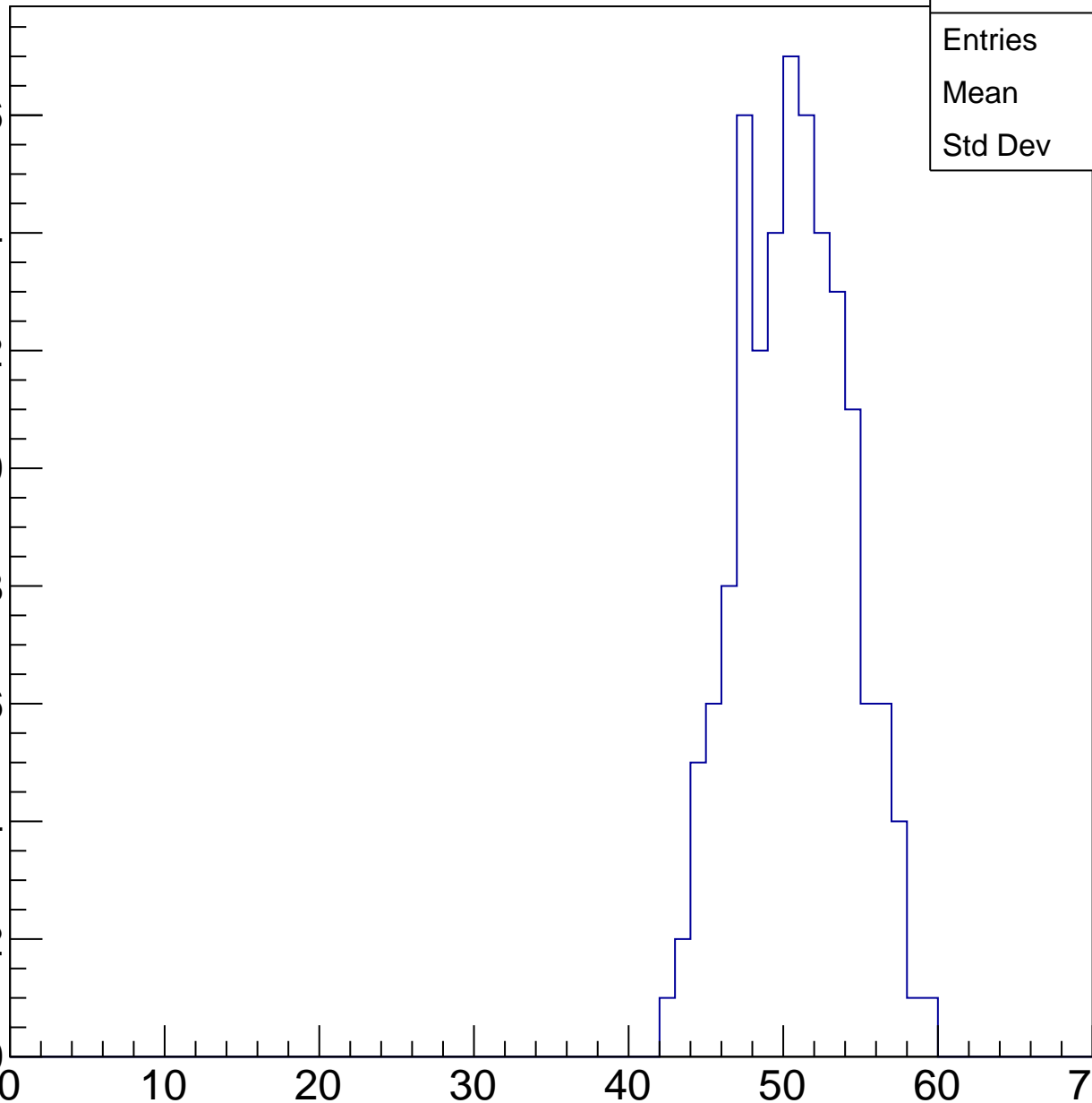
30

40

50

60

ampl

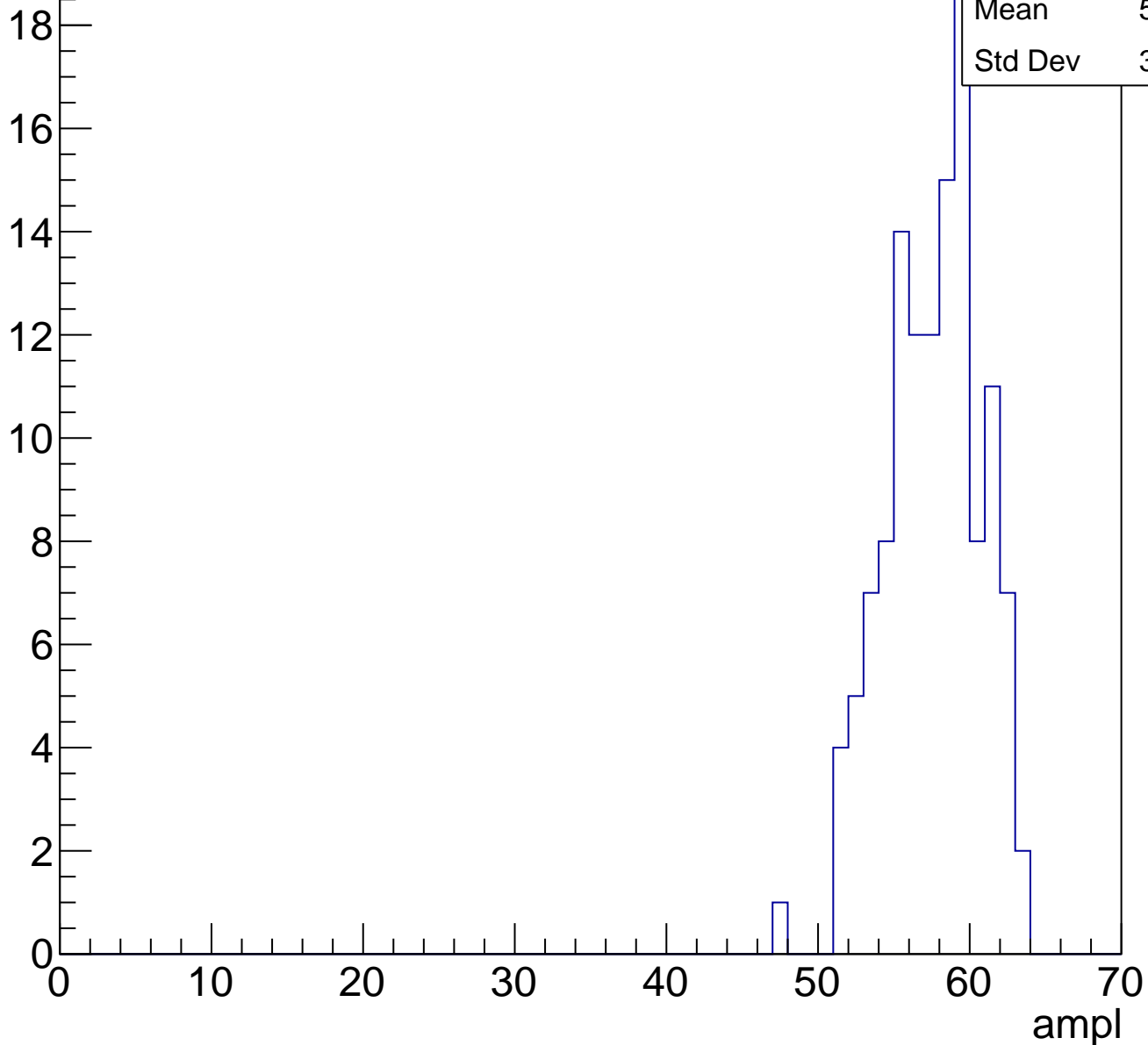


B1L001S, U26-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 57.14 |
| Std Dev | 3.104 |

Entry



B1L001S, U26-ch93, adc5

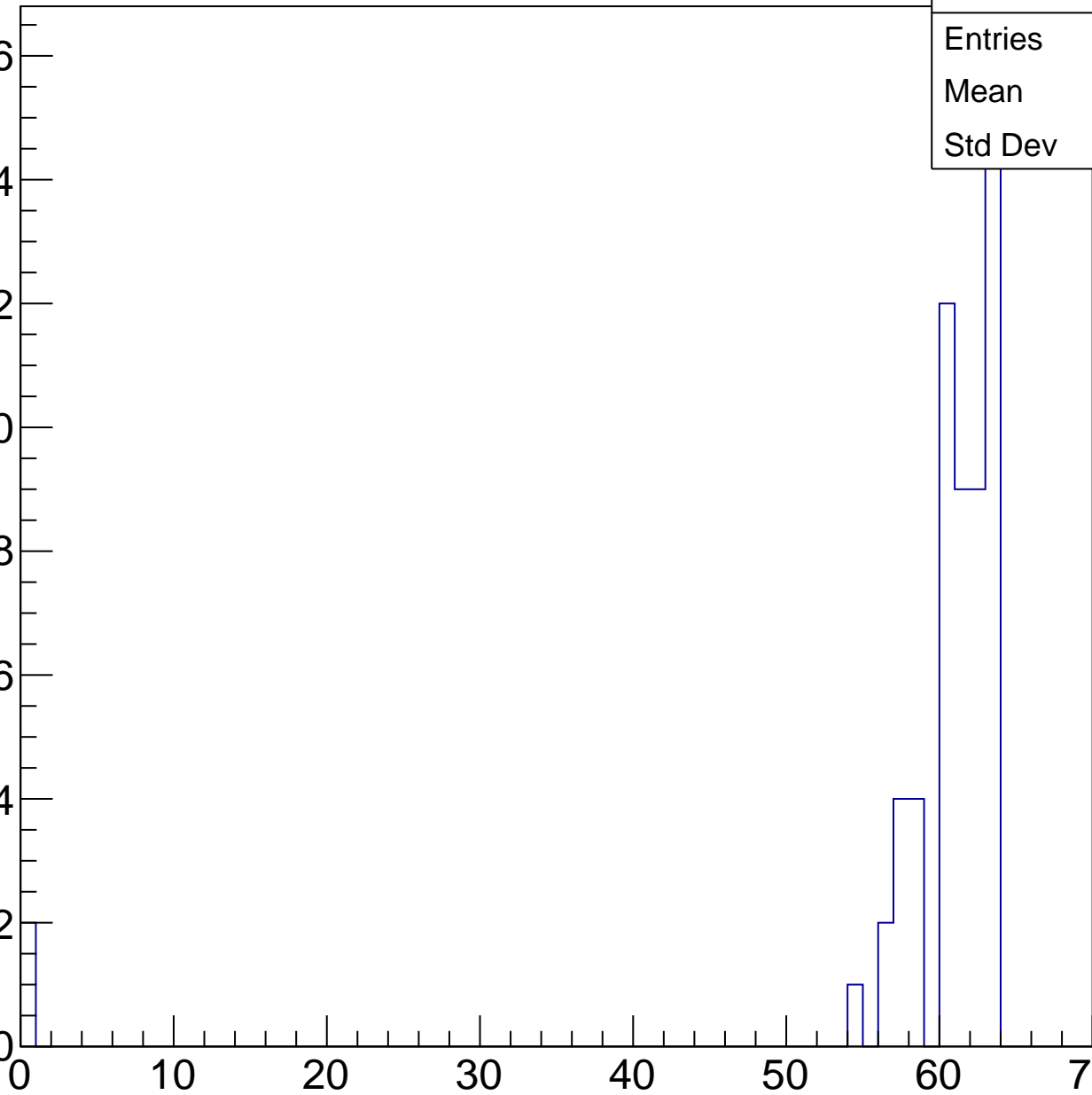
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.66 |
| Std Dev | 11.2 |

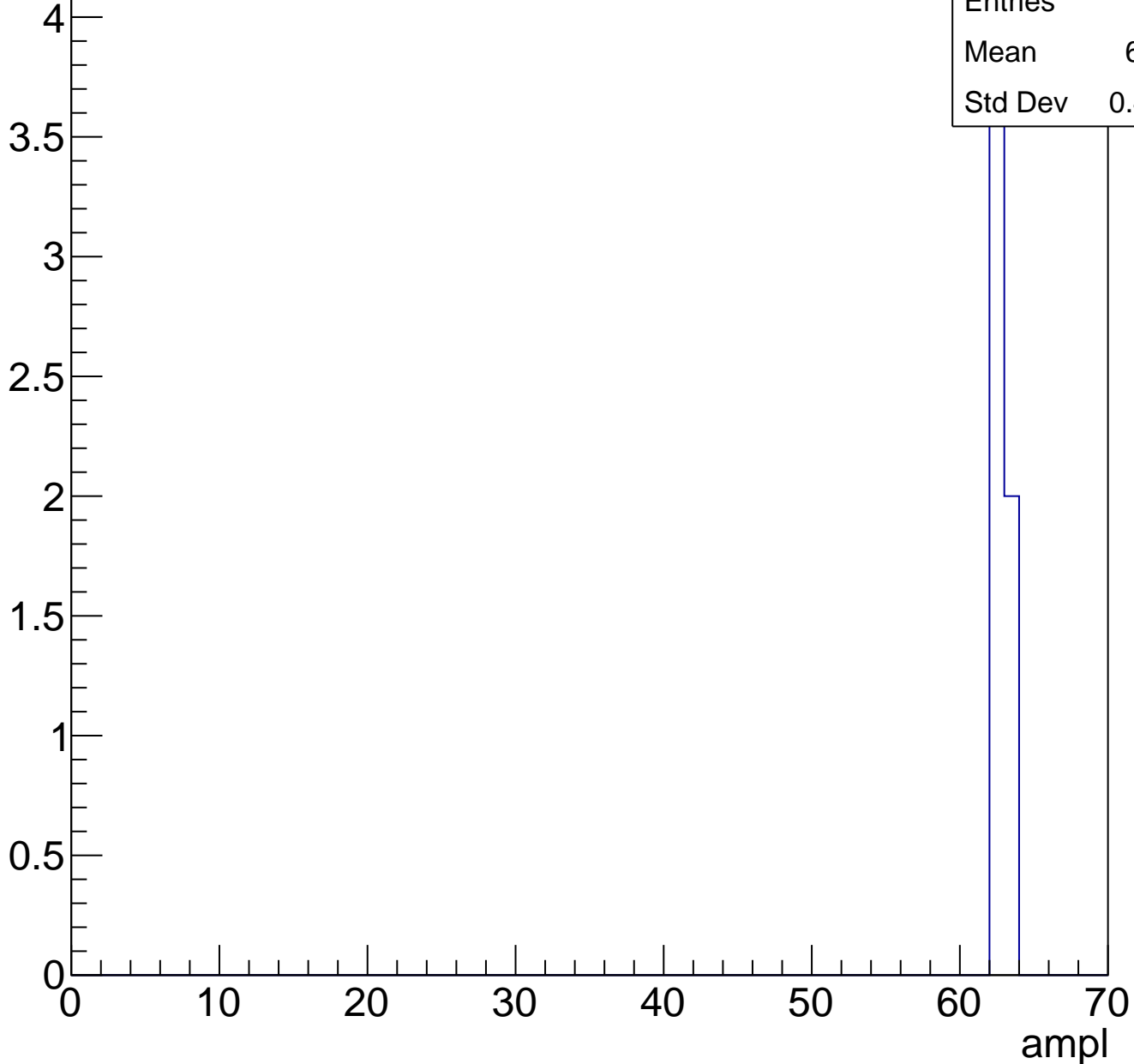
ampl



B1L001S, U26-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

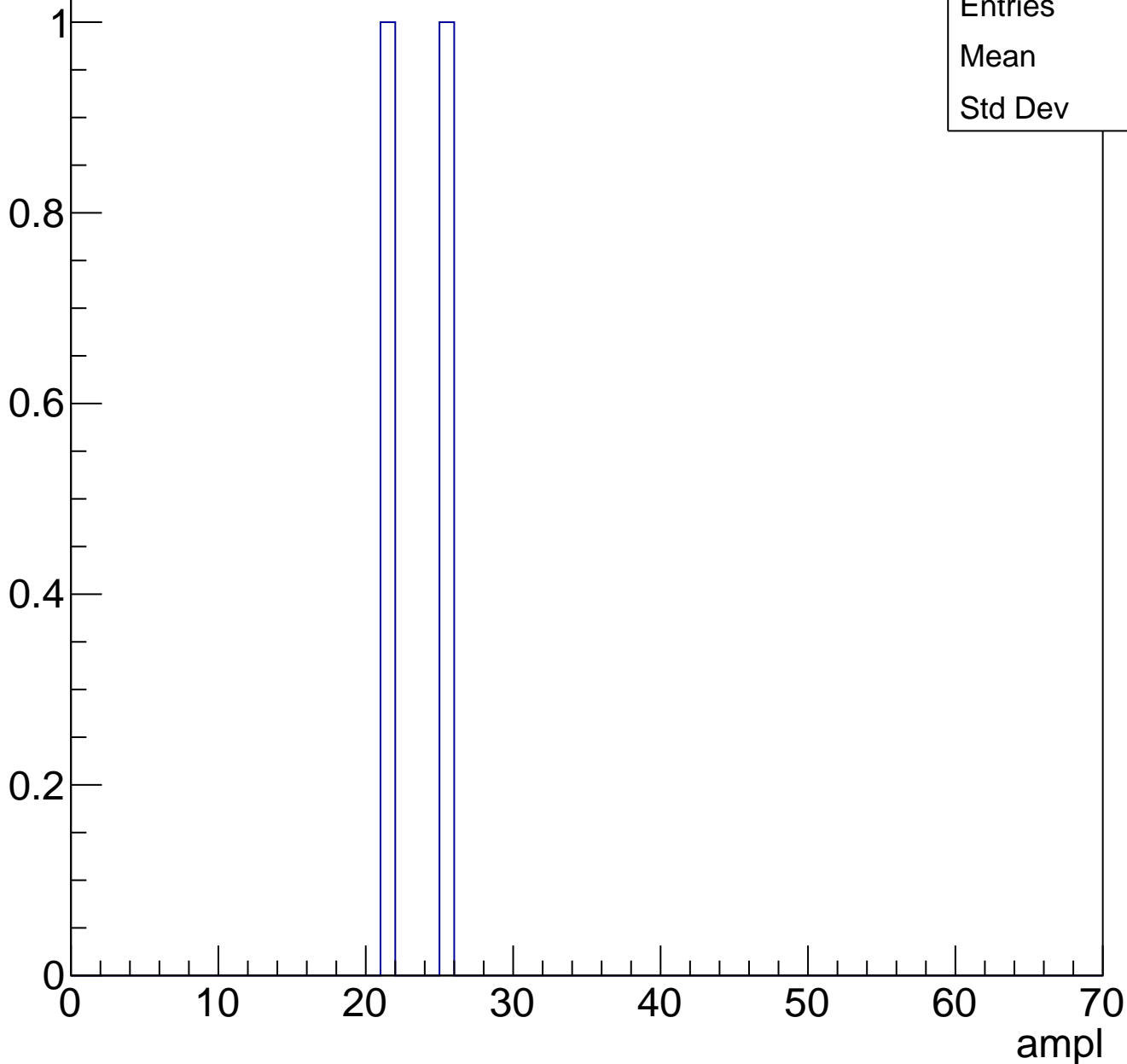
Entry



B1L001S, U26-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch94, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 28.99 |
| Std Dev | 2.863 |

Gaus mean : 29.8194

Gaus Width: 2.6966

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

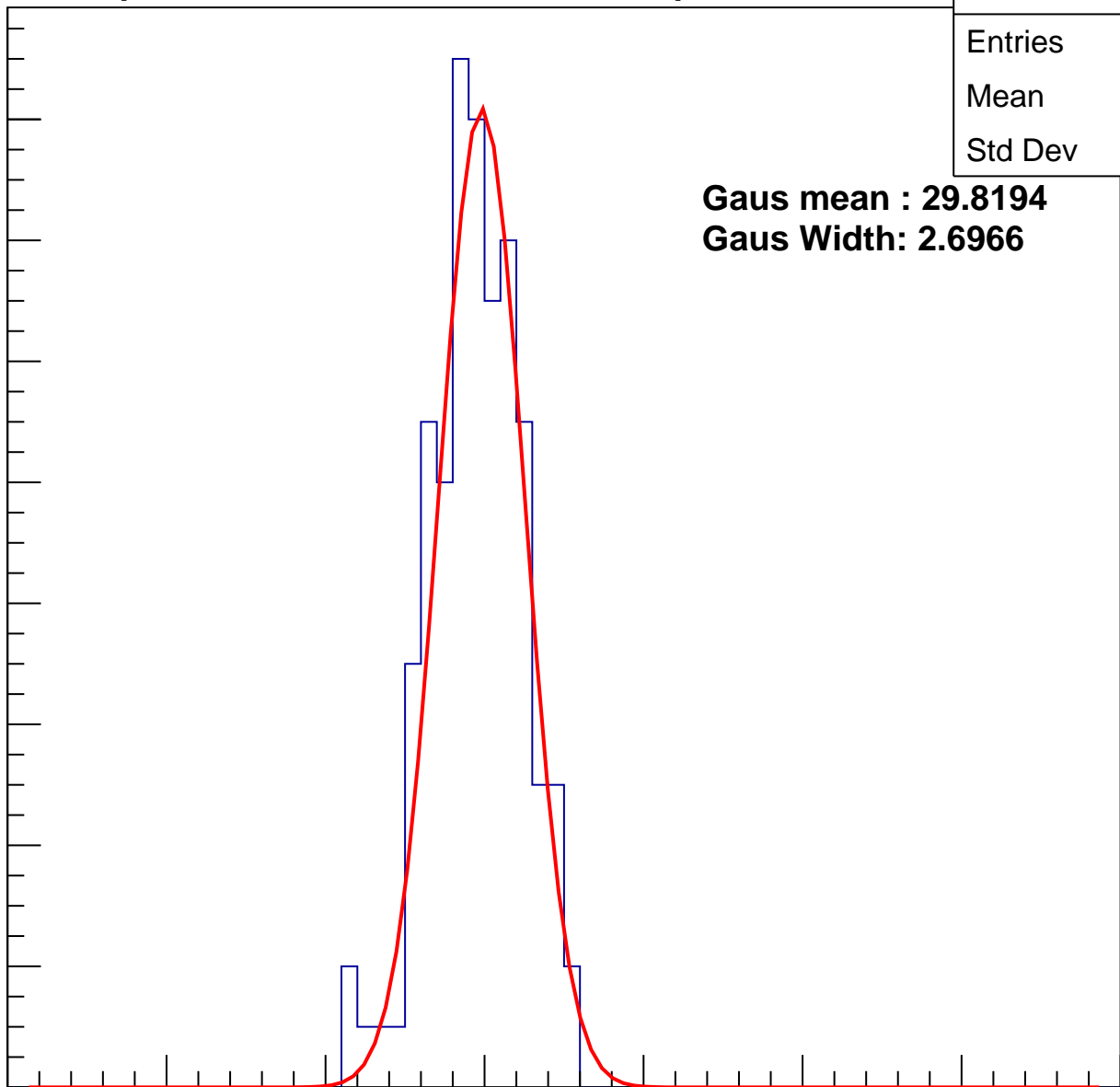
40

50

60

70

ampl



B1L001S, U26-ch94, adc1

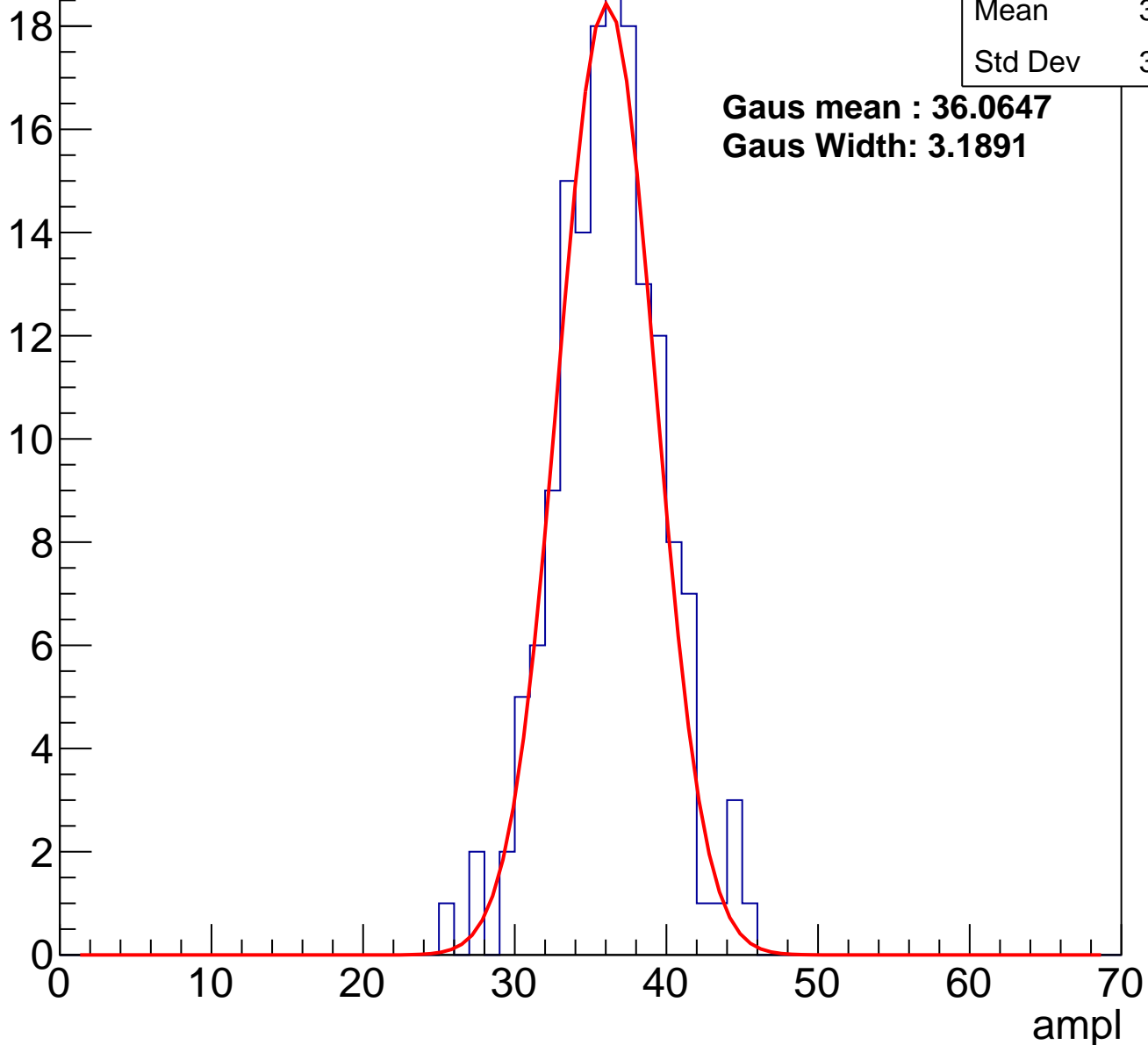
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 155 |
| Mean | 35.76 |
| Std Dev | 3.496 |

Gaus mean : 36.0647

Gaus Width: 3.1891

Entry



B1L001S, U26-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

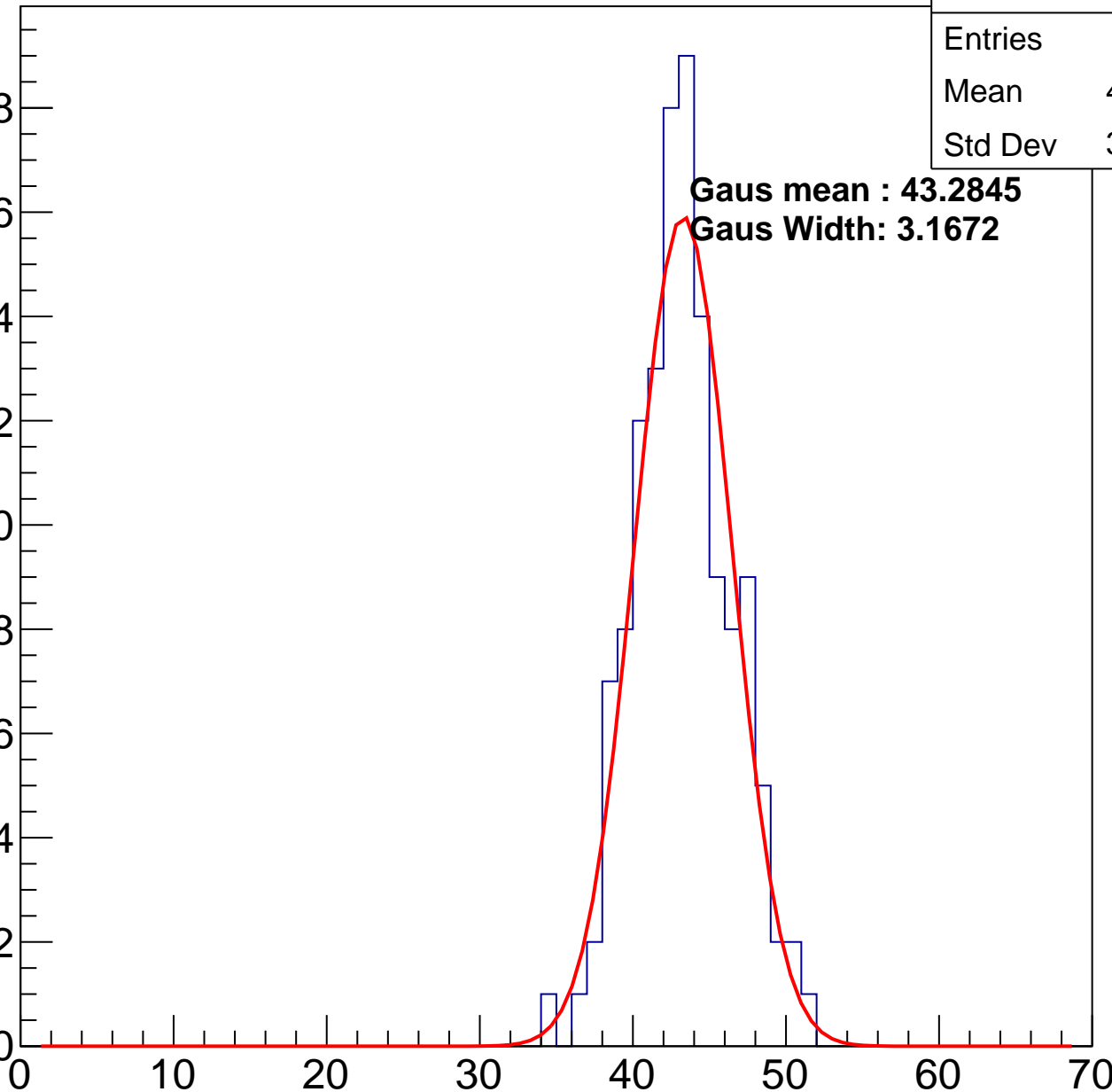
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 131 |
| Mean | 42.82 |
| Std Dev | 3.155 |

Gaus mean : 43.2845

Gaus Width: 3.1672

ampl



B1L001S, U26-ch94, adc3

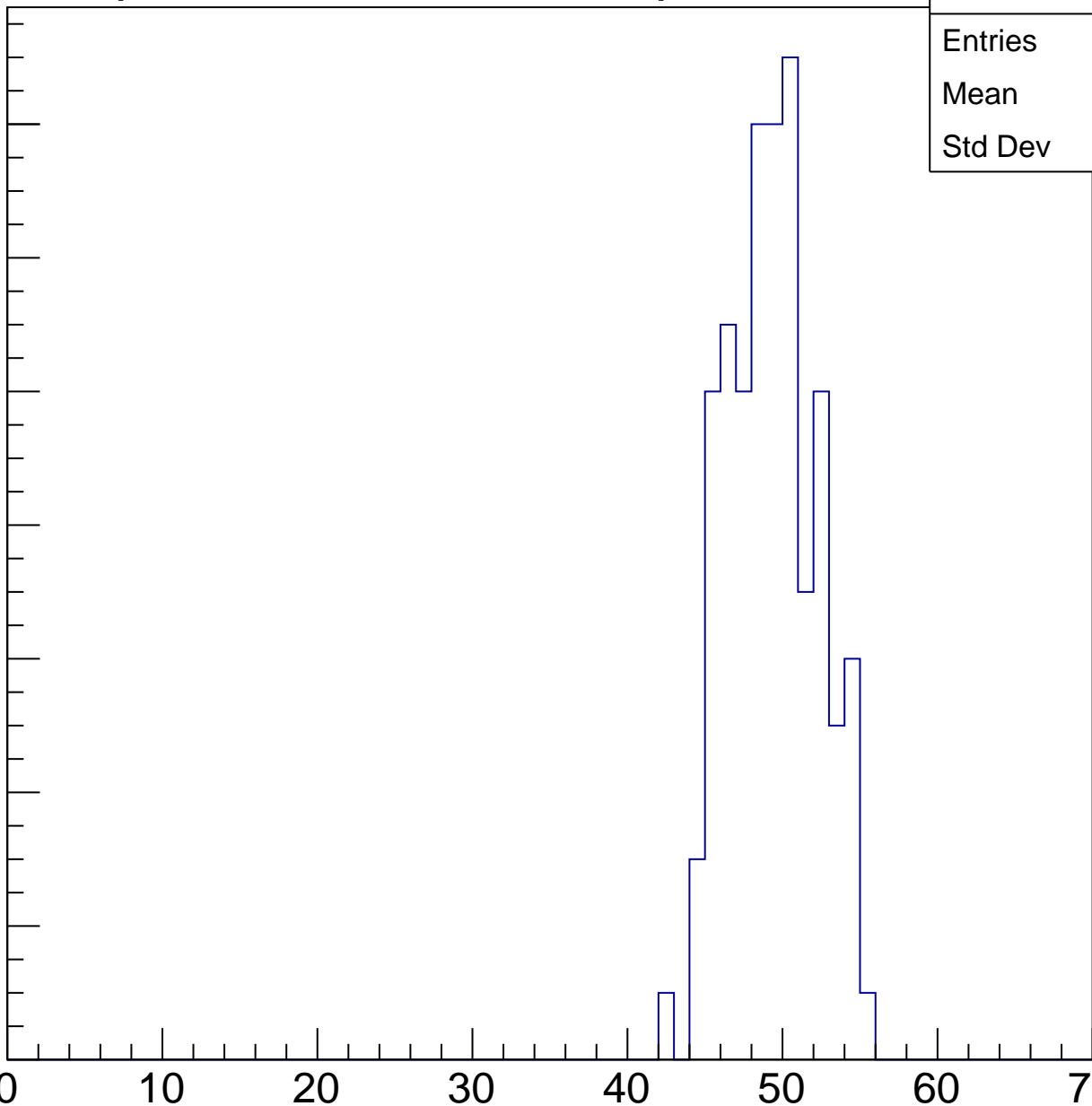
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 107 |
| Mean | 48.87 |
| Std Dev | 2.792 |

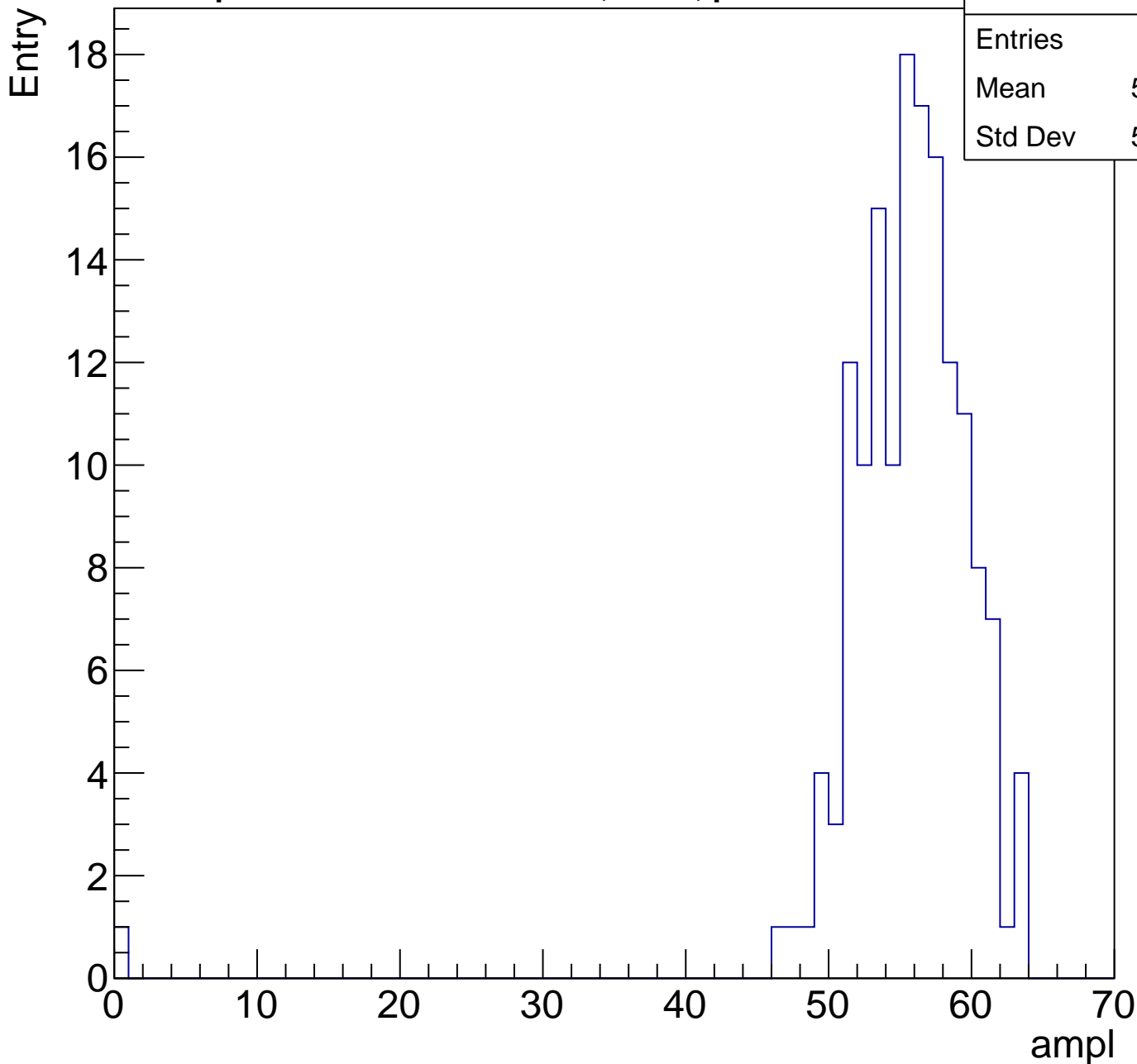
ampl



B1L001S, U26-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 55.09 |
| Std Dev | 5.684 |



B1L001S, U26-ch94, adc5

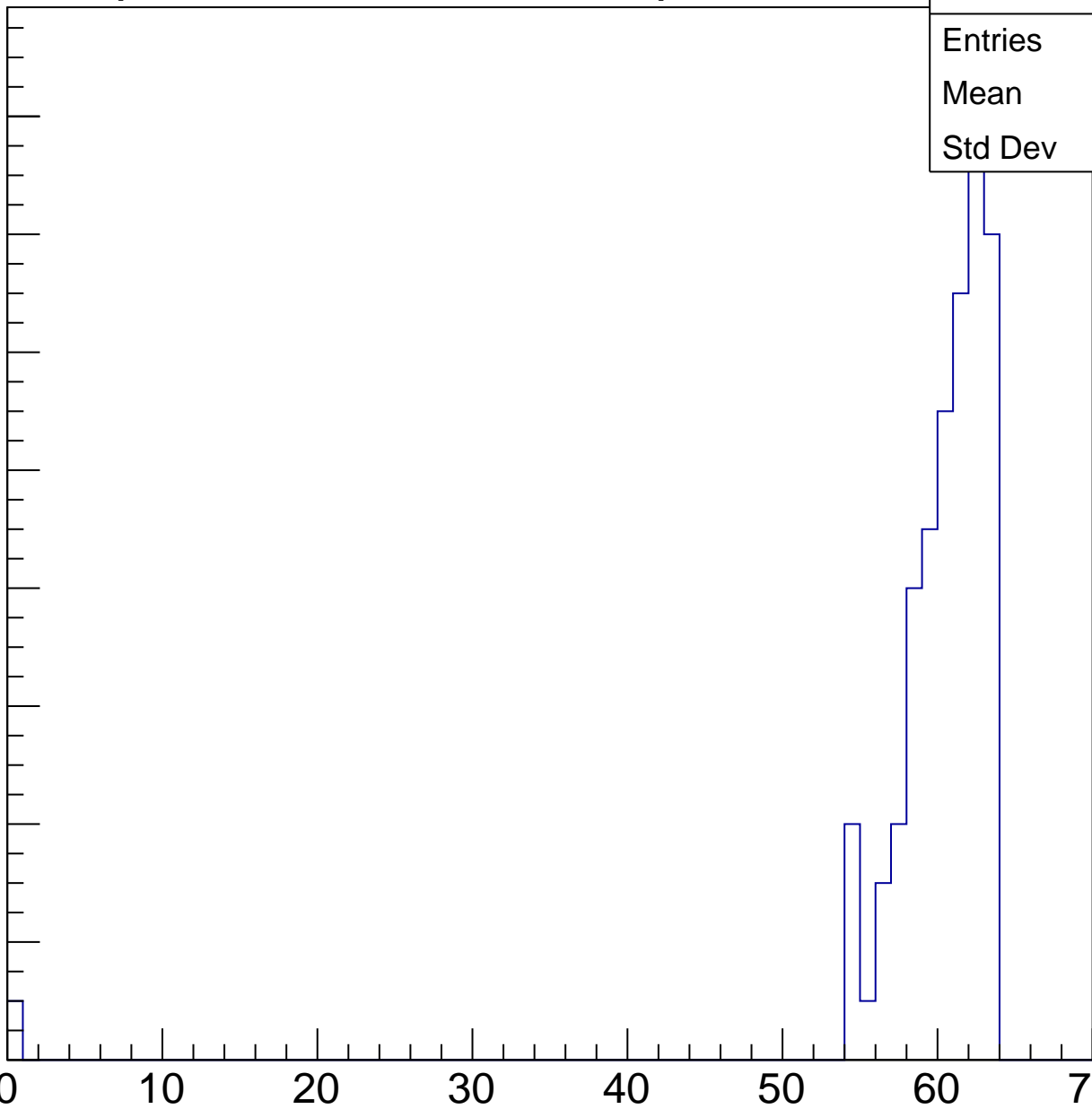
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 59.42 |
| Std Dev | 6.922 |

ampl



B1L001S, U26-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

B1L001S, U26-ch95, adc0

calib_packv5_042523_0143.root, FC#2, port C2

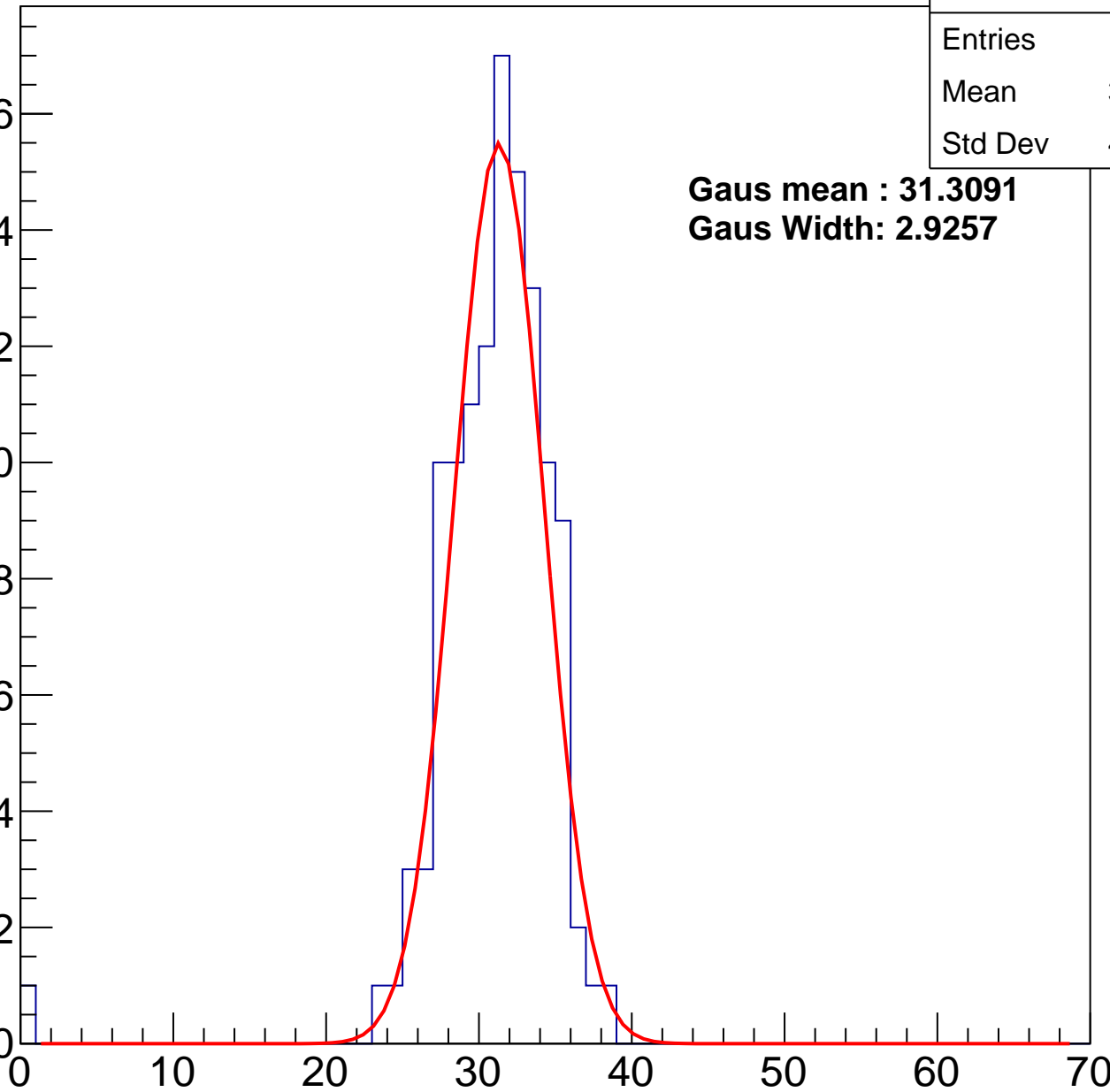
| | |
|---------|-------|
| Entries | 120 |
| Mean | 30.56 |
| Std Dev | 4.057 |

Gaus mean : 31.3091
Gaus Width: 2.9257

Entry

16
14
12
10
8
6
4
2
0

ampl

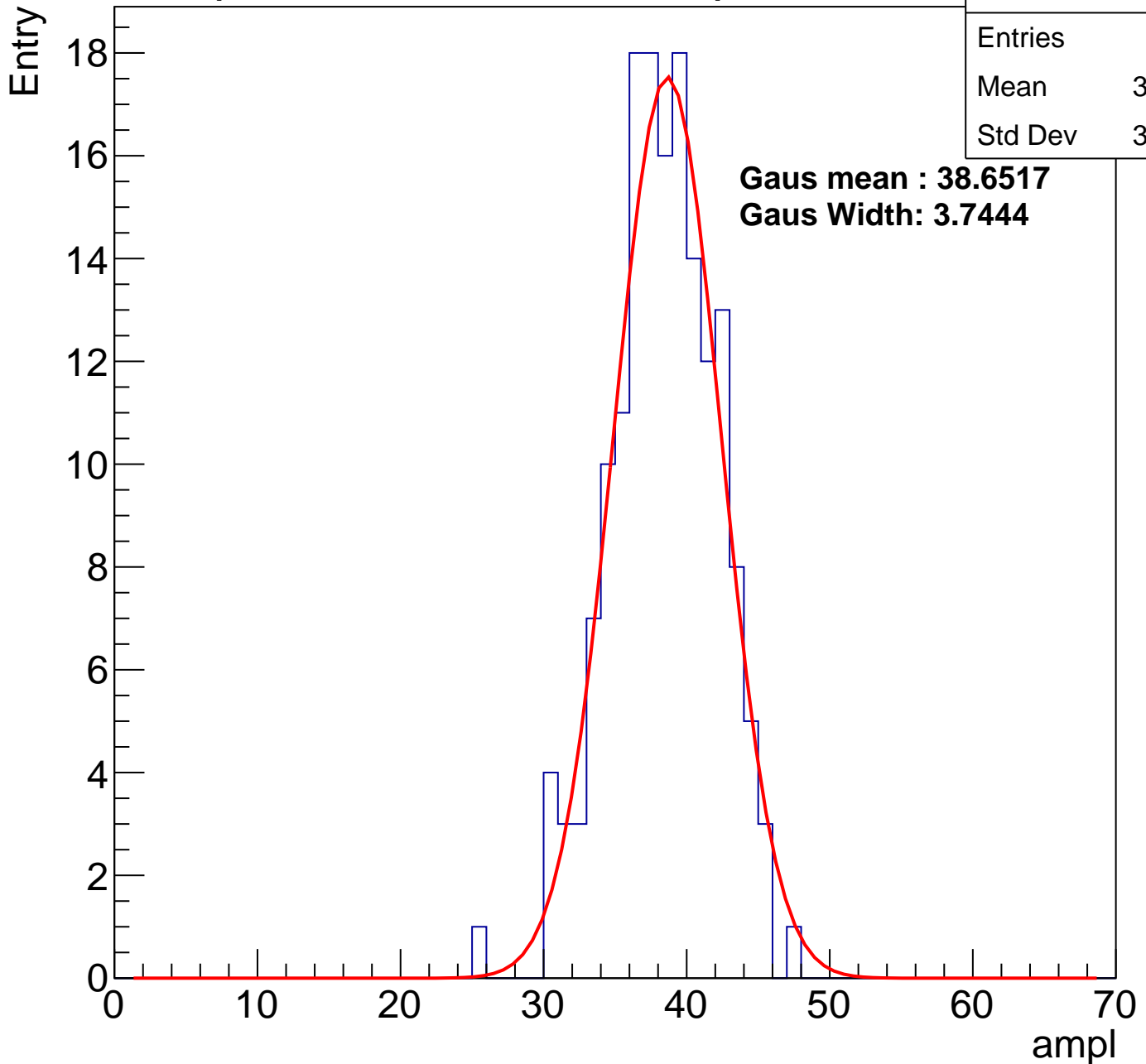


B1L001S, U26-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 165 |
| Mean | 37.93 |
| Std Dev | 3.652 |

Gaus mean : 38.6517
Gaus Width: 3.7444



B1L001S, U26-ch95, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 111 |
| Mean | 44.73 |
| Std Dev | 3.241 |

Gaus mean : 45.4895

Gaus Width: 3.5863

0

10

20

30

40

50

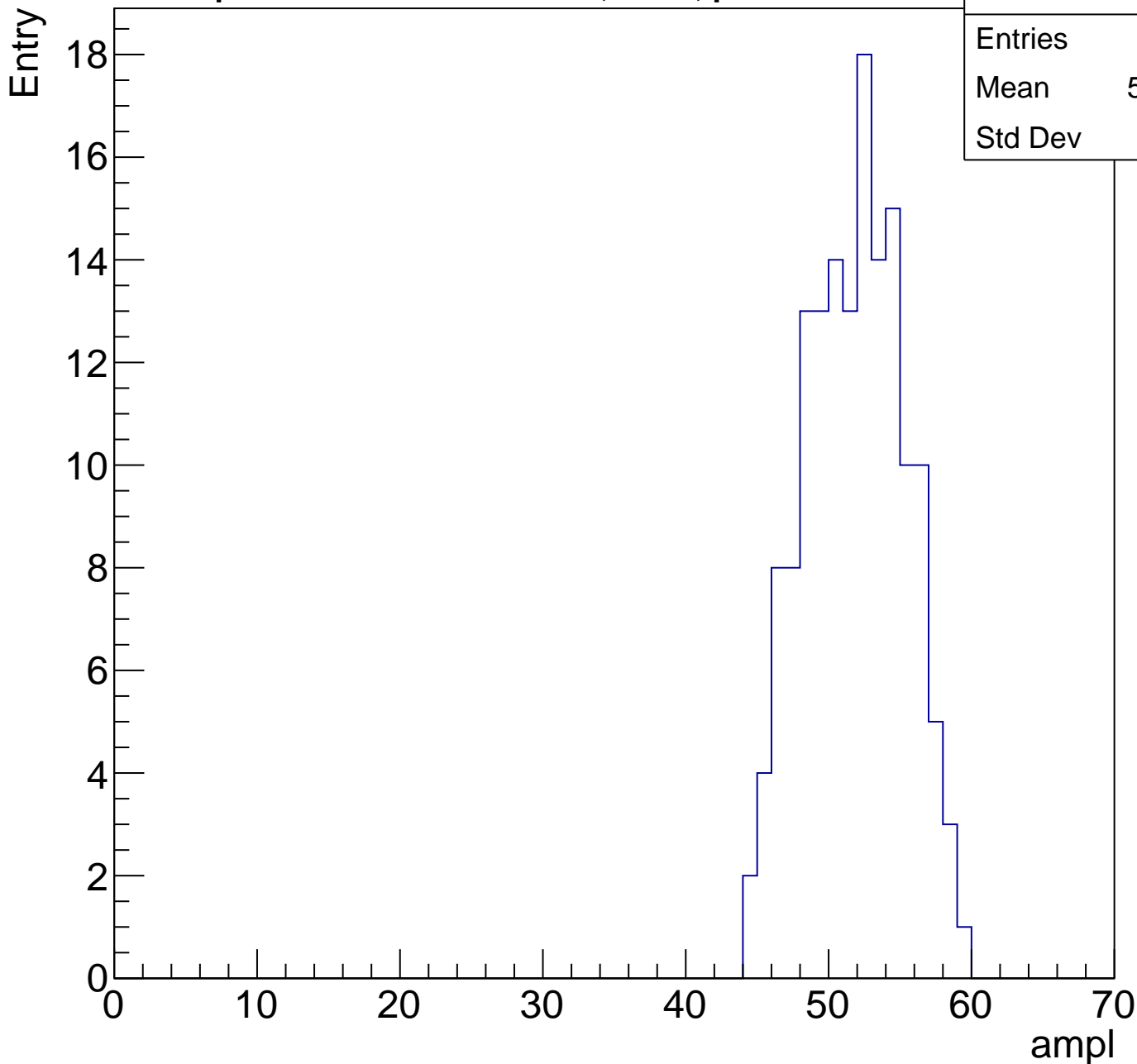
60

ampl

B1L001S, U26-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 151 |
| Mean | 51.34 |
| Std Dev | 3.4 |



B1L001S, U26-ch95, adc4

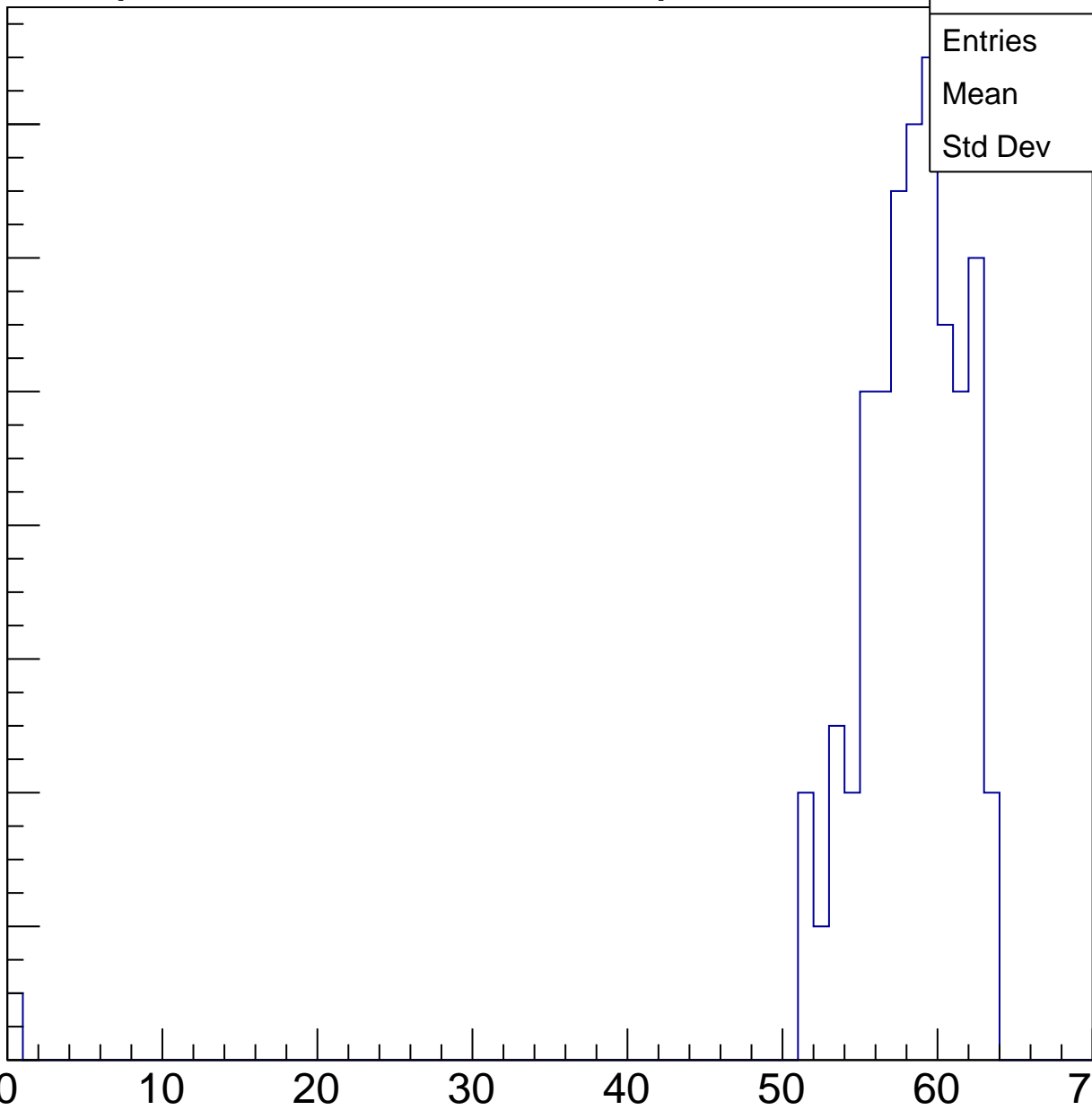
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 57.42 |
| Std Dev | 6.162 |

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch95, adc5

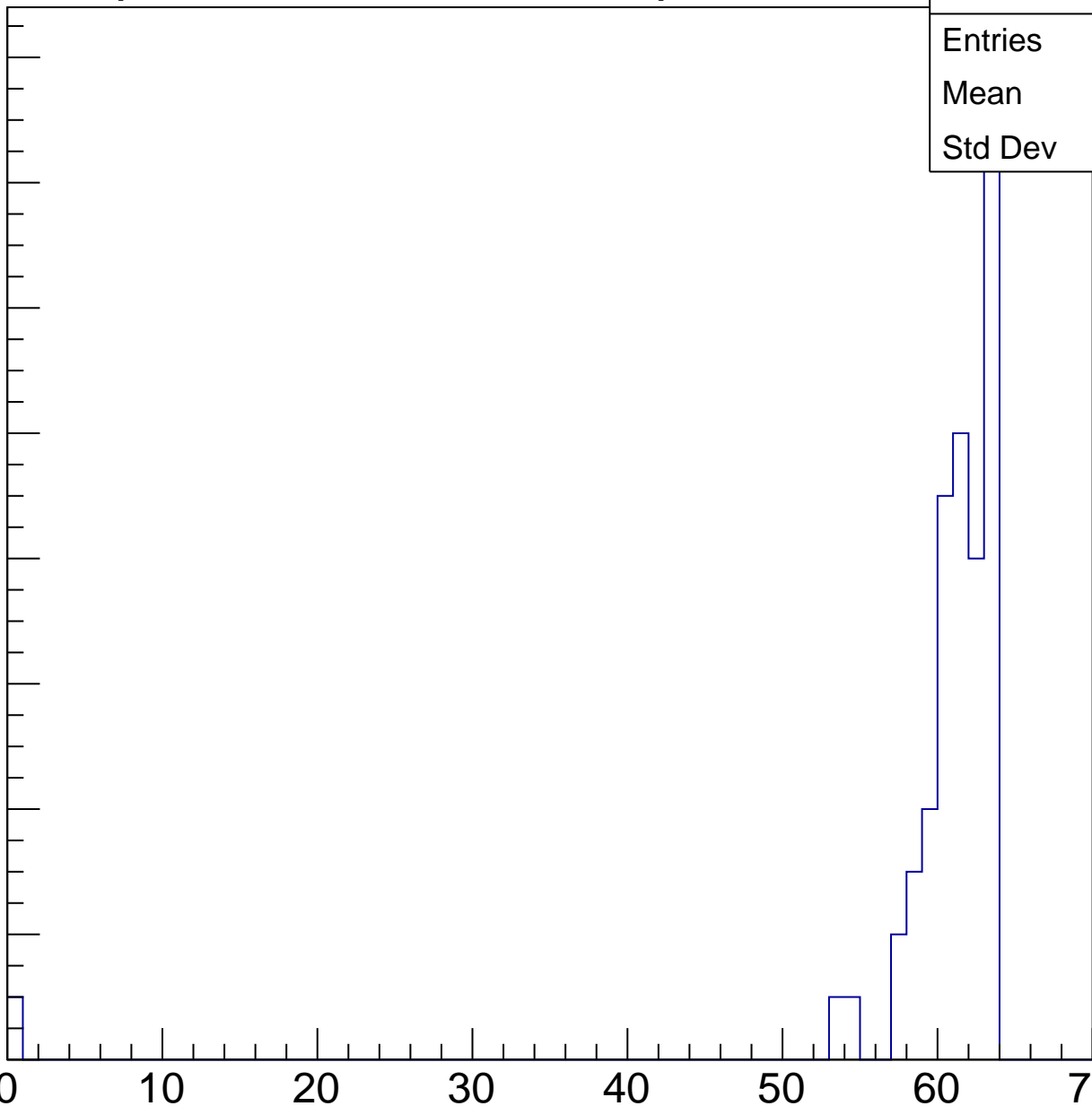
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 55 |
| Mean | 59.73 |
| Std Dev | 8.422 |

ampl



B1L001S, U26-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch96, adc0

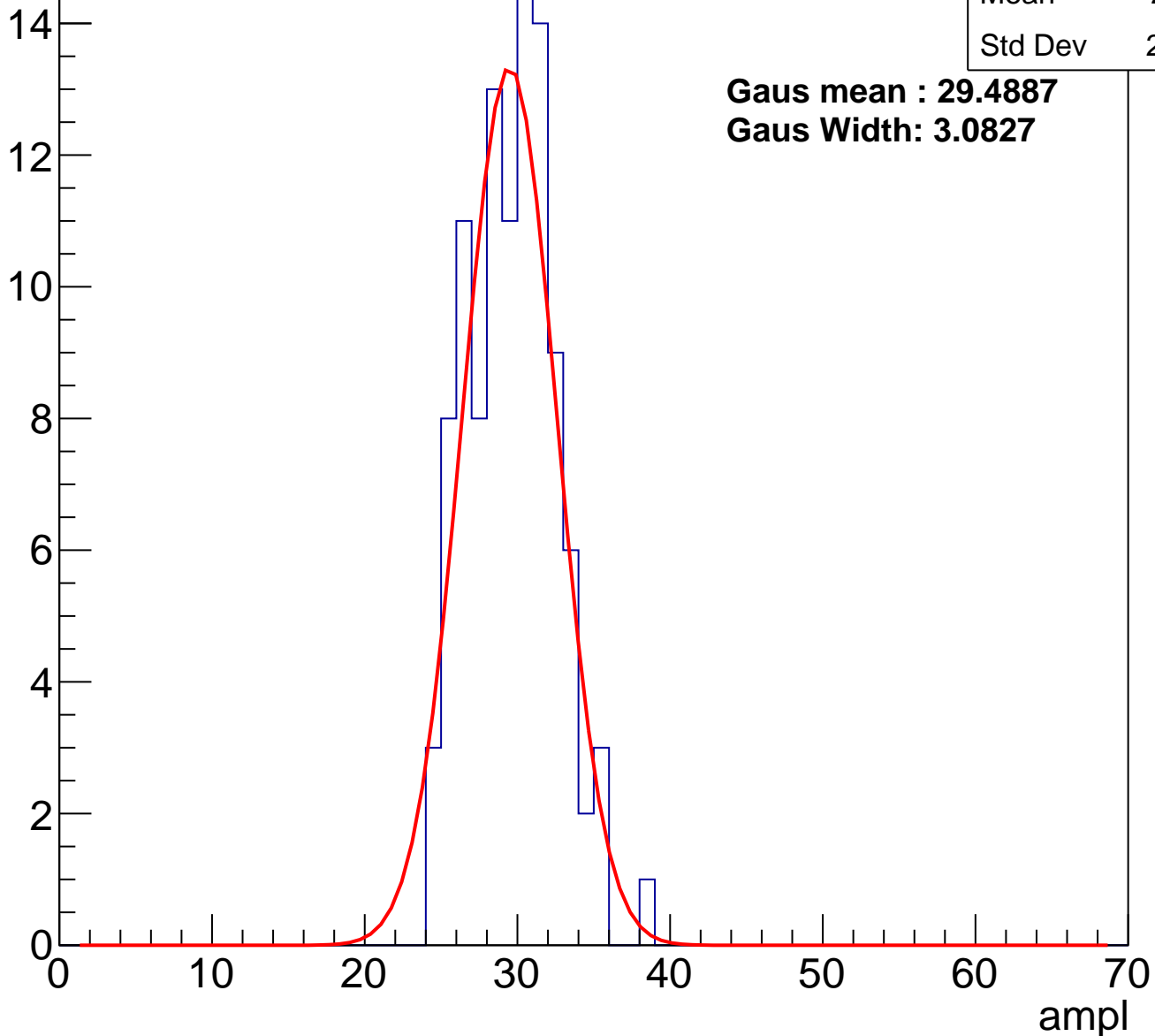
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 104 |
| Mean | 29.21 |
| Std Dev | 2.824 |

Gaus mean : 29.4887

Gaus Width: 3.0827

Entry



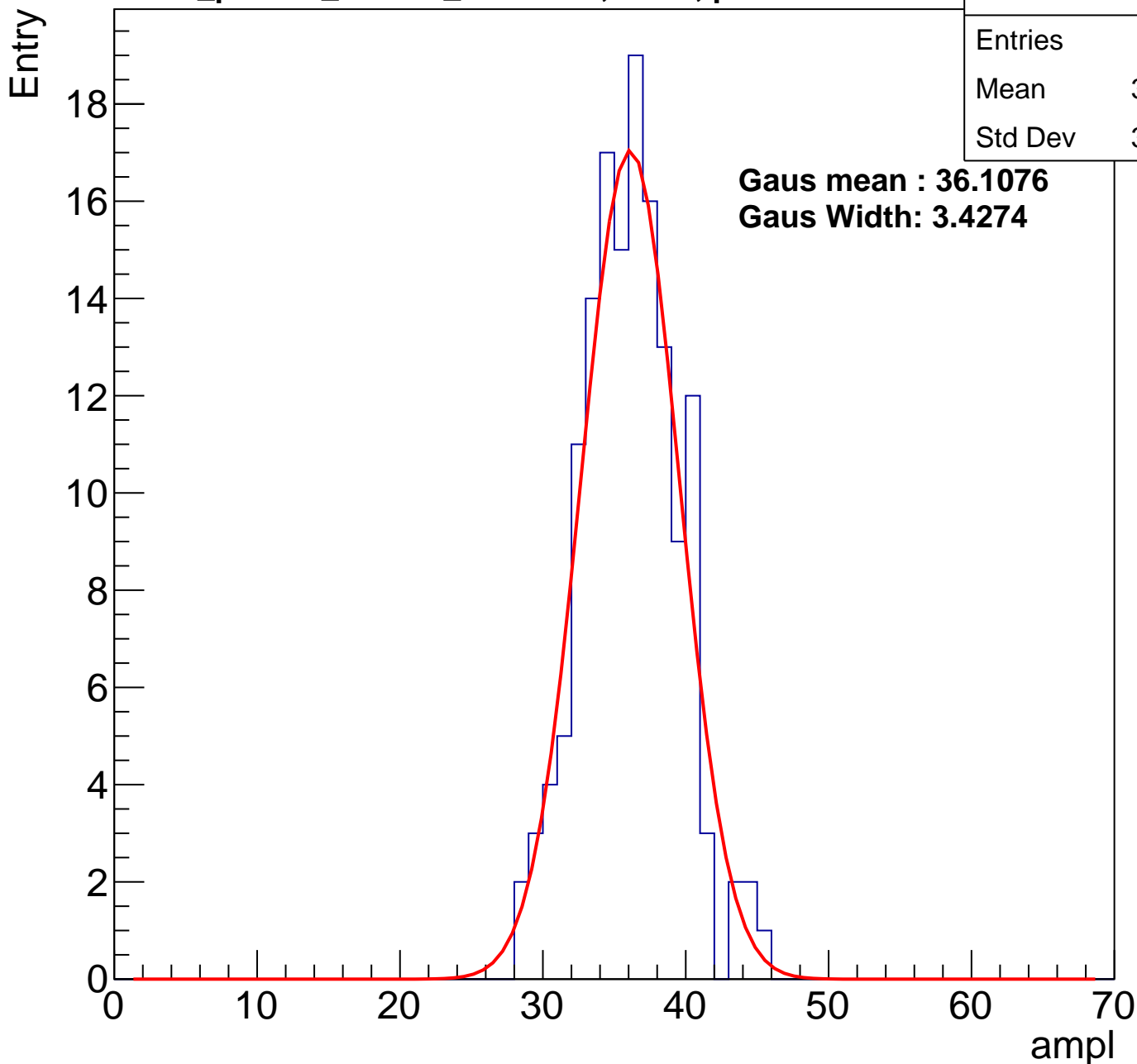
B1L001S, U26-ch96, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 148 |
| Mean | 35.66 |
| Std Dev | 3.324 |

Gaus mean : 36.1076

Gaus Width: 3.4274



B1L001S, U26-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 42.48 |
| Std Dev | 3.273 |

Gaus mean : 43.0056

Gaus Width: 3.3454

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

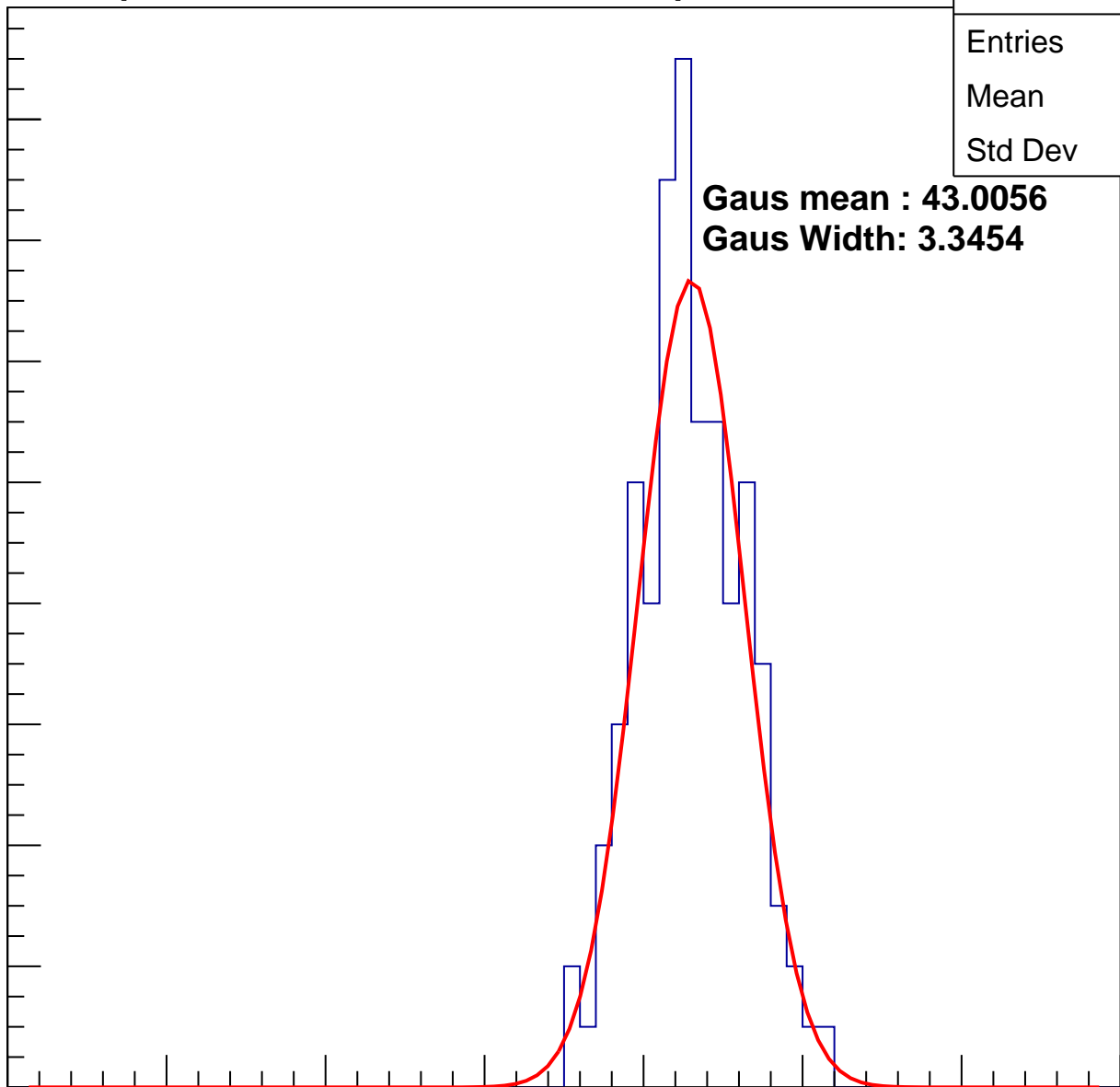
40

50

60

70

ampl



B1L001S, U26-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

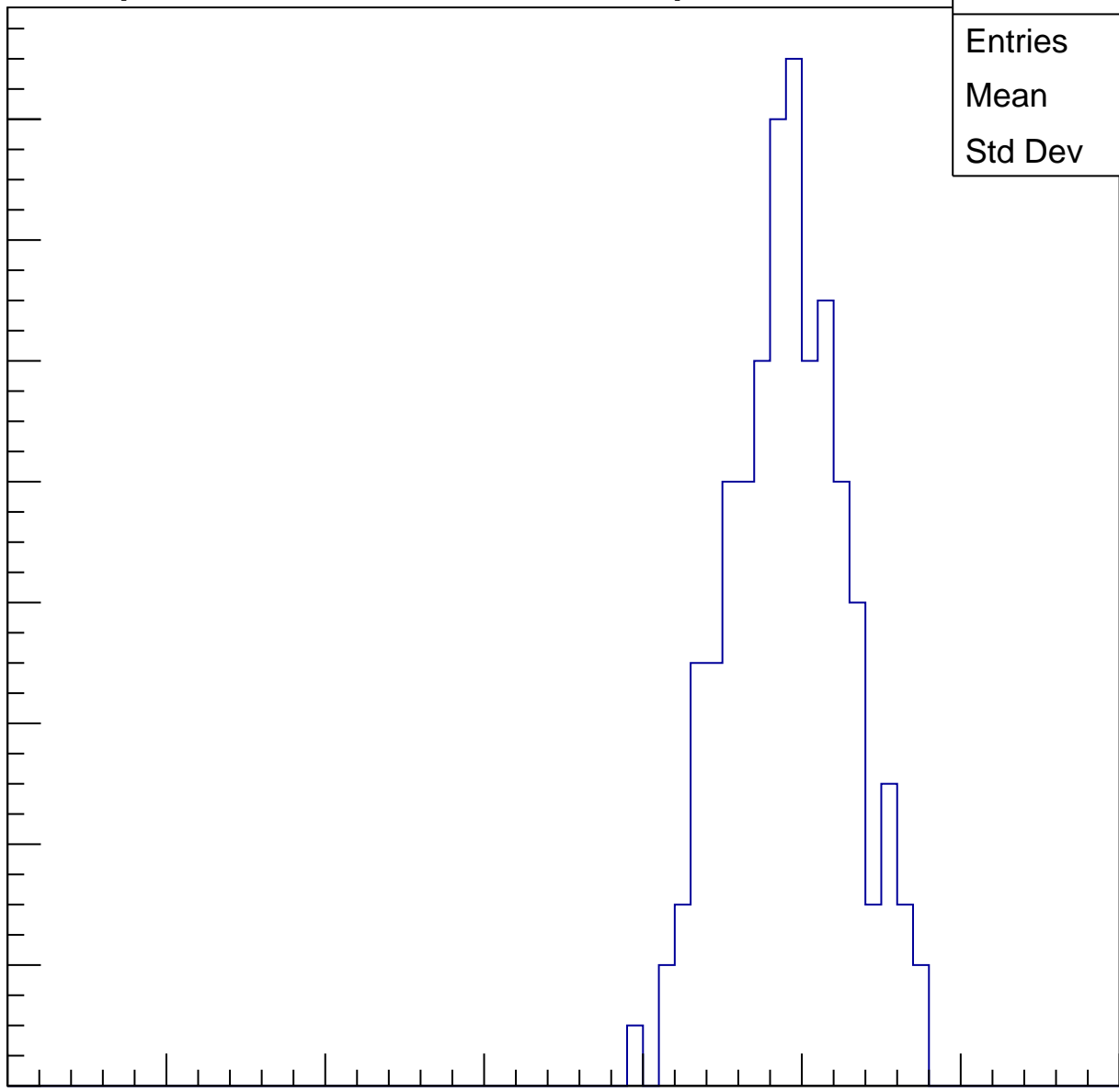
| | |
|---------|-------|
| Entries | 141 |
| Mean | 48.63 |
| Std Dev | 3.679 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch96, adc4

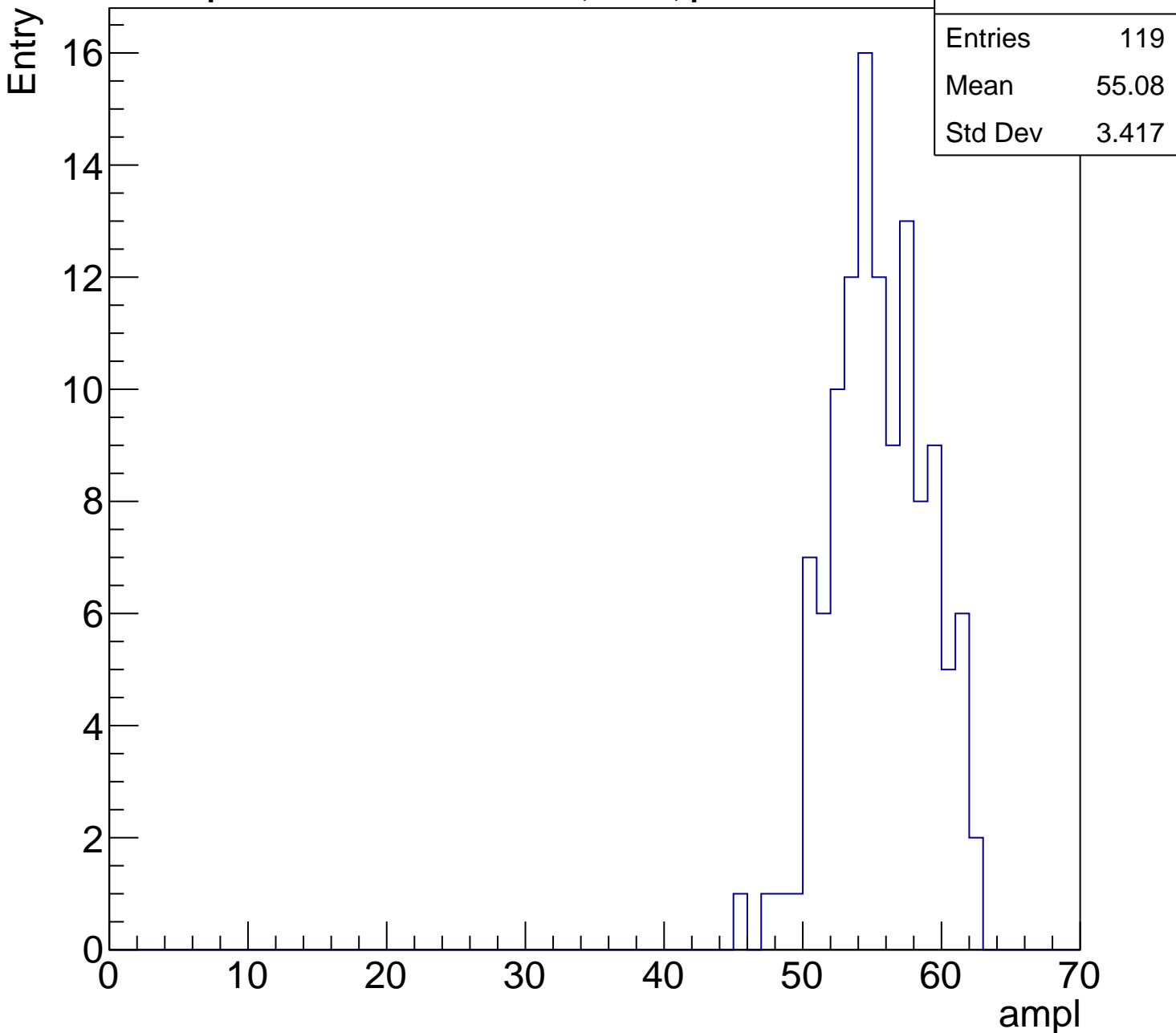
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 55.08 |
| Std Dev | 3.417 |

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch96, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

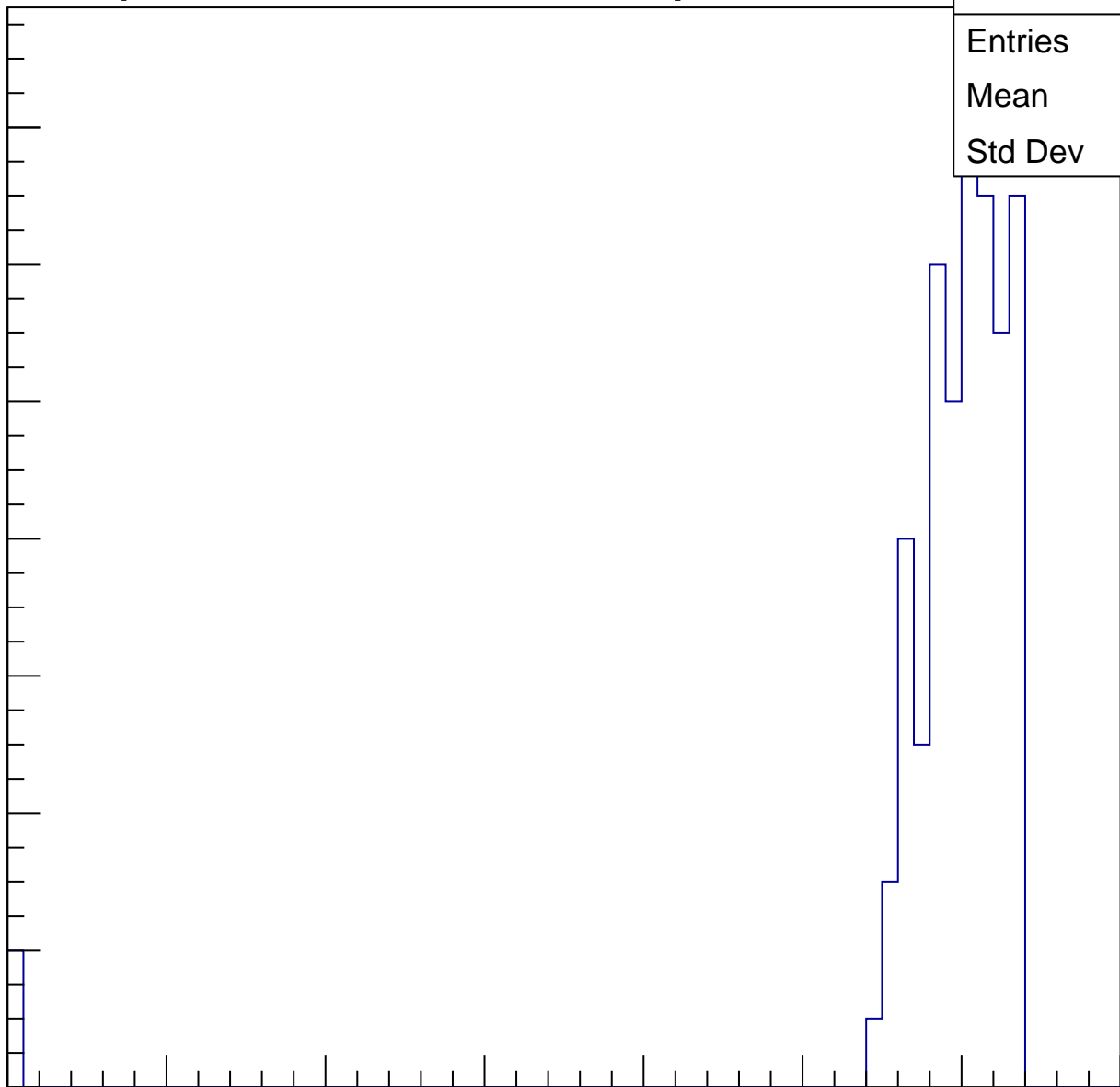
50

60

70

ampl

| | |
|---------|-------|
| Entries | 93 |
| Mean | 58.41 |
| Std Dev | 8.969 |

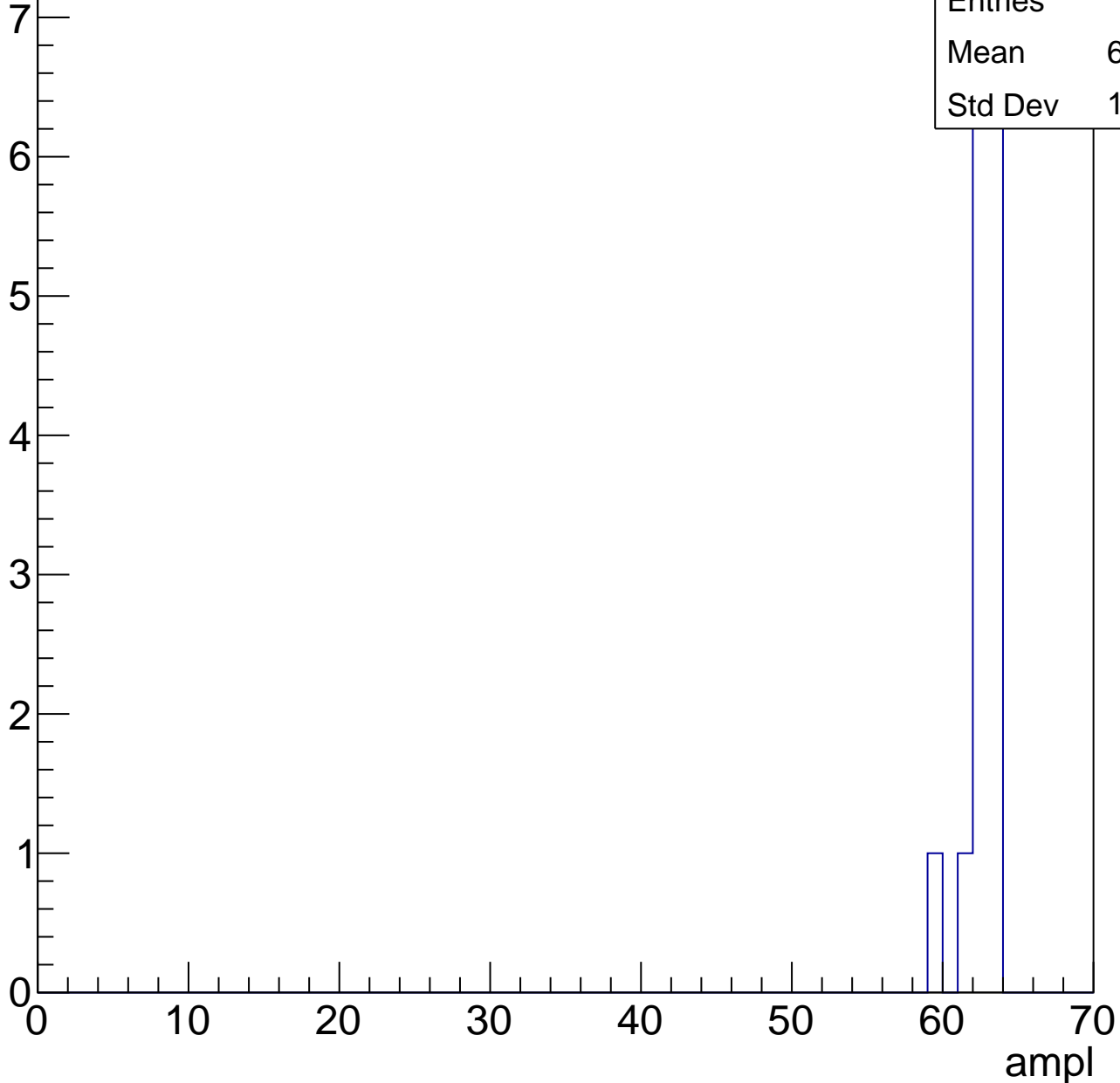


B1L001S, U26-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 16 |
| Mean | 62.19 |
| Std Dev | 1.014 |



B1L001S, U26-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



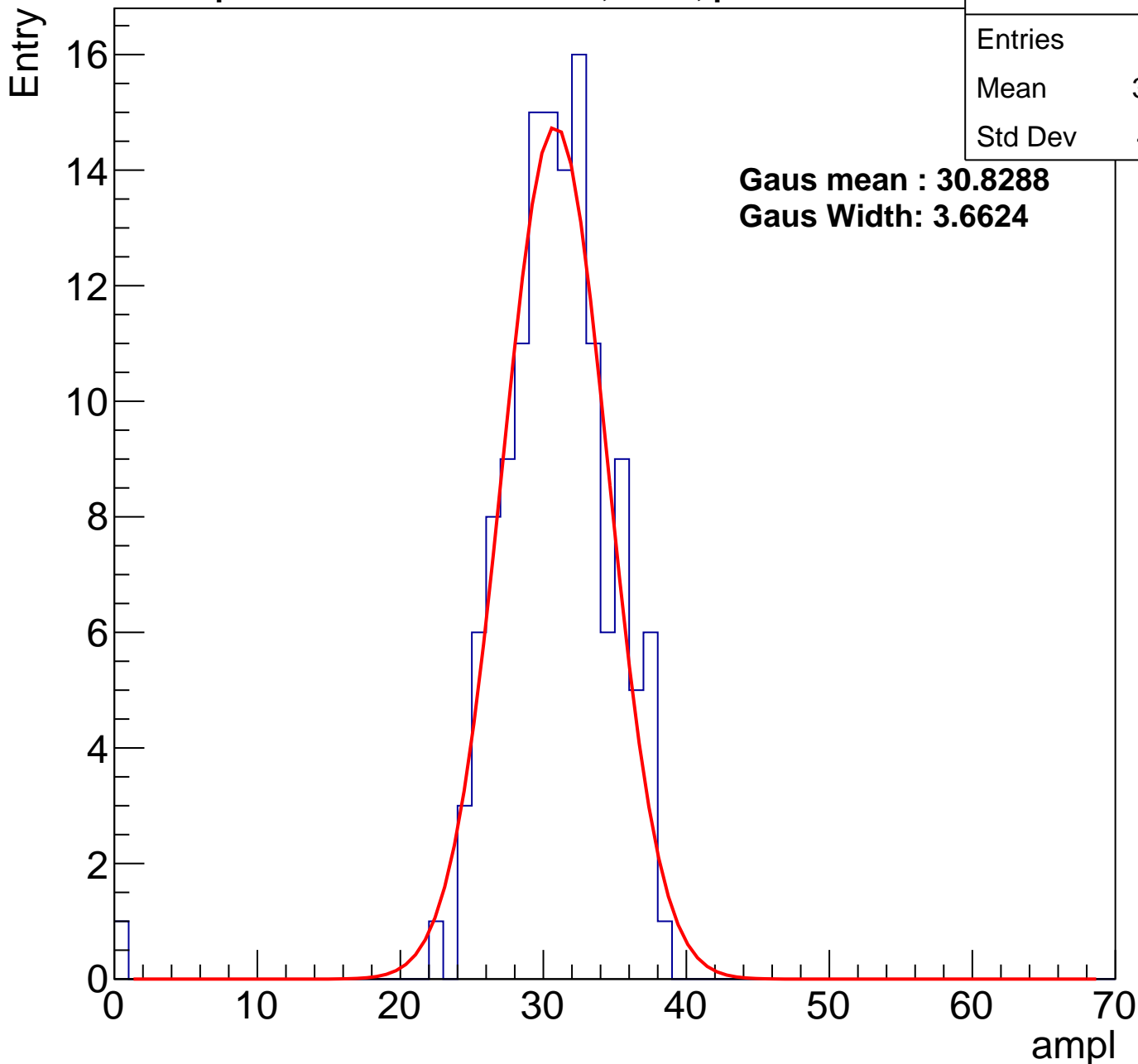
B1L001S, U26-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 137 |
| Mean | 30.34 |
| Std Dev | 4.281 |

Gaus mean : 30.8288

Gaus Width: 3.6624



B1L001S, U26-ch97, adc1

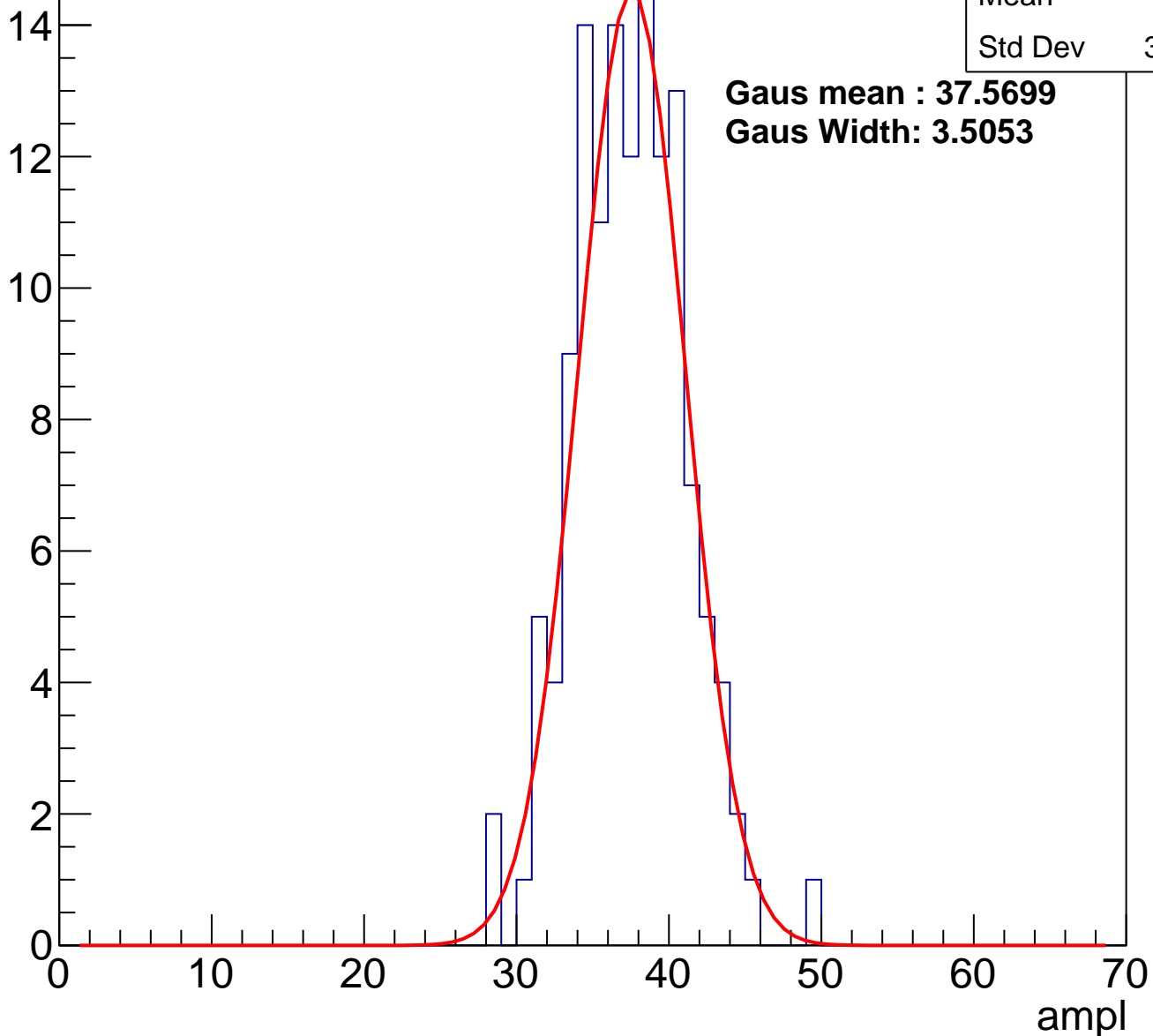
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 37 |
| Std Dev | 3.574 |

Gaus mean : 37.5699

Gaus Width: 3.5053

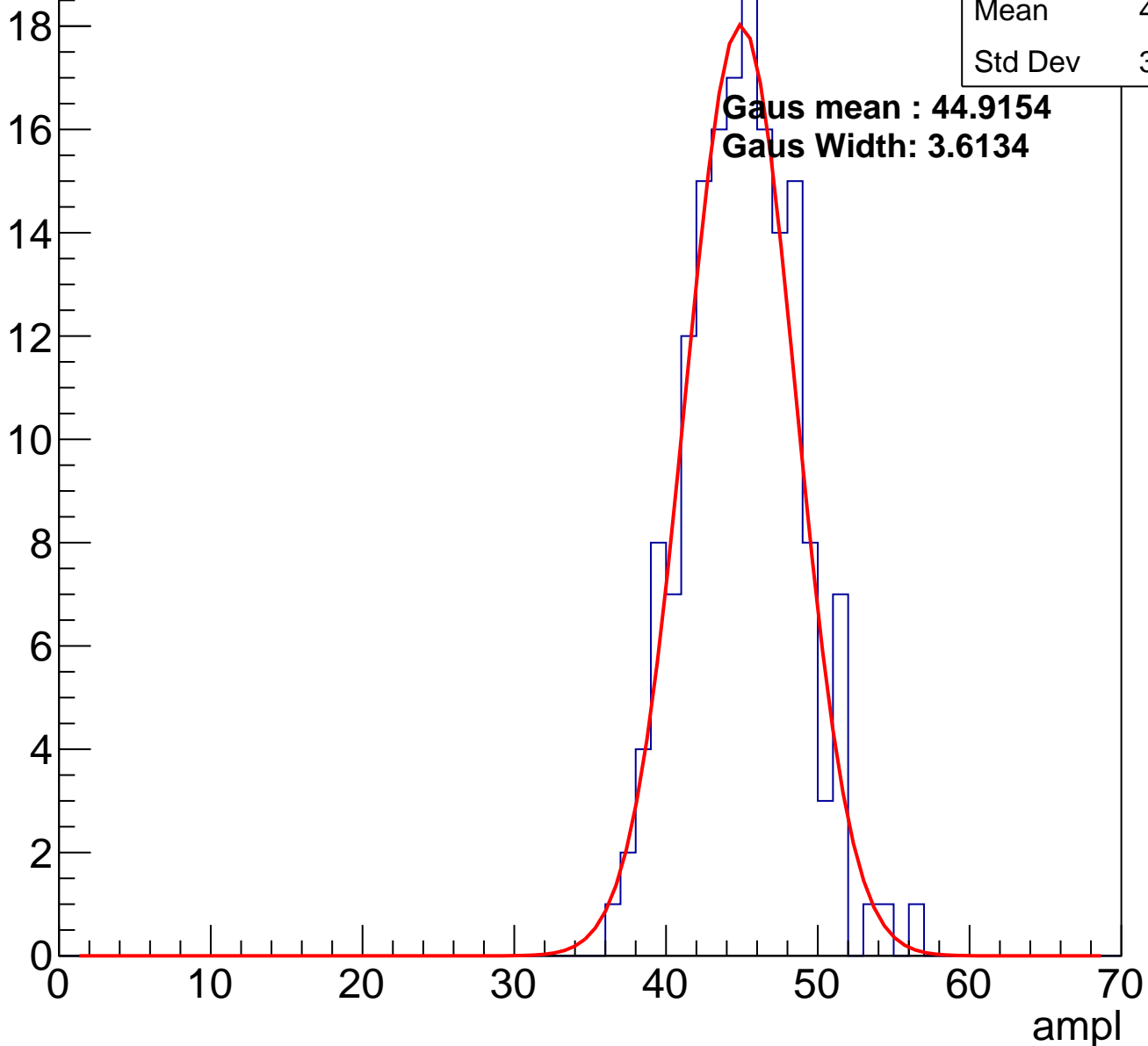
Entry



B1L001S, U26-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

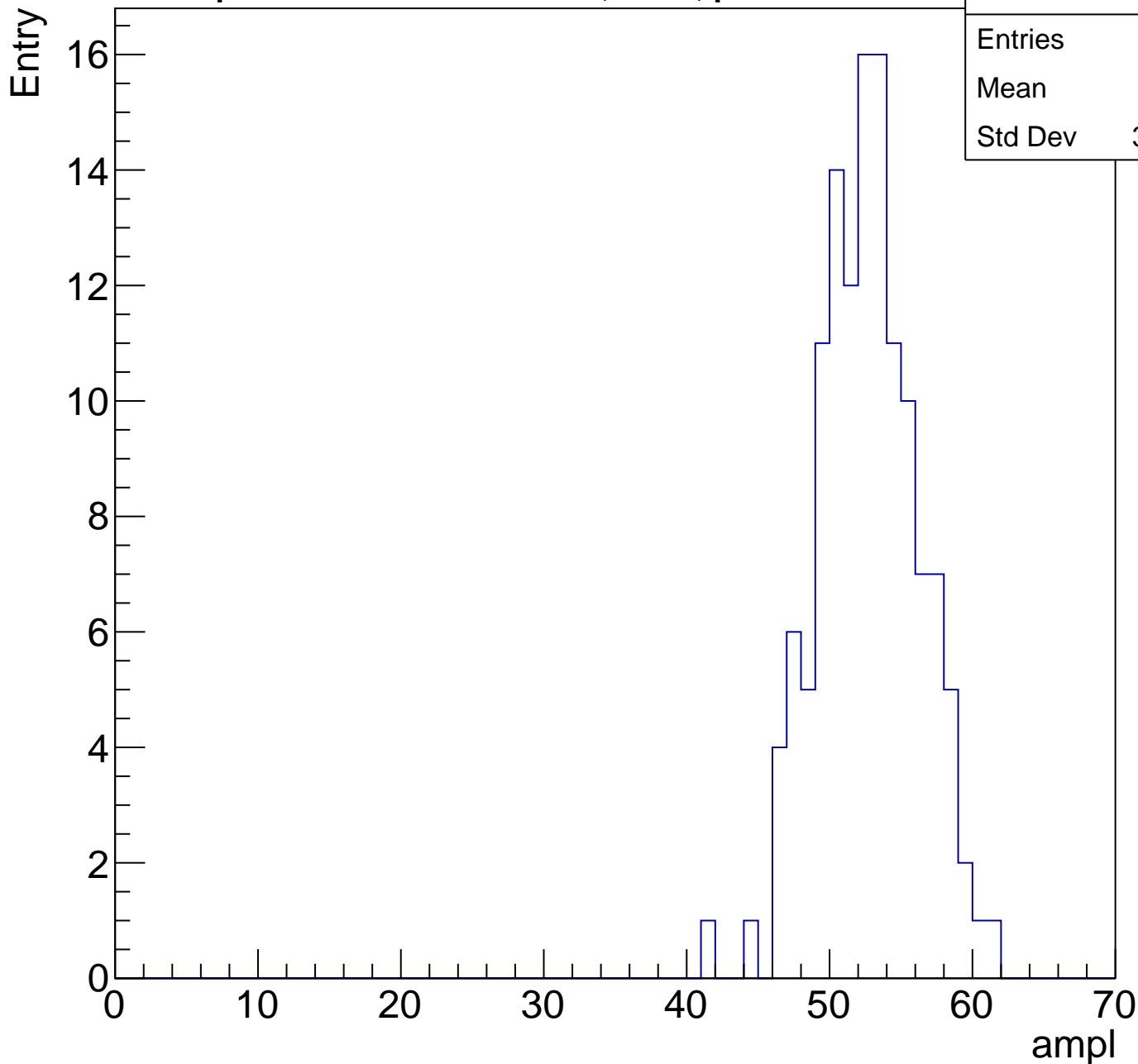
Entry



B1L001S, U26-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 130 |
| Mean | 52.2 |
| Std Dev | 3.487 |



B1L001S, U26-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

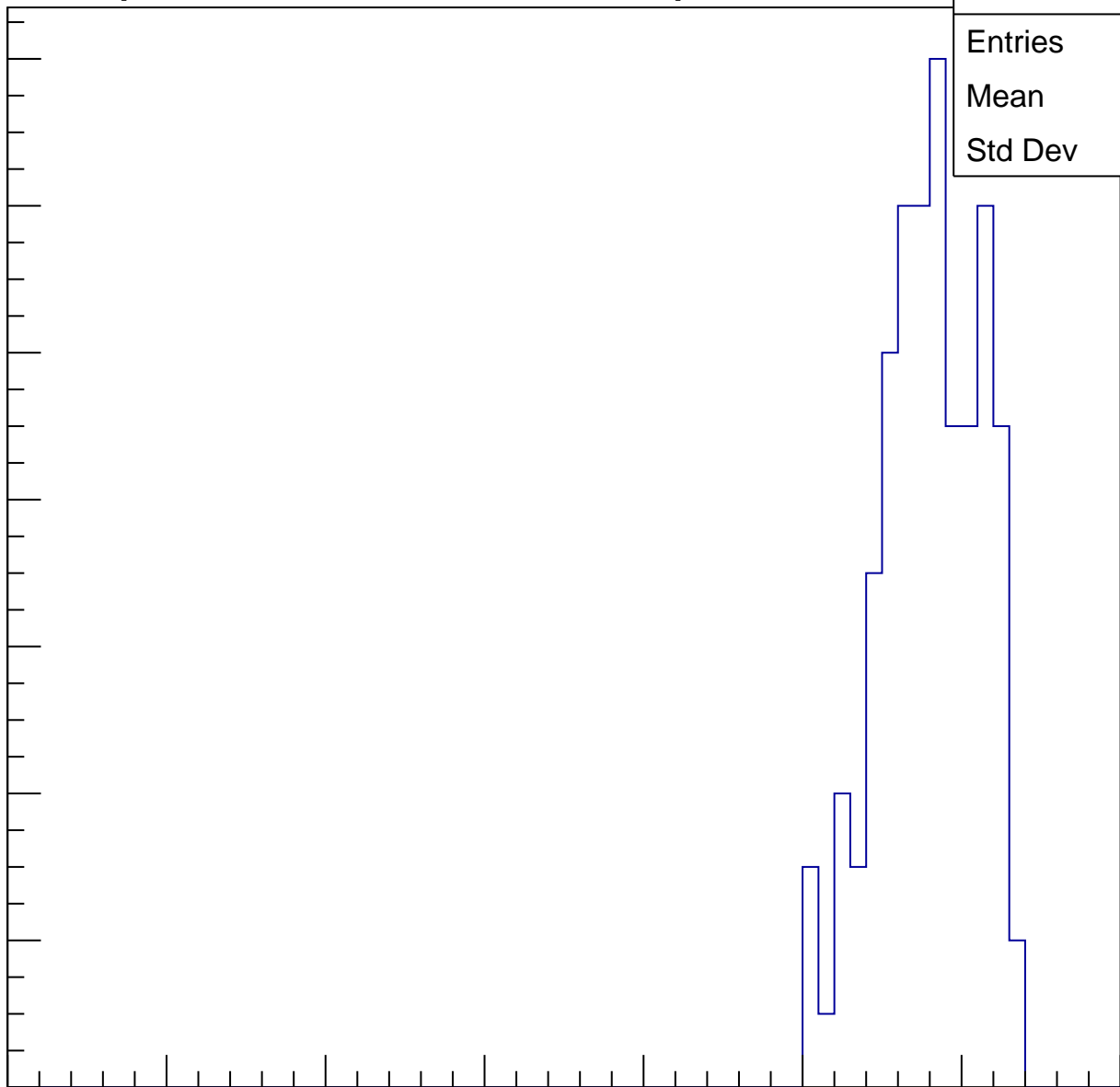
| | |
|---------|-------|
| Entries | 107 |
| Mean | 57.49 |
| Std Dev | 3.119 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch97, adc5

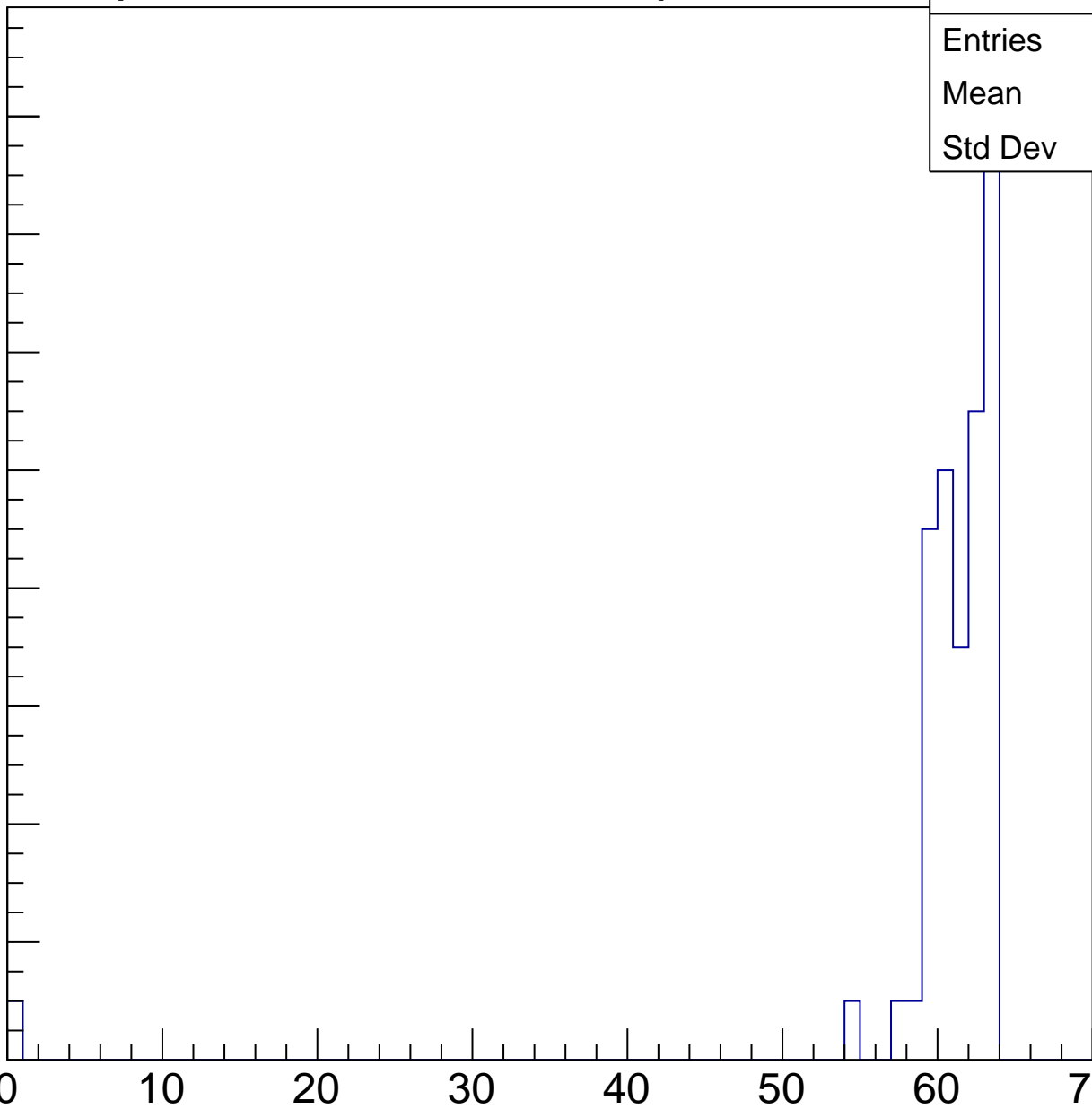
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 58 |
| Mean | 60 |
| Std Dev | 8.16 |

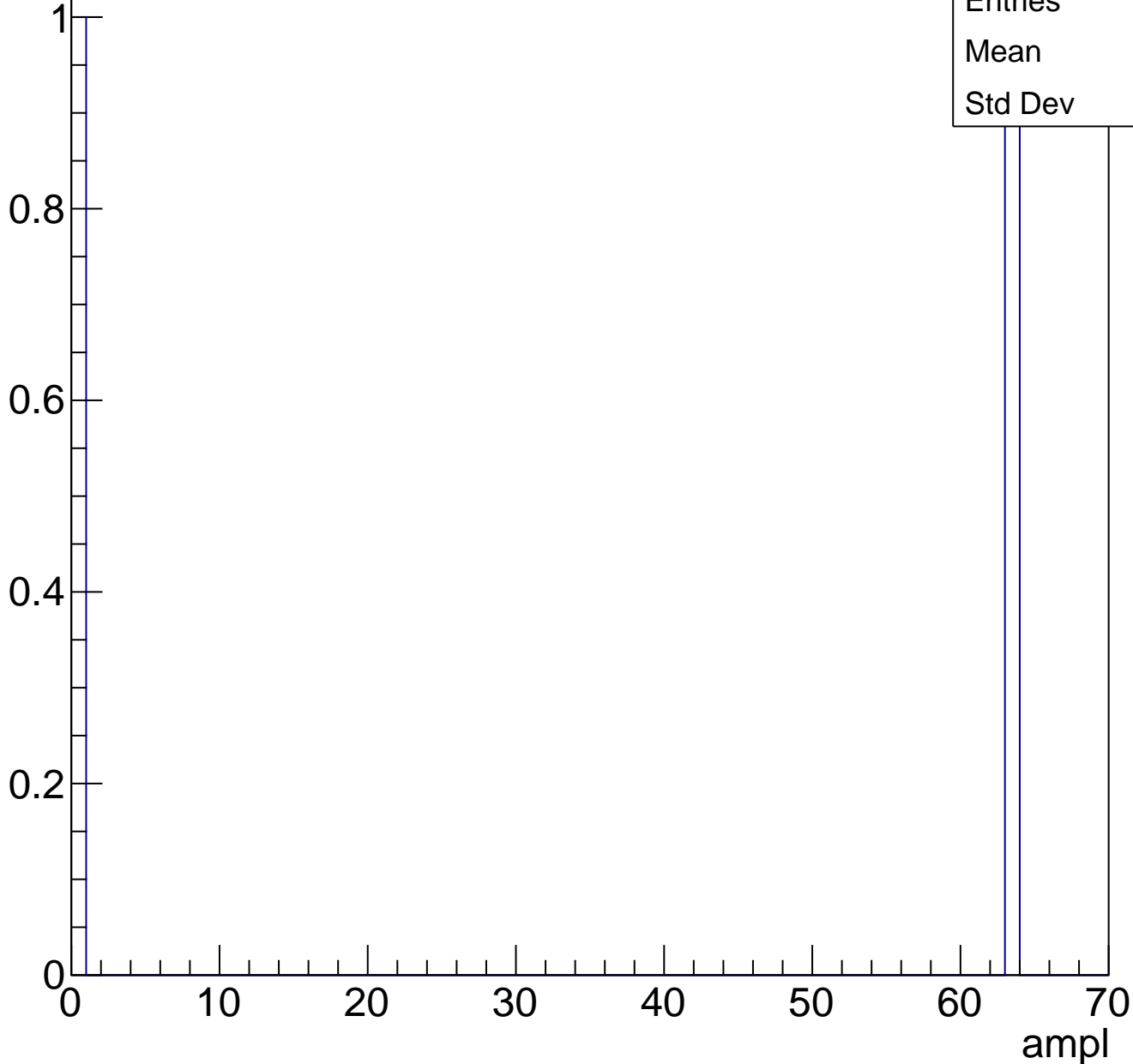
ampl



B1L001S, U26-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

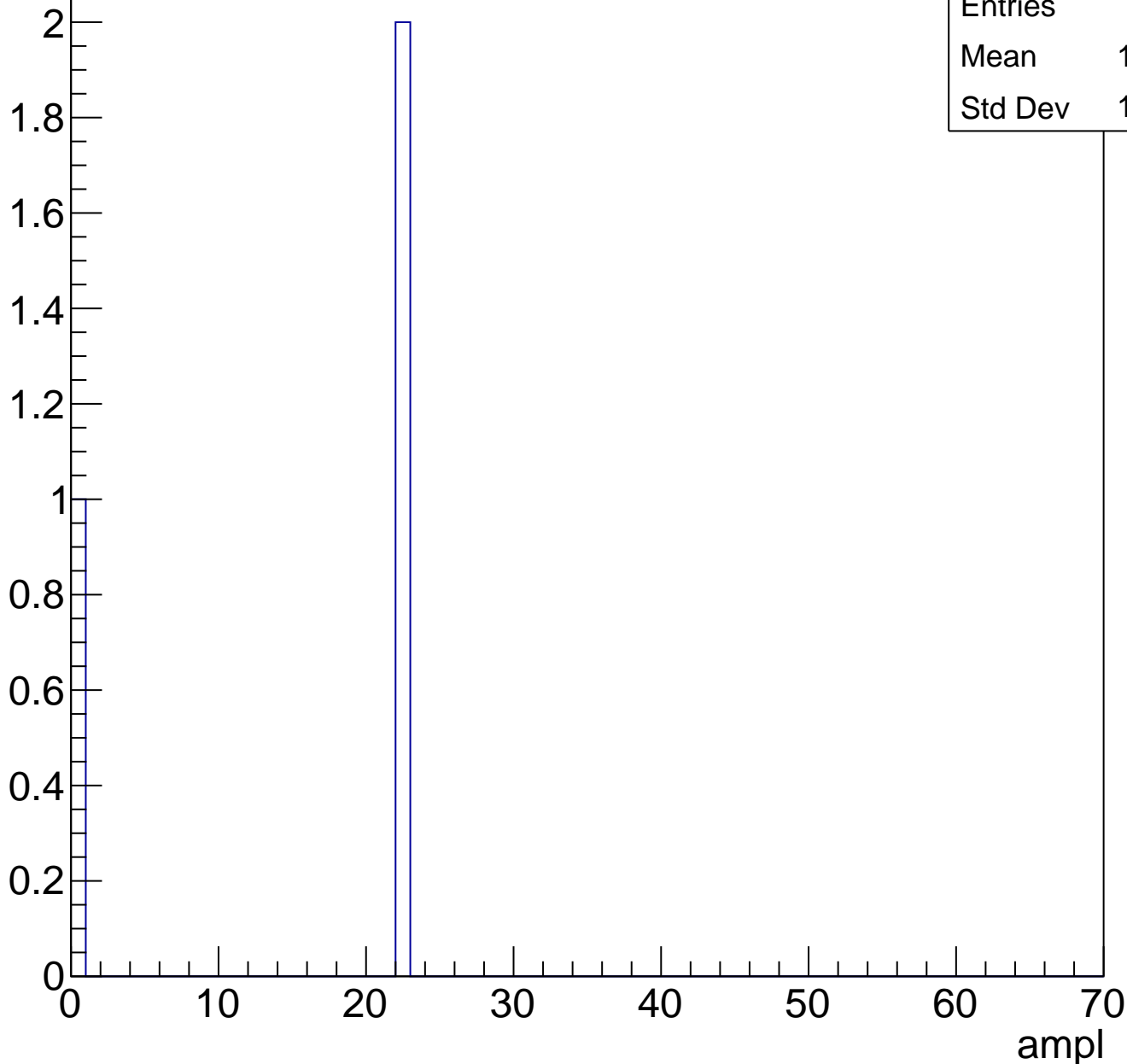
Entry



B1L001S, U26-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch98, adc0

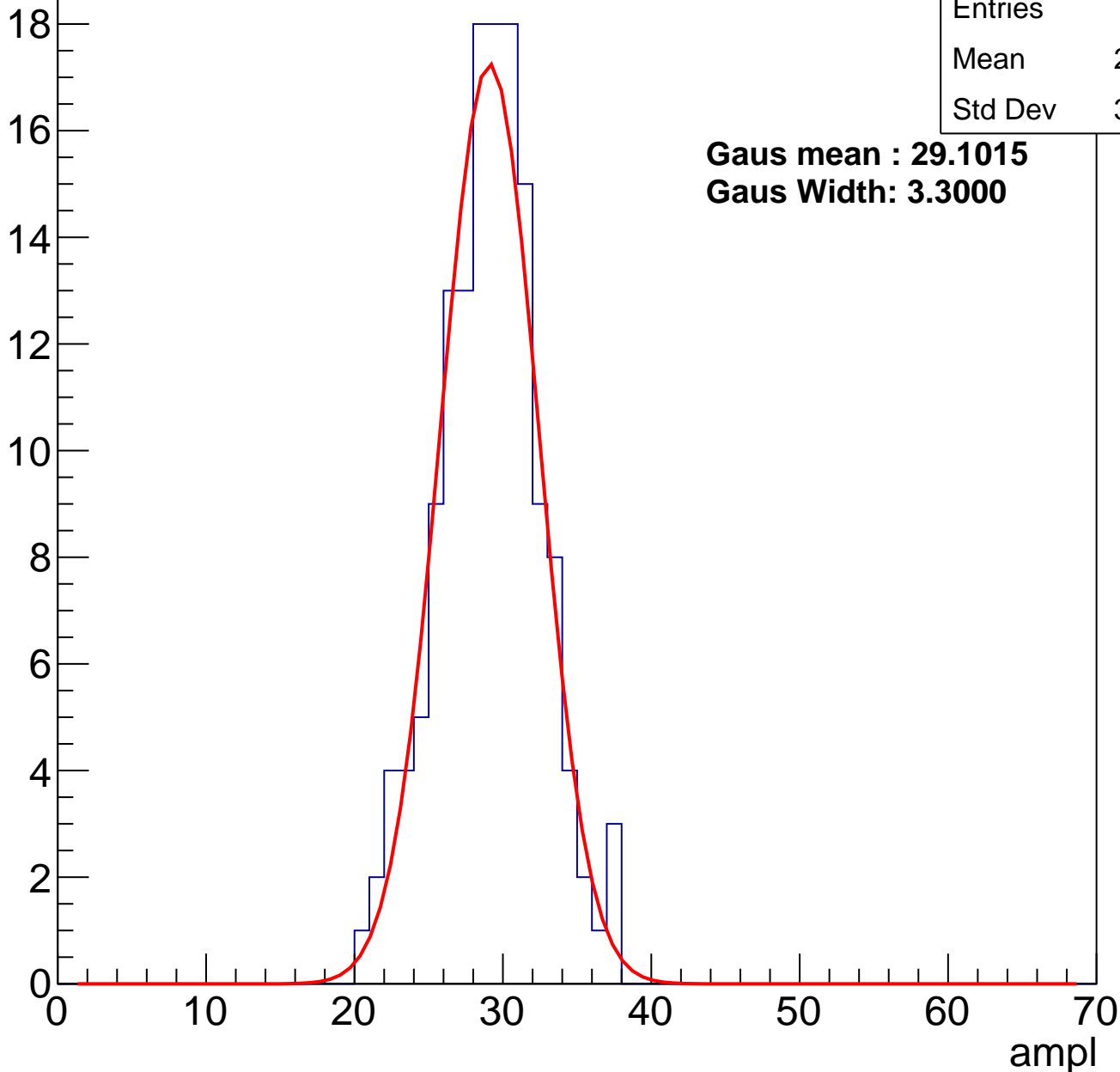
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 147 |
| Mean | 28.65 |
| Std Dev | 3.388 |

Gaus mean : 29.1015

Gaus Width: 3.3000

Entry



B1L001S, U26-ch98, adc1

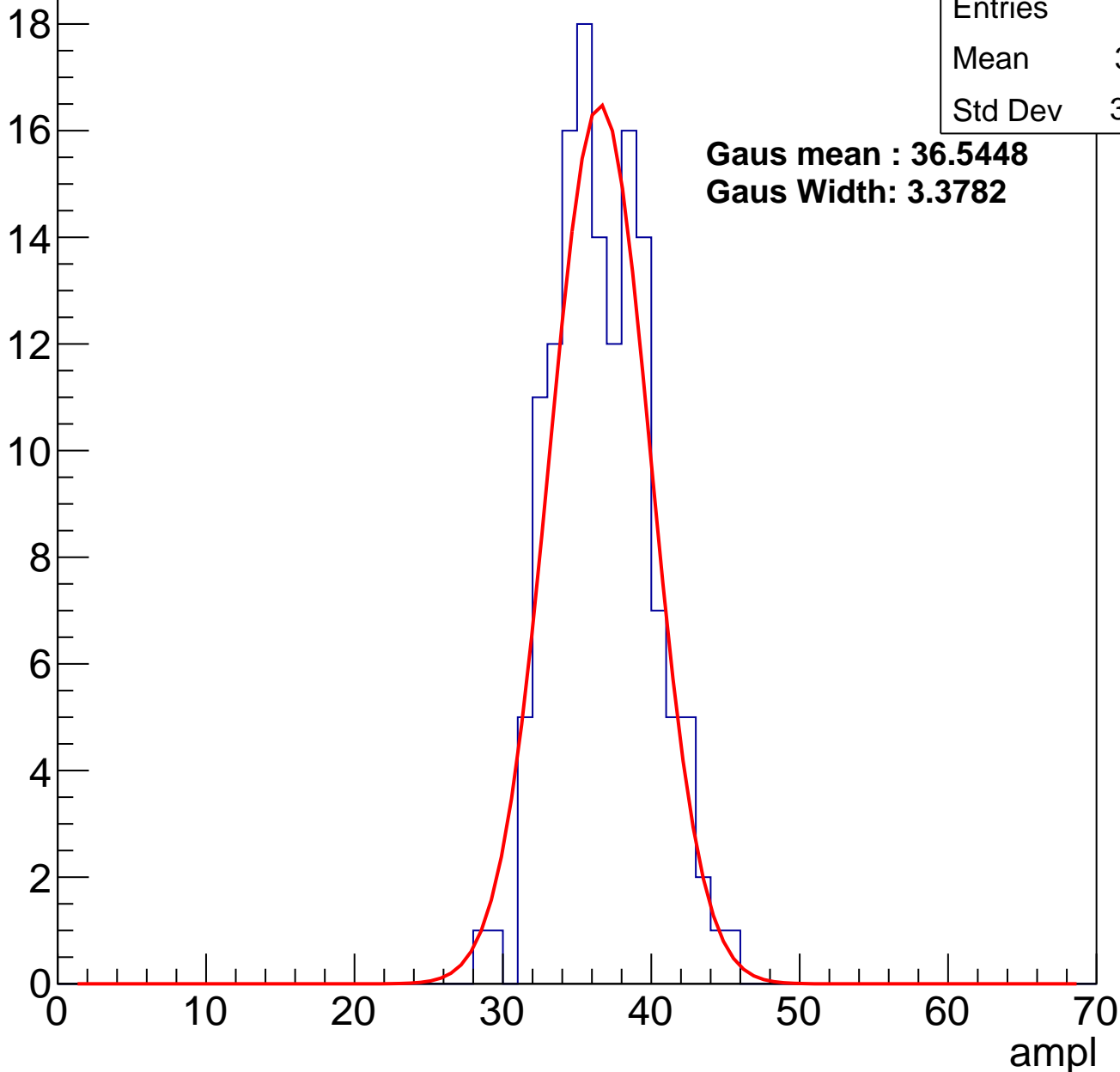
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 36.21 |
| Std Dev | 3.217 |

Gaus mean : 36.5448

Gaus Width: 3.3782

Entry



B1L001S, U26-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

113

Mean

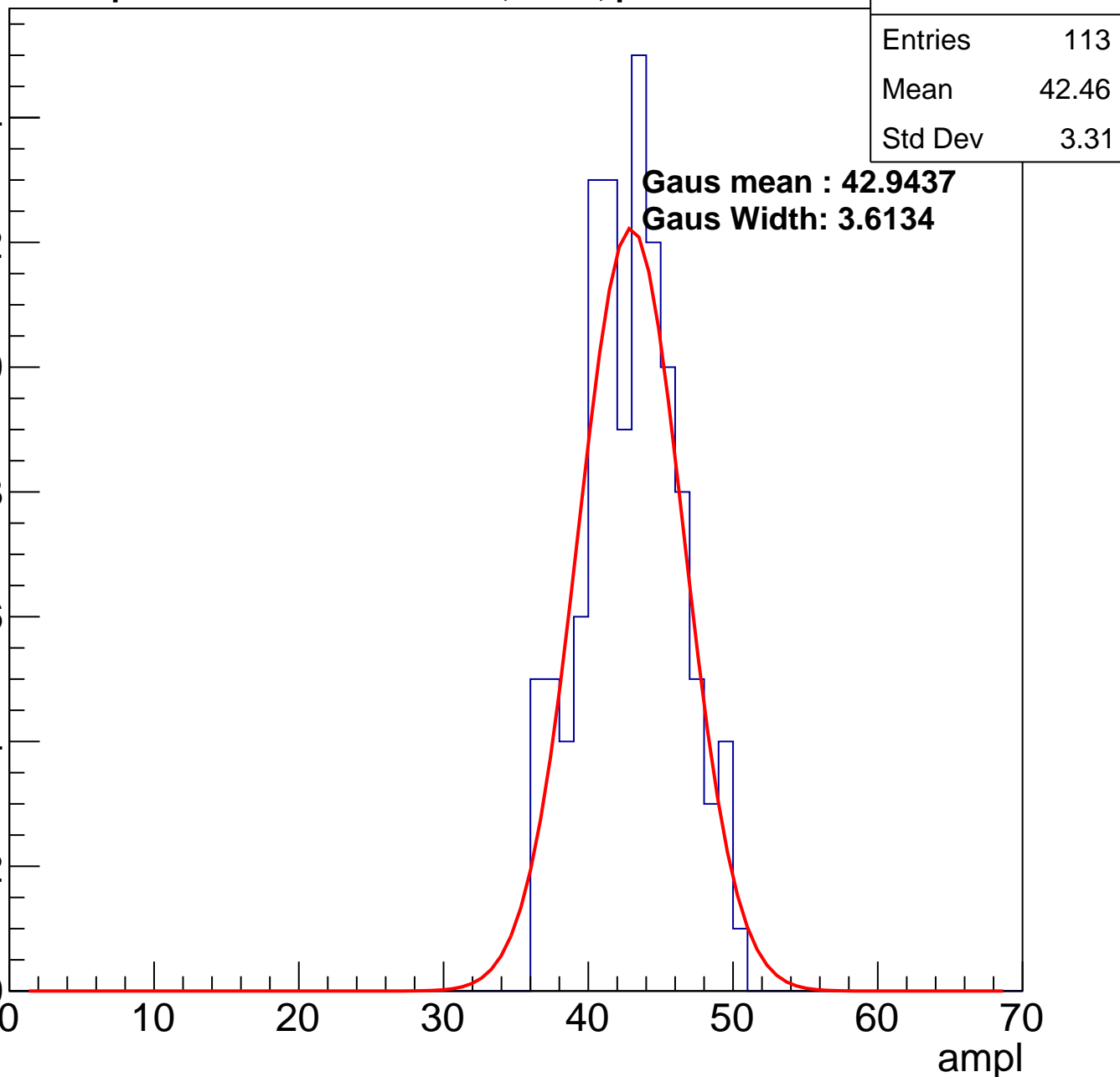
42.46

Std Dev

3.31

Gaus mean : 42.9437

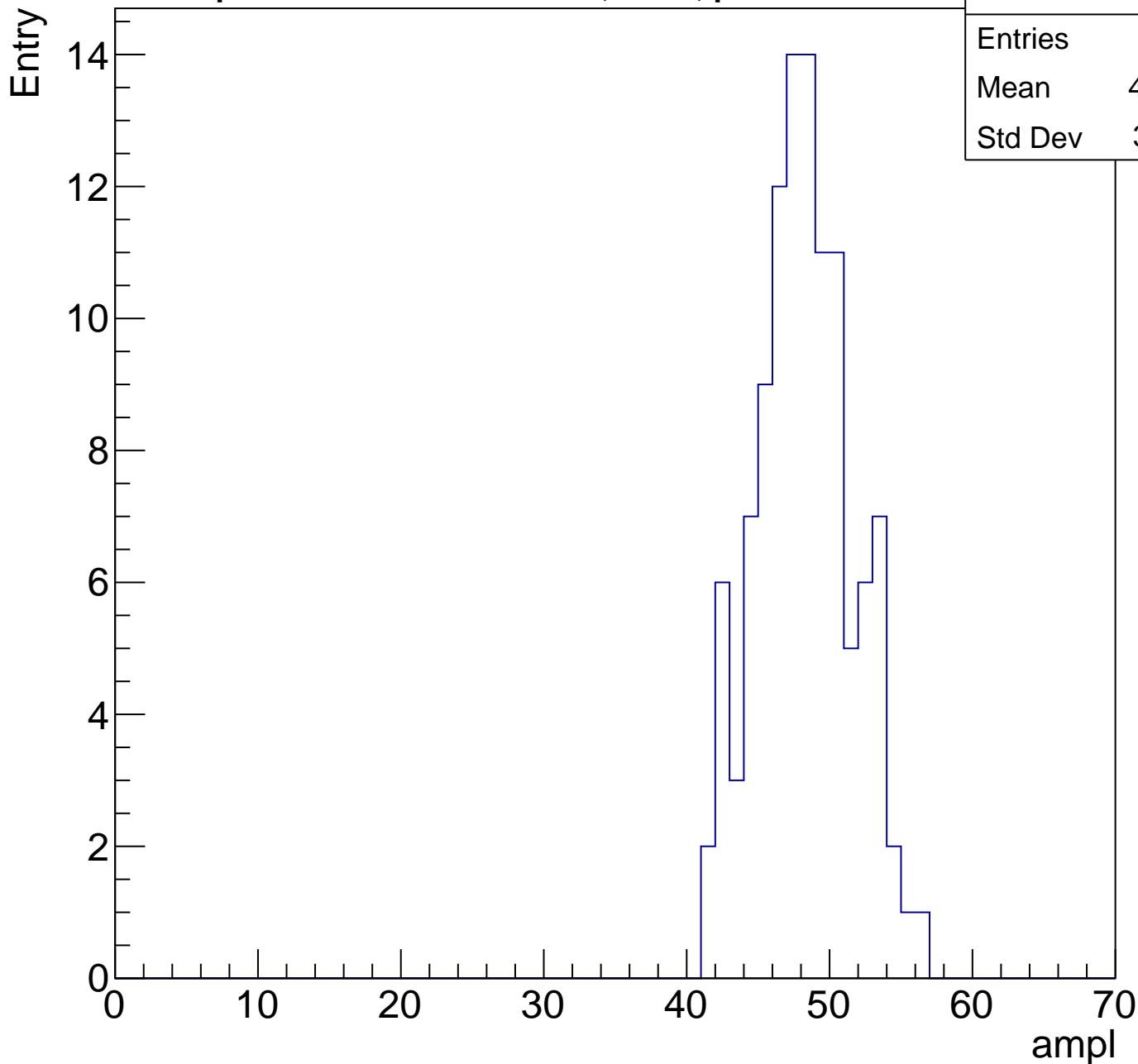
Gaus Width: 3.6134



B1L001S, U26-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

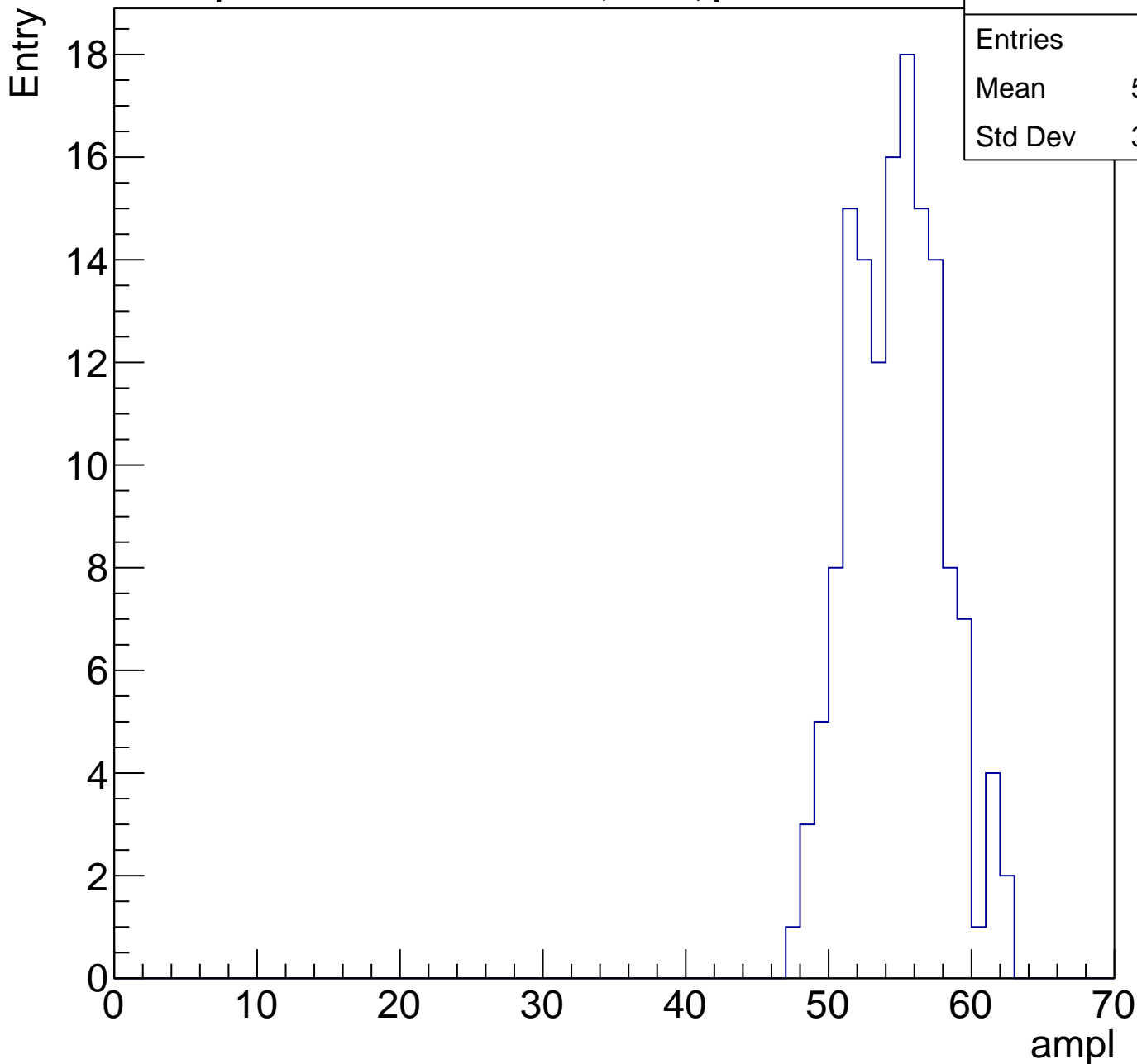
| | |
|---------|-------|
| Entries | 111 |
| Mean | 47.78 |
| Std Dev | 3.281 |



B1L001S, U26-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 54.28 |
| Std Dev | 3.205 |



B1L001S, U26-ch98, adc5

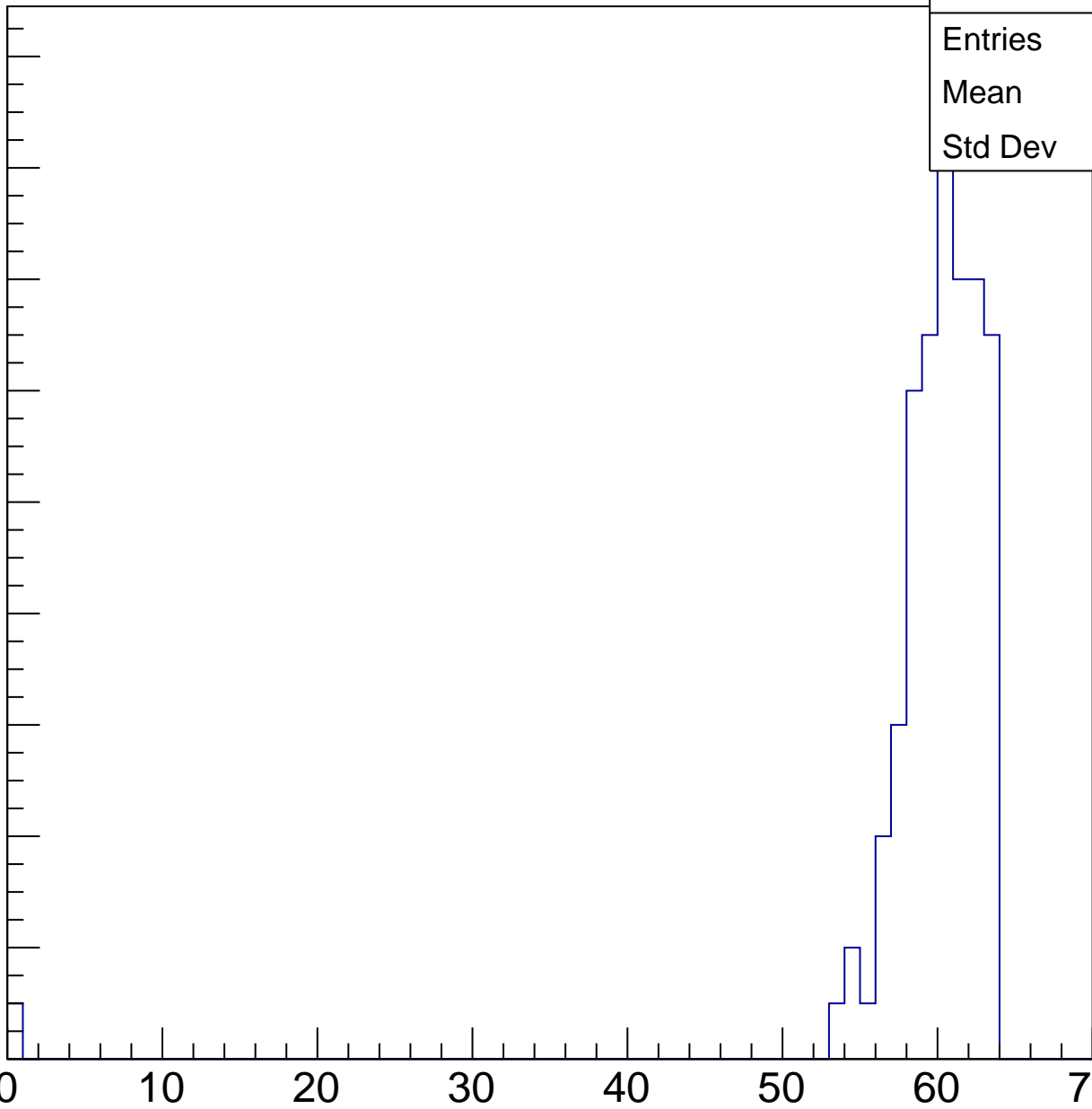
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 99 |
| Mean | 59.25 |
| Std Dev | 6.401 |

ampl

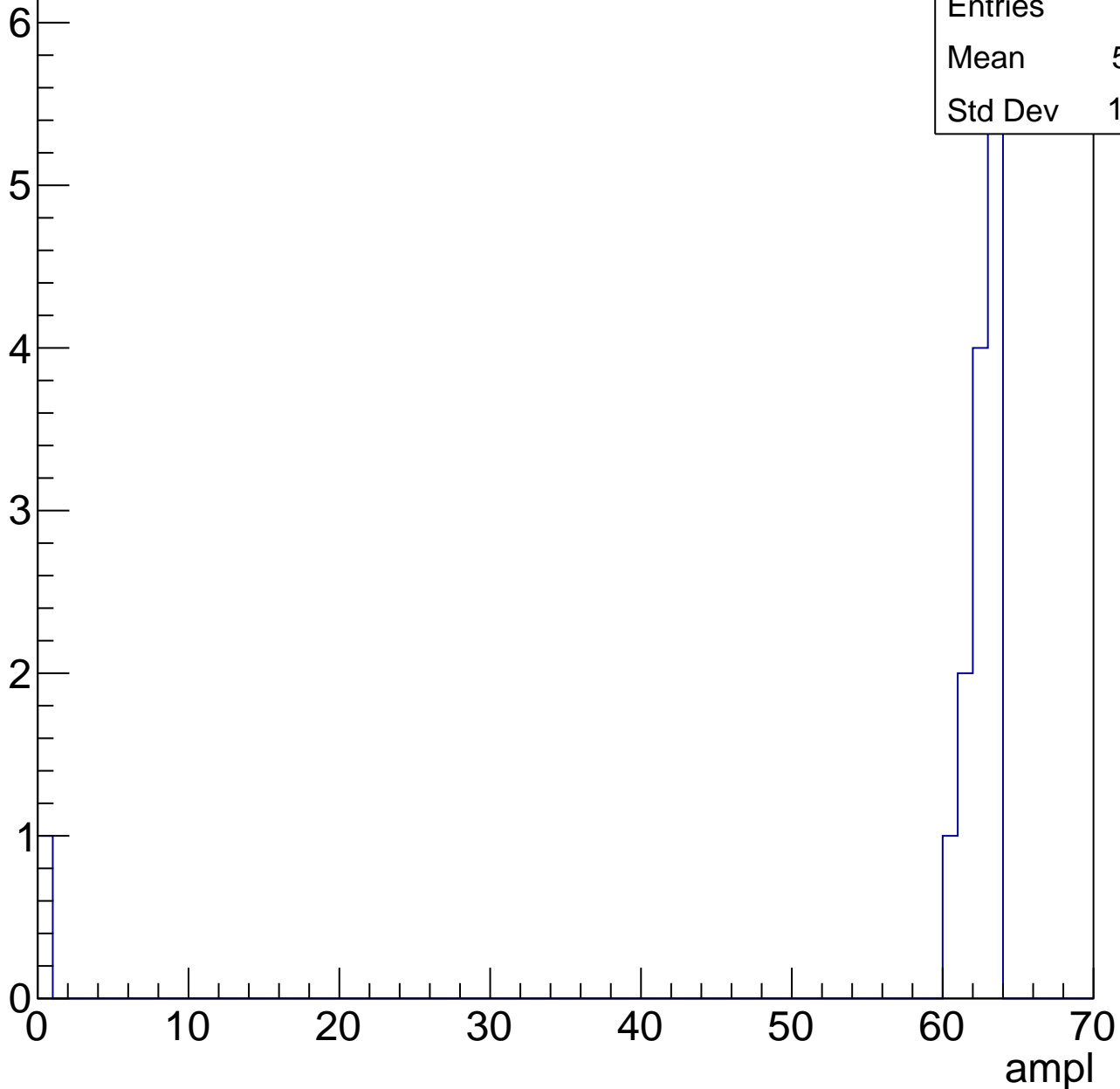


B1L001S, U26-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.71 |
| Std Dev | 16.03 |



B1L001S, U26-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch99, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 119 |
| Mean | 30.5 |
| Std Dev | 3.305 |

Gaus mean : 30.8019

Gaus Width: 3.6613

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

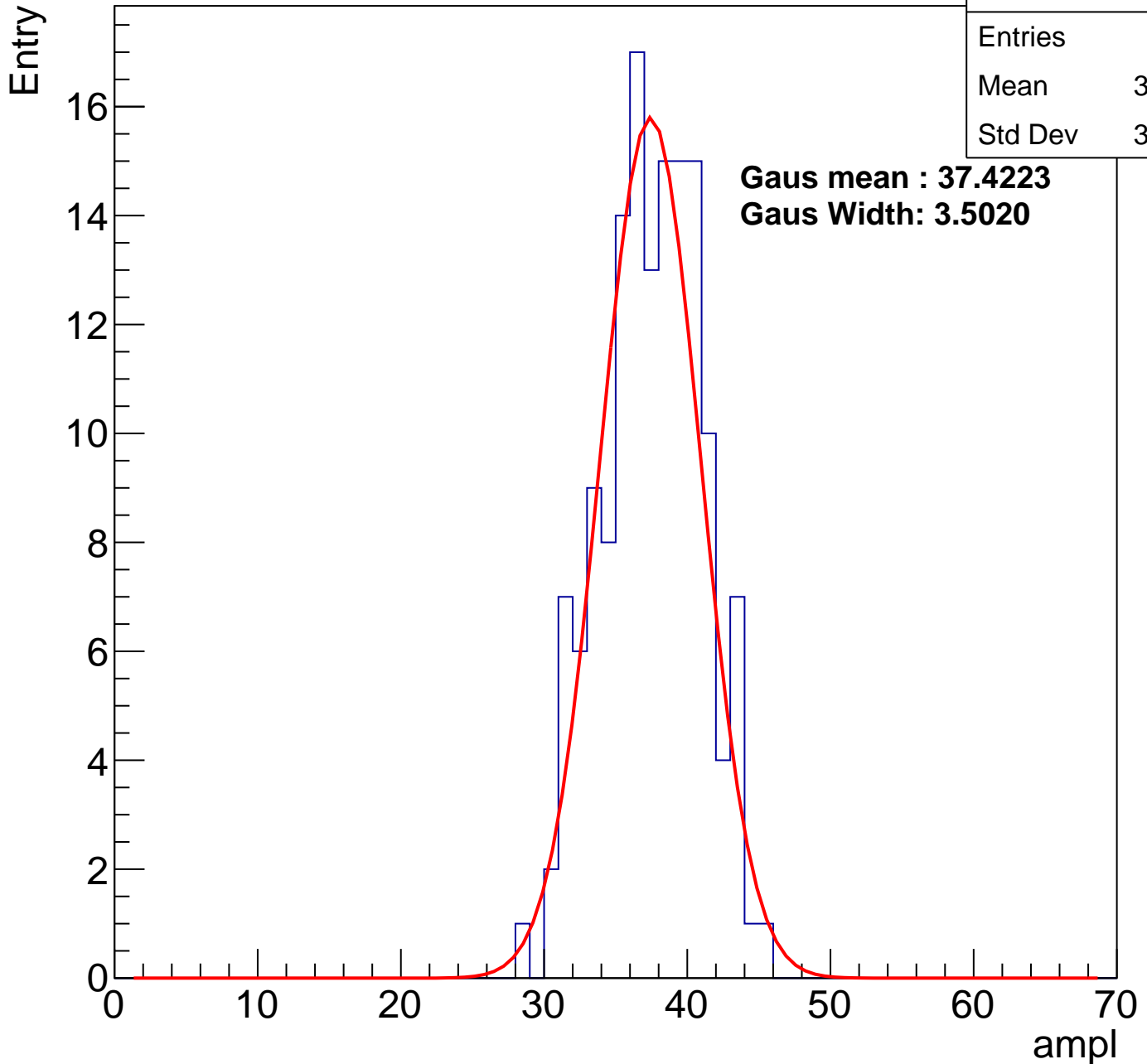
70

B1L001S, U26-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 37.05 |
| Std Dev | 3.429 |

Gaus mean : 37.4223
Gaus Width: 3.5020



B1L001S, U26-ch99, adc2

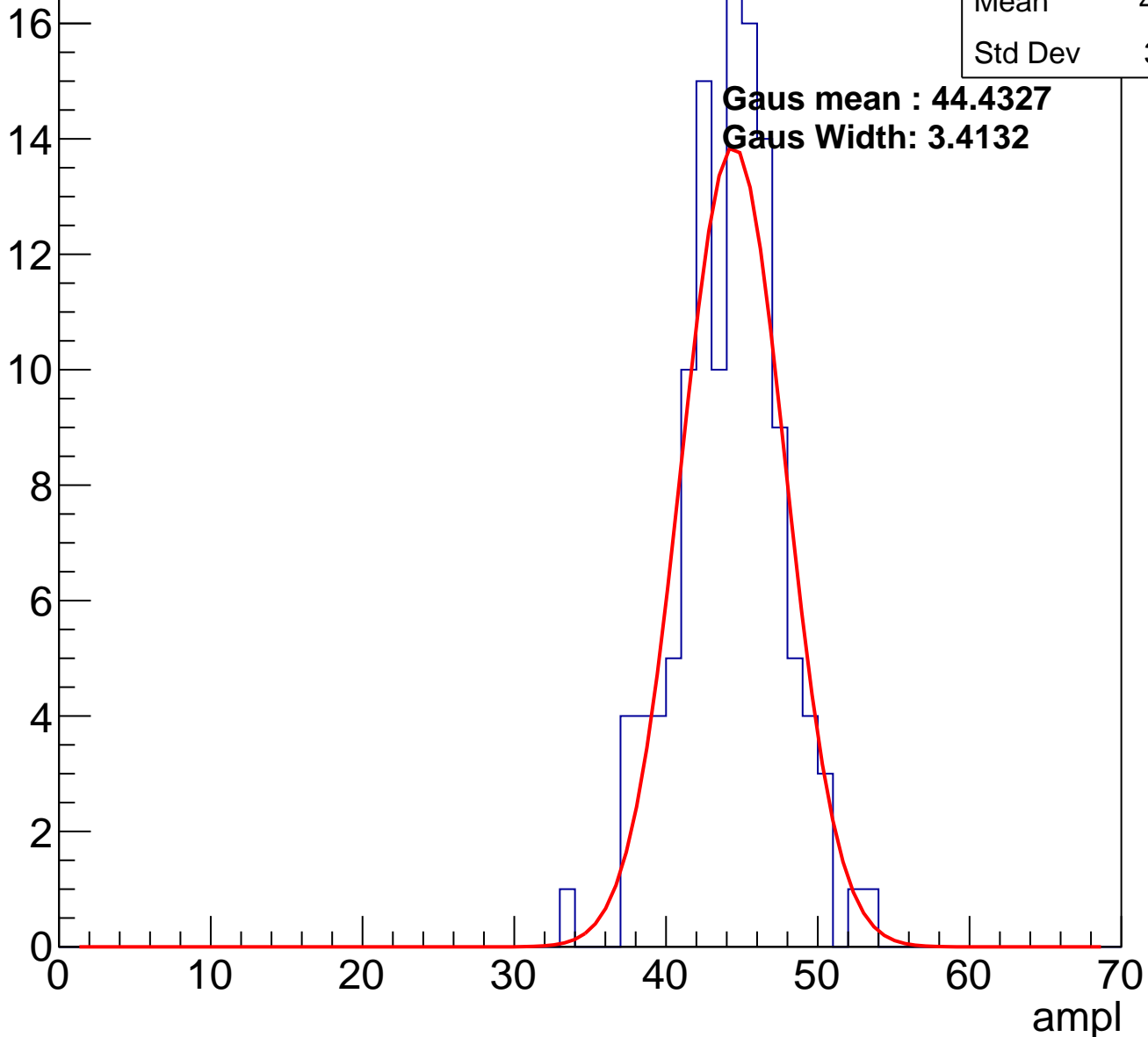
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 43.78 |
| Std Dev | 3.351 |

Gaus mean : 44.4327

Gaus Width: 3.4132

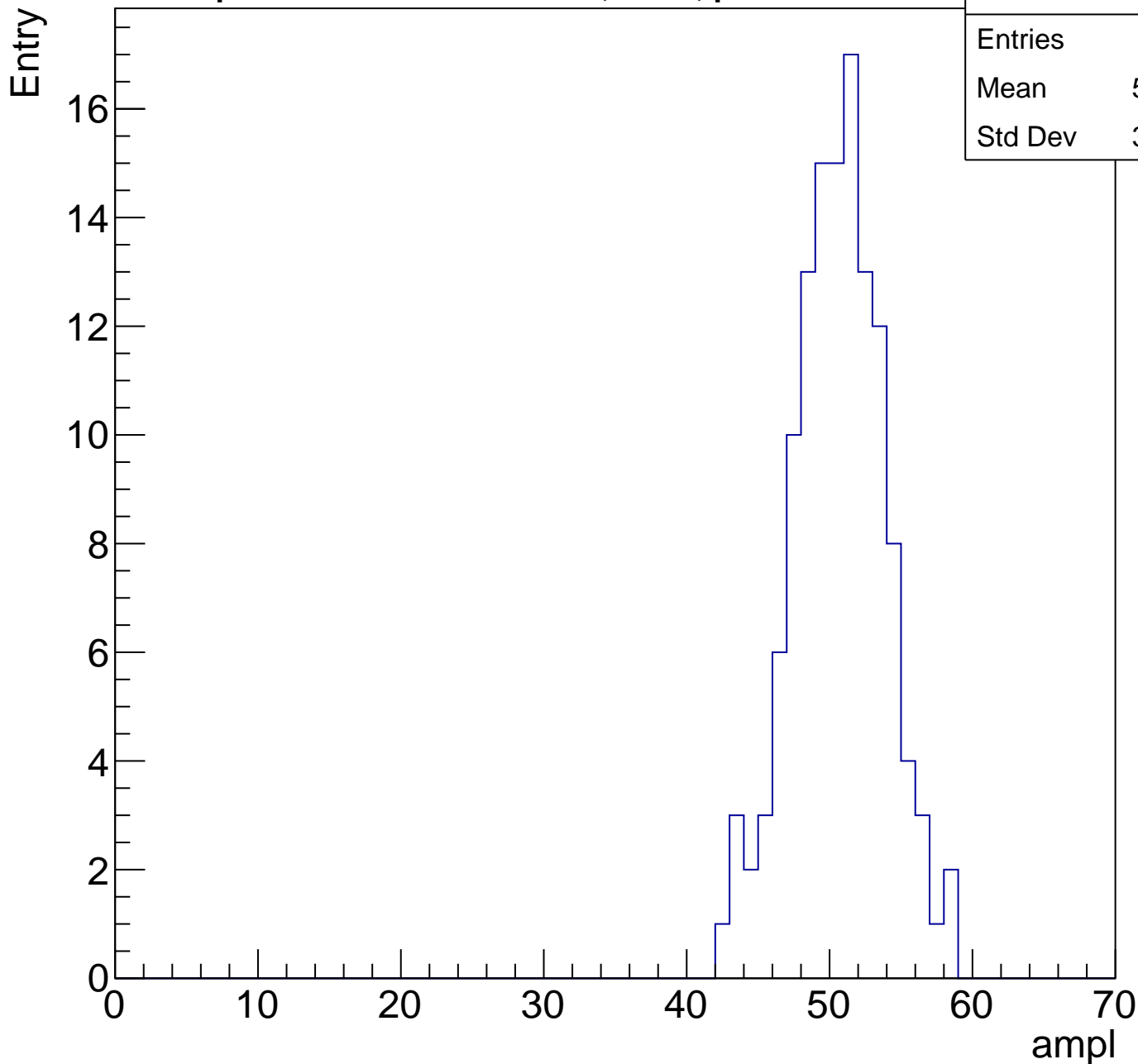
Entry



B1L001S, U26-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

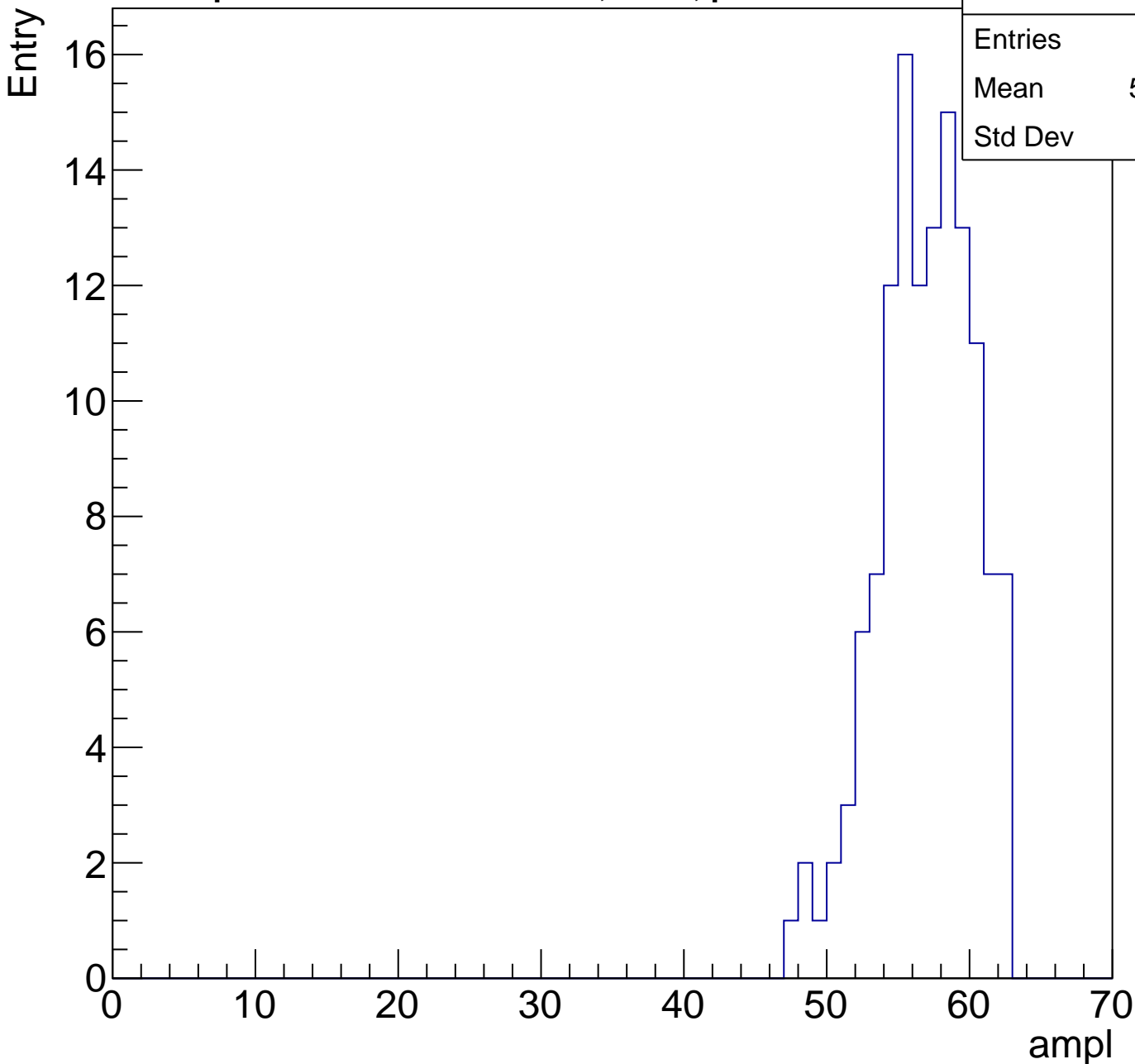
| | |
|---------|-------|
| Entries | 128 |
| Mean | 50.16 |
| Std Dev | 3.184 |



B1L001S, U26-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 56.46 |
| Std Dev | 3.3 |



B1L001S, U26-ch99, adc5

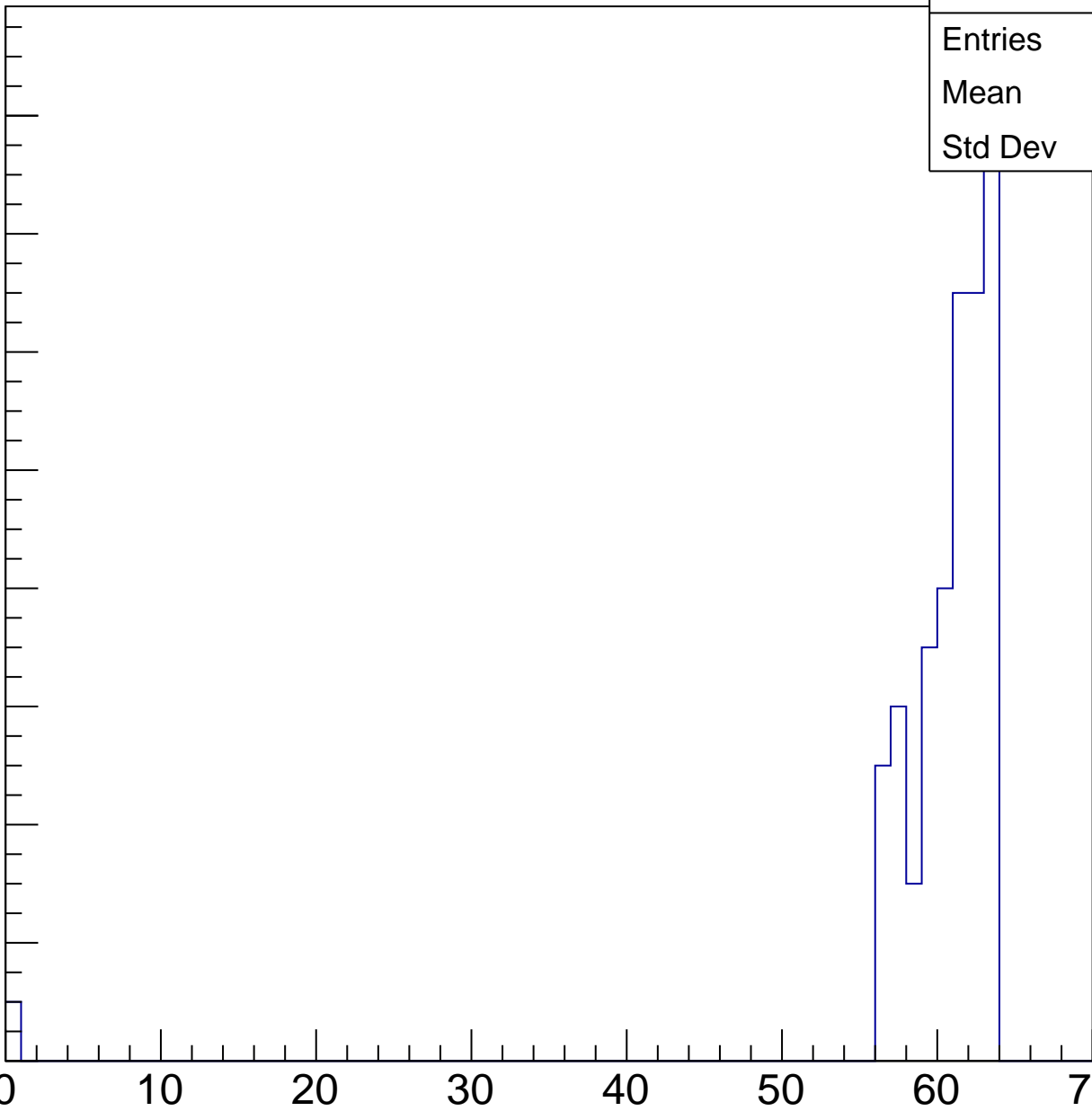
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 59.71 |
| Std Dev | 7.37 |

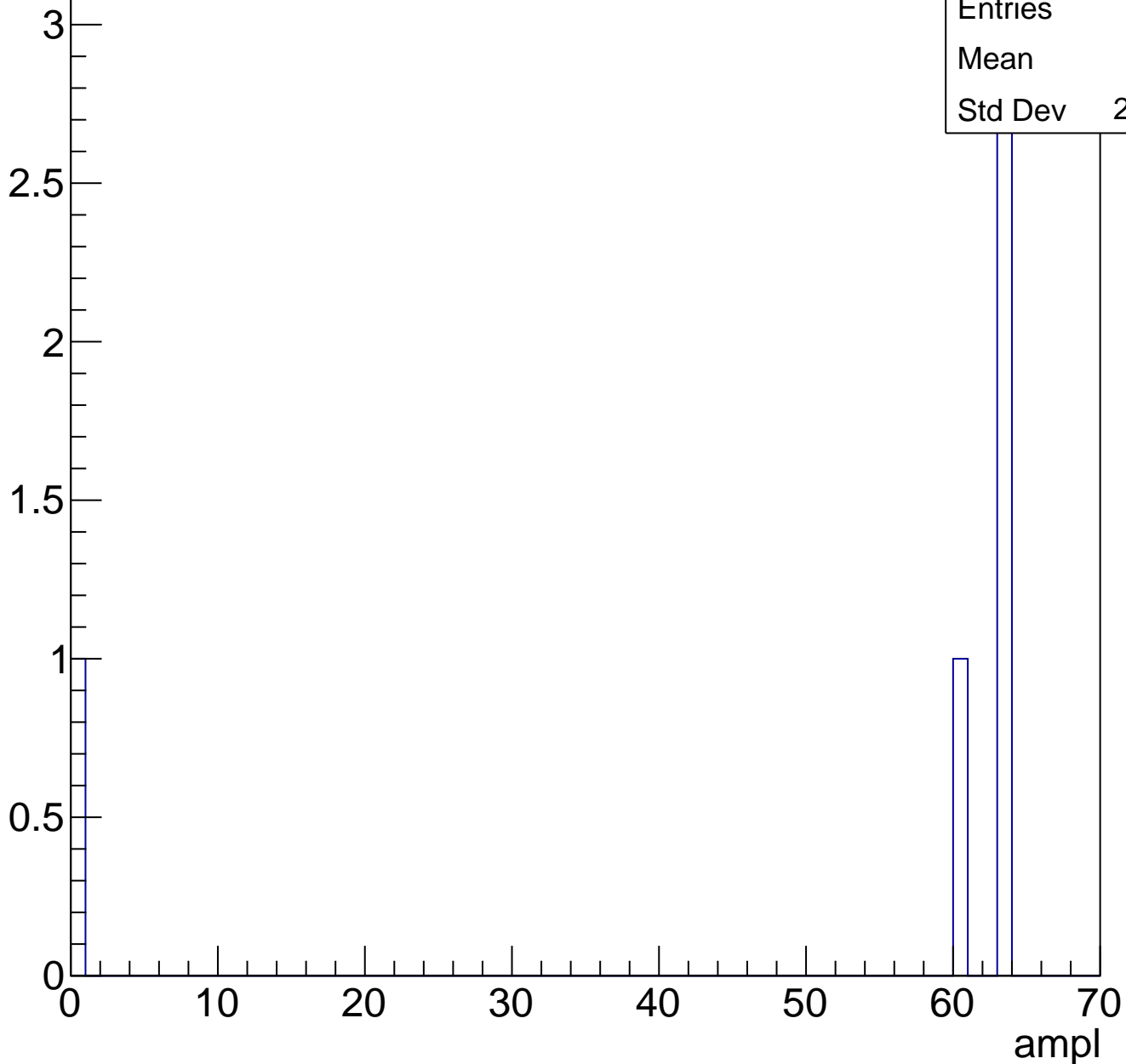
ampl



B1L001S, U26-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch100, adc0

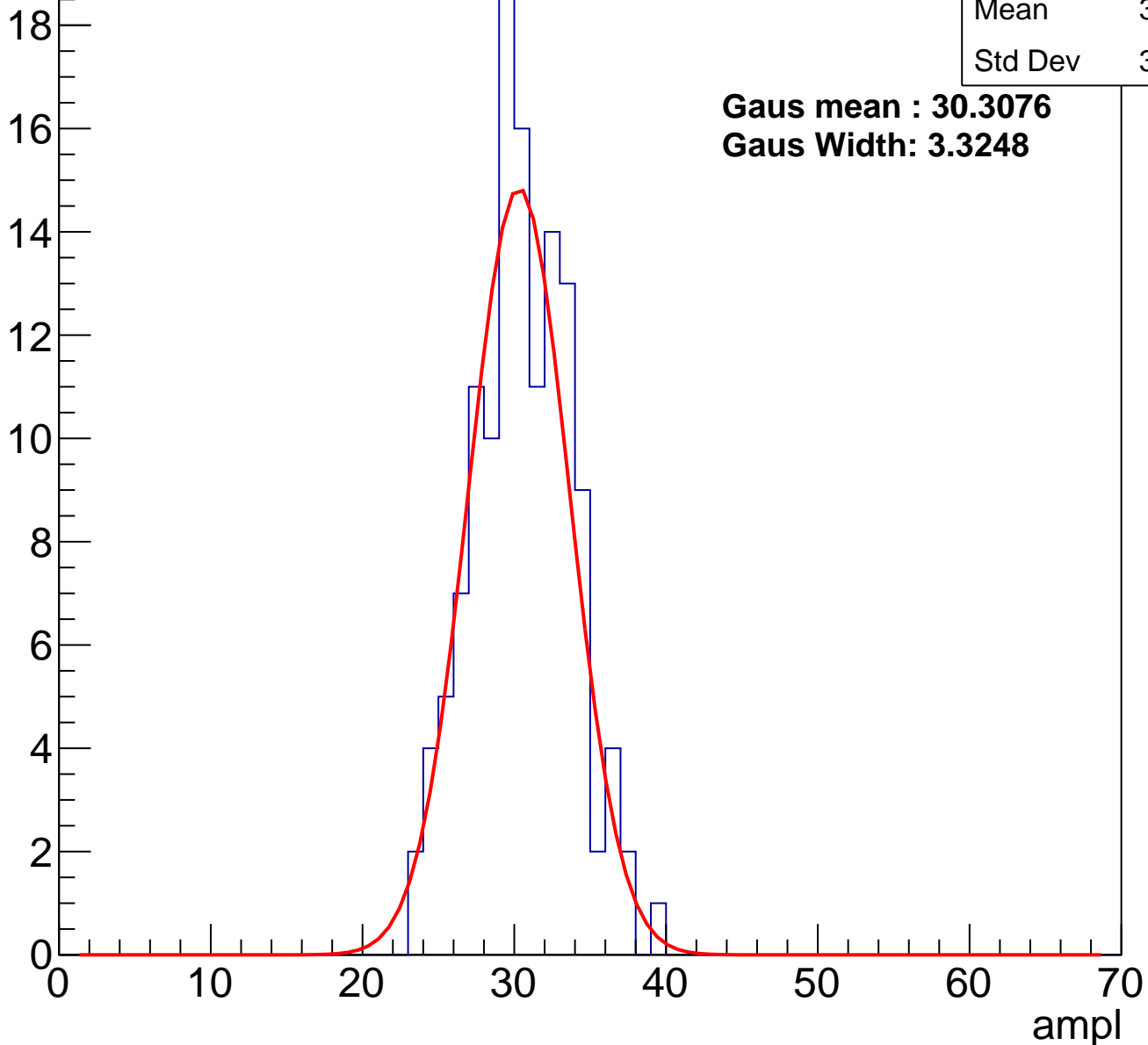
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 130 |
| Mean | 30.06 |
| Std Dev | 3.208 |

Gaus mean : 30.3076

Gaus Width: 3.3248

Entry



B1L001S, U26-ch100, adc1

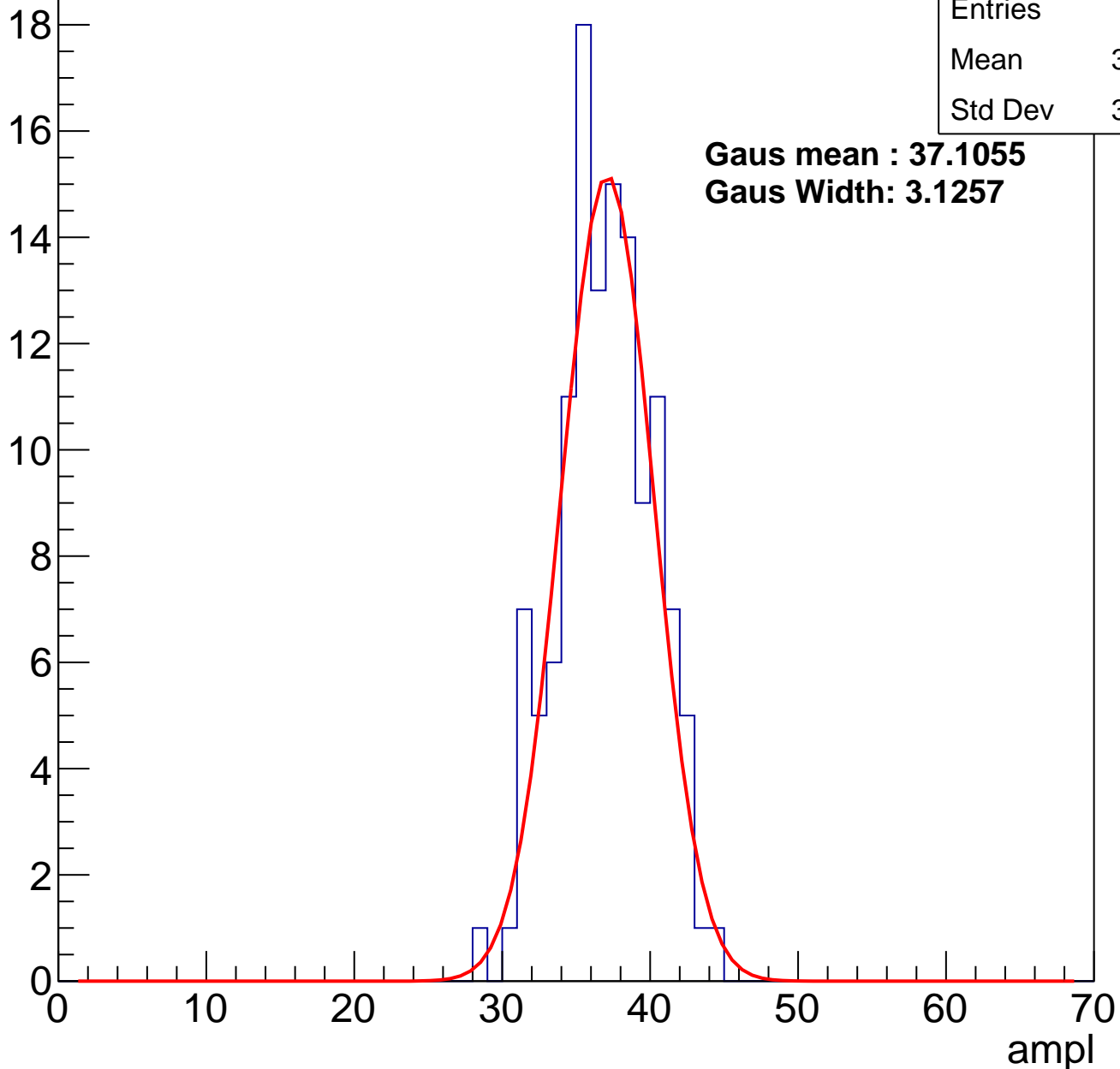
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 36.54 |
| Std Dev | 3.138 |

Gaus mean : 37.1055

Gaus Width: 3.1257

Entry



B1L001S, U26-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

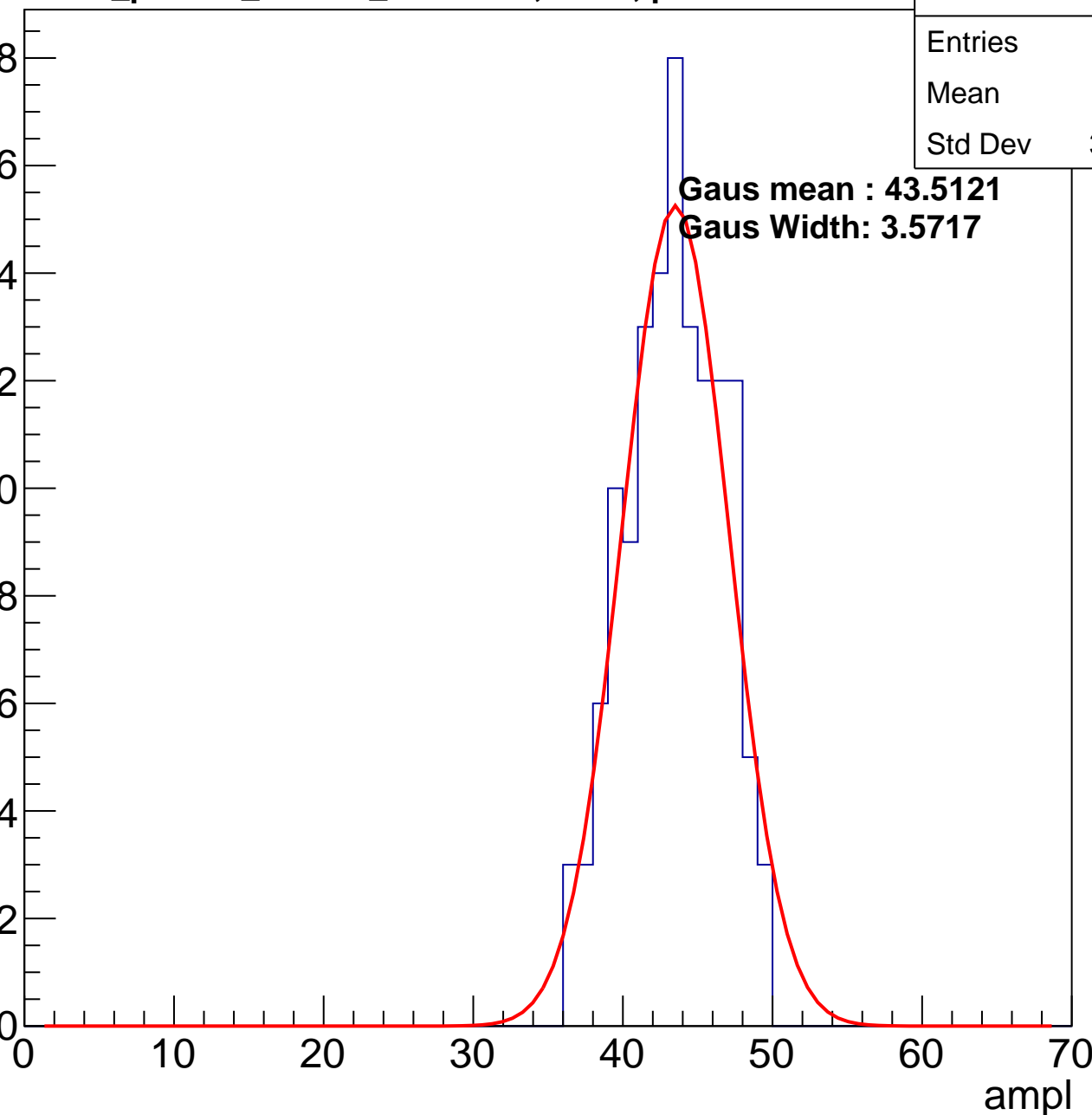
Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 133 |
| Mean | 42.91 |
| Std Dev | 3.132 |

Gaus mean : 43.5121

Gaus Width: 3.5717

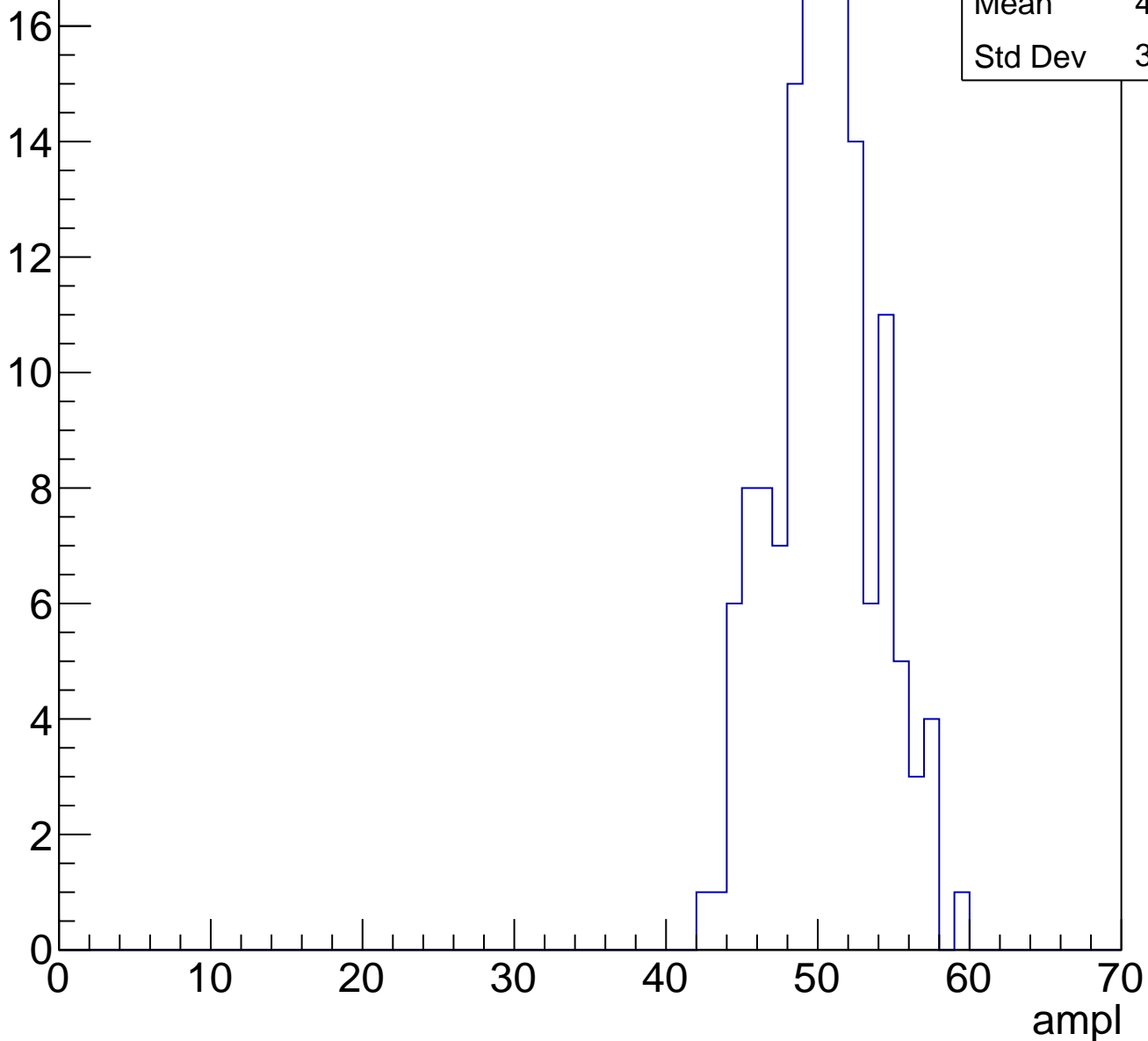


B1L001S, U26-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 141 |
| Mean | 49.97 |
| Std Dev | 3.379 |



B1L001S, U26-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

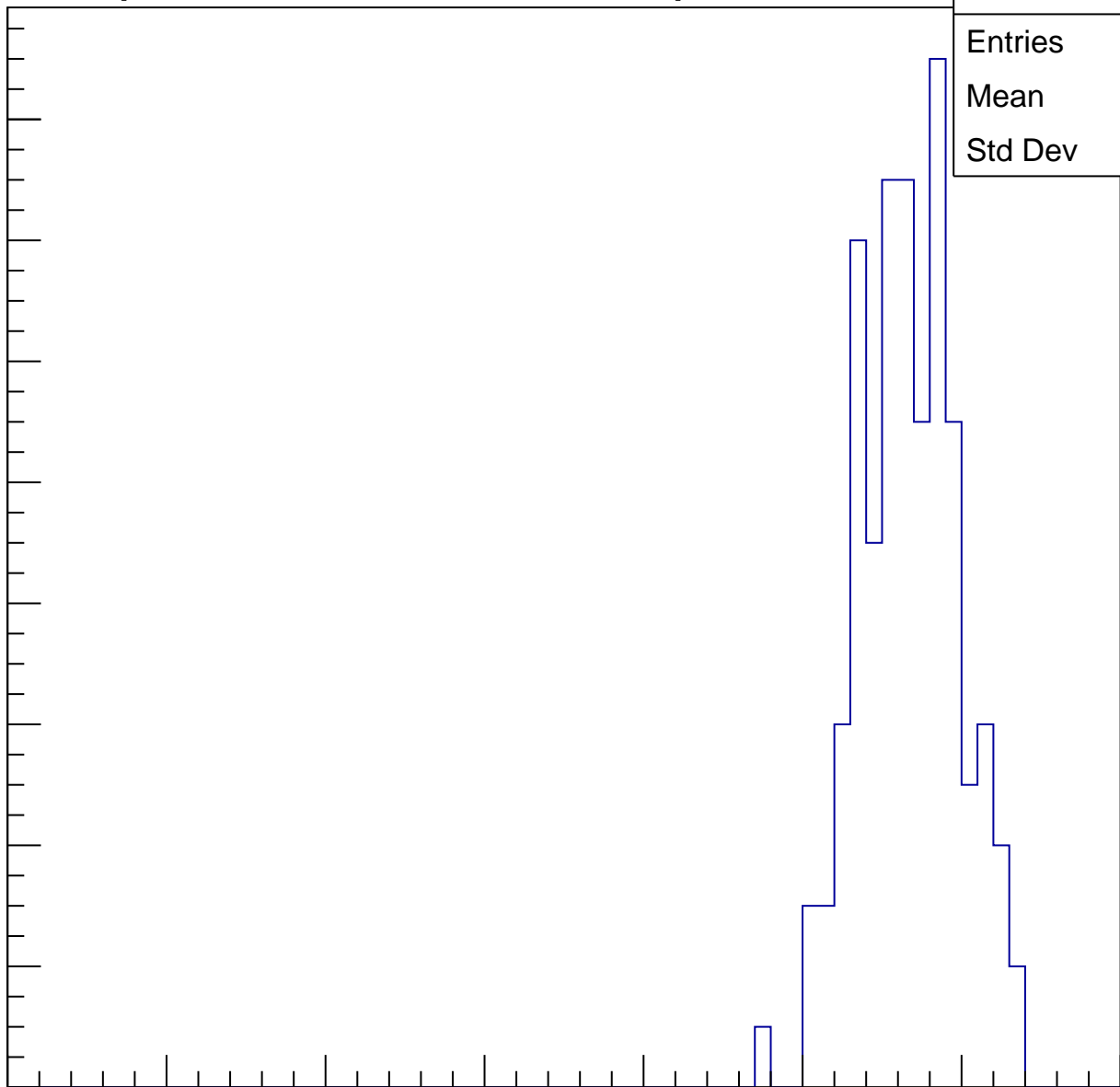
| | |
|---------|-------|
| Entries | 122 |
| Mean | 56.2 |
| Std Dev | 3.112 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch100, adc5

calib_packv5_042523_0143.root, FC#2, port C2

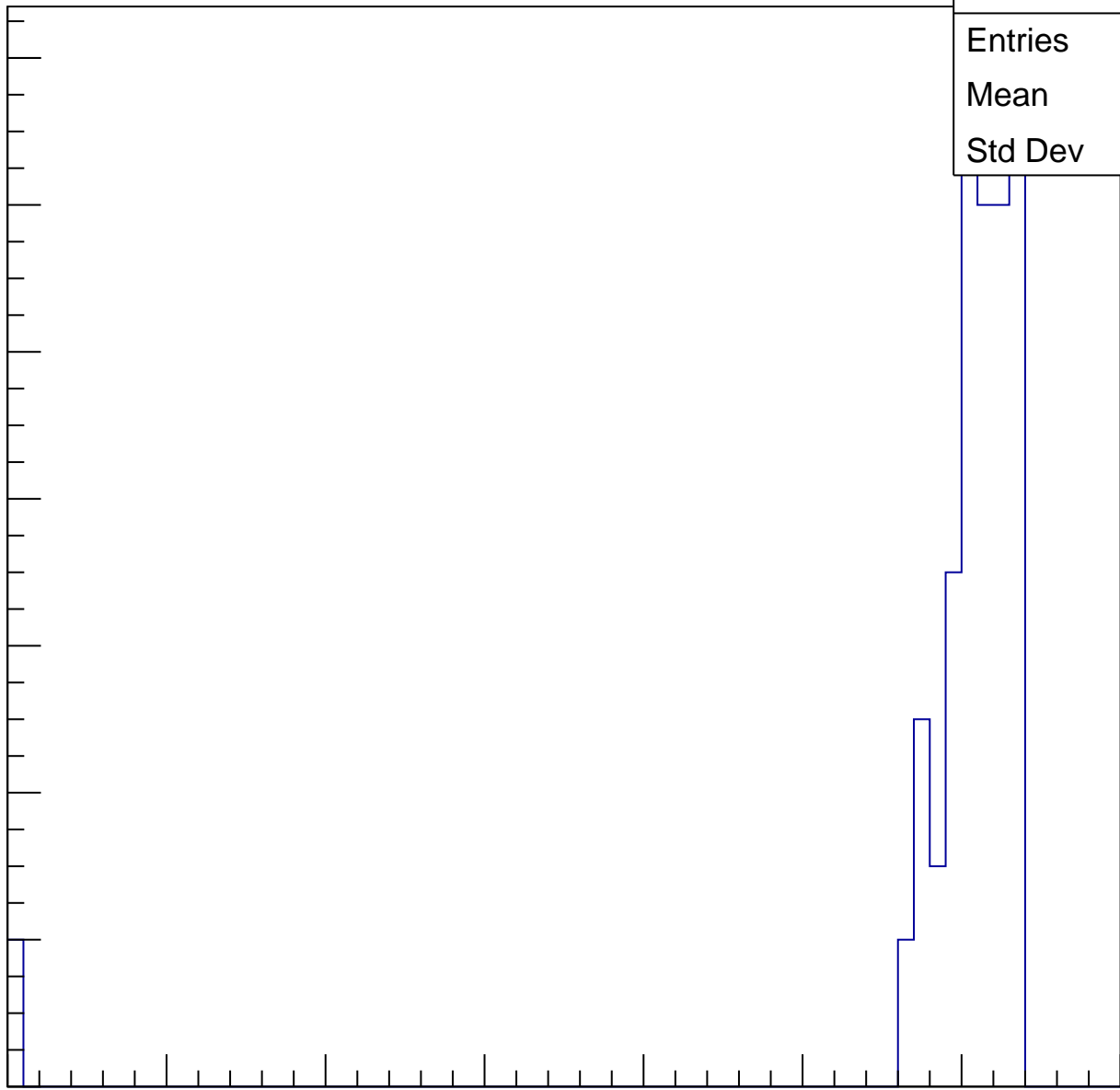
| | |
|---------|-------|
| Entries | 70 |
| Mean | 58.89 |
| Std Dev | 10.28 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

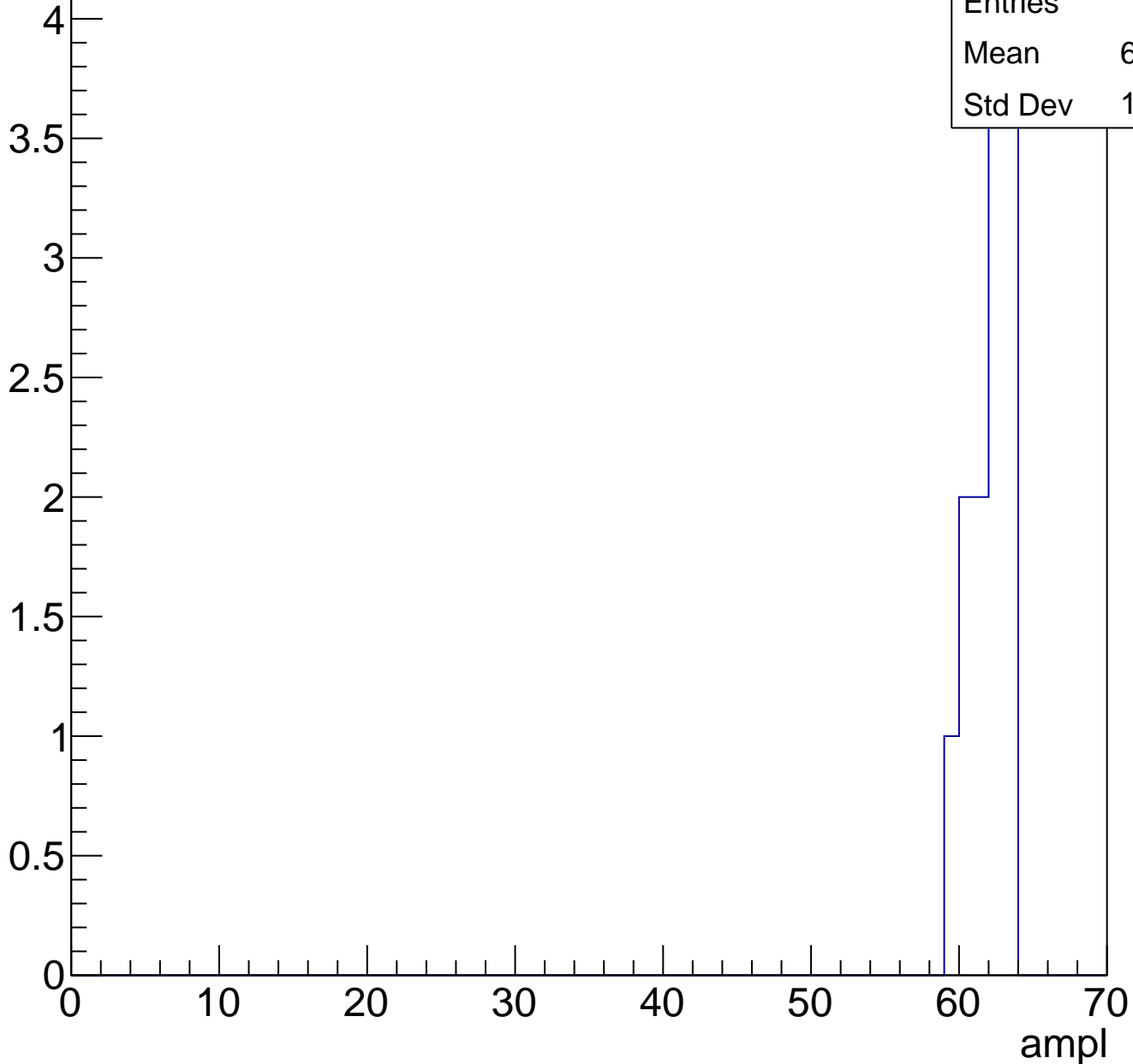
ampl



B1L001S, U26-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

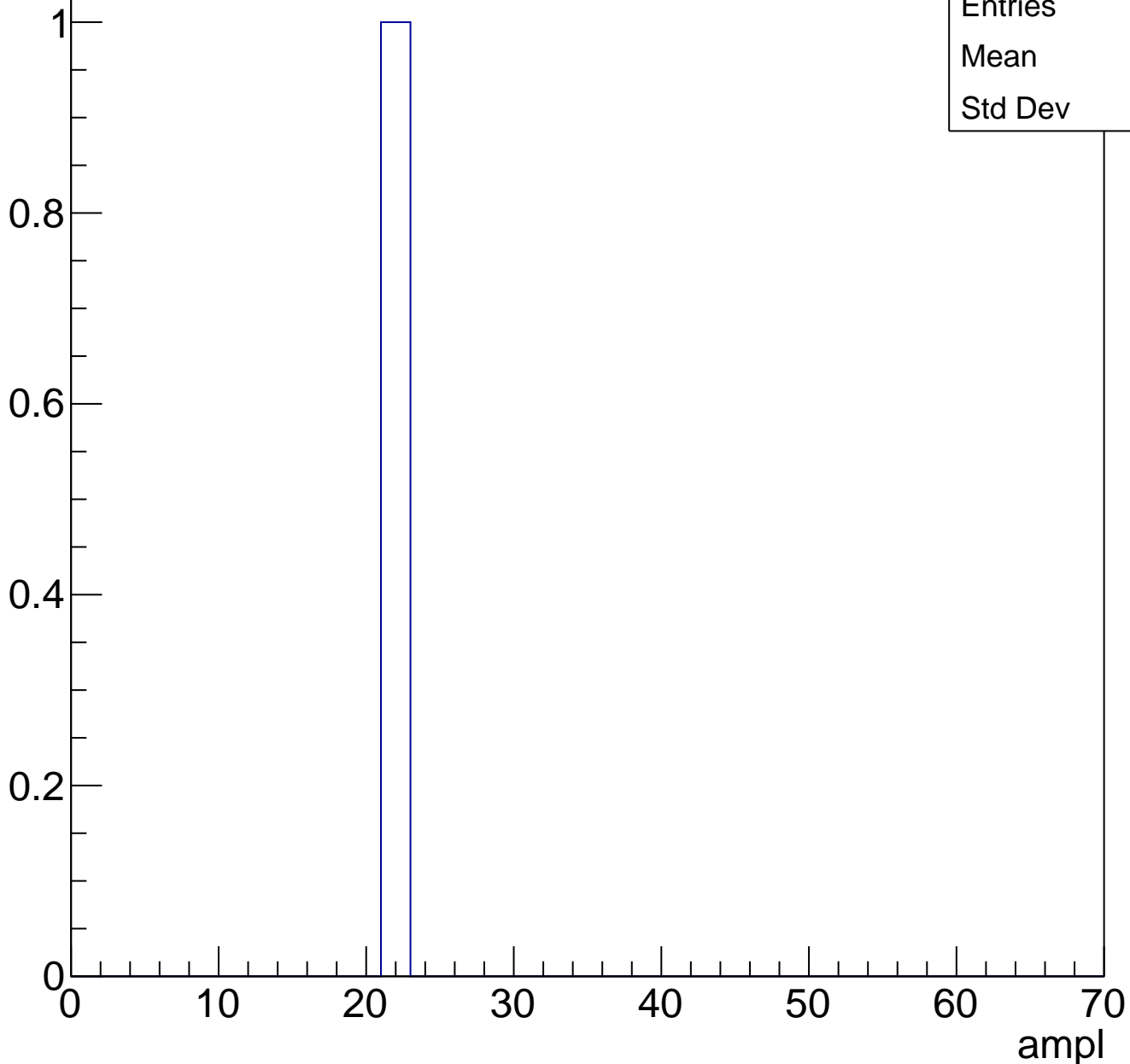
Entry



B1L001S, U26-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch101, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 30.68 |
| Std Dev | 3.038 |

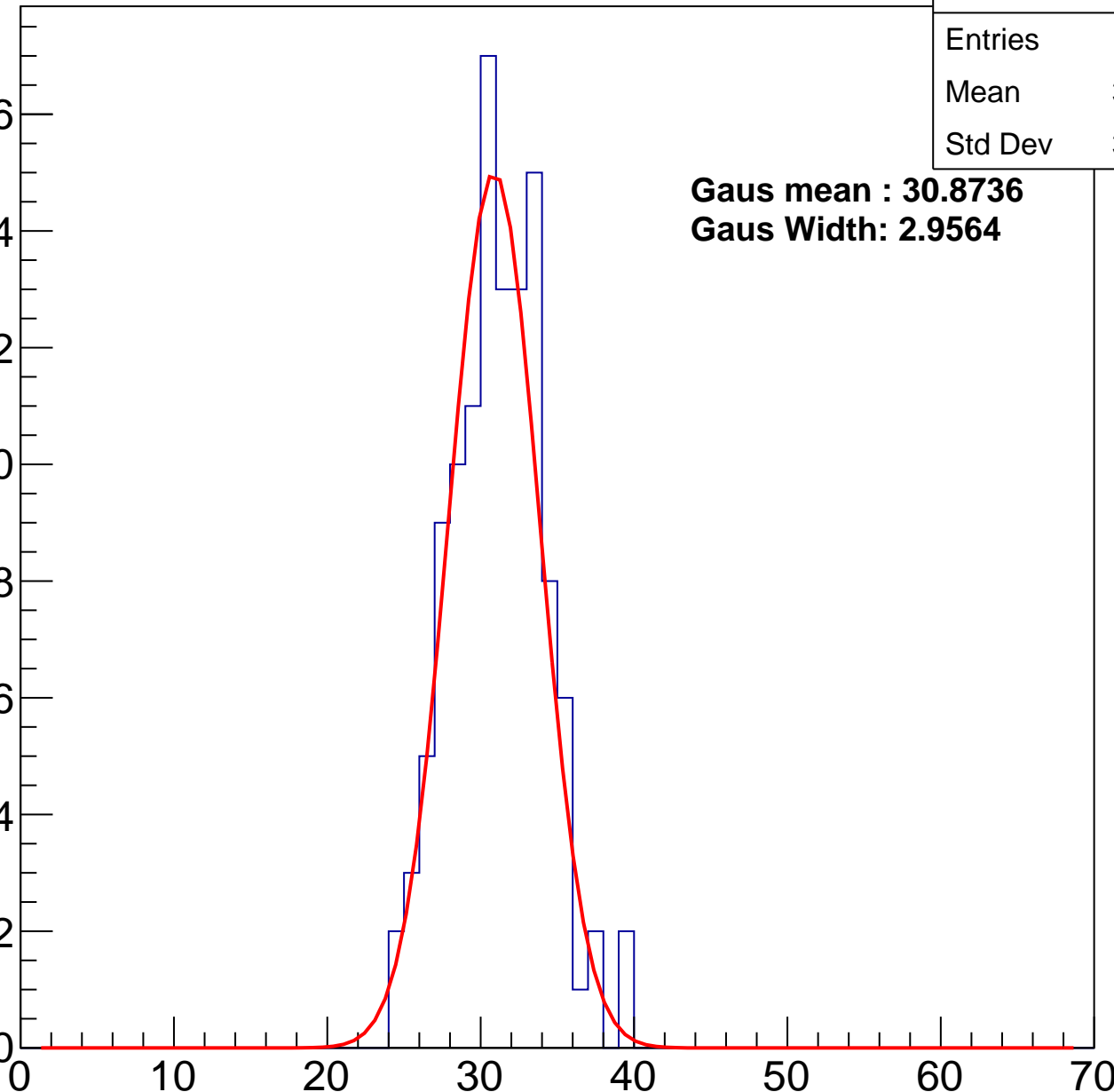
Gaus mean : 30.8736

Gaus Width: 2.9564

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch101, adc1

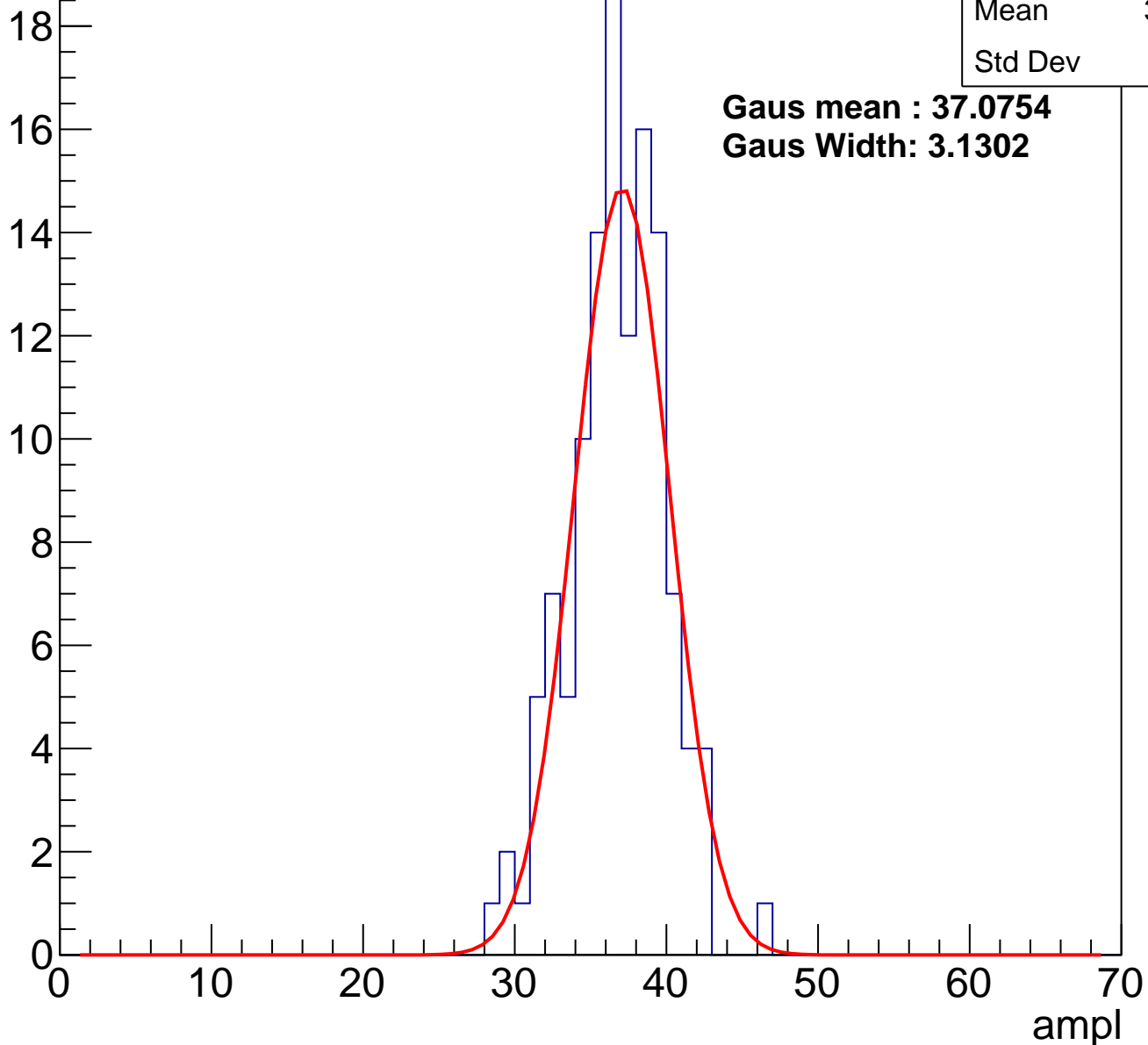
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 36.31 |
| Std Dev | 3.11 |

Gaus mean : 37.0754

Gaus Width: 3.1302

Entry



B1L001S, U26-ch101, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

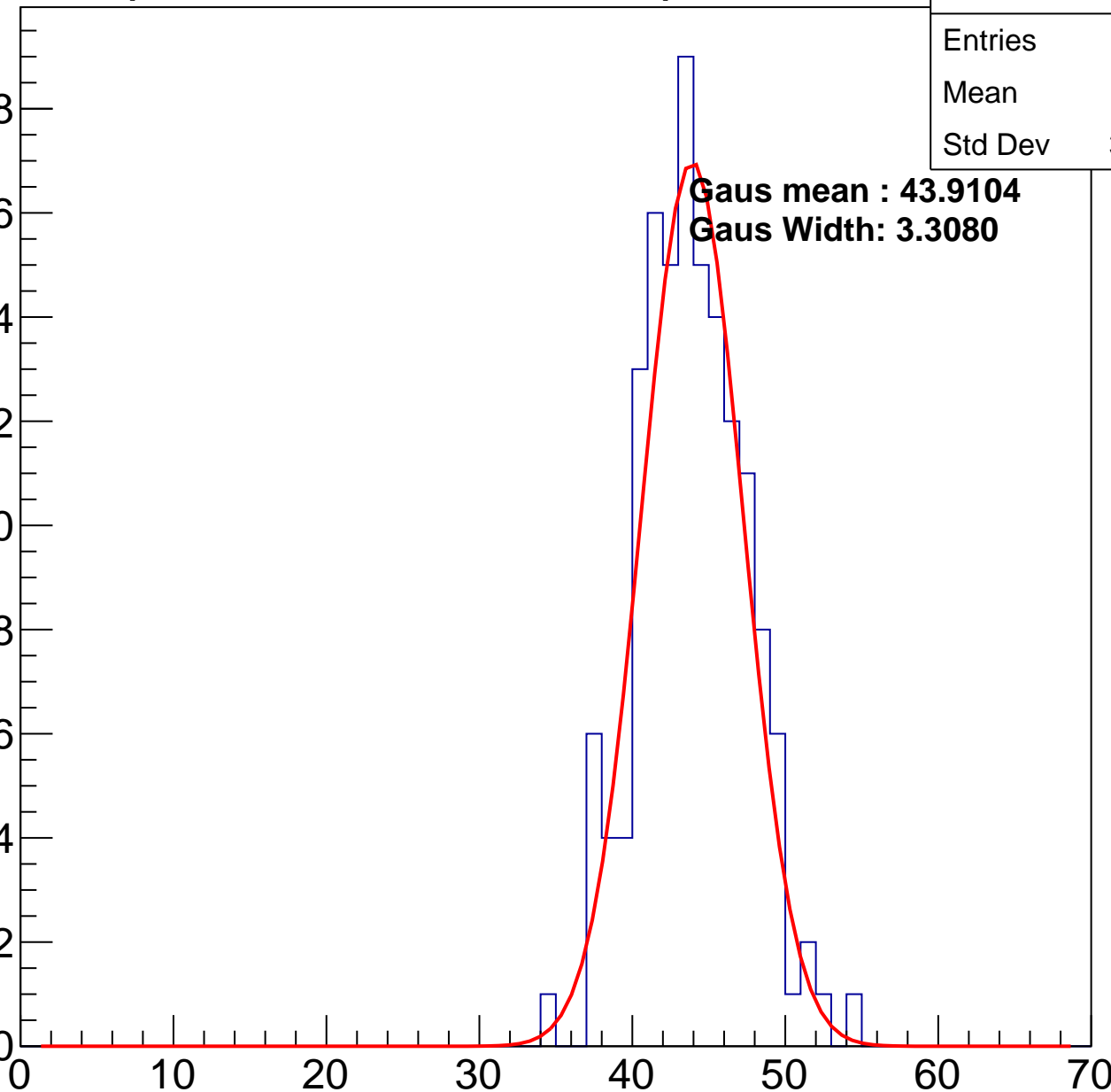
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 149 |
| Mean | 43.5 |
| Std Dev | 3.446 |

Gaus mean : 43.9104

Gaus Width: 3.3080

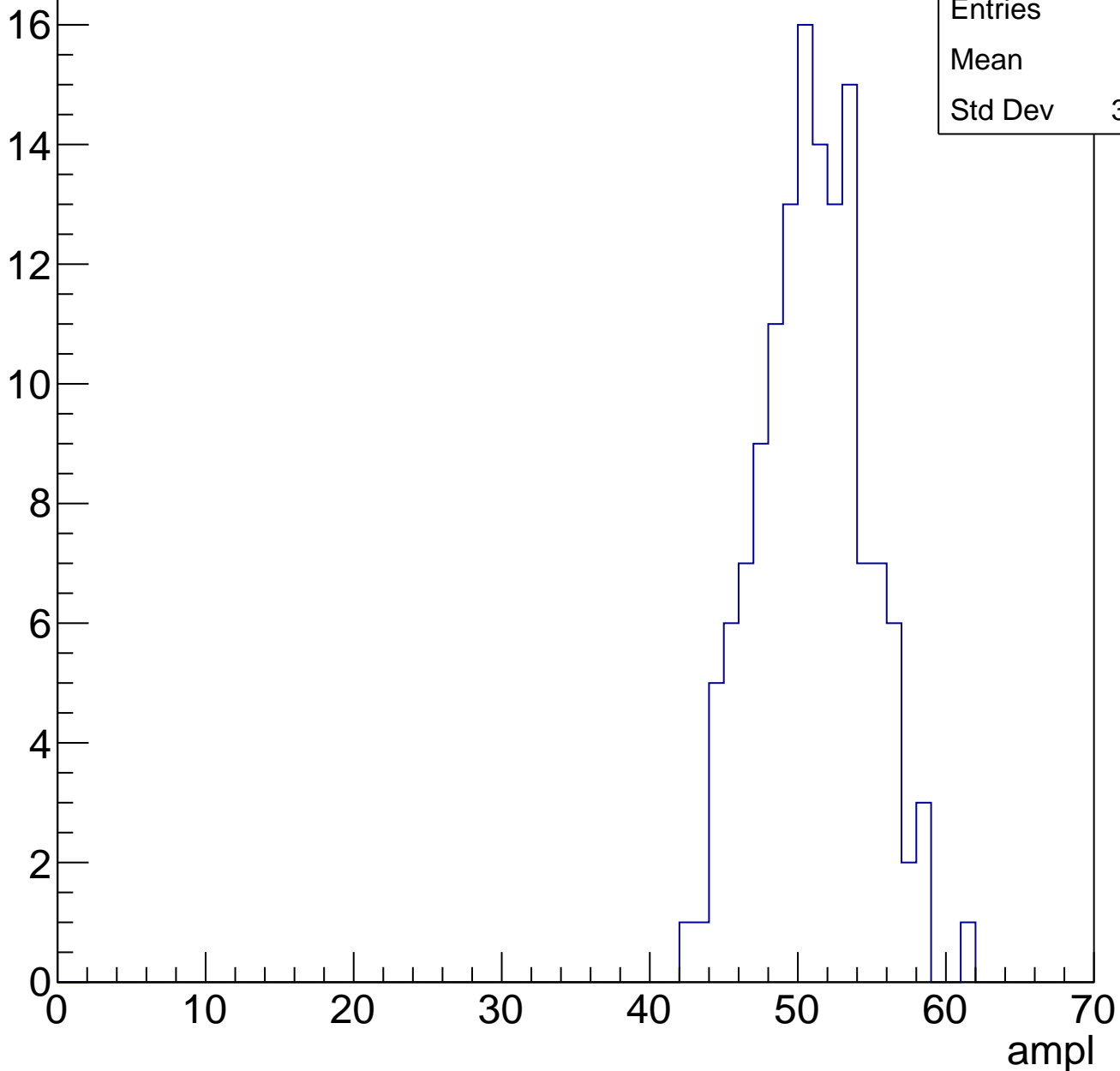
ampl



B1L001S, U26-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch101, adc4

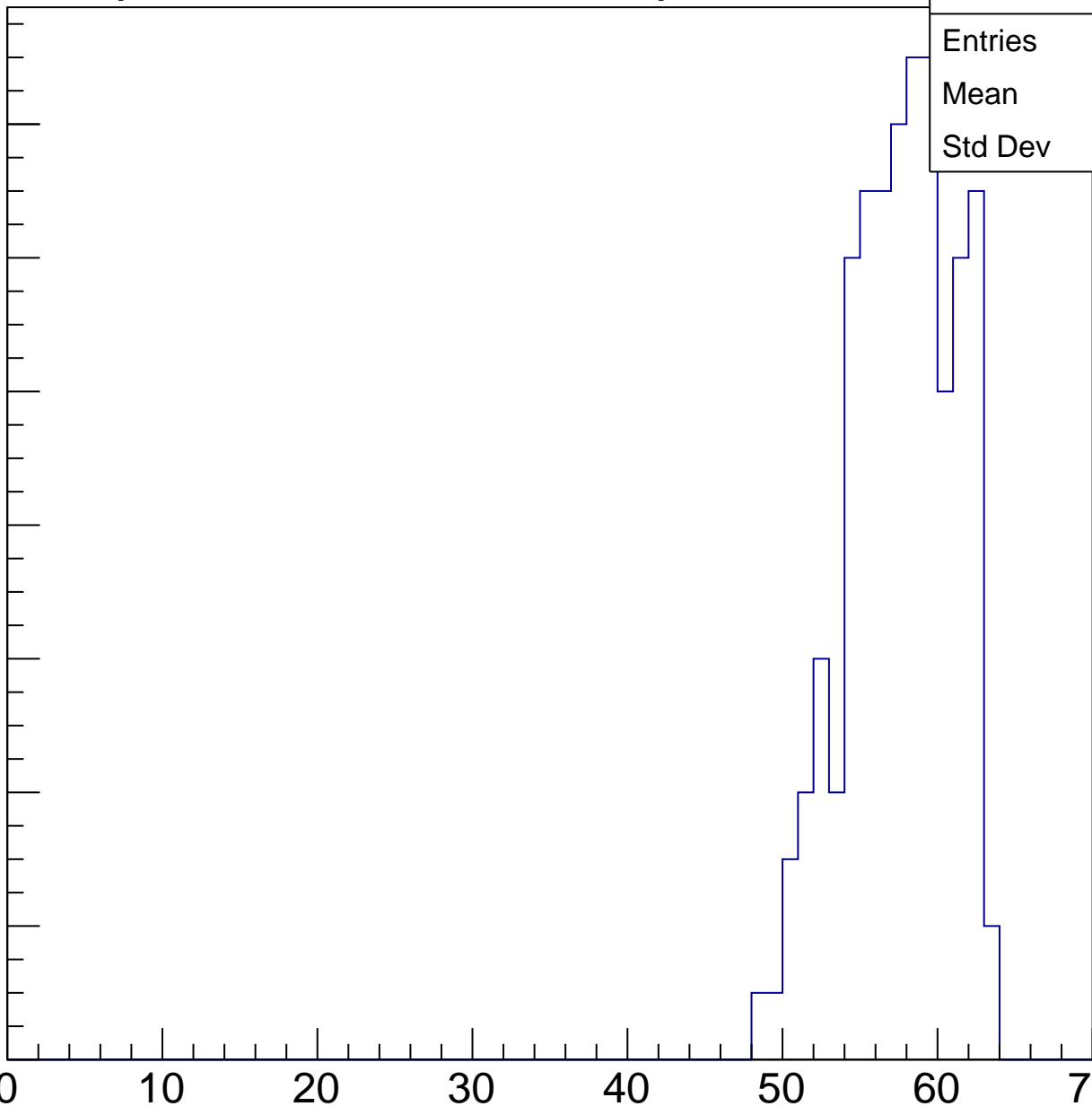
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 138 |
| Mean | 57.12 |
| Std Dev | 3.378 |

ampl



B1L001S, U26-ch101, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 57 |
| Mean | 58.65 |
| Std Dev | 11.4 |

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

30

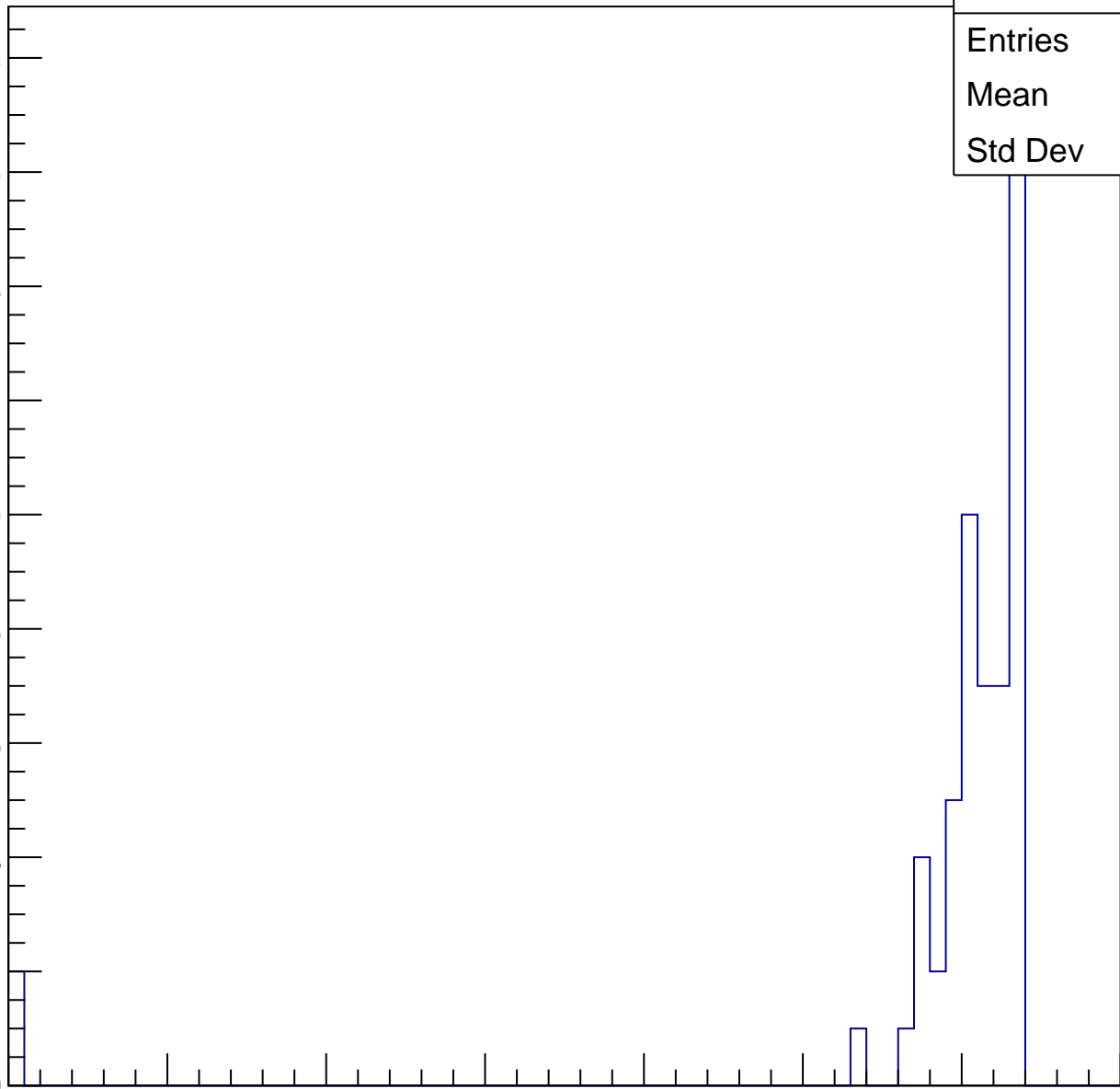
40

50

60

70

ampl



B1L001S, U26-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch101, adc7

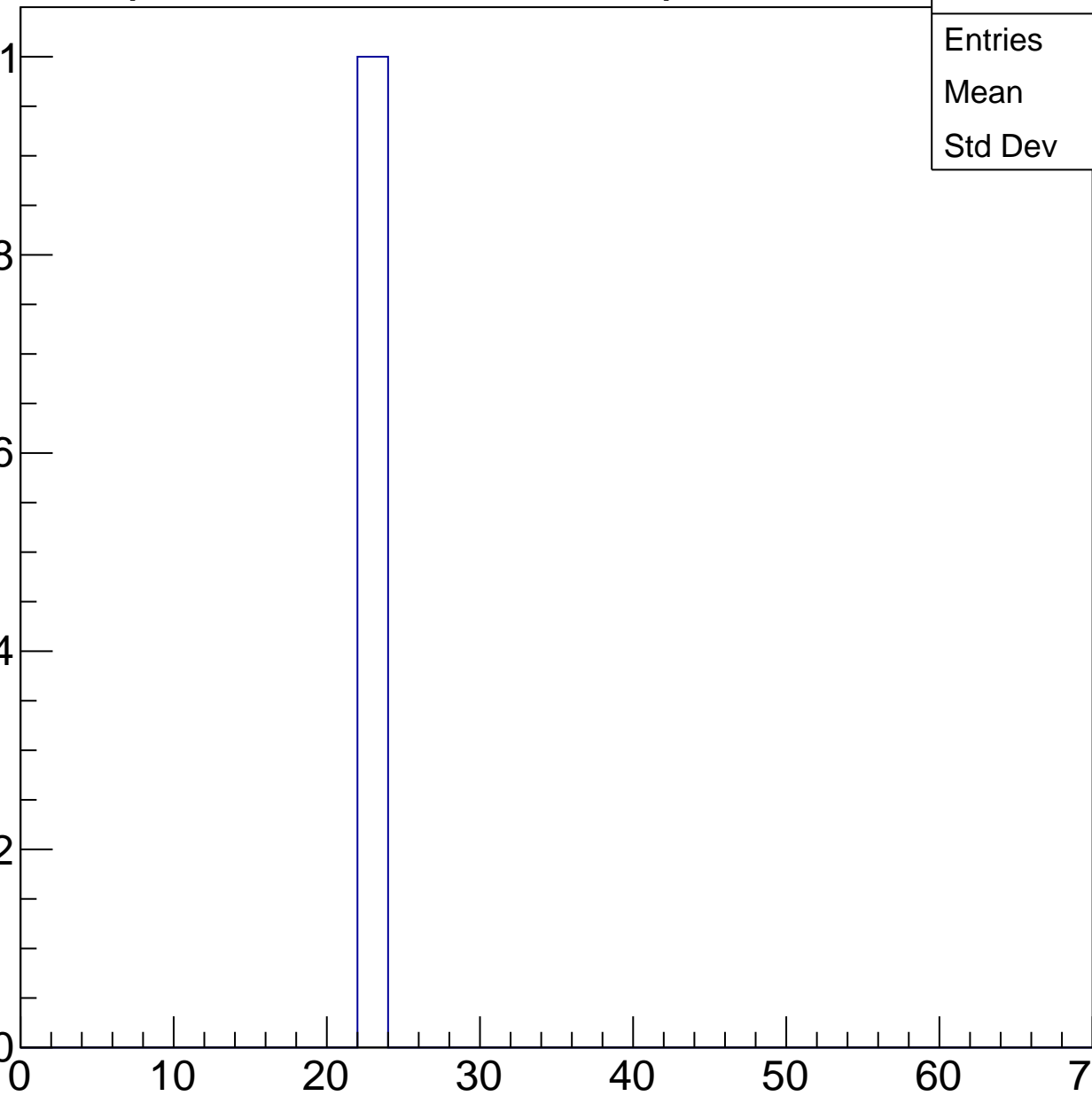
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 2 |
| Mean | 22.5 |
| Std Dev | 0.5 |

ampl



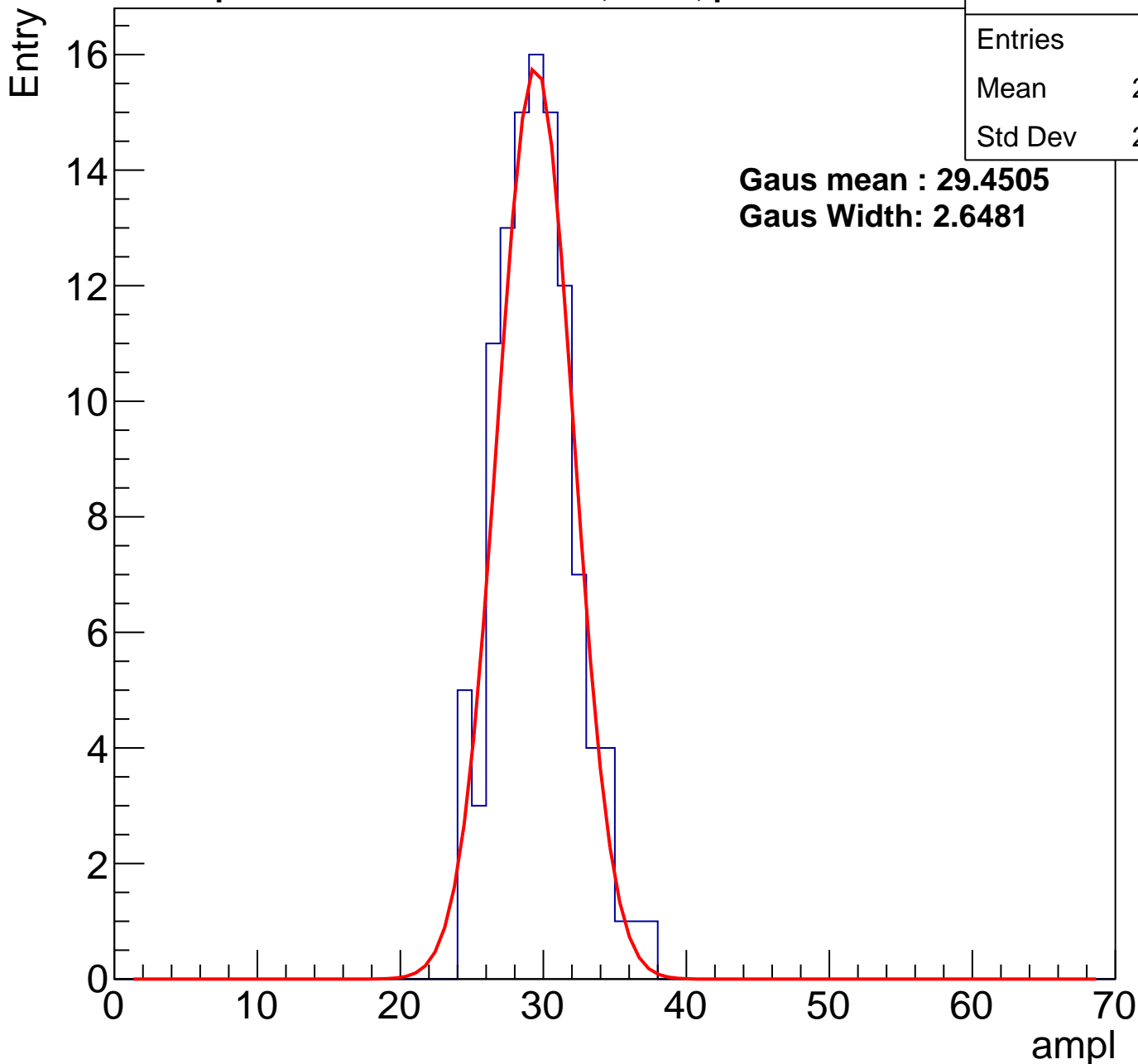
B1L001S, U26-ch102, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 108 |
| Mean | 29.06 |
| Std Dev | 2.683 |

Gaus mean : 29.4505

Gaus Width: 2.6481



B1L001S, U26-ch102, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 35.53 |
| Std Dev | 3.423 |

Gaus mean : 35.8579

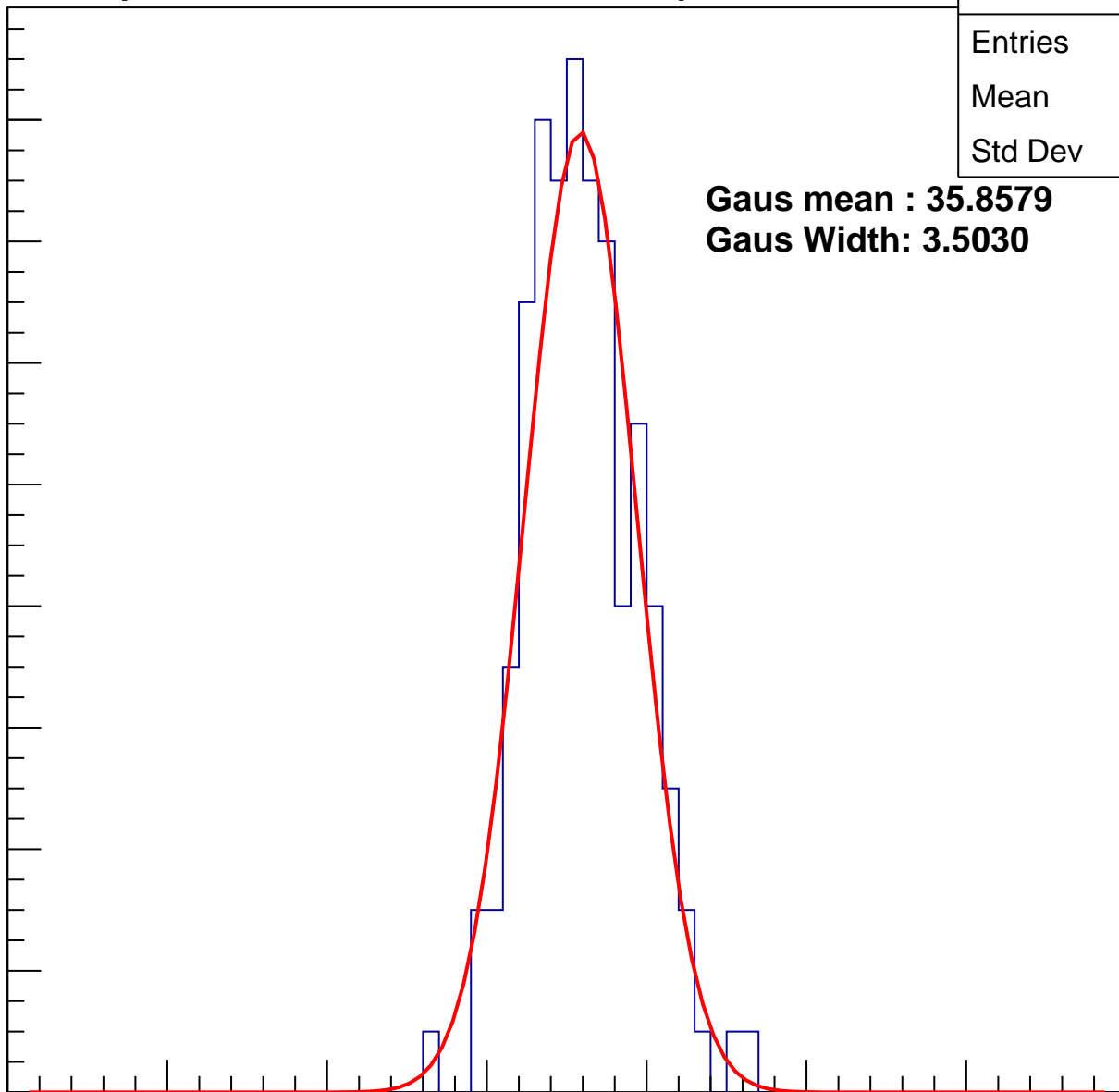
Gaus Width: 3.5030

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch102, adc2

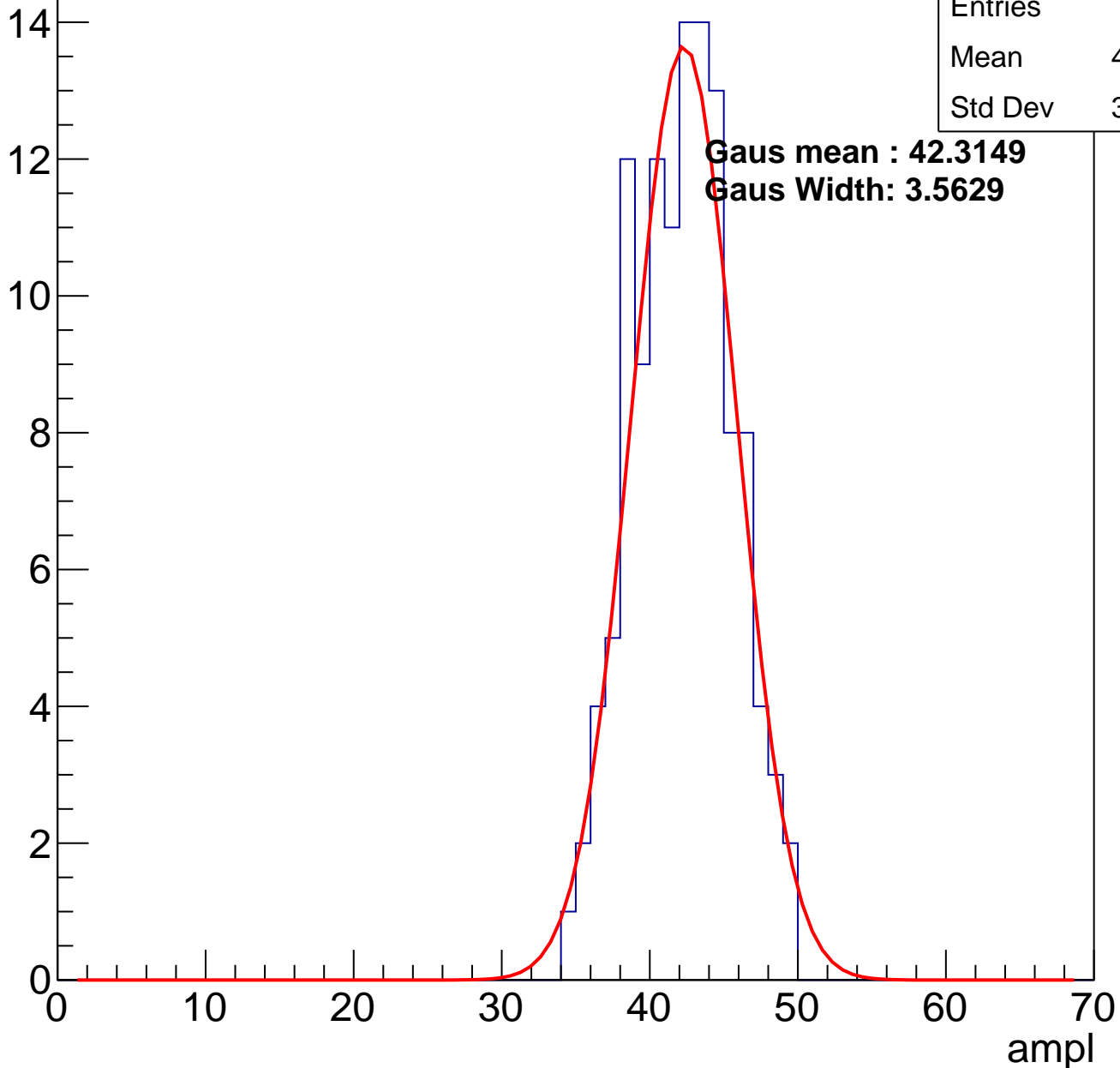
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 122 |
| Mean | 41.73 |
| Std Dev | 3.297 |

Gaus mean : 42.3149

Gaus Width: 3.5629

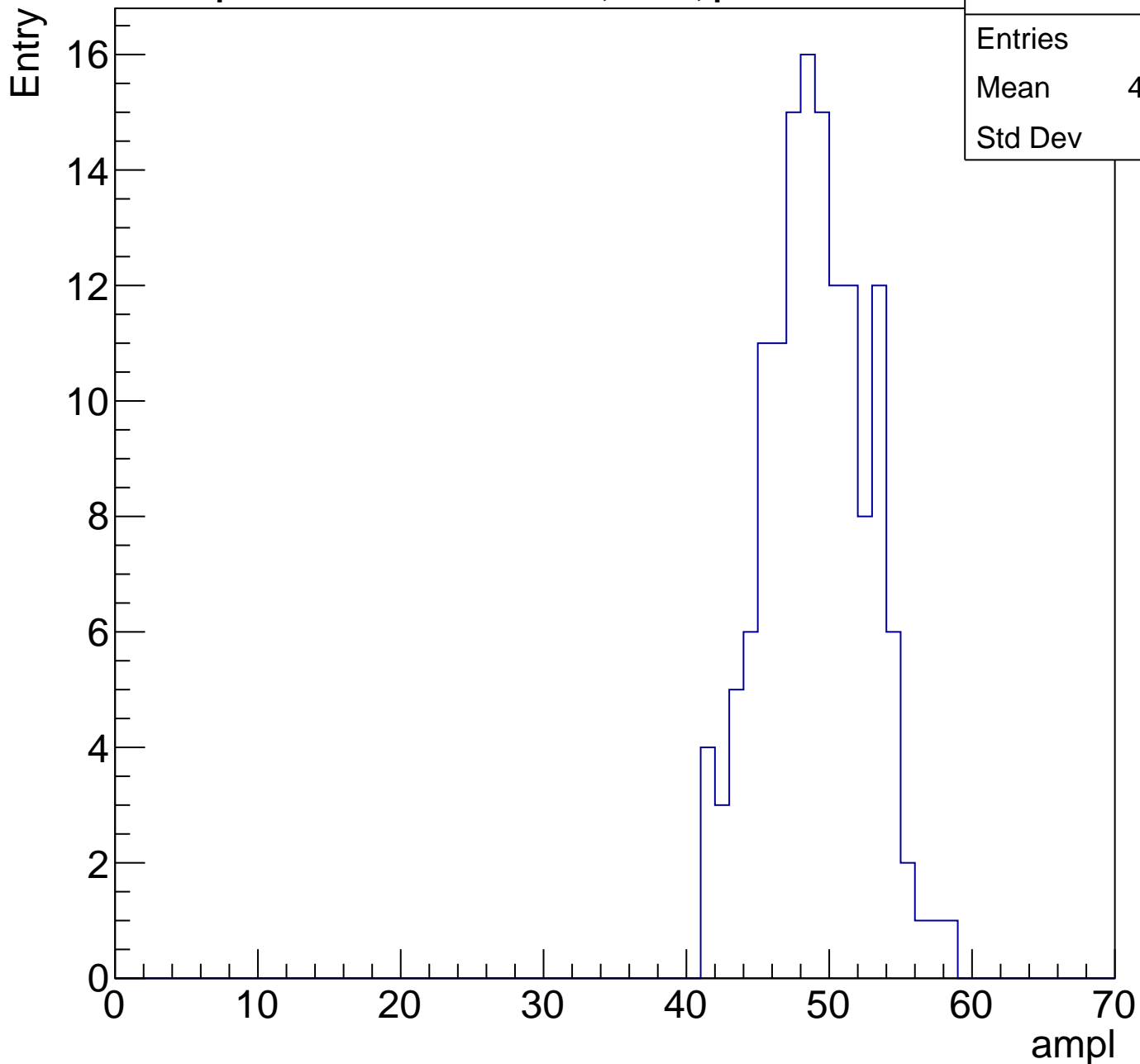
Entry



B1L001S, U26-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

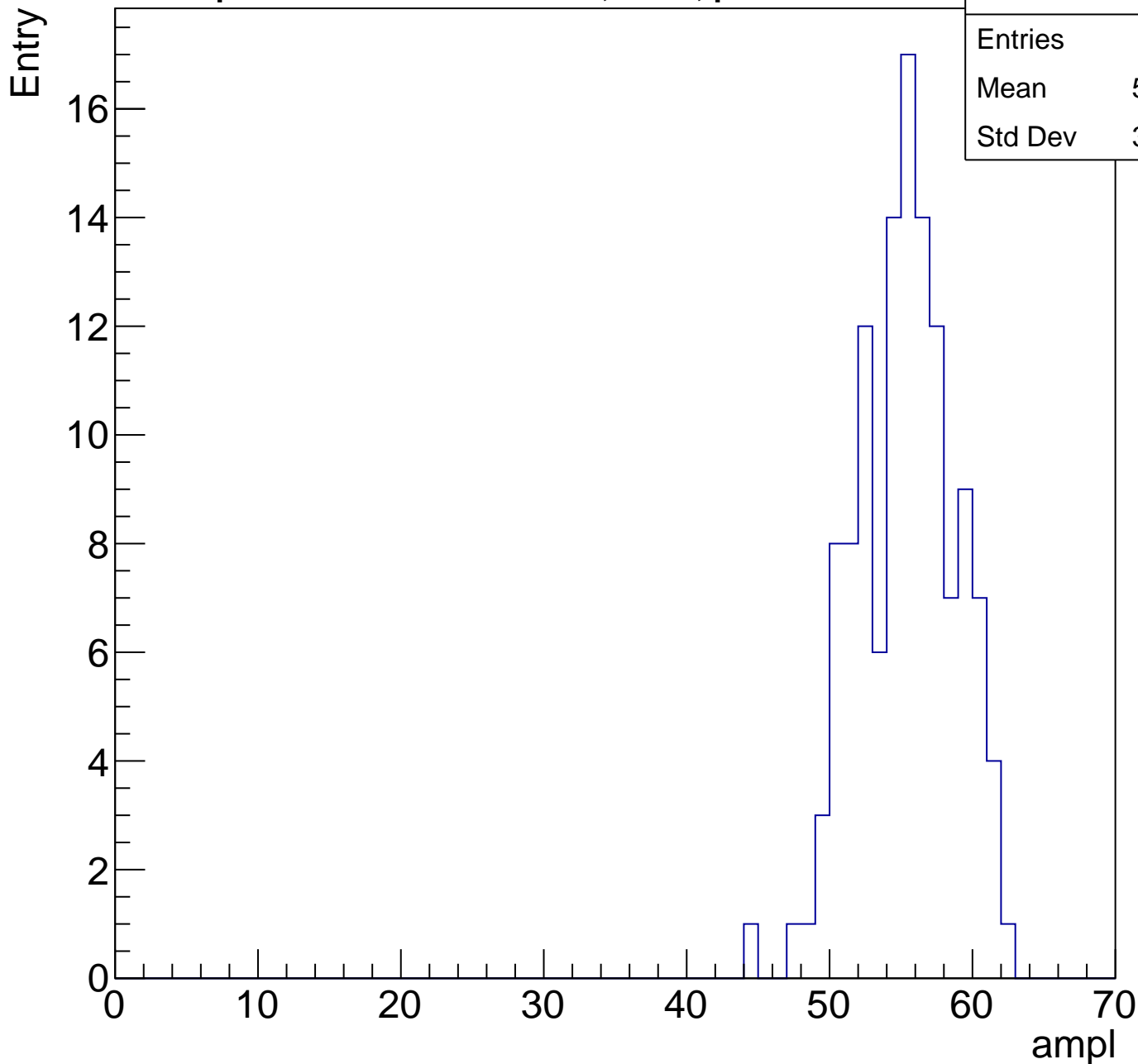
| | |
|---------|-------|
| Entries | 141 |
| Mean | 48.56 |
| Std Dev | 3.57 |



B1L001S, U26-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 54.86 |
| Std Dev | 3.407 |



B1L001S, U26-ch102, adc5

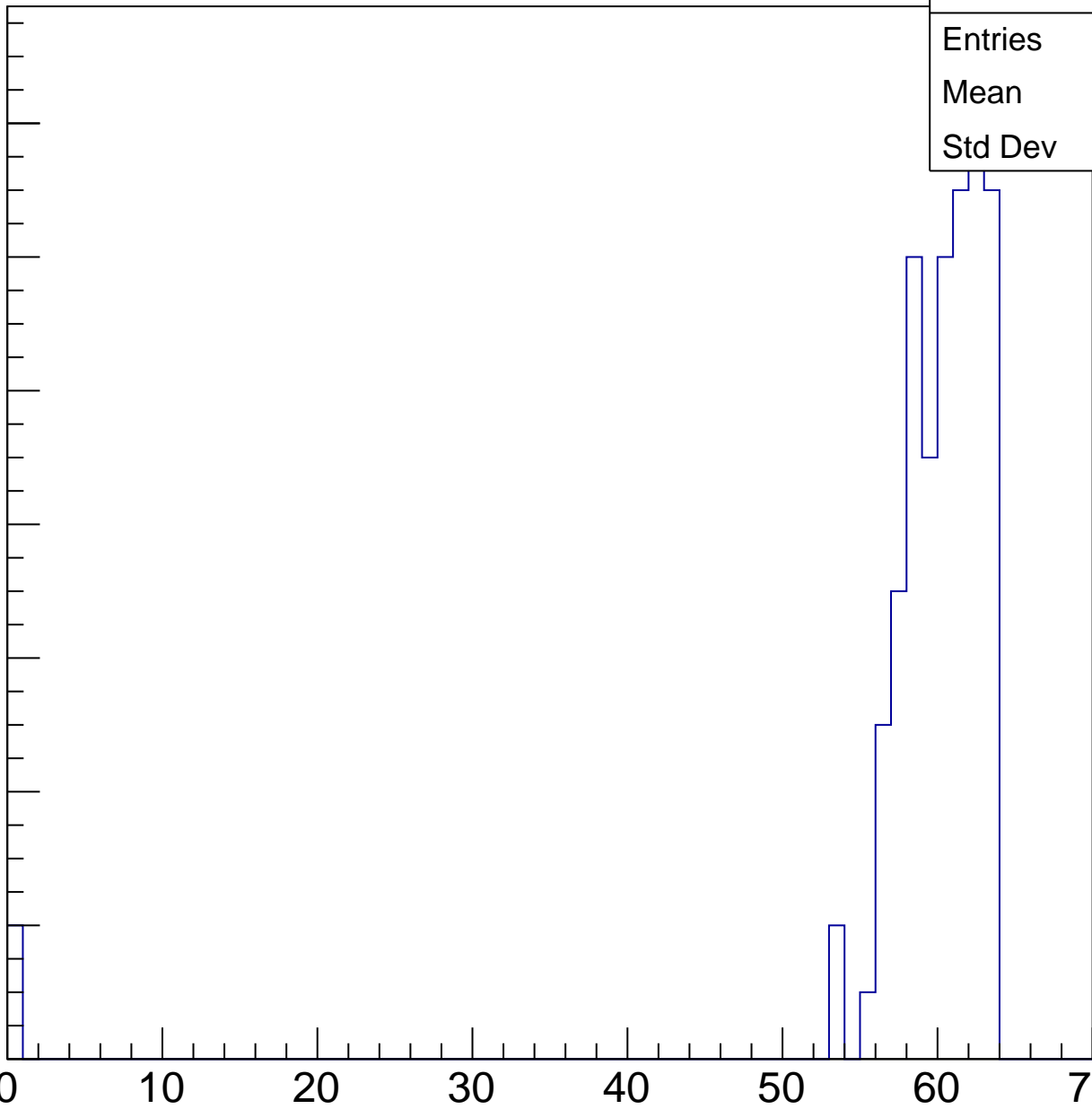
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 91 |
| Mean | 58.56 |
| Std Dev | 9.093 |

ampl

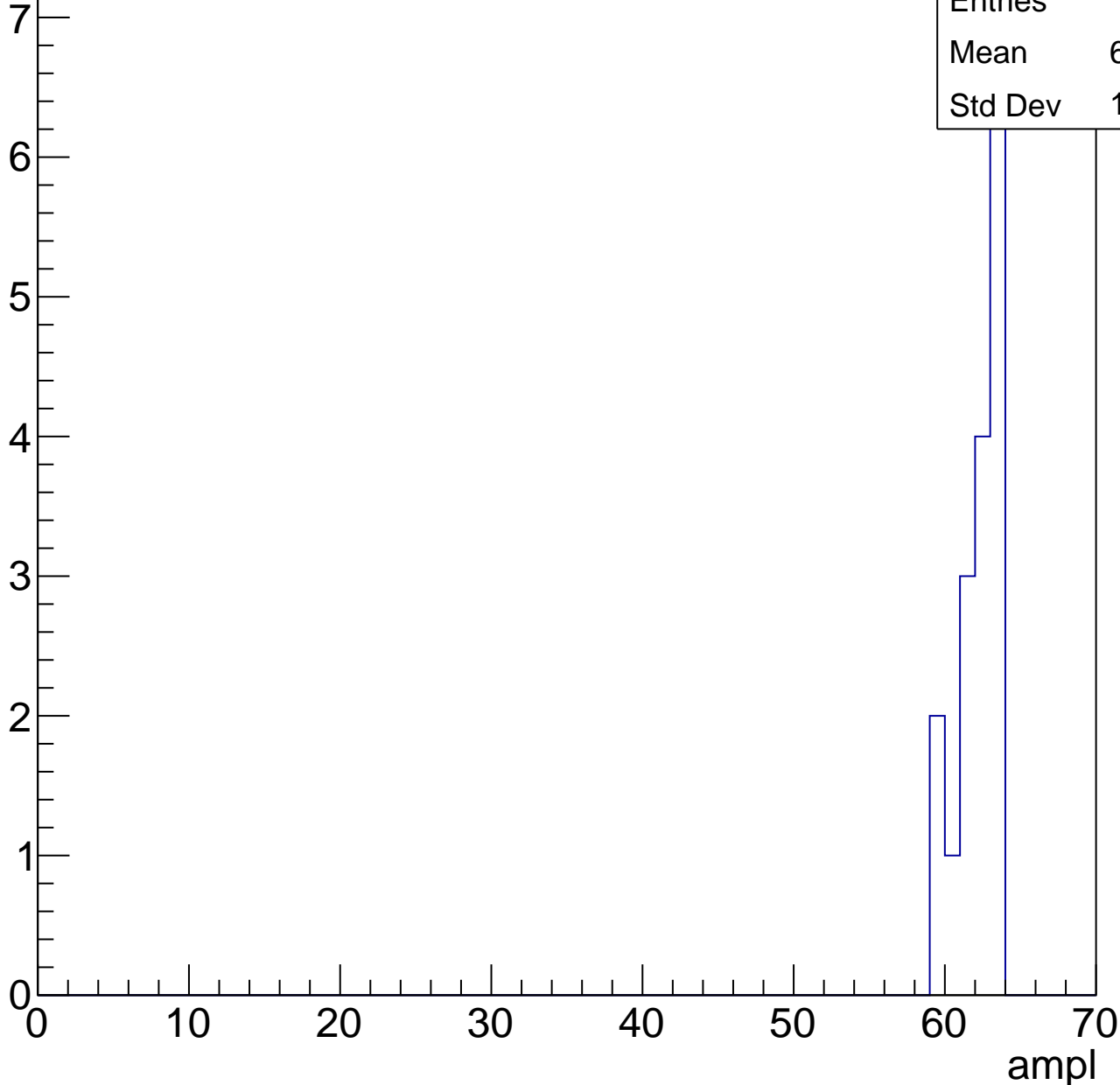


B1L001S, U26-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.76 |
| Std Dev | 1.352 |



B1L001S, U26-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 3 |
| Mean | 15.33 |
| Std Dev | 10.84 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch103, adc0

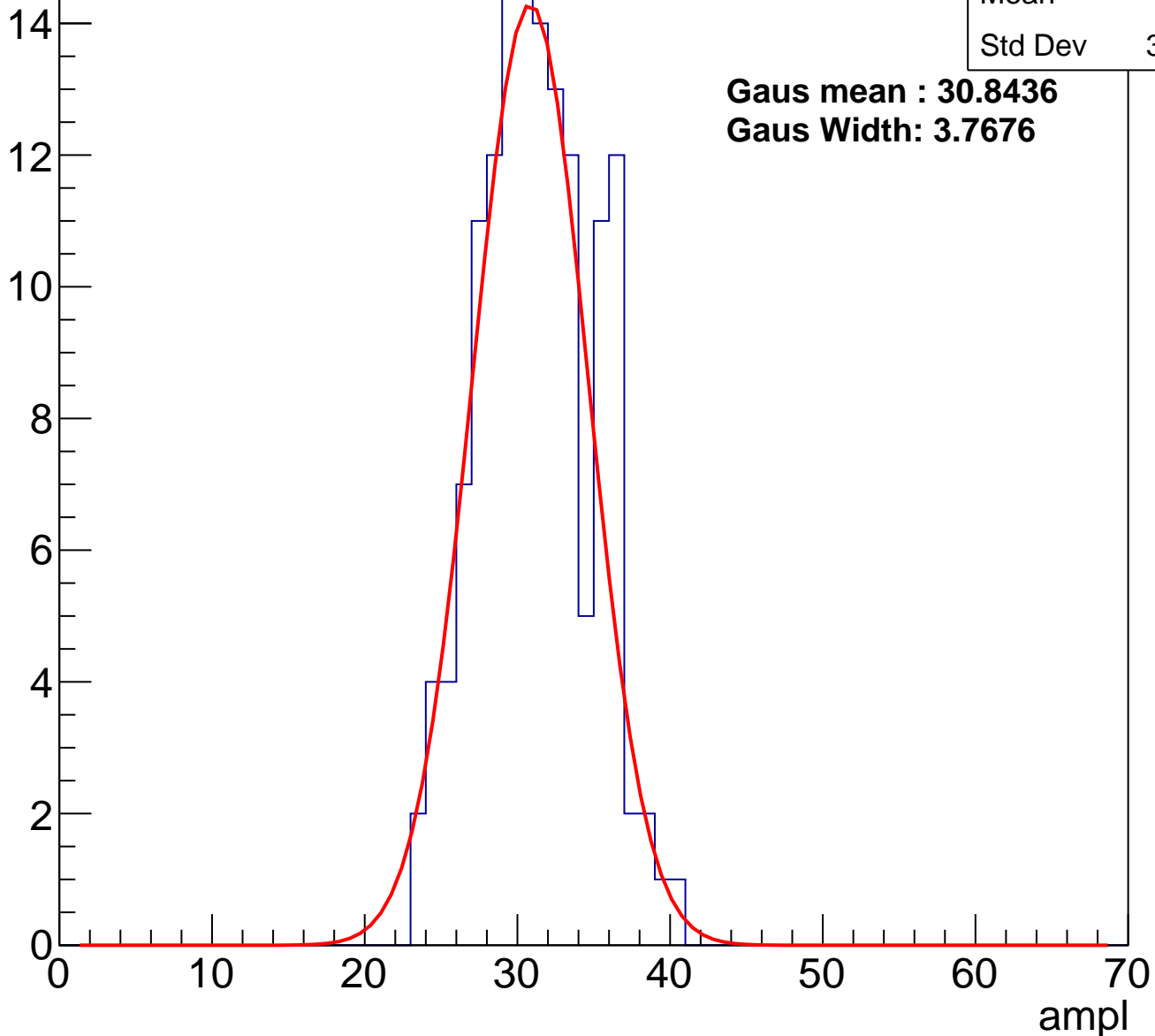
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 30.8 |
| Std Dev | 3.638 |

Gaus mean : 30.8436

Gaus Width: 3.7676

Entry



B1L001S, U26-ch103, adc1

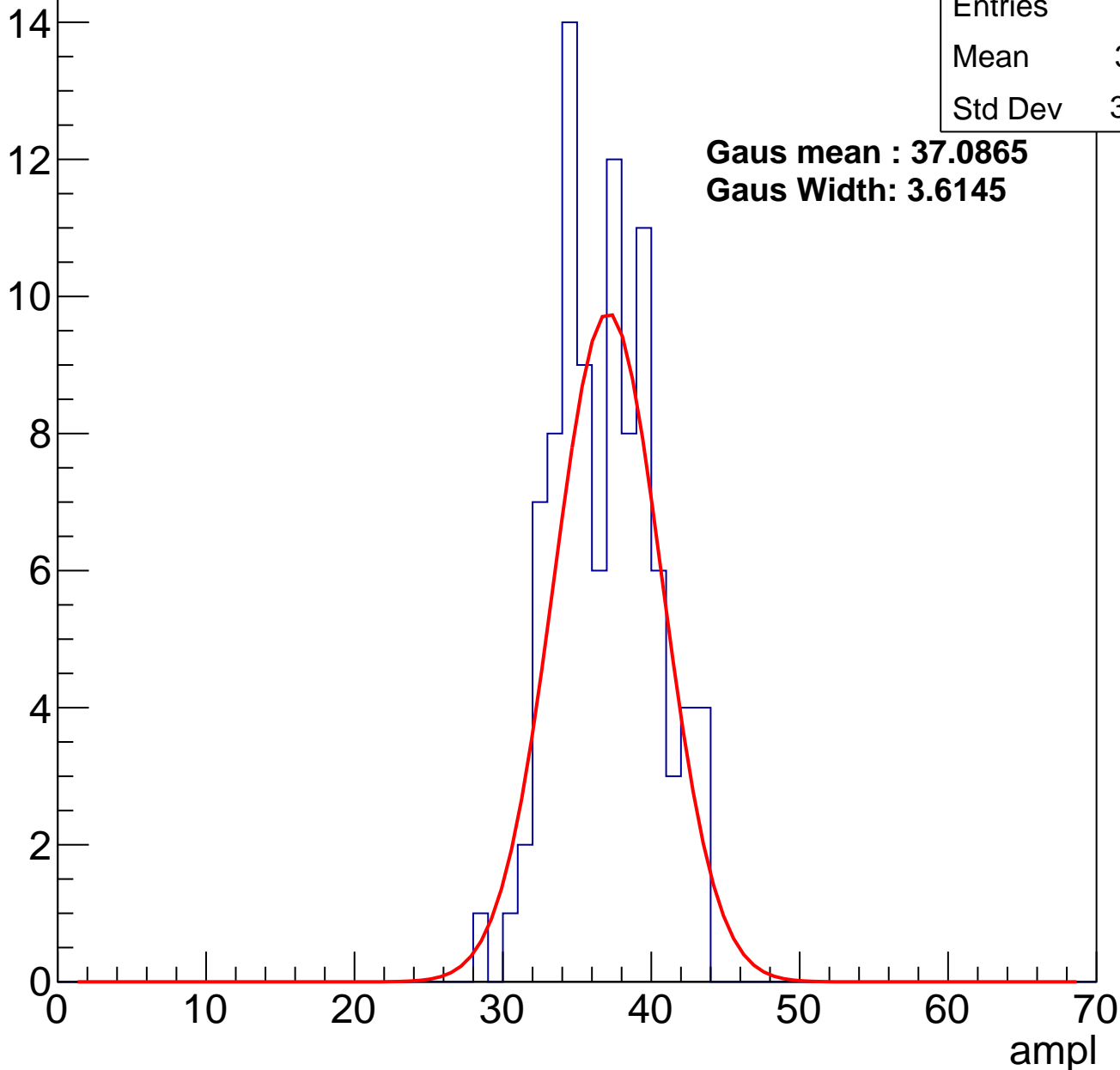
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 96 |
| Mean | 36.41 |
| Std Dev | 3.287 |

Gaus mean : 37.0865

Gaus Width: 3.6145

Entry



B1L001S, U26-ch103, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

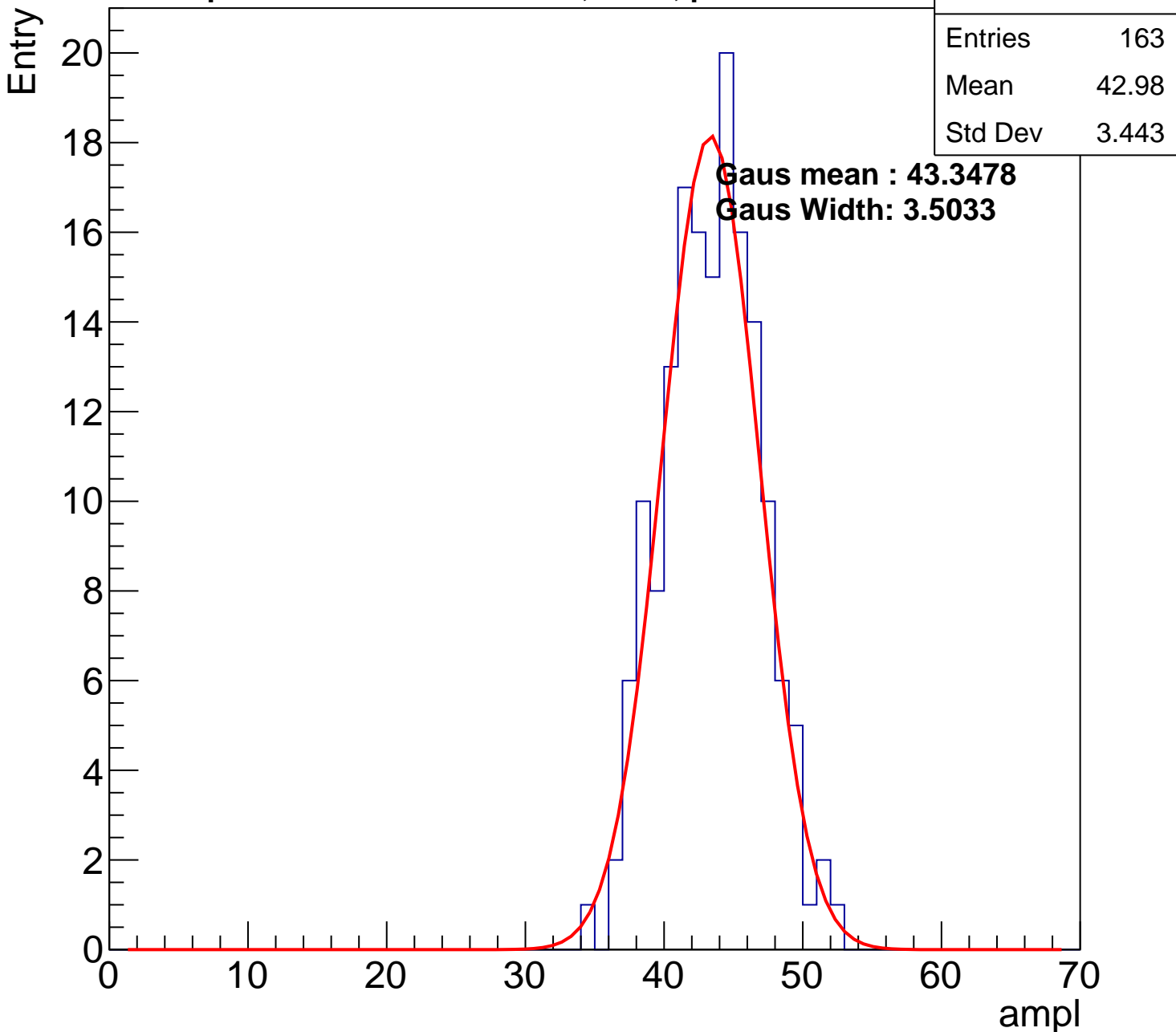
20
18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 163 |
| Mean | 42.98 |
| Std Dev | 3.443 |

Gaus mean : 43.3478

Gaus Width: 3.5033

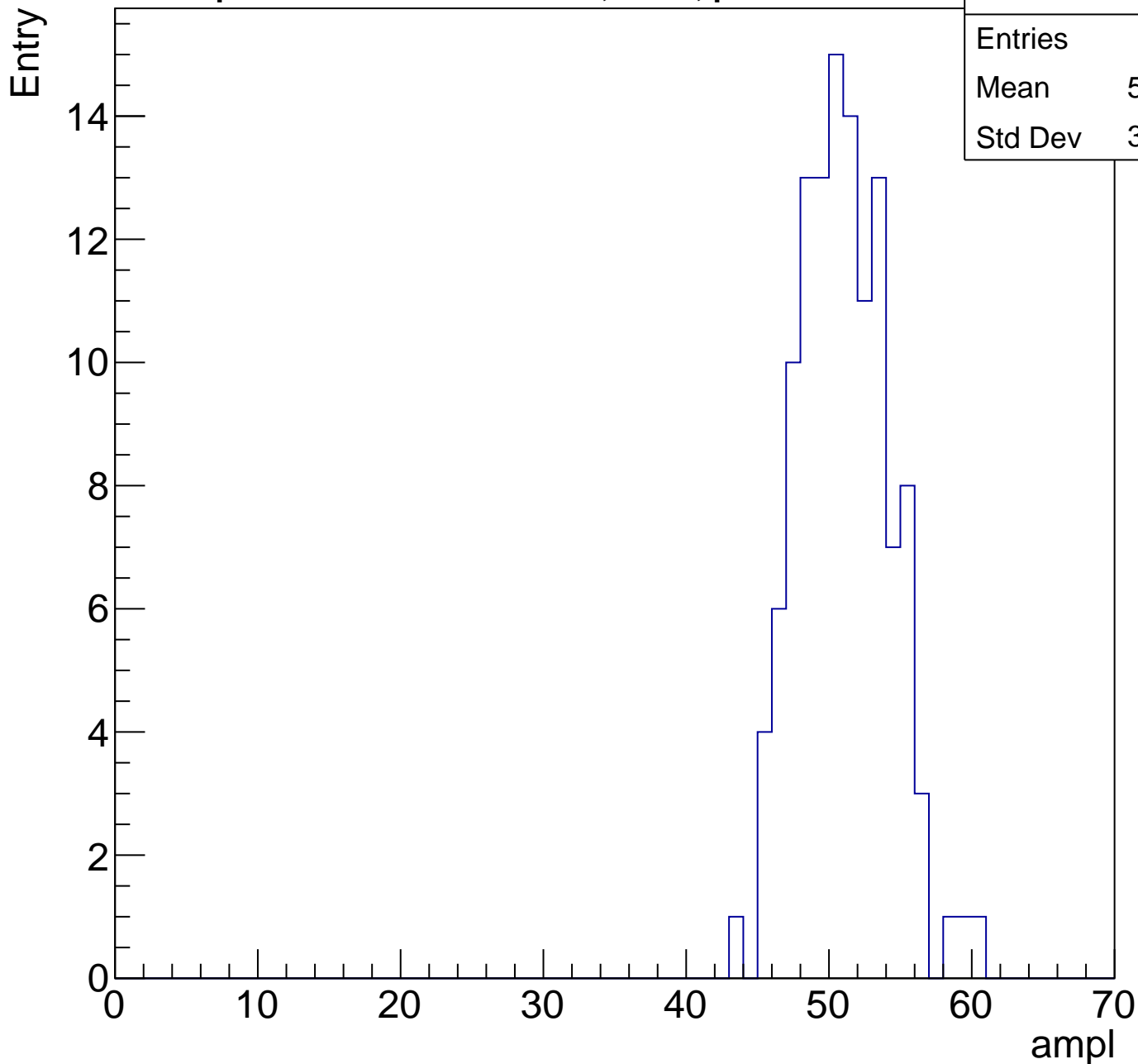
0 10 20 30 40 50 60 70
ampl



B1L001S, U26-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 50.56 |
| Std Dev | 3.152 |



B1L001S, U26-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

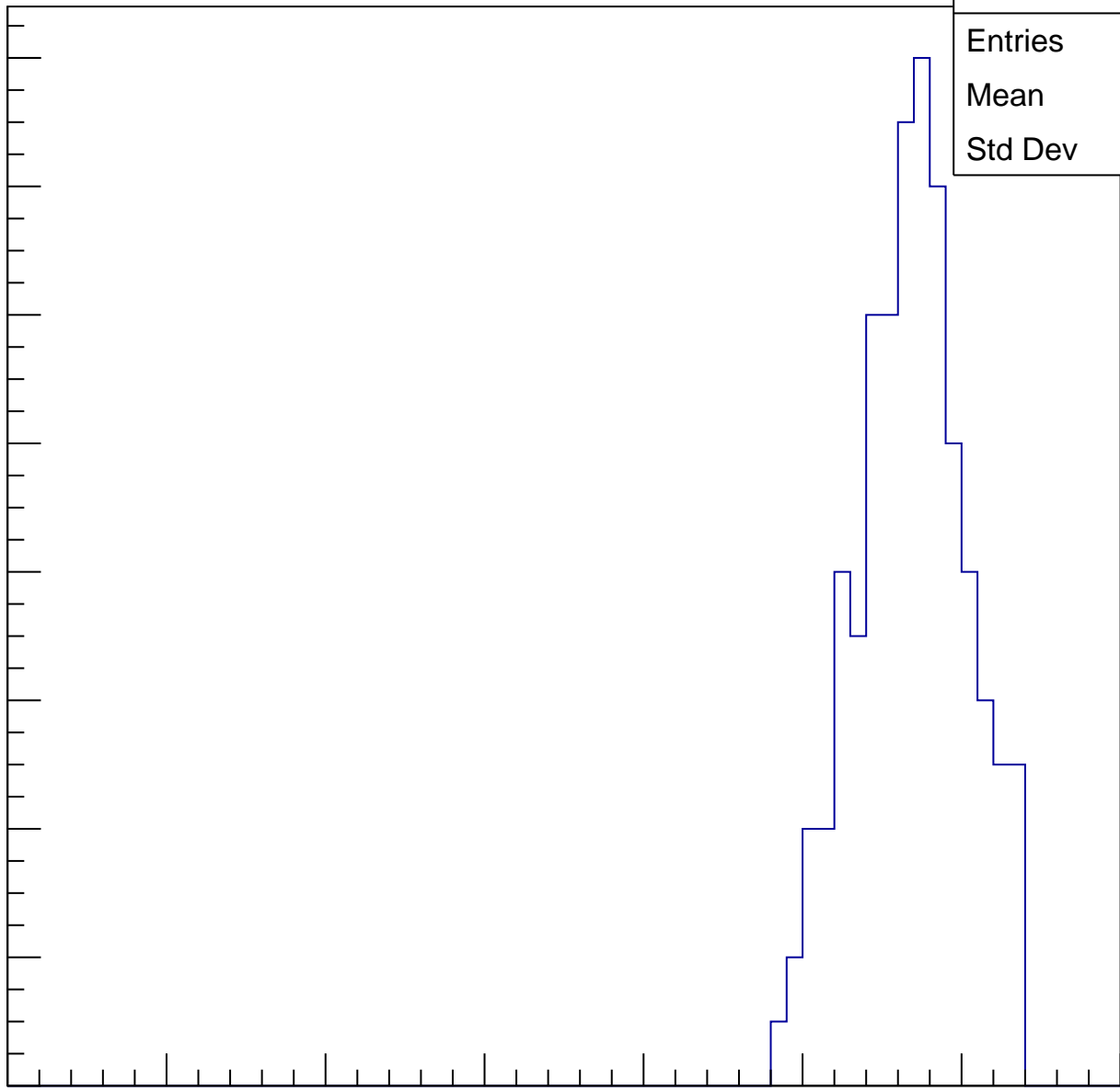
| | |
|---------|-------|
| Entries | 129 |
| Mean | 56.36 |
| Std Dev | 3.416 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch103, adc5

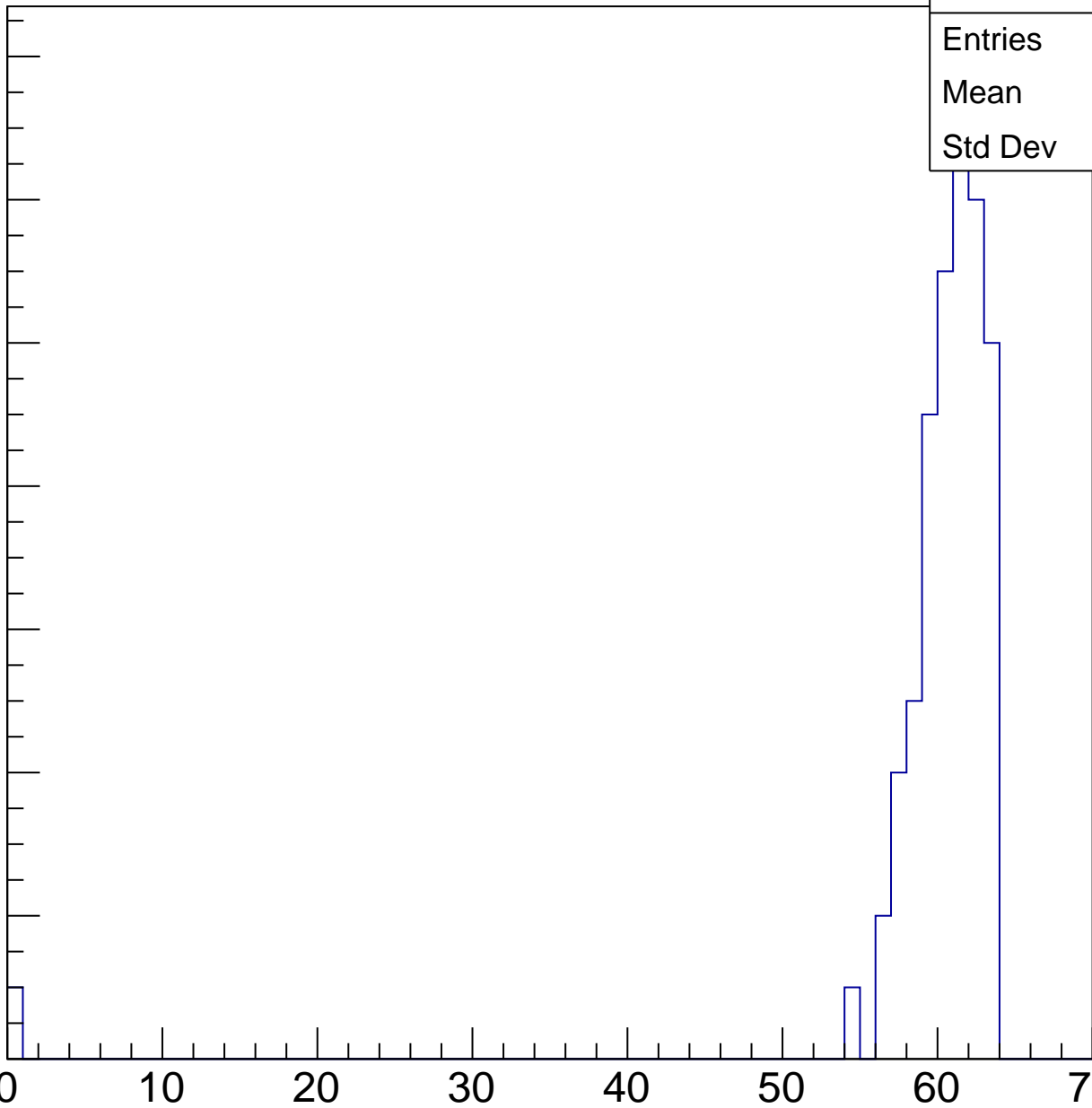
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 69 |
| Mean | 59.46 |
| Std Dev | 7.484 |

ampl

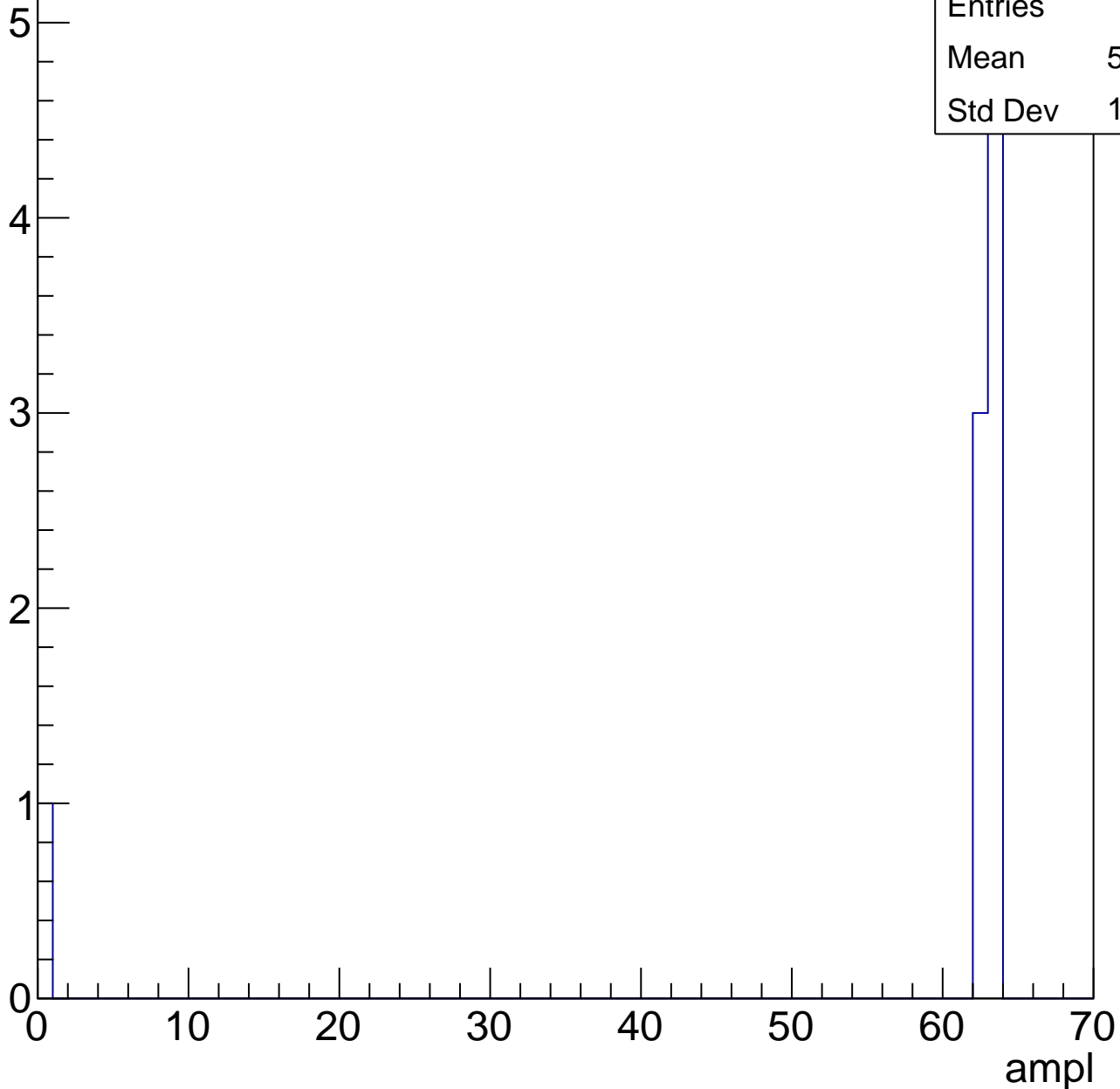


B1L001S, U26-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 55.67 |
| Std Dev | 19.69 |



B1L001S, U26-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch104, adc0

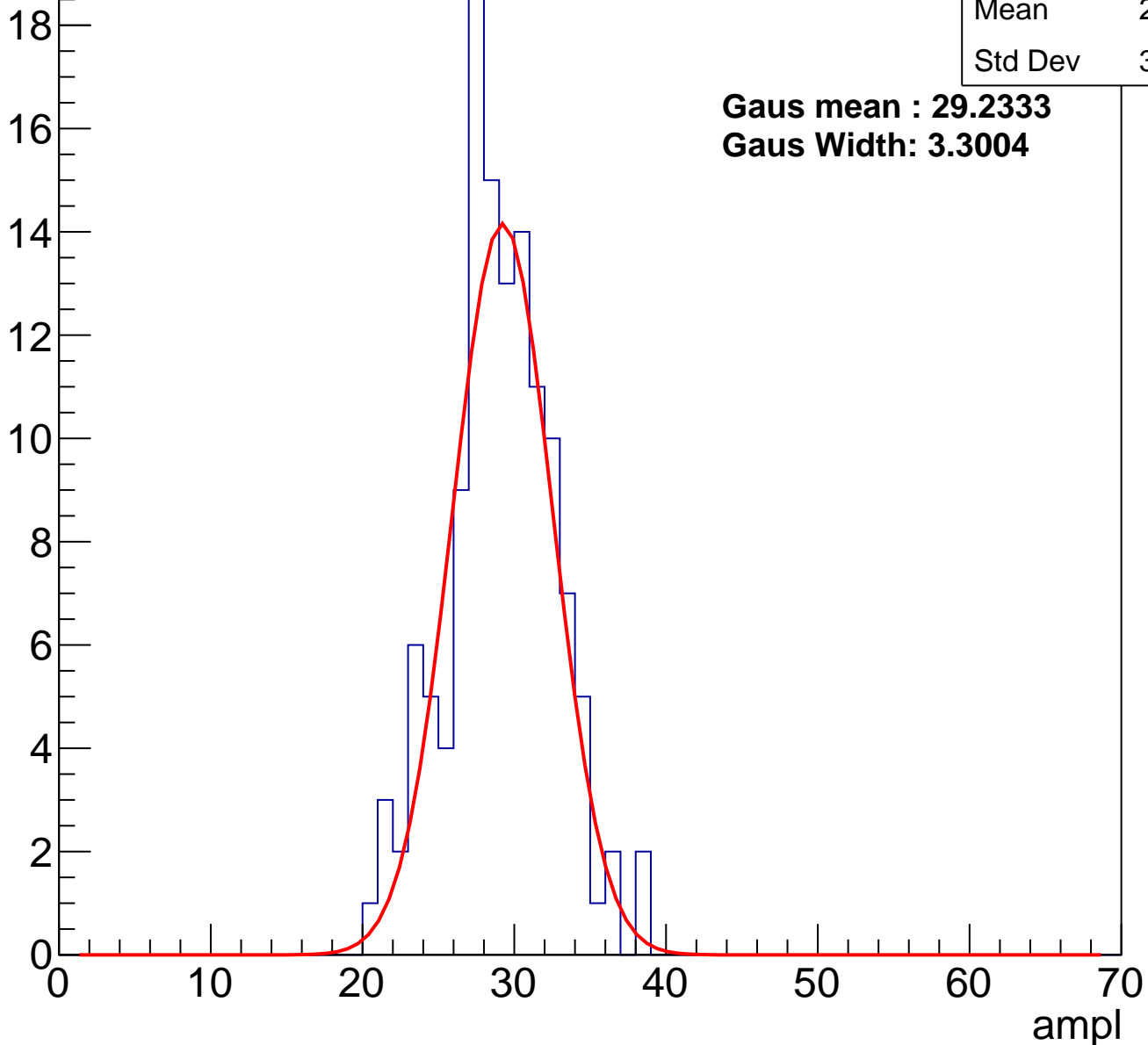
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 28.64 |
| Std Dev | 3.535 |

Gaus mean : 29.2333

Gaus Width: 3.3004

Entry



B1L001S, U26-ch104, adc1

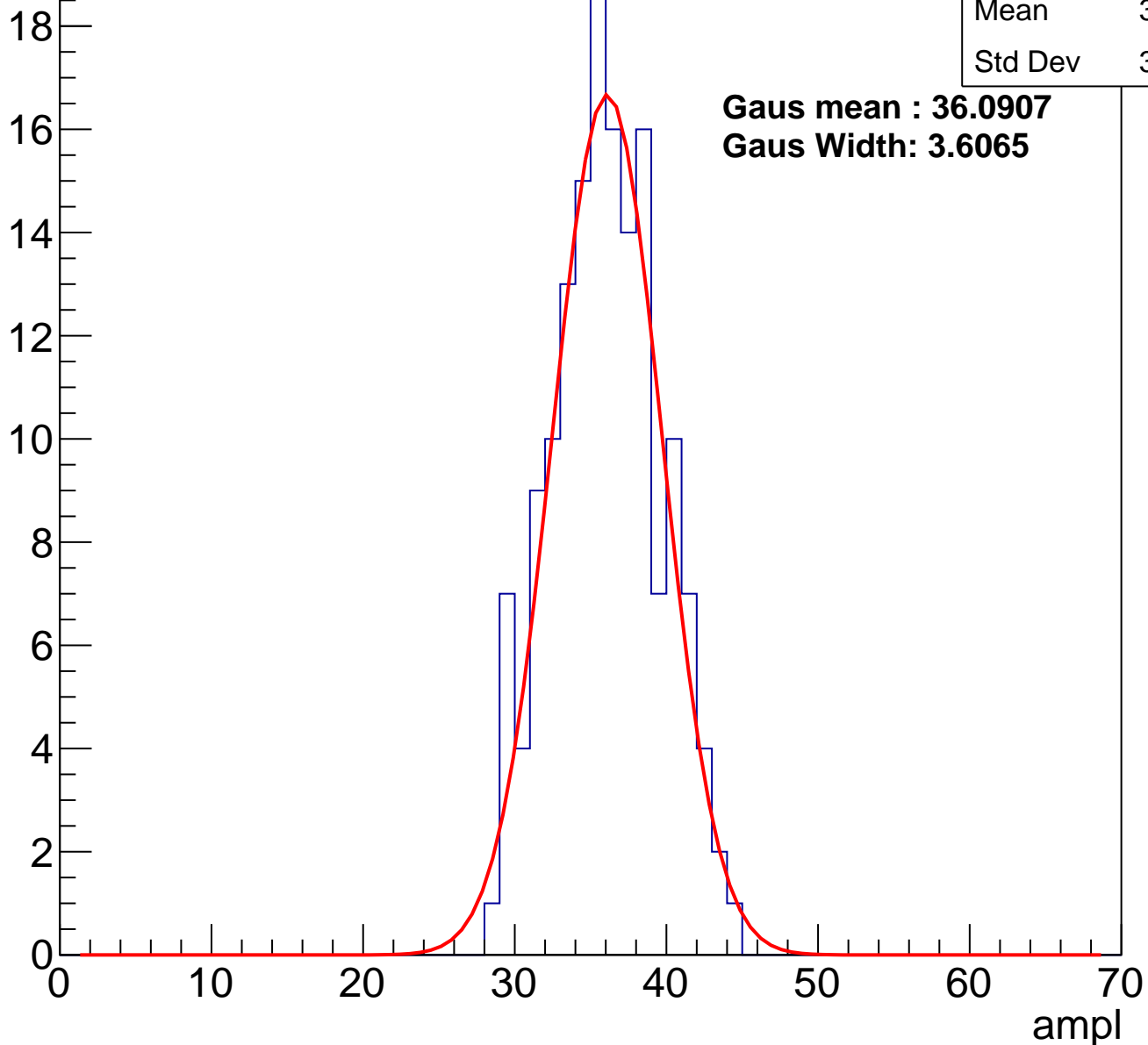
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 155 |
| Mean | 35.57 |
| Std Dev | 3.488 |

Gaus mean : 36.0907

Gaus Width: 3.6065

Entry

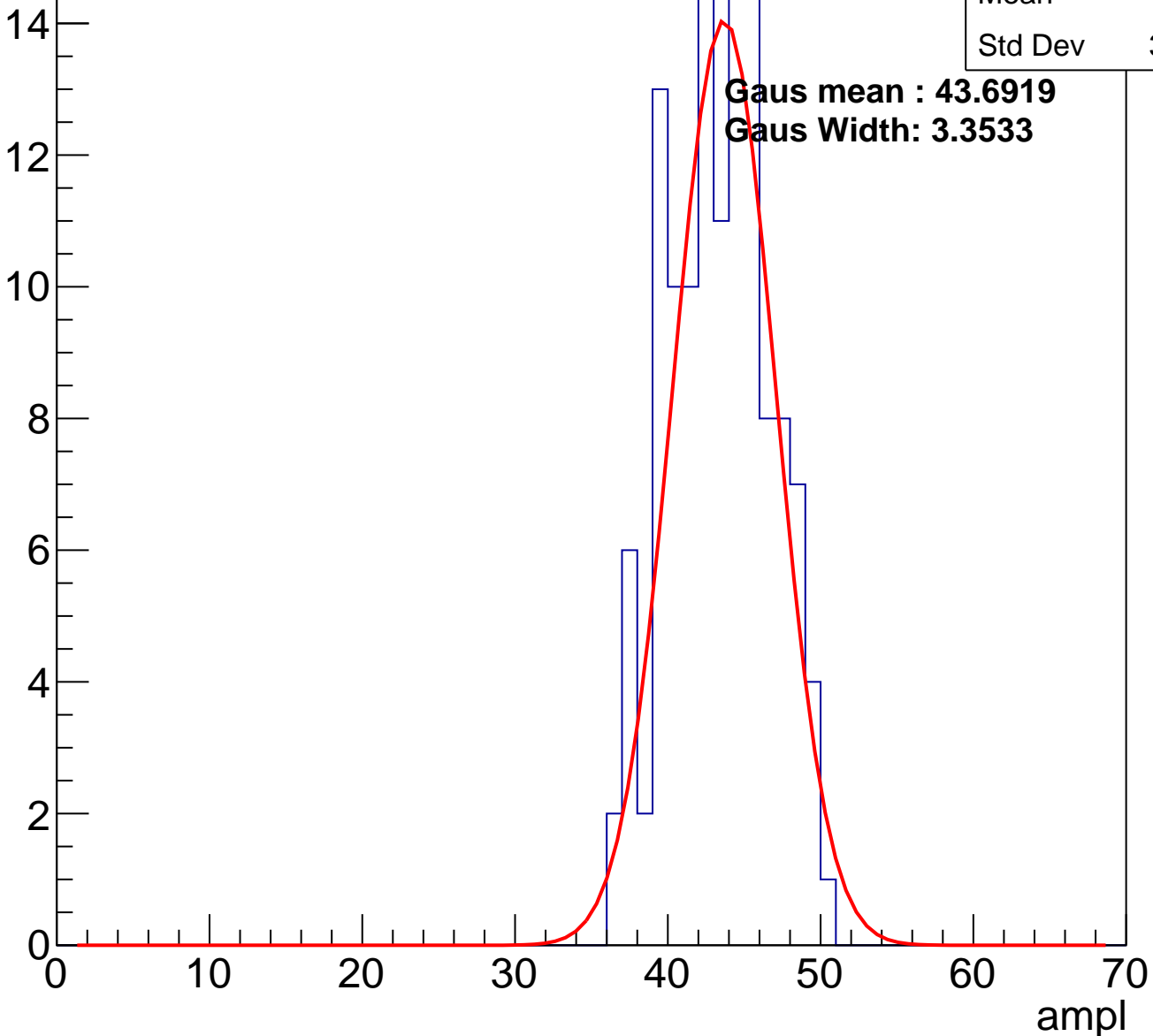


B1L001S, U26-ch104, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 42.92 |
| Std Dev | 3.271 |

Entry



B1L001S, U26-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

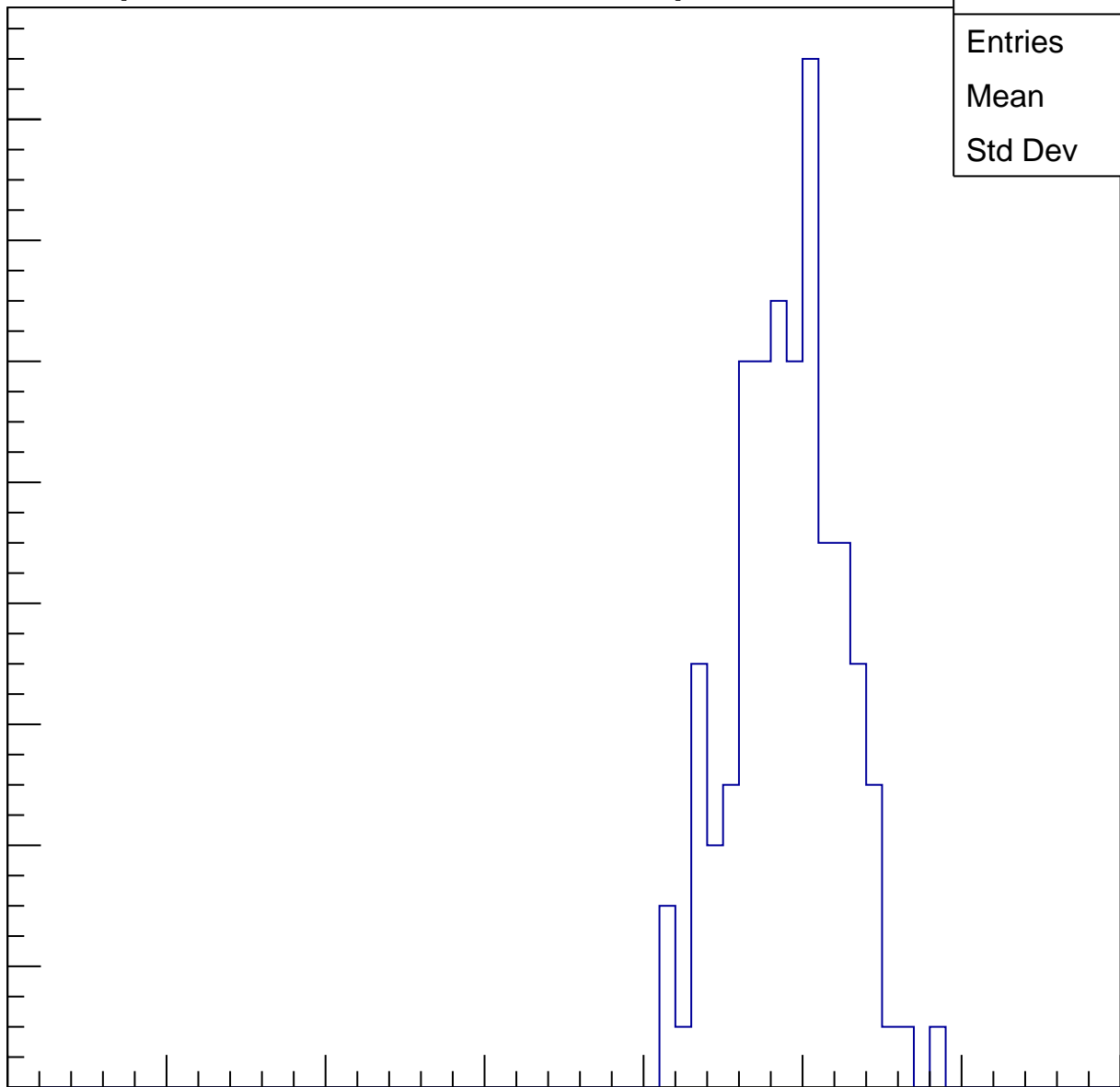
| | |
|---------|-------|
| Entries | 119 |
| Mean | 48.59 |
| Std Dev | 3.379 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

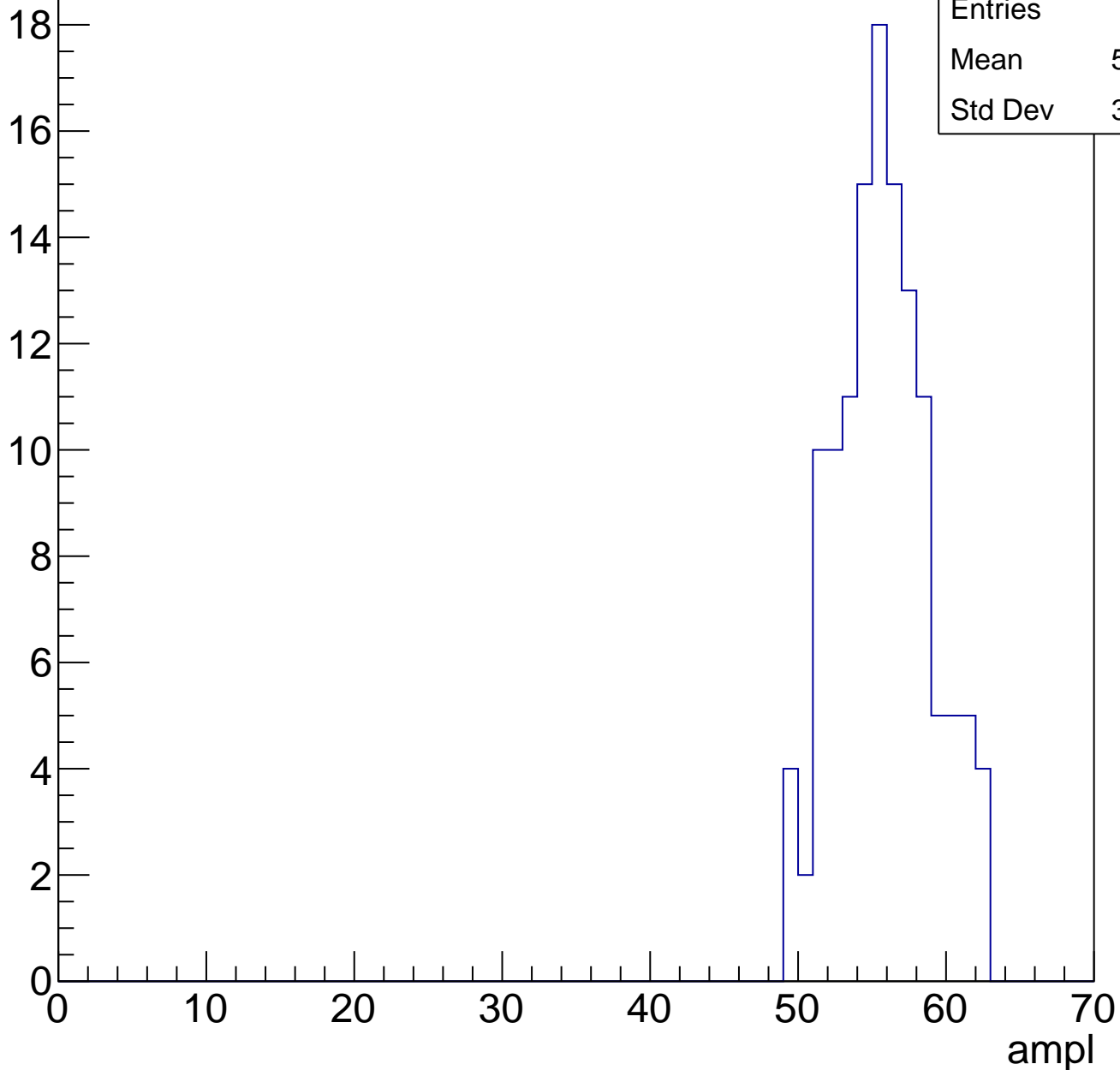
ampl



B1L001S, U26-ch104, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch104, adc5

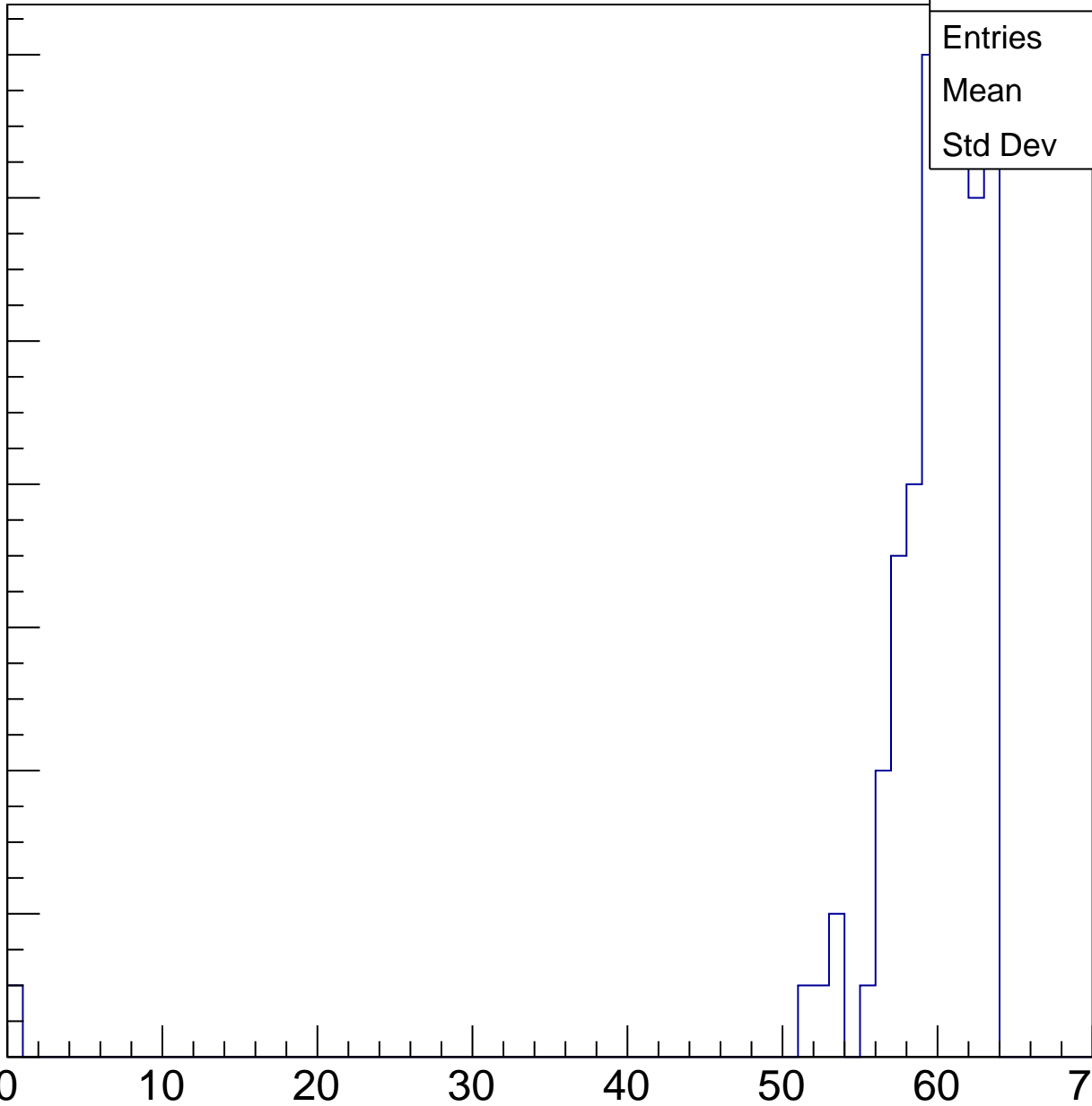
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|------|
| Entries | 92 |
| Mean | 59.1 |
| Std Dev | 6.72 |

ampl

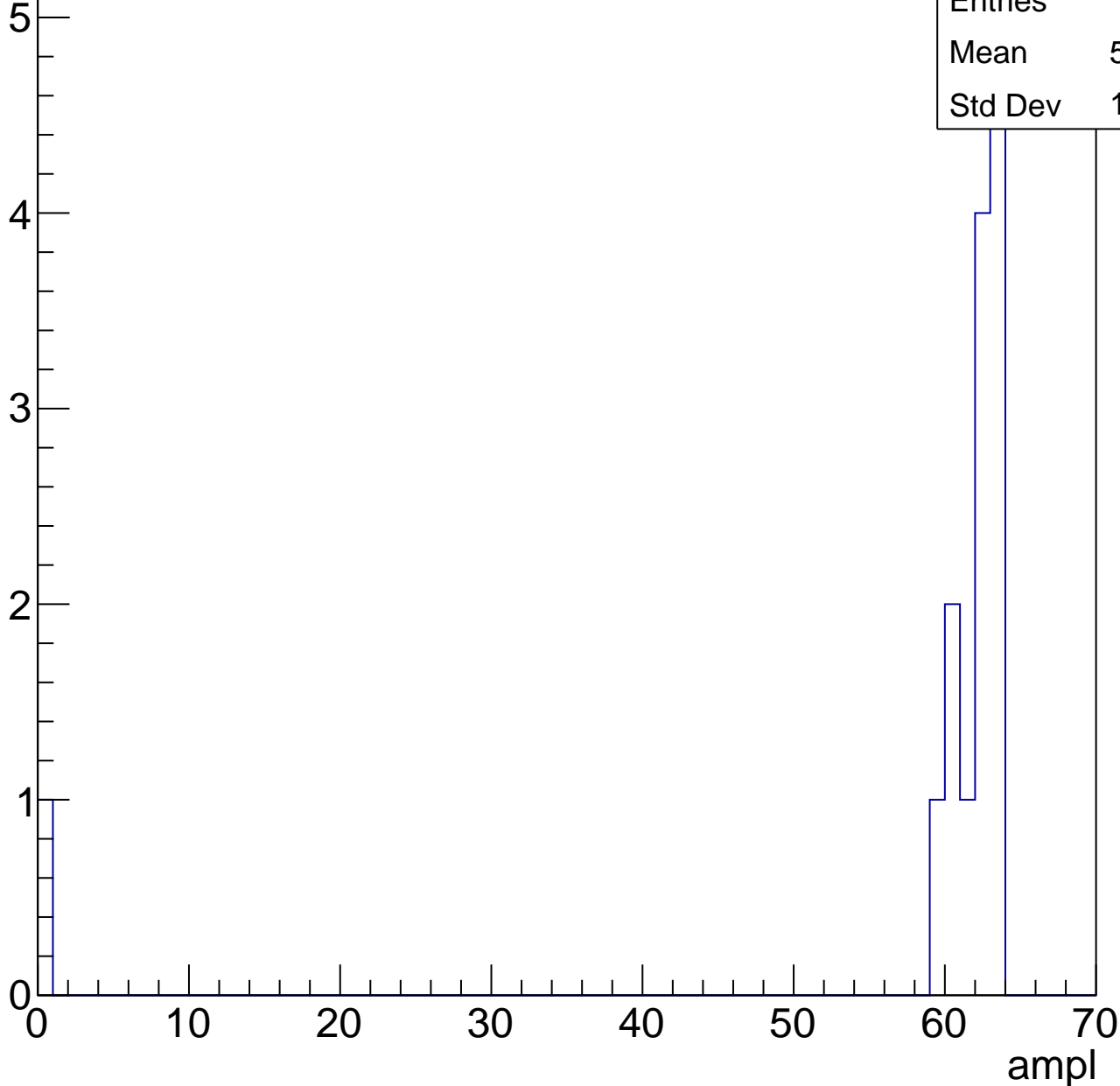


B1L001S, U26-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

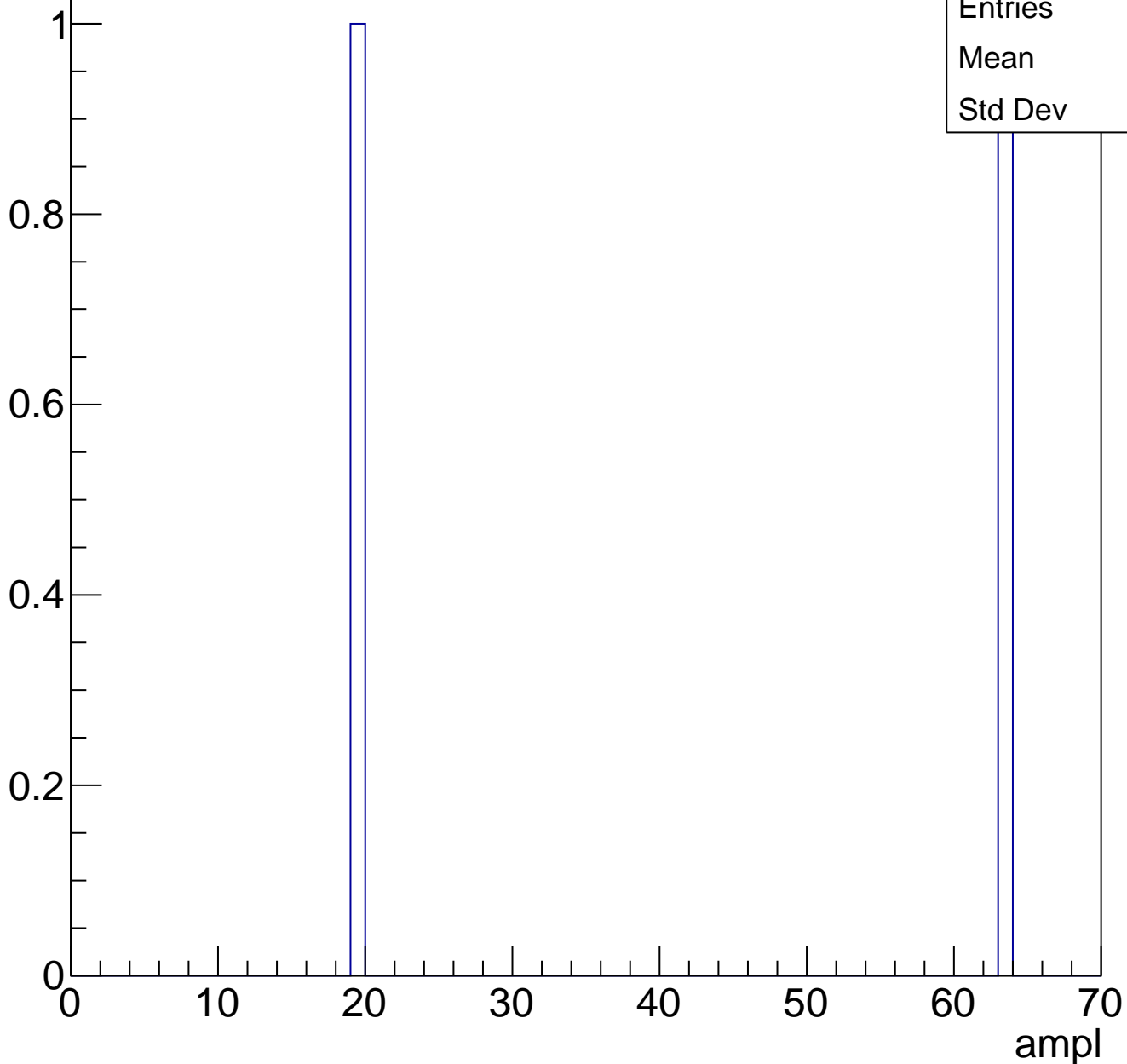
| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.36 |
| Std Dev | 15.96 |



B1L001S, U26-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch105, adc0

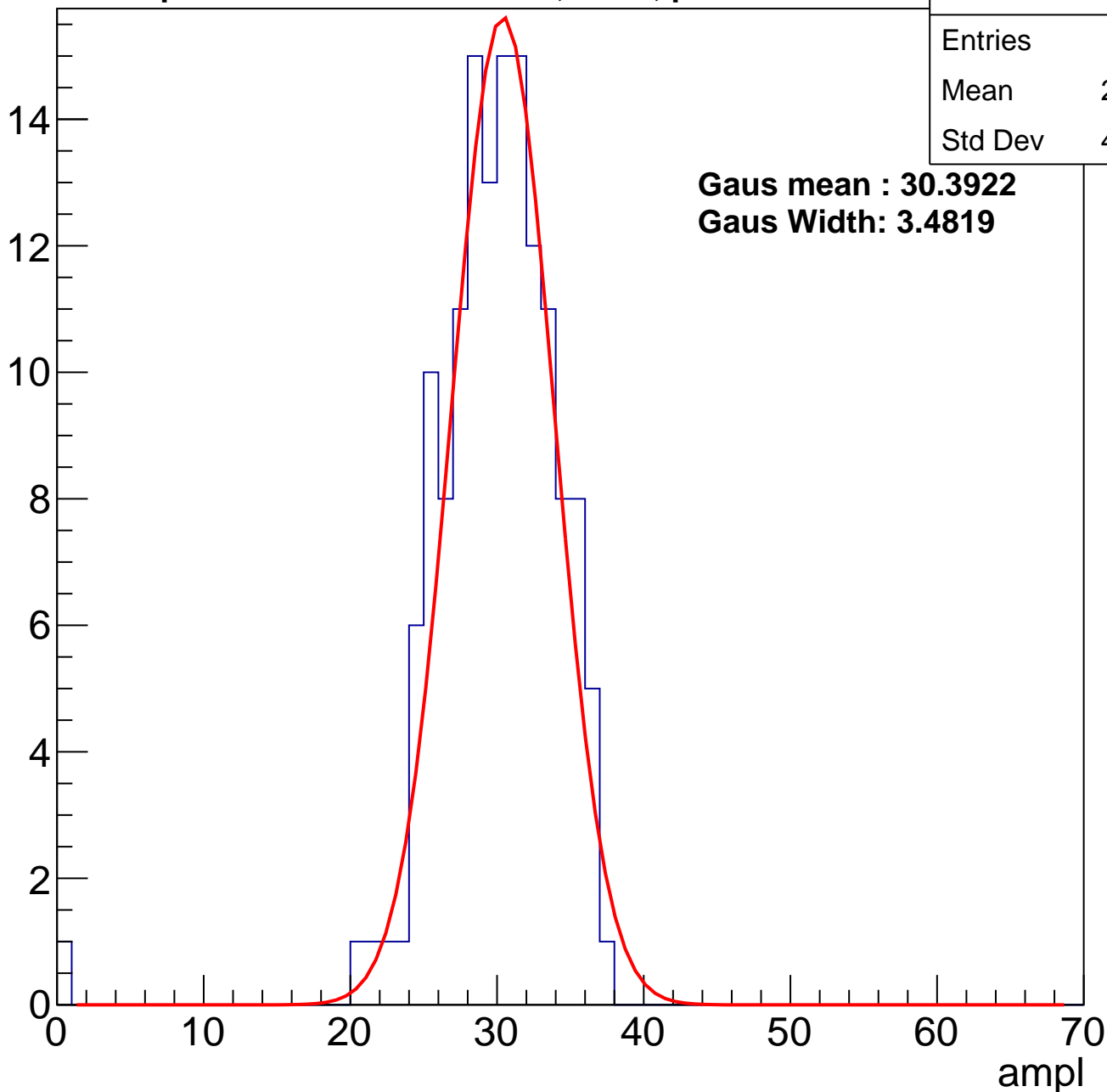
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 29.46 |
| Std Dev | 4.295 |

Gaus mean : 30.3922

Gaus Width: 3.4819

Entry



B1L001S, U26-ch105, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 37.02 |
| Std Dev | 3.52 |

Gaus mean : 37.6500

Gaus Width: 3.9861

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U26-ch105, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

117

Mean

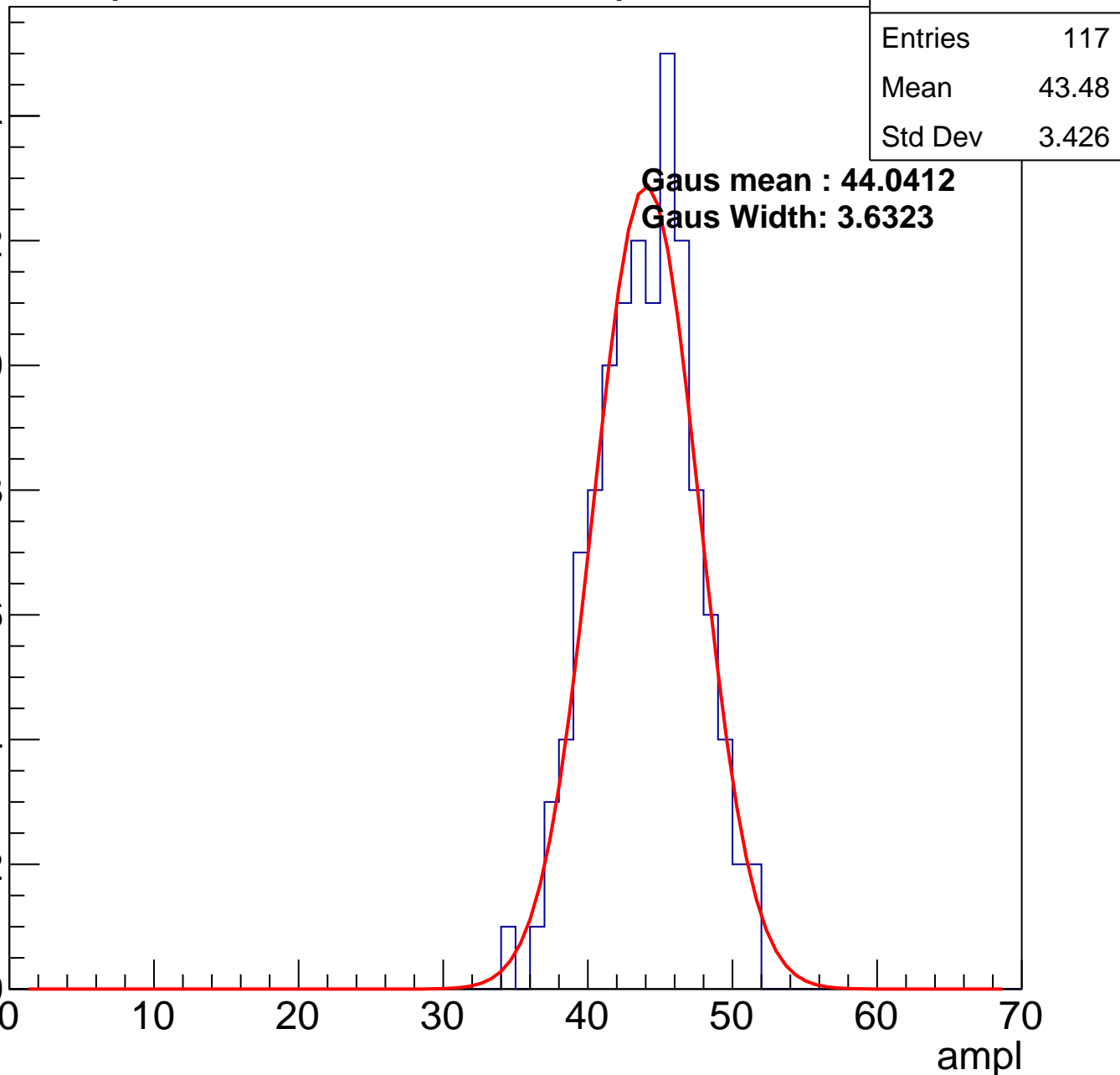
43.48

Std Dev

3.426

Gaus mean : 44.0412

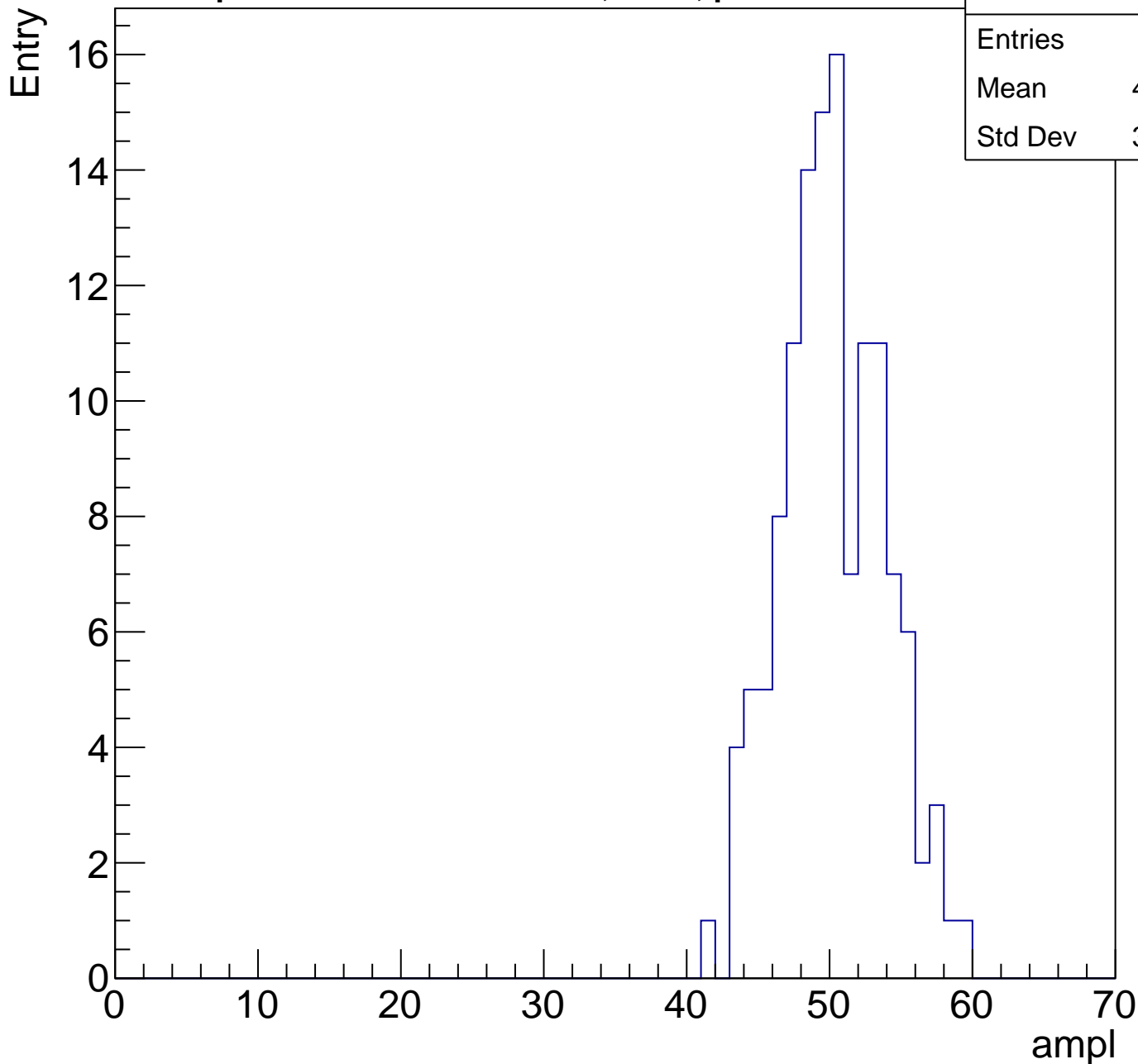
Gaus Width: 3.6323



B1L001S, U26-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

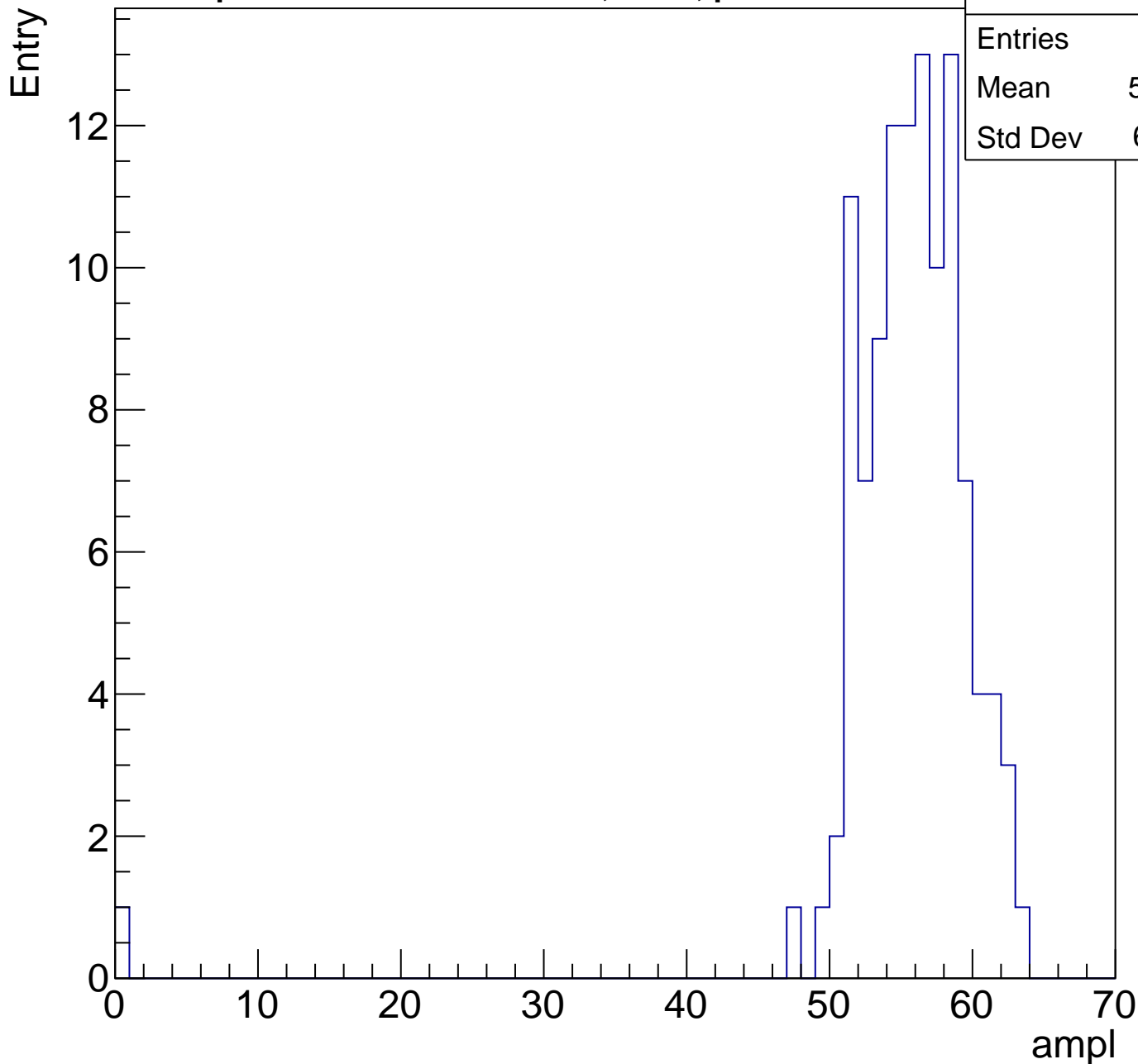
| | |
|---------|-------|
| Entries | 128 |
| Mean | 49.77 |
| Std Dev | 3.589 |



B1L001S, U26-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 111 |
| Mean | 54.99 |
| Std Dev | 6.151 |



B1L001S, U26-ch105, adc5

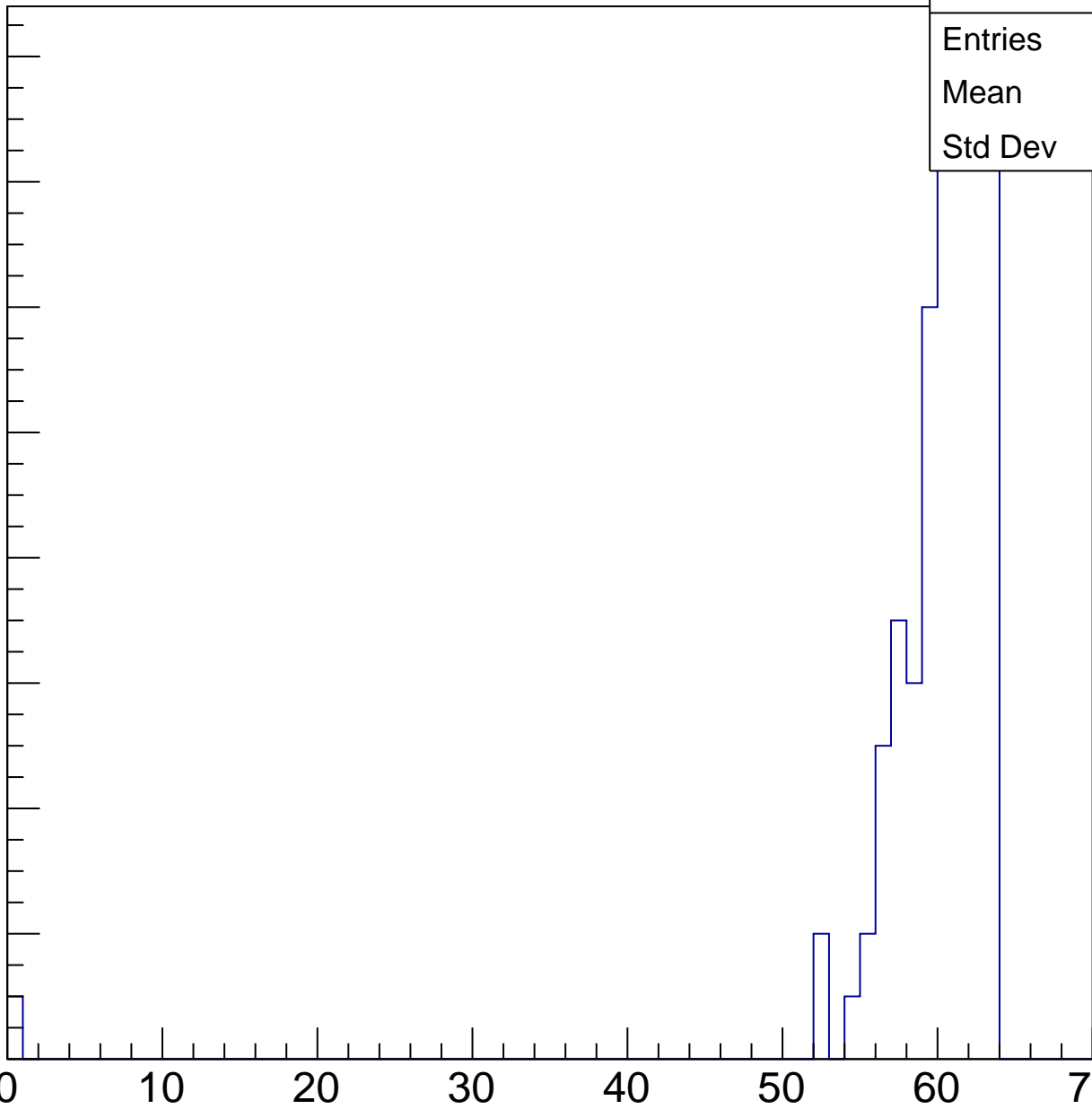
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 59.35 |
| Std Dev | 6.52 |

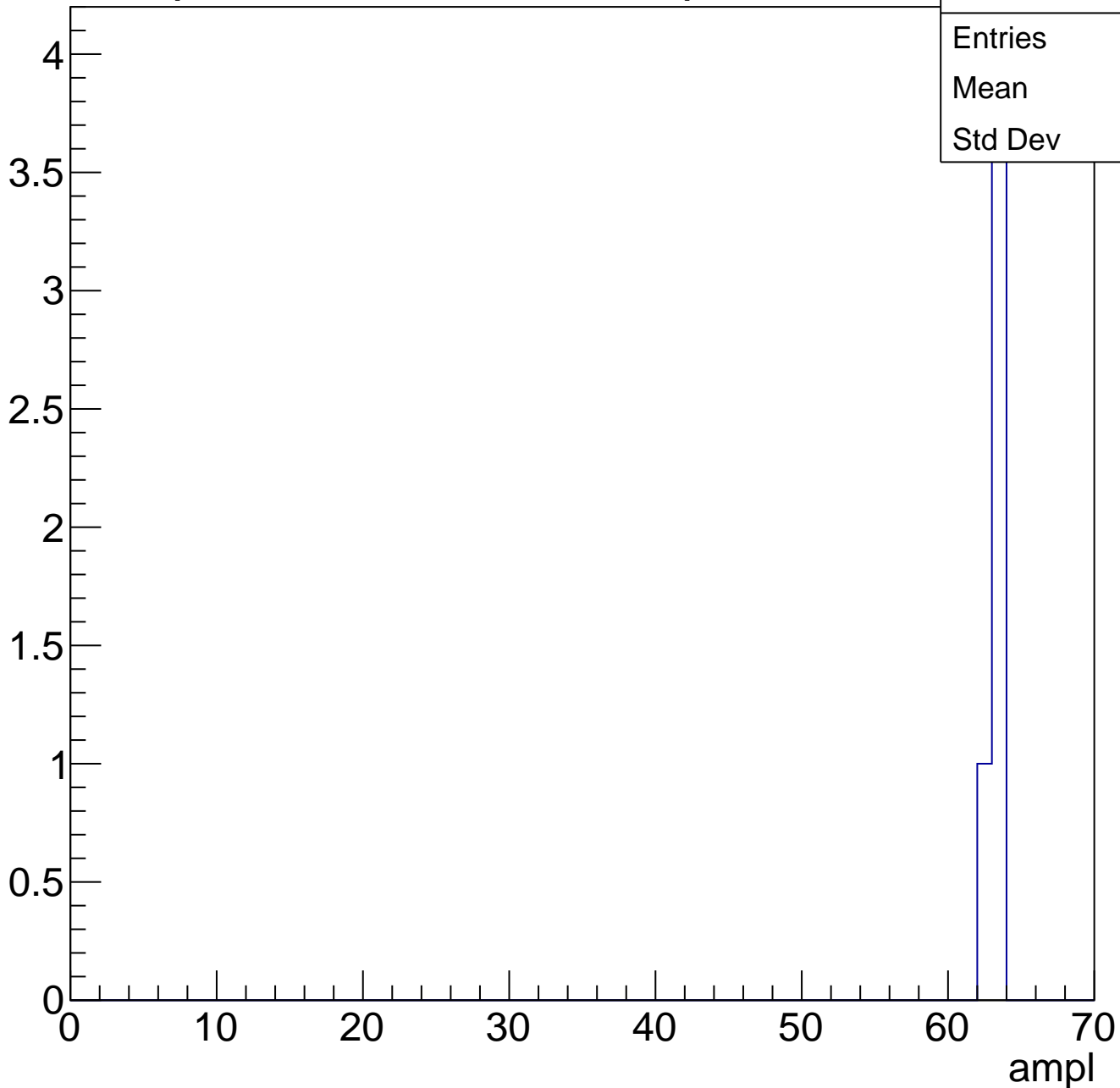
ampl



B1L001S, U26-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

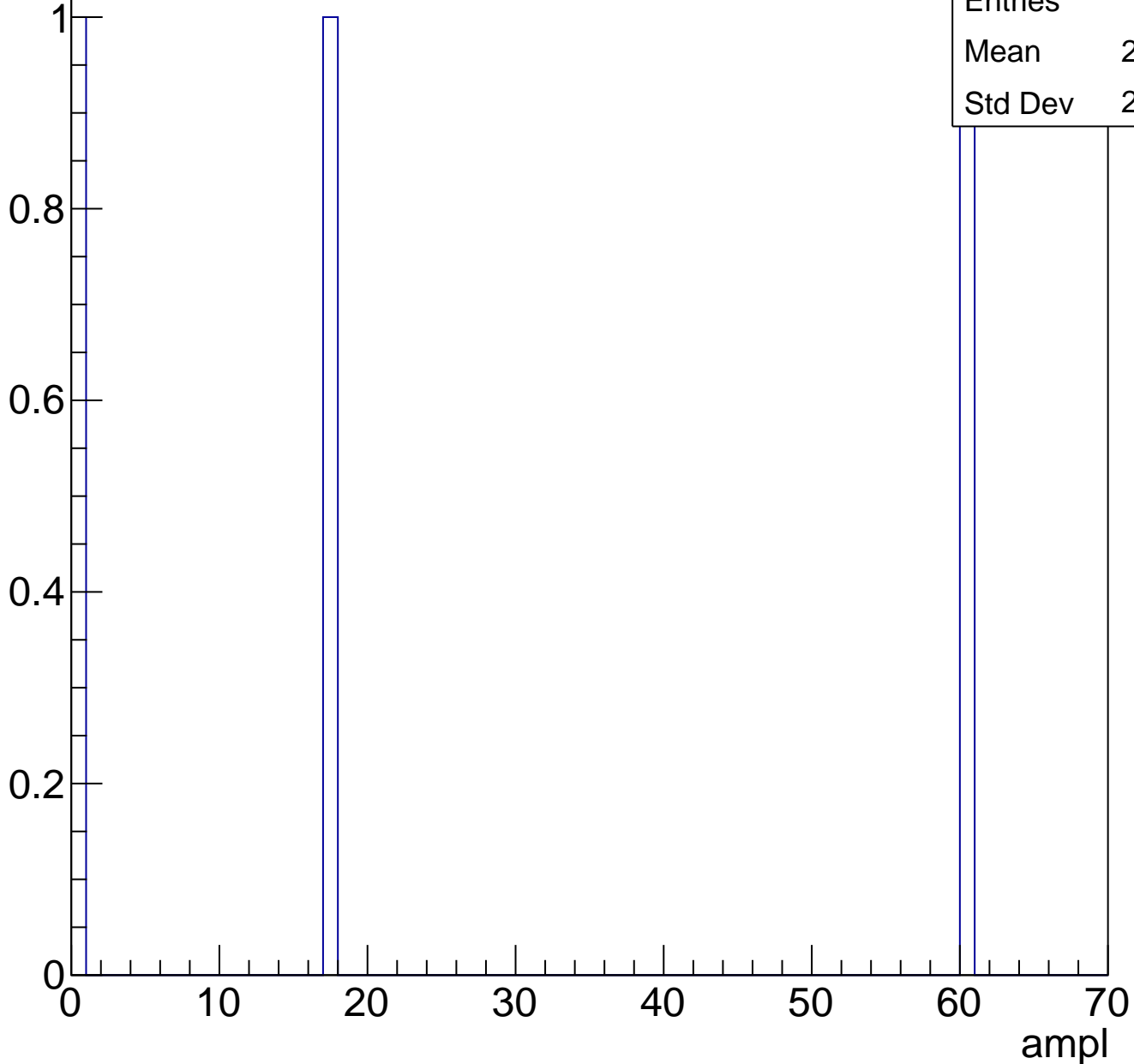


| | |
|---------|------|
| Entries | 5 |
| Mean | 62.8 |
| Std Dev | 0.4 |

B1L001S, U26-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

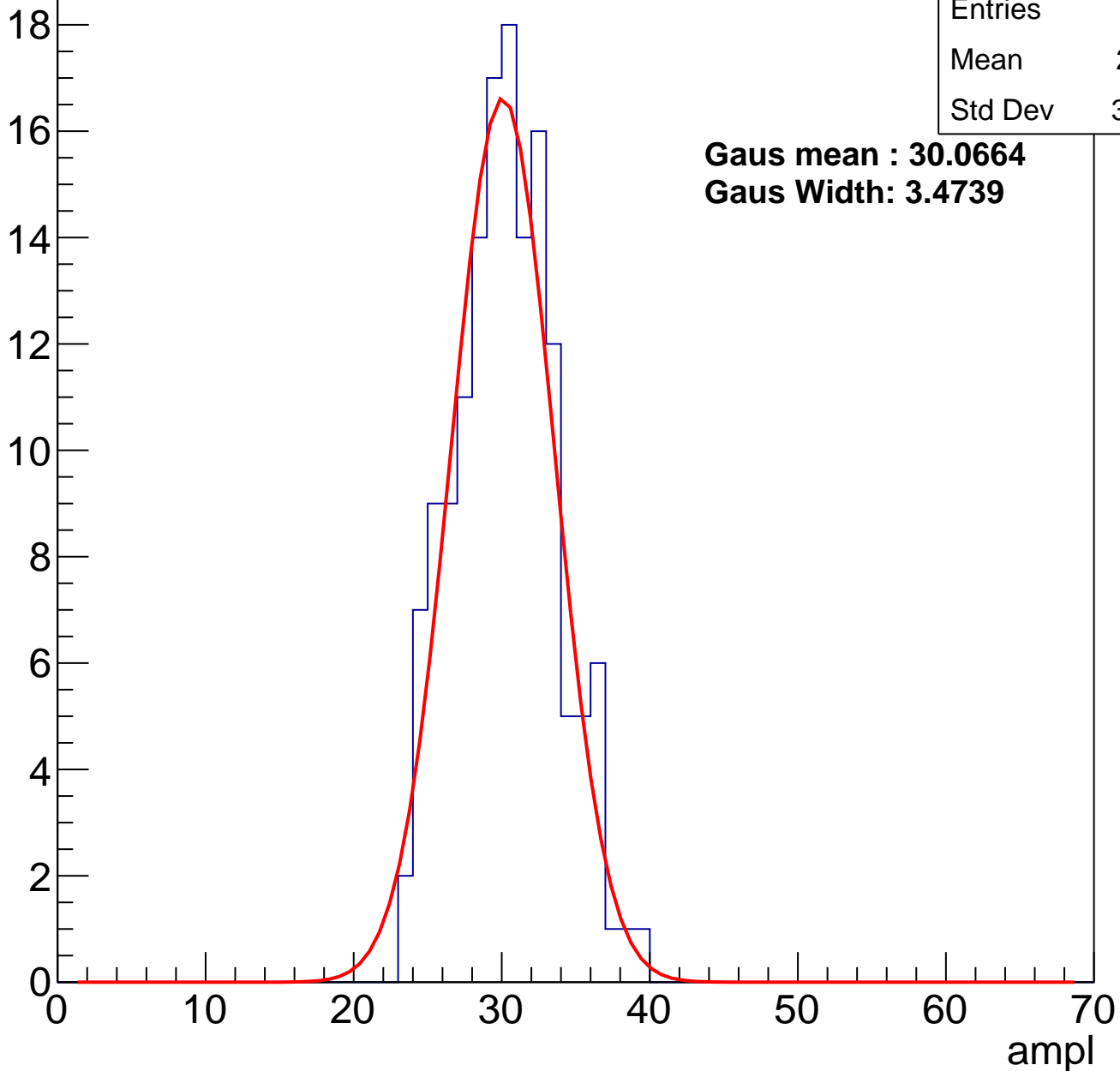


| | |
|---------|-------|
| Entries | 3 |
| Mean | 25.67 |
| Std Dev | 25.25 |

B1L001S, U26-ch106, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 37.06 |
| Std Dev | 3.217 |

Gaus mean : 37.6860

Gaus Width: 3.4871

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

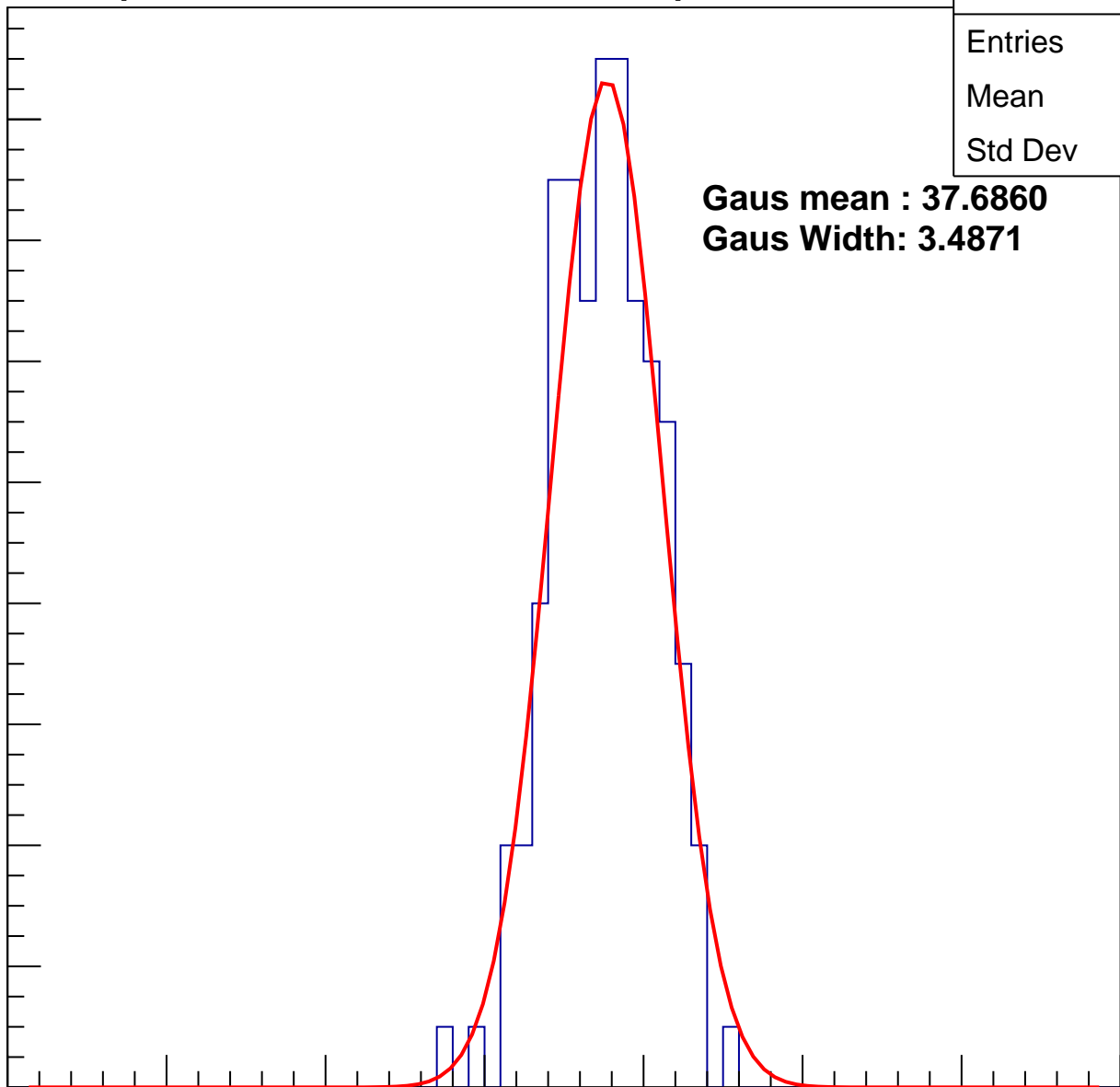
40

50

60

70

ampl



B1L001S, U26-ch106, adc2

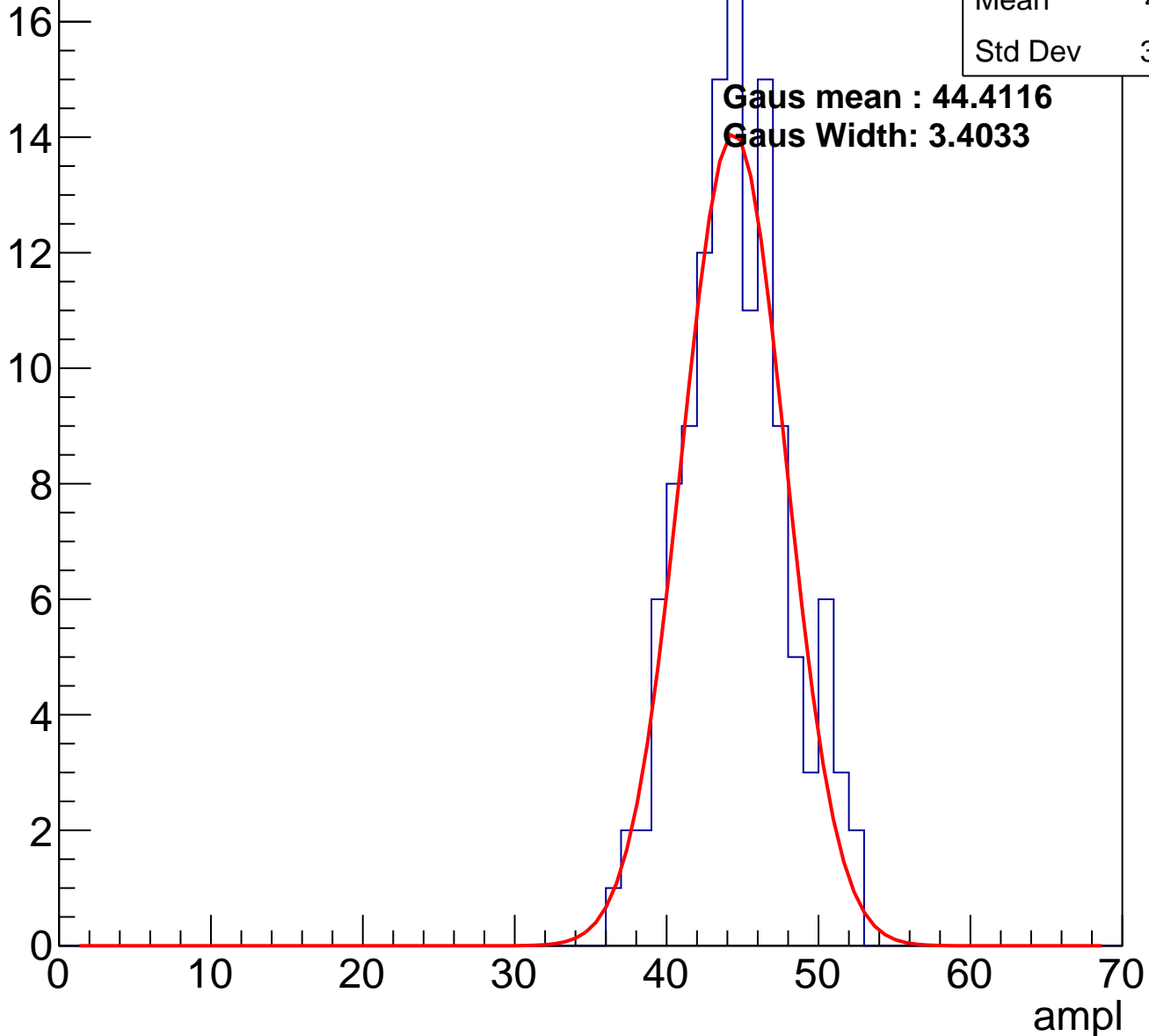
calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 126 |
| Mean | 44.11 |
| Std Dev | 3.395 |

Gaus mean : 44.4116

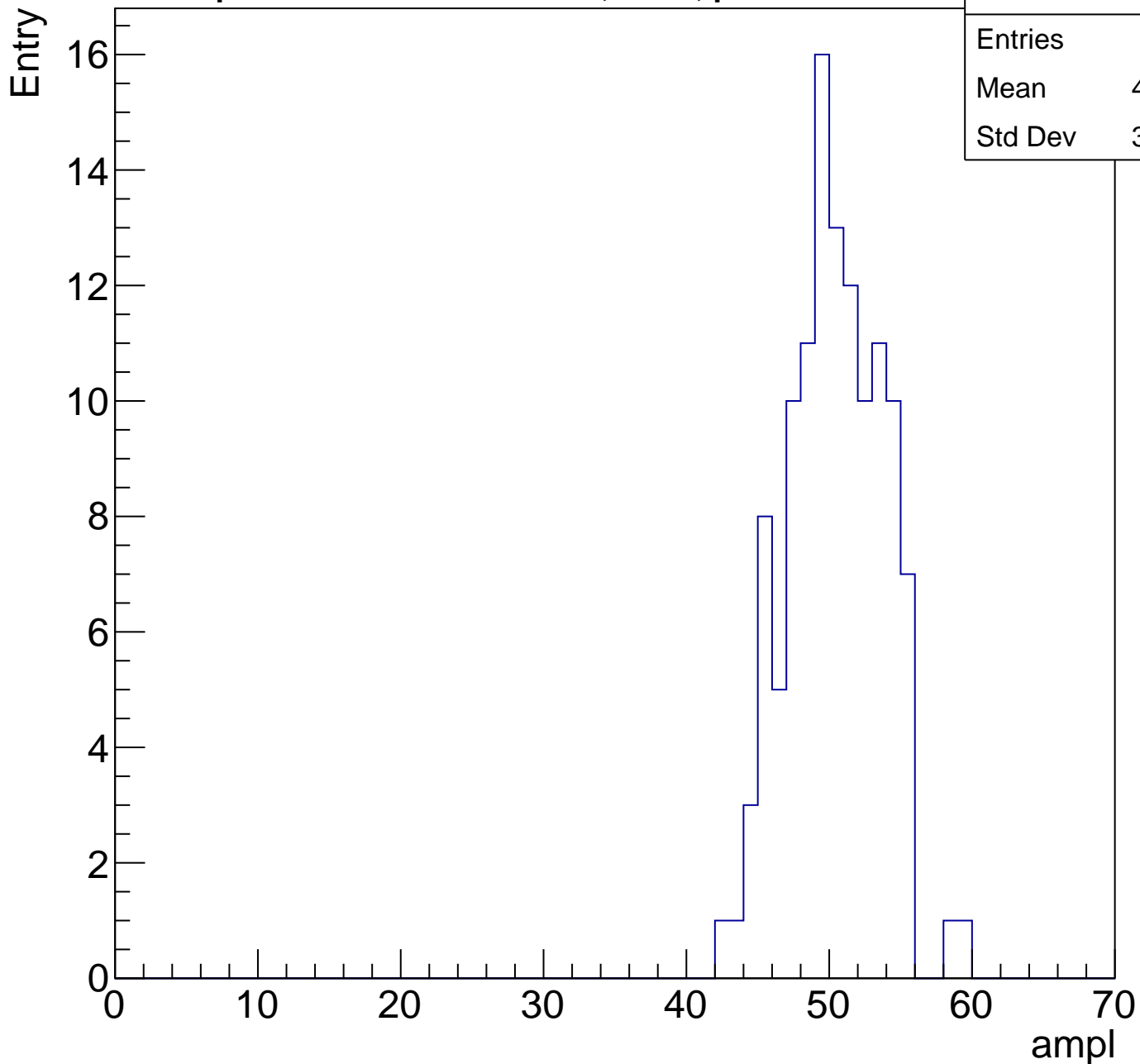
Gaus Width: 3.4033



B1L001S, U26-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 49.97 |
| Std Dev | 3.268 |



B1L001S, U26-ch106, adc4

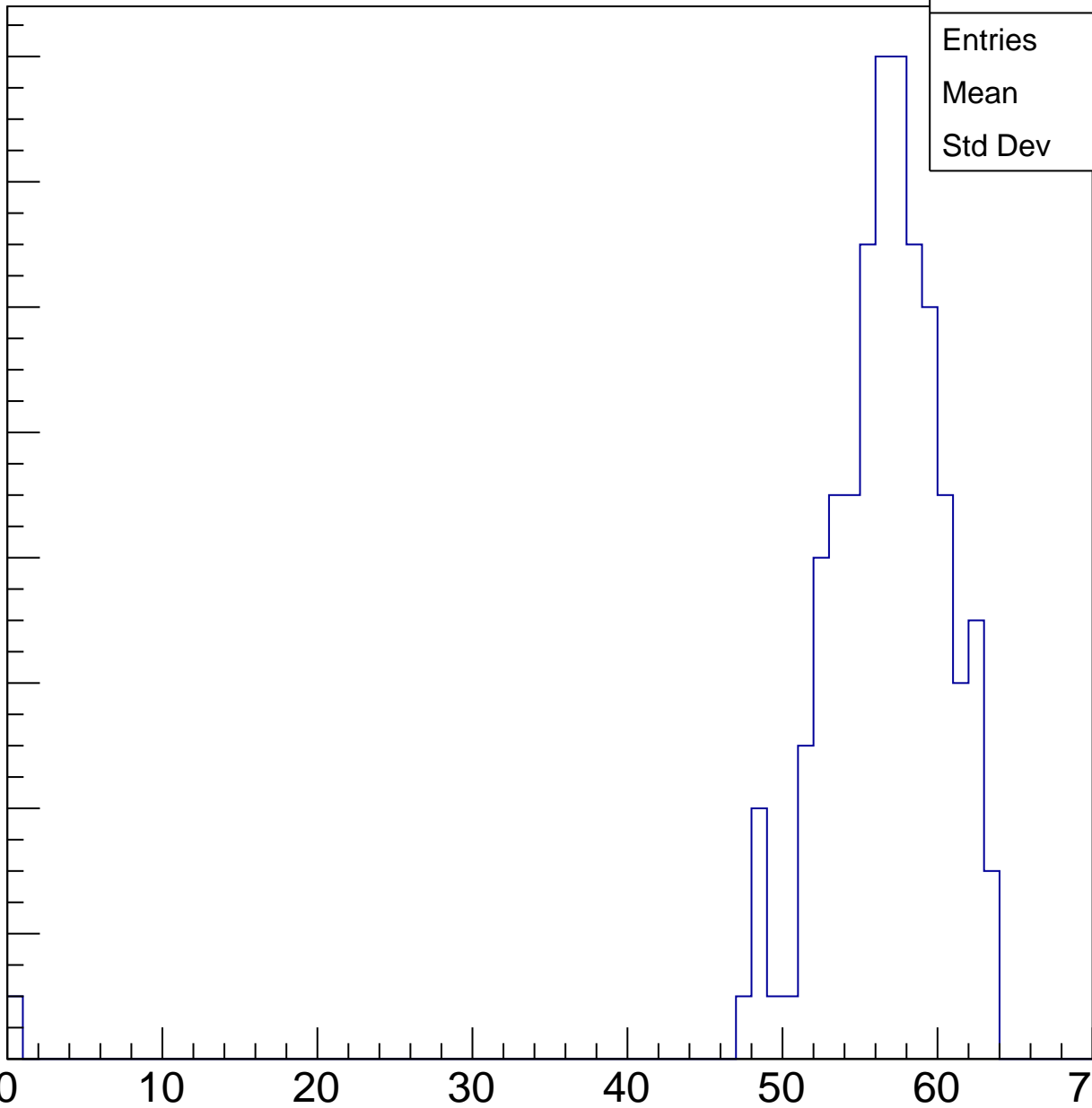
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 55.87 |
| Std Dev | 5.99 |

ampl



B1L001S, U26-ch106, adc5

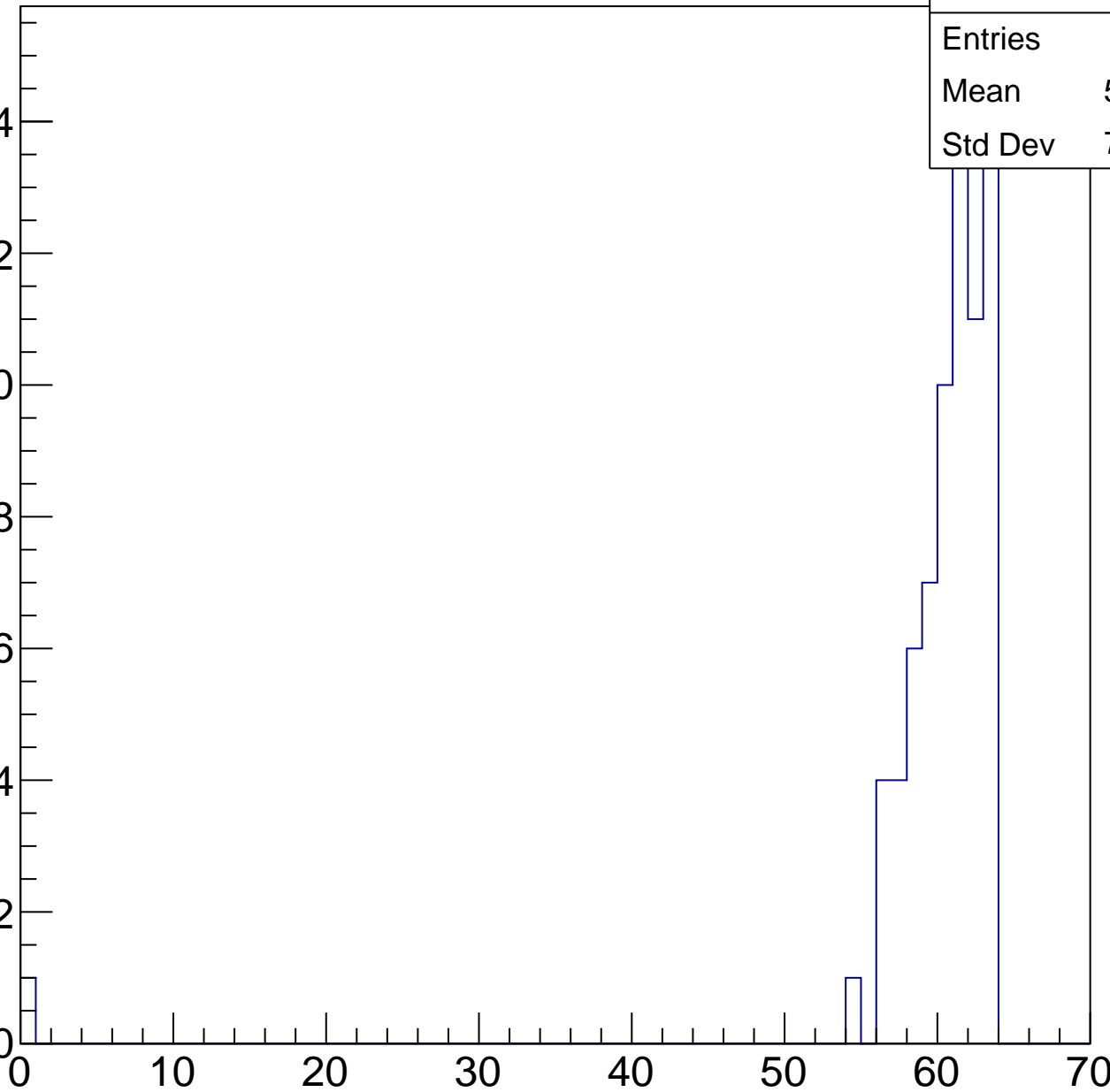
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 59.56 |
| Std Dev | 7.352 |

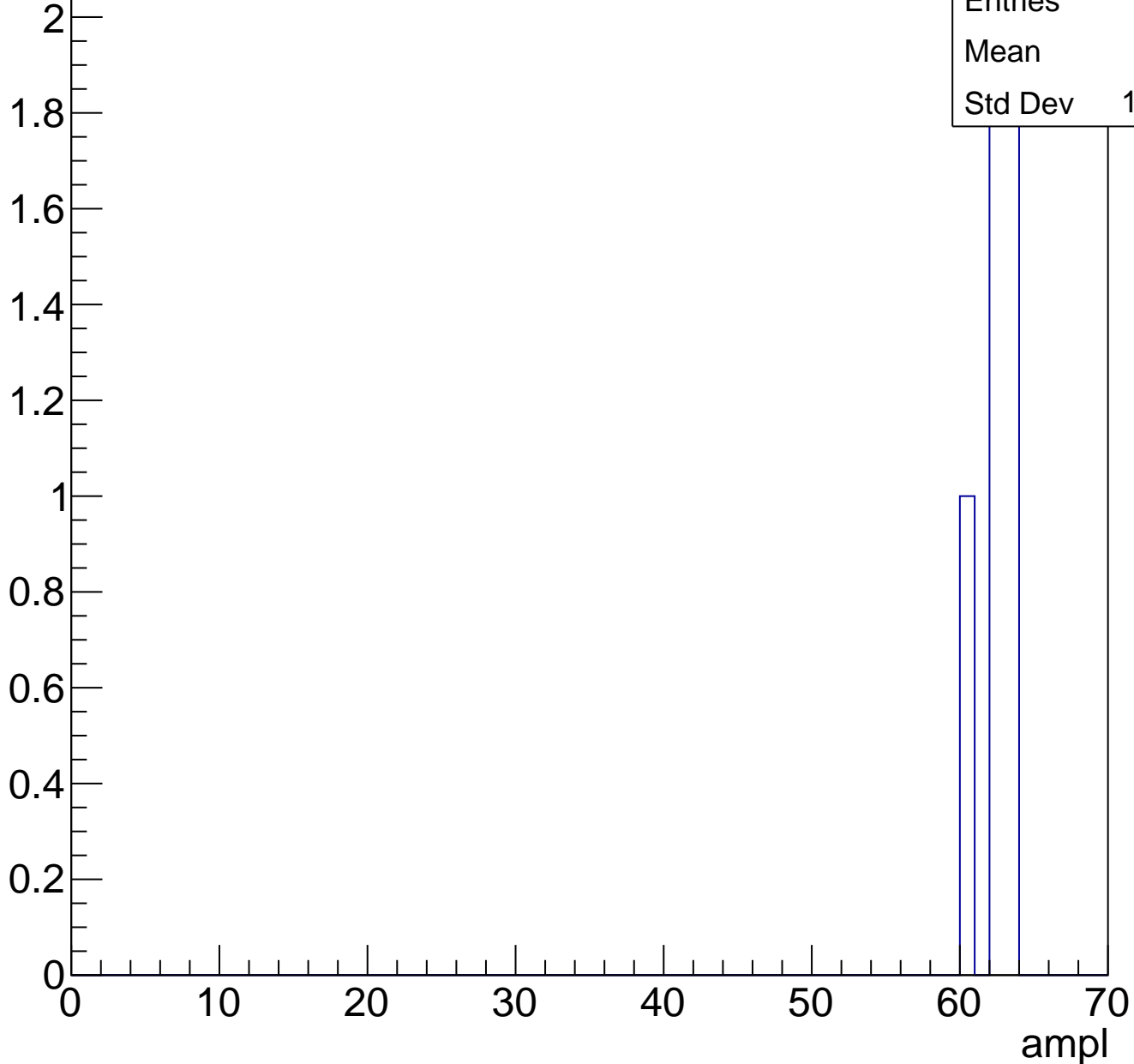
ampl



B1L001S, U26-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 23 |
| Std Dev | 0 |

B1L001S, U26-ch107, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 30.18 |
| Std Dev | 3.688 |

Gaus mean : 30.8175

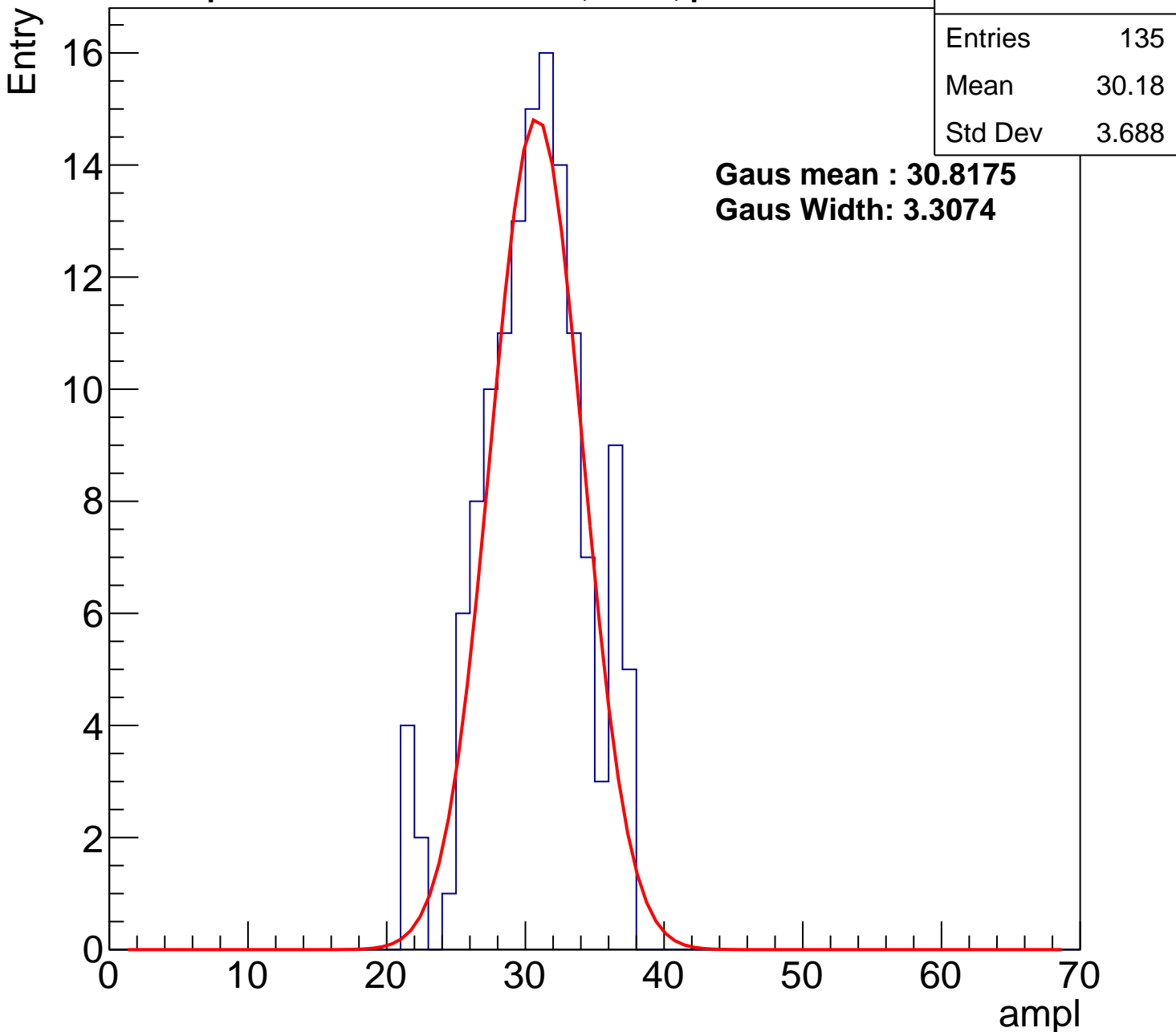
Gaus Width: 3.3074

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch107, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 129 |
| Mean | 37.07 |
| Std Dev | 3.192 |

Gaus mean : 37.4818

Gaus Width: 3.2621

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

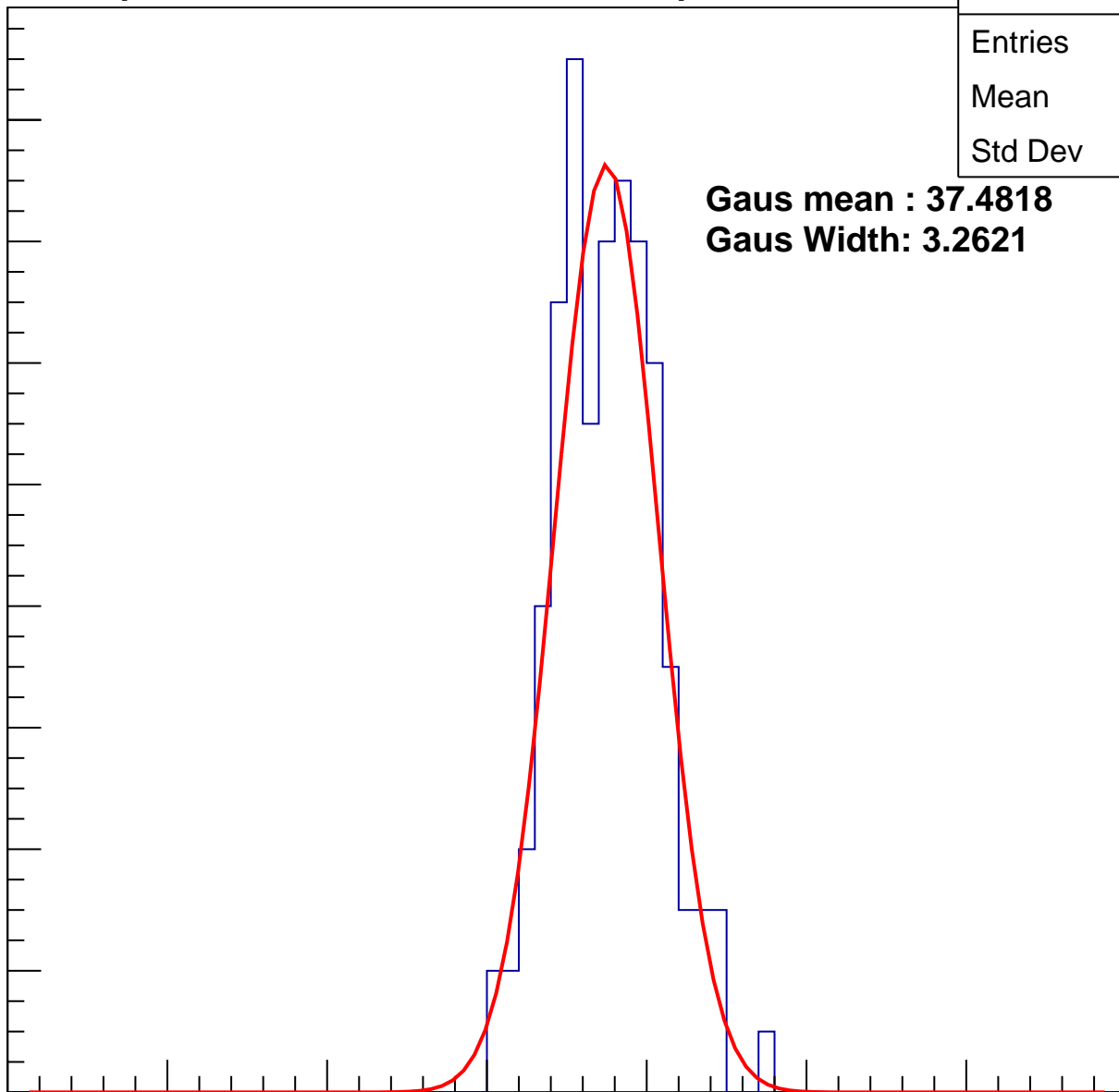
40

50

60

70

ampl



B1L001S, U26-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 43.85 |
| Std Dev | 3.276 |

Gaus mean : 44.2068

Gaus Width: 3.2823

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

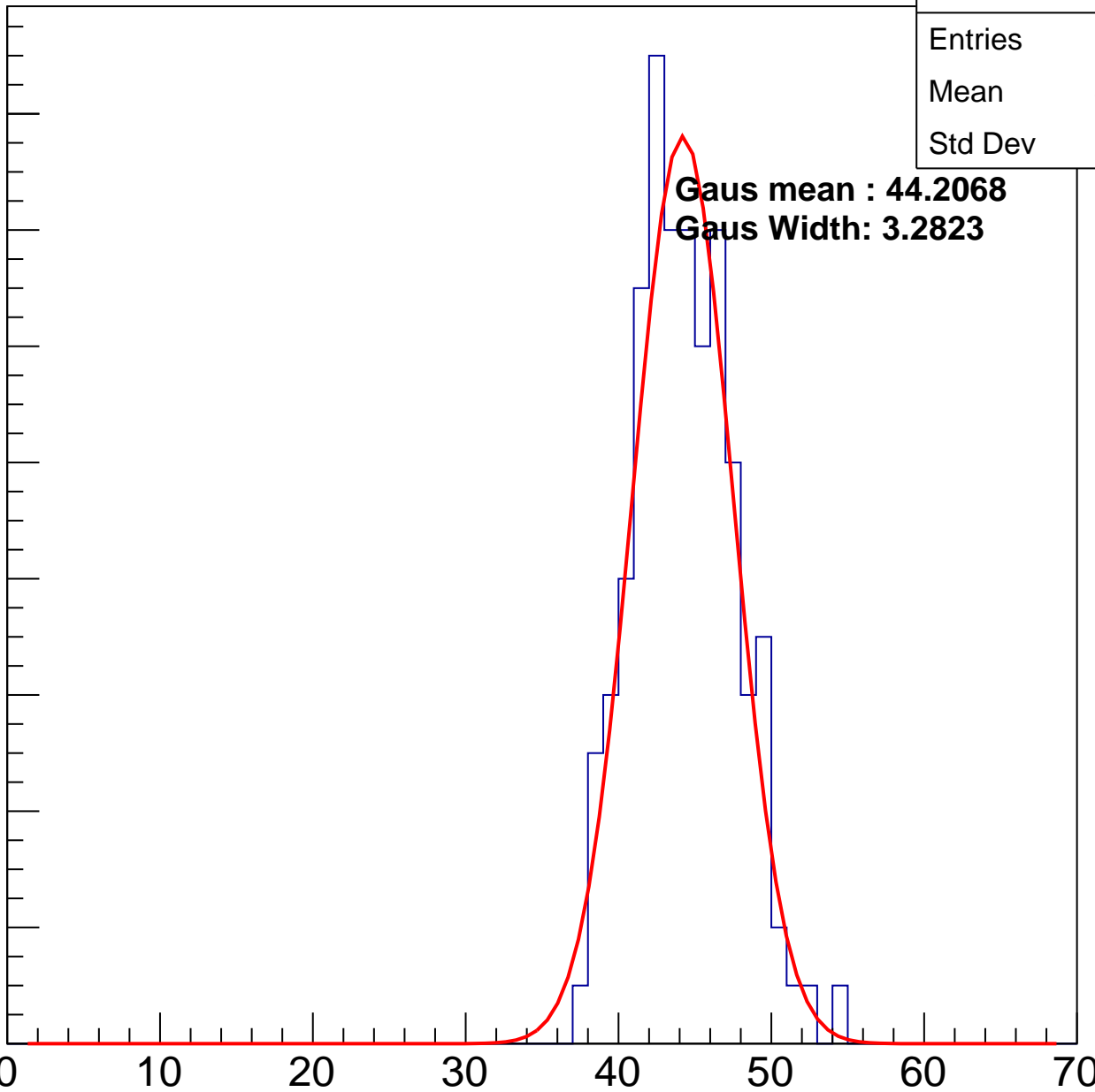
40

50

60

70

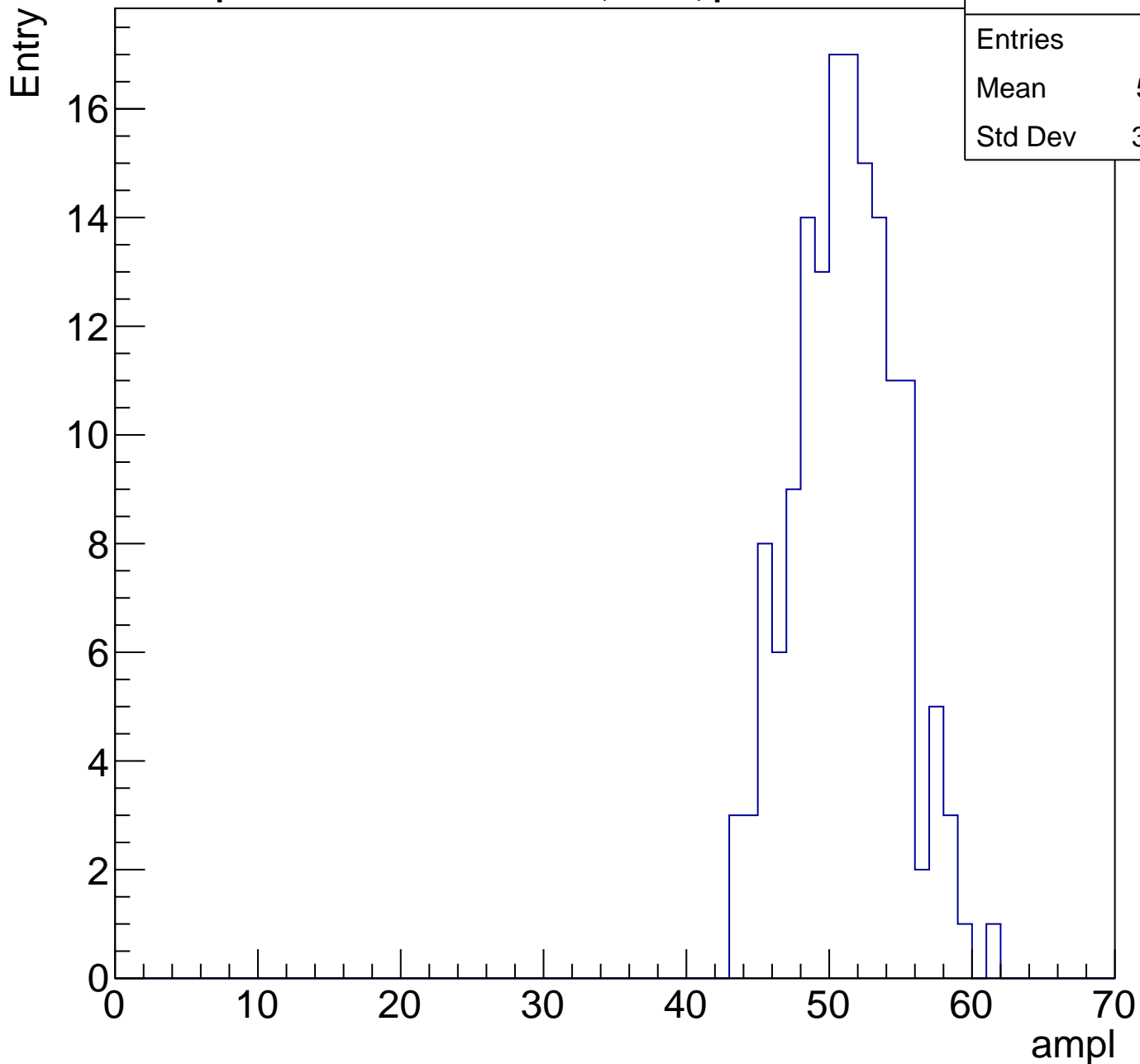
ampl



B1L001S, U26-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

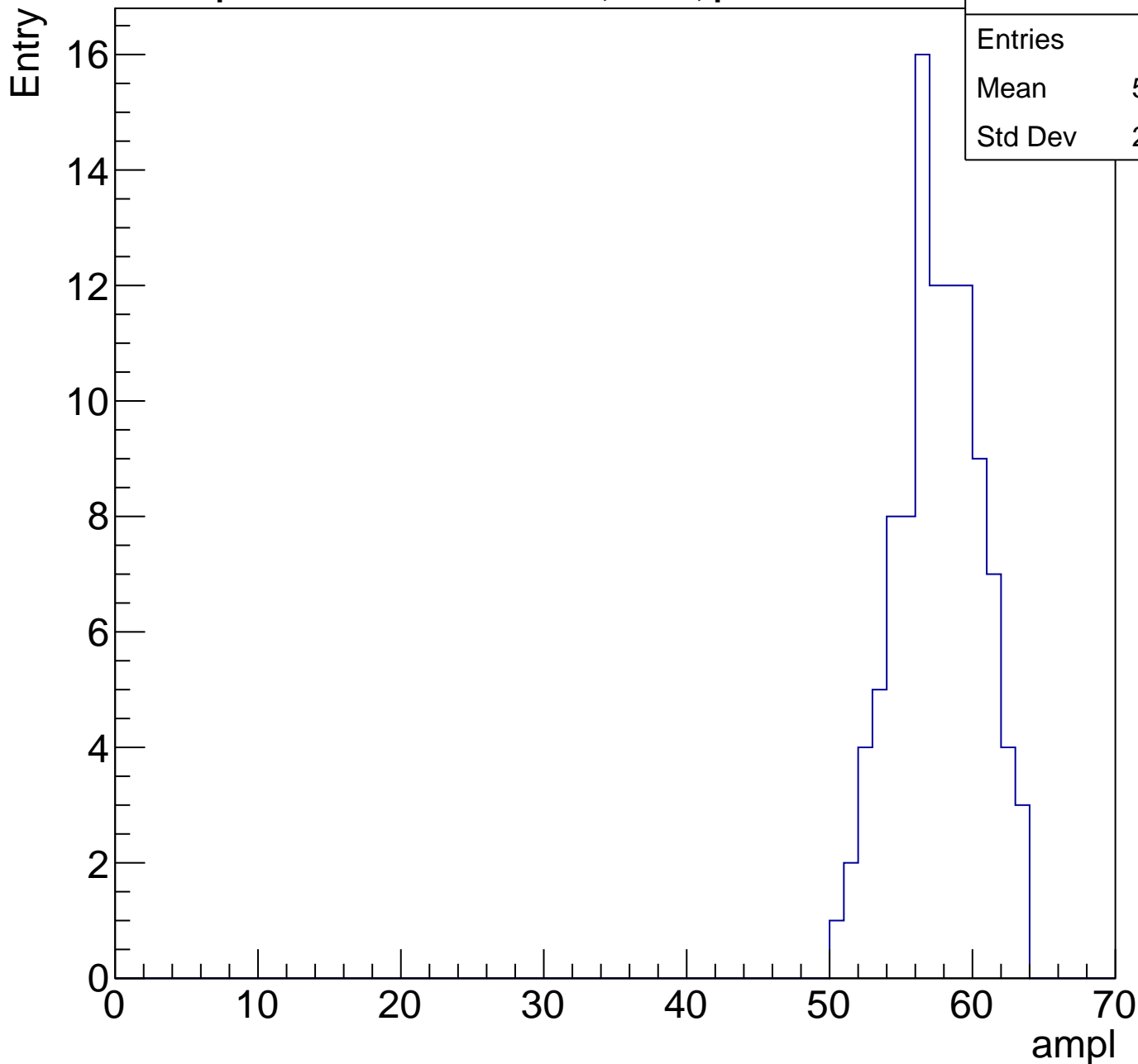
| | |
|---------|-------|
| Entries | 153 |
| Mean | 50.71 |
| Std Dev | 3.606 |



B1L001S, U26-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 103 |
| Mean | 57.14 |
| Std Dev | 2.923 |

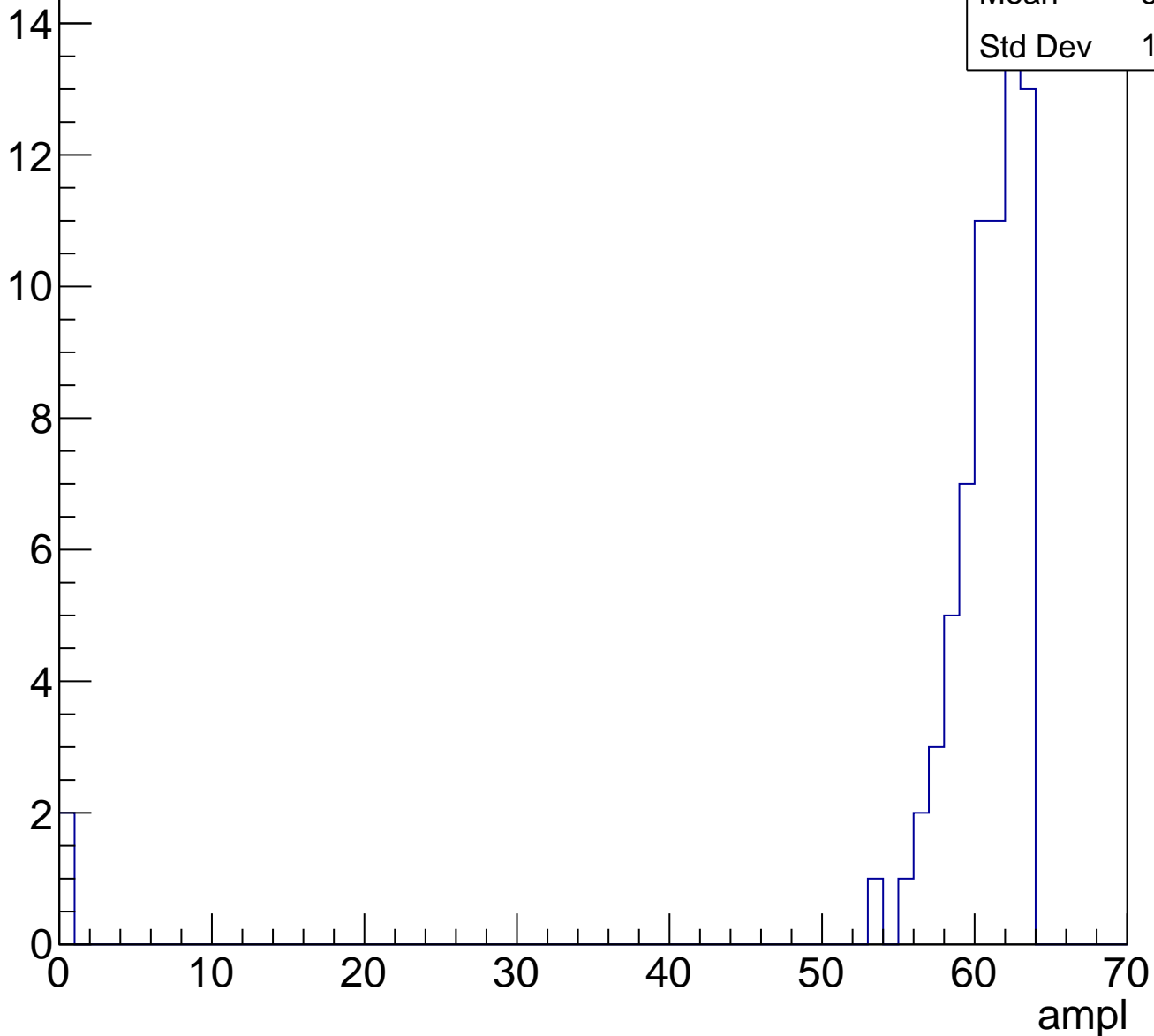


B1L001S, U26-ch107, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 58.79 |
| Std Dev | 10.24 |

Entry



B1L001S, U26-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



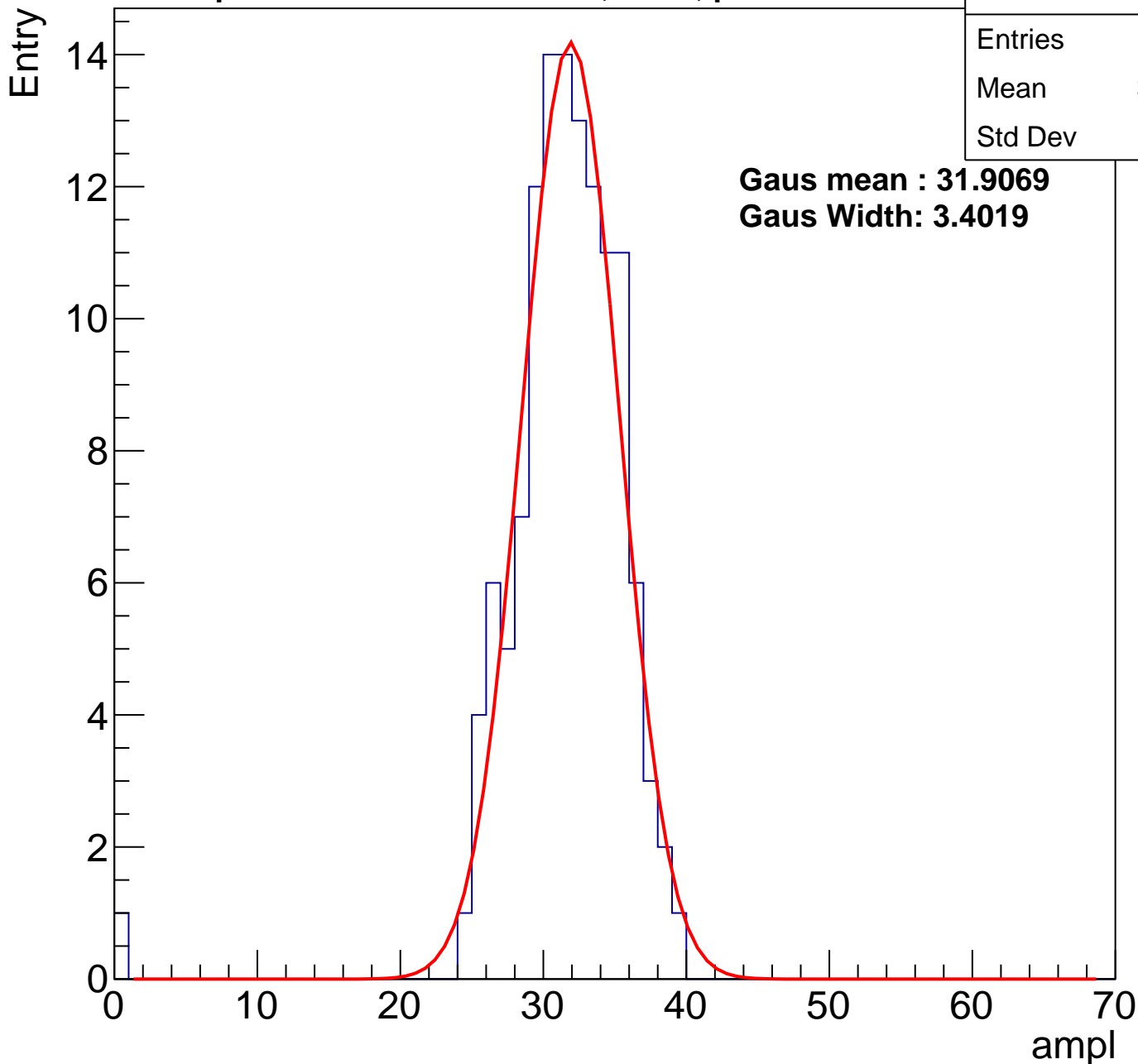
B1L001S, U26-ch108, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 123 |
| Mean | 31.11 |
| Std Dev | 4.29 |

Gaus mean : 31.9069

Gaus Width: 3.4019



B1L001S, U26-ch108, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 37.9 |
| Std Dev | 3.223 |

Gaus mean : 38.6228

Gaus Width: 3.4479

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

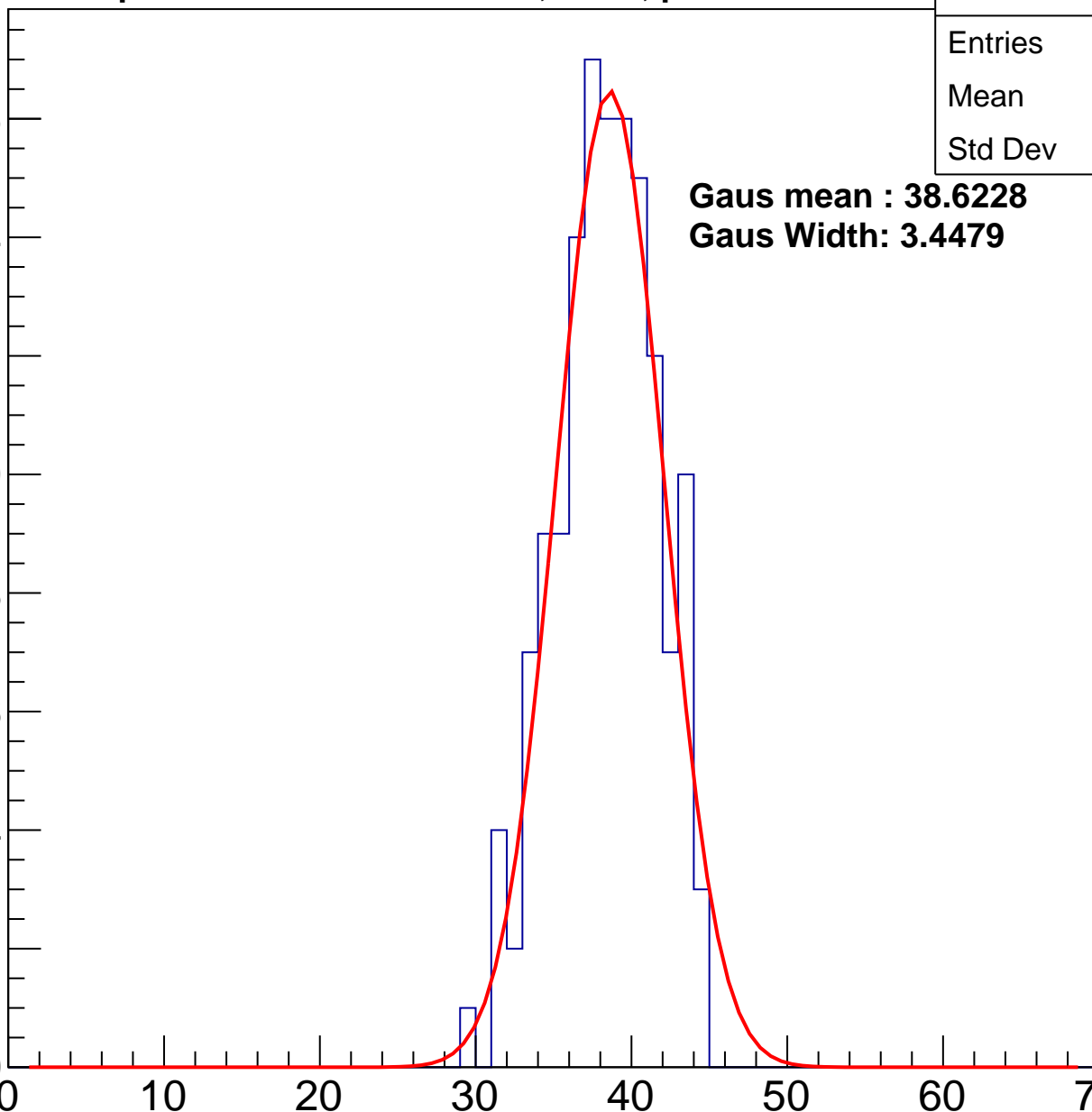
40

50

60

70

ampl



B1L001S, U26-ch108, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

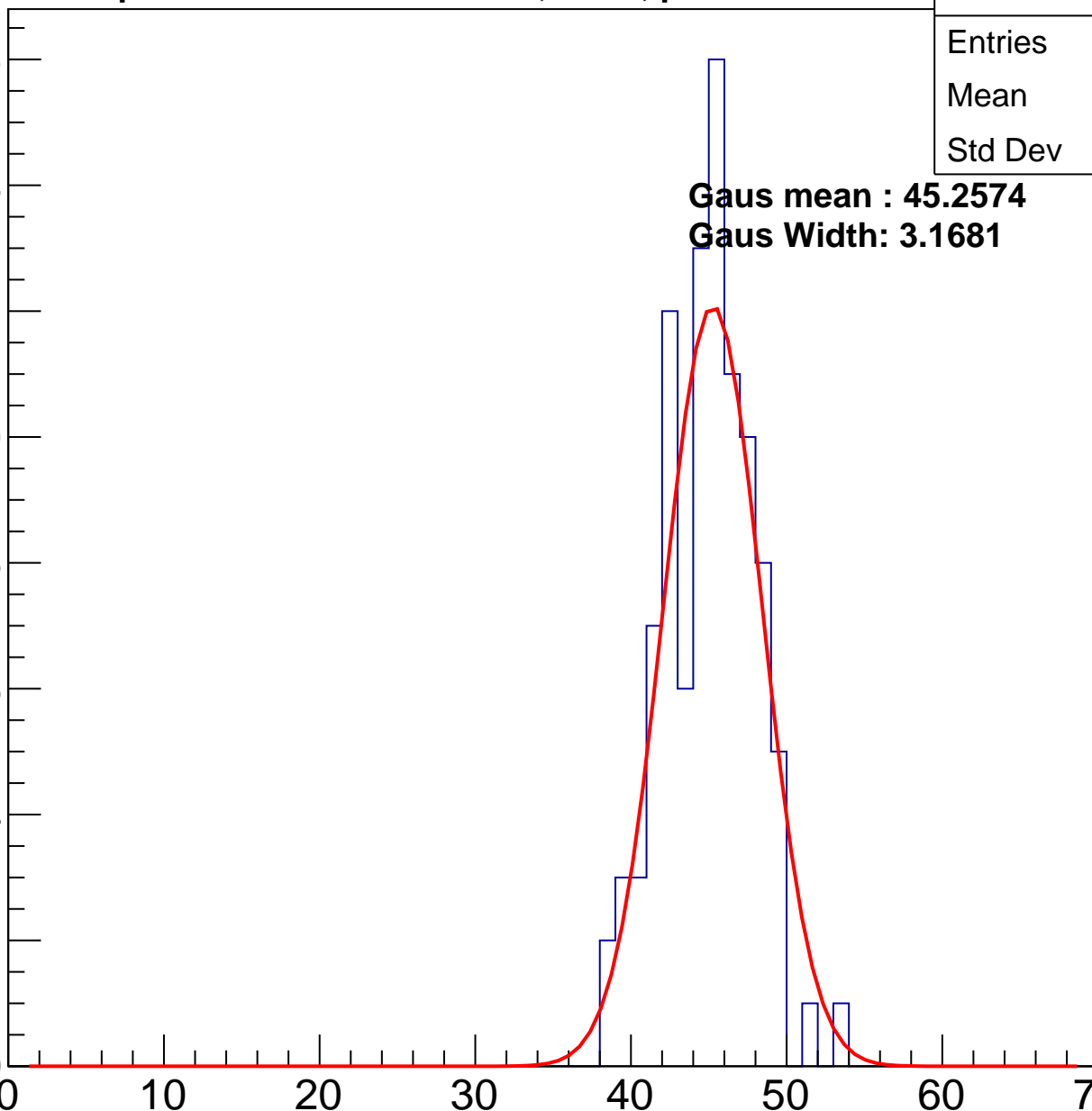
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 98 |
| Mean | 44.52 |
| Std Dev | 2.89 |

Gaus mean : 45.2574

Gaus Width: 3.1681

ampl



B1L001S, U26-ch108, adc3

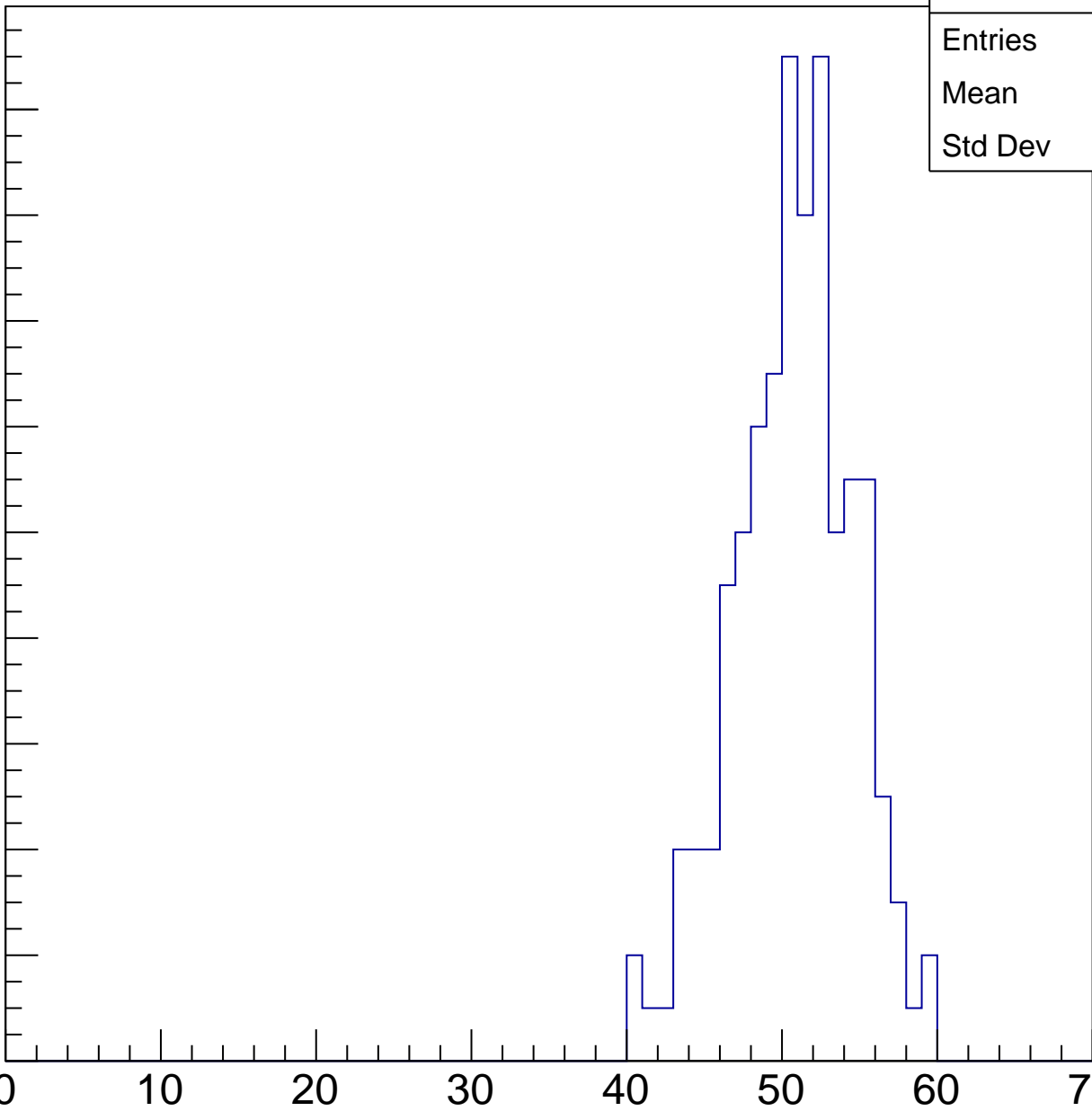
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 157 |
| Mean | 50.31 |
| Std Dev | 3.787 |

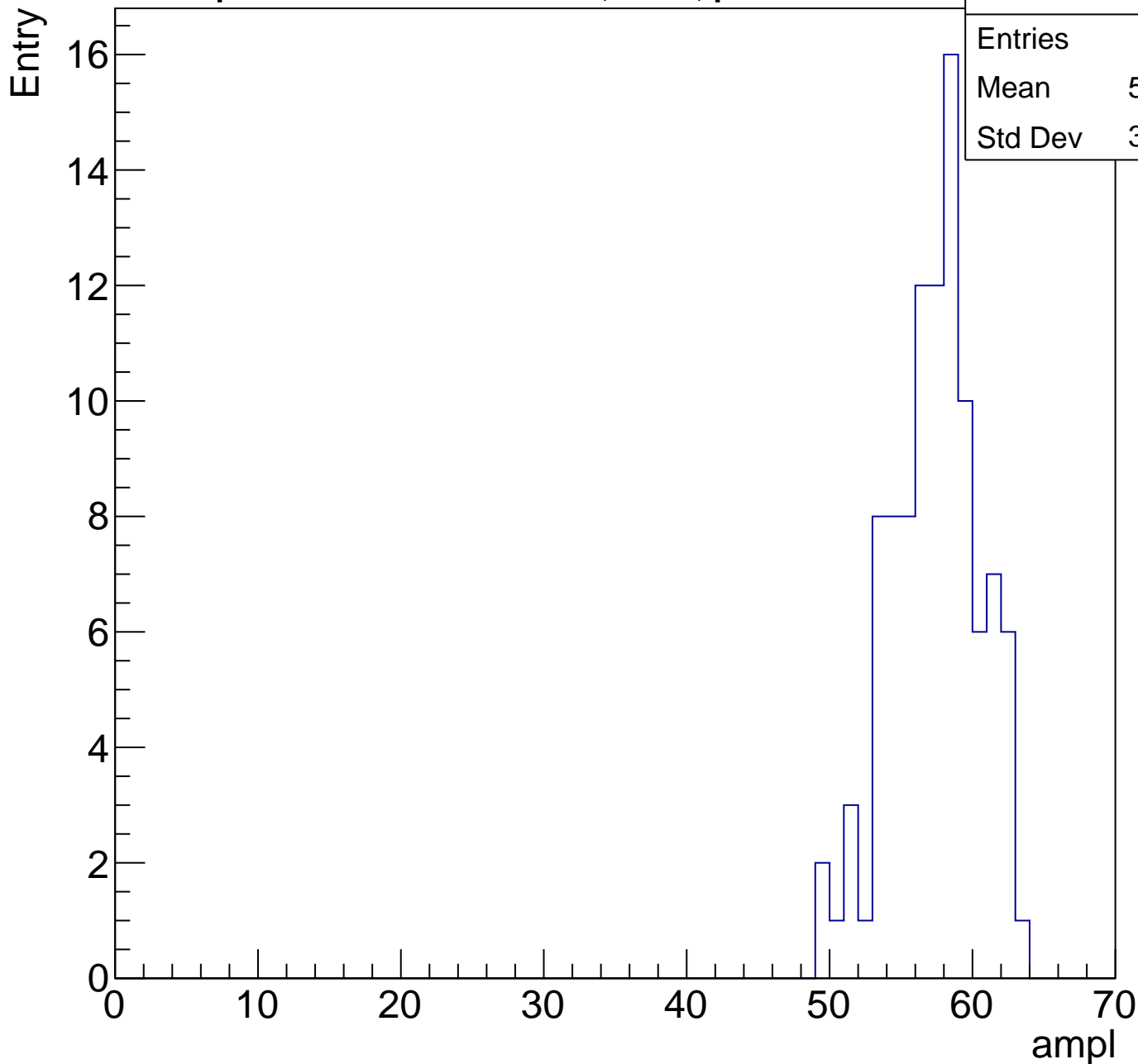
ampl



B1L001S, U26-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 101 |
| Mean | 56.88 |
| Std Dev | 3.084 |



B1L001S, U26-ch108, adc5

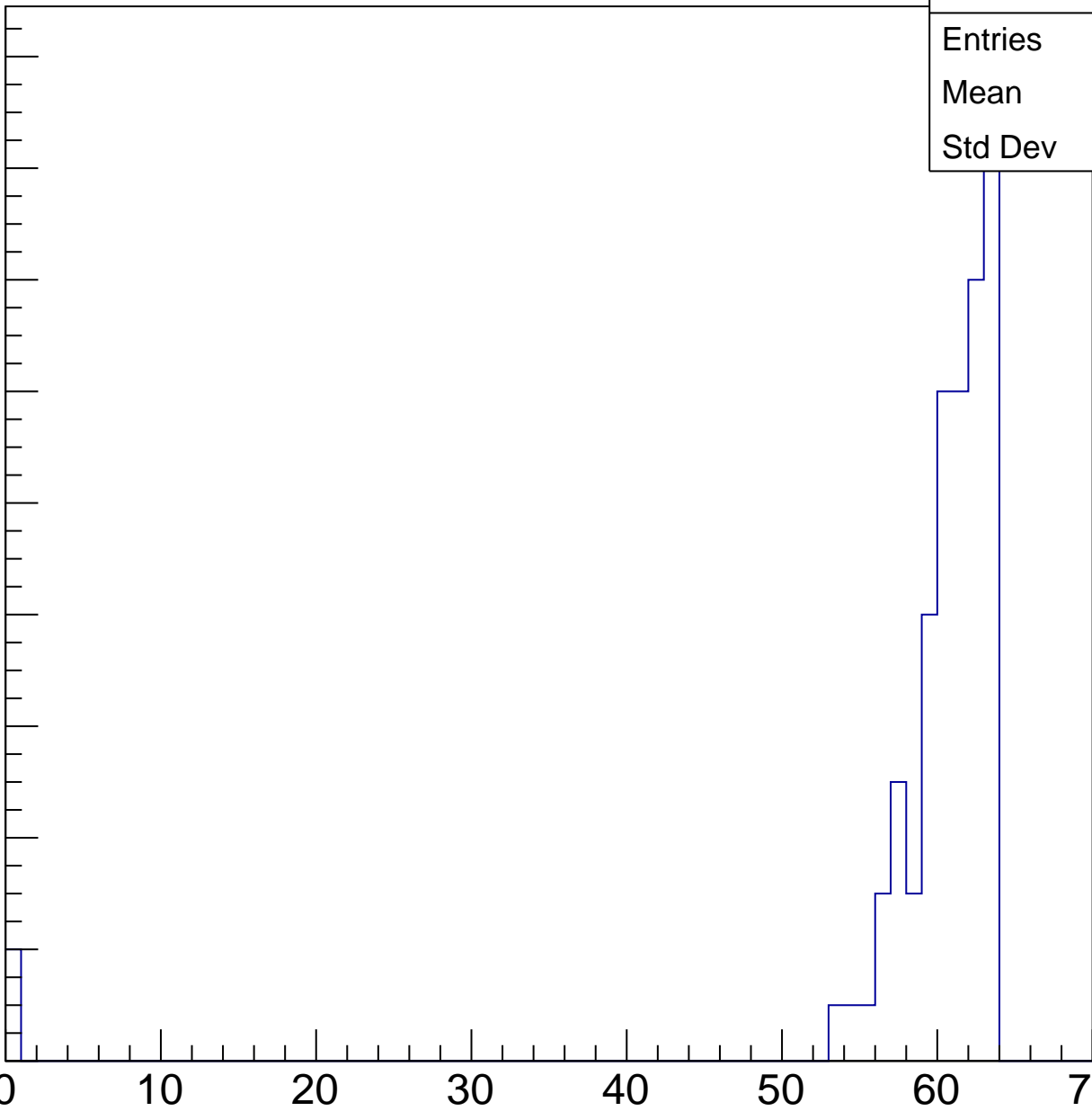
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 80 |
| Mean | 58.94 |
| Std Dev | 9.723 |

ampl



B1L001S, U26-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

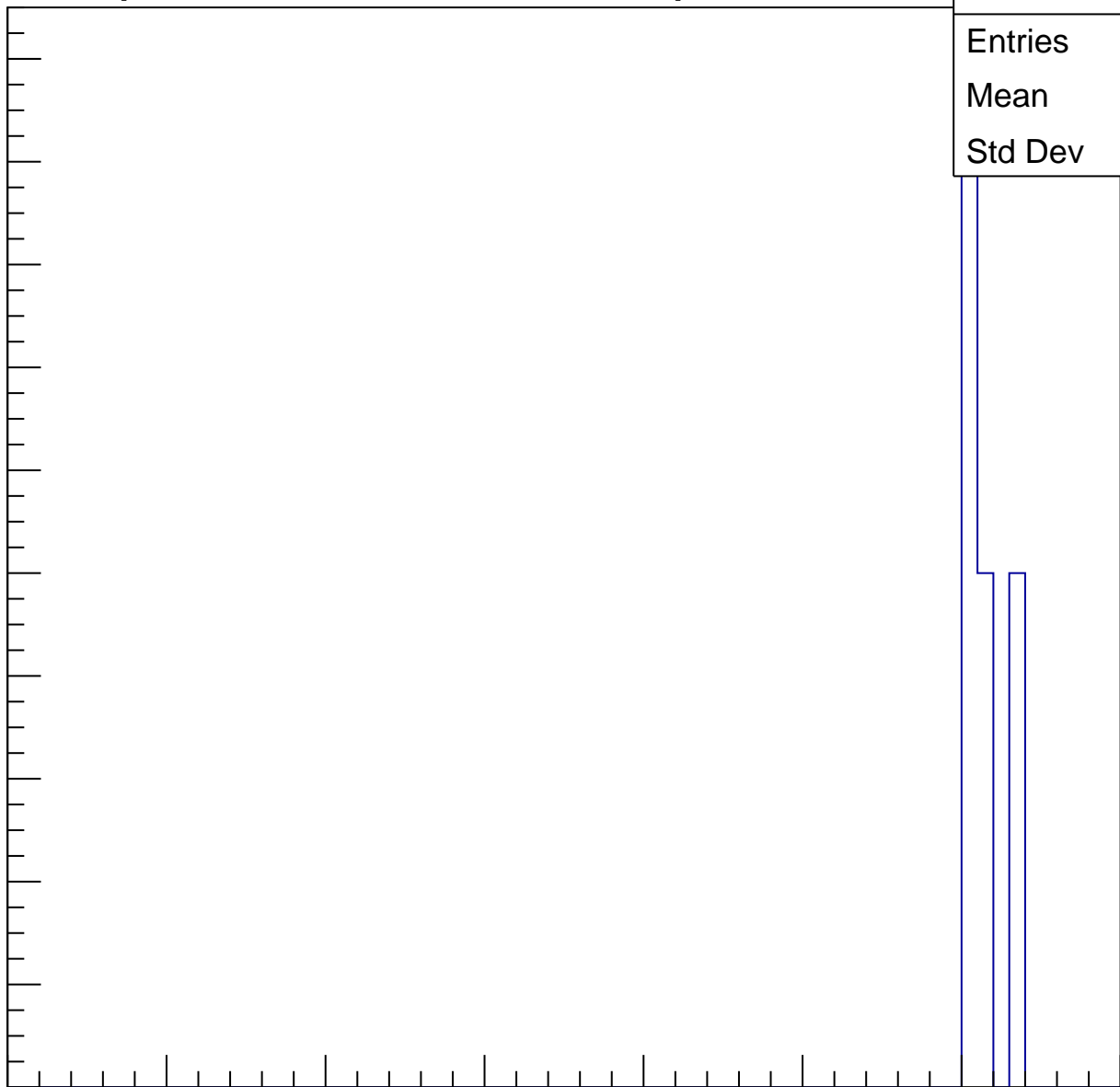
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 61 |
| Std Dev | 1.225 |

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

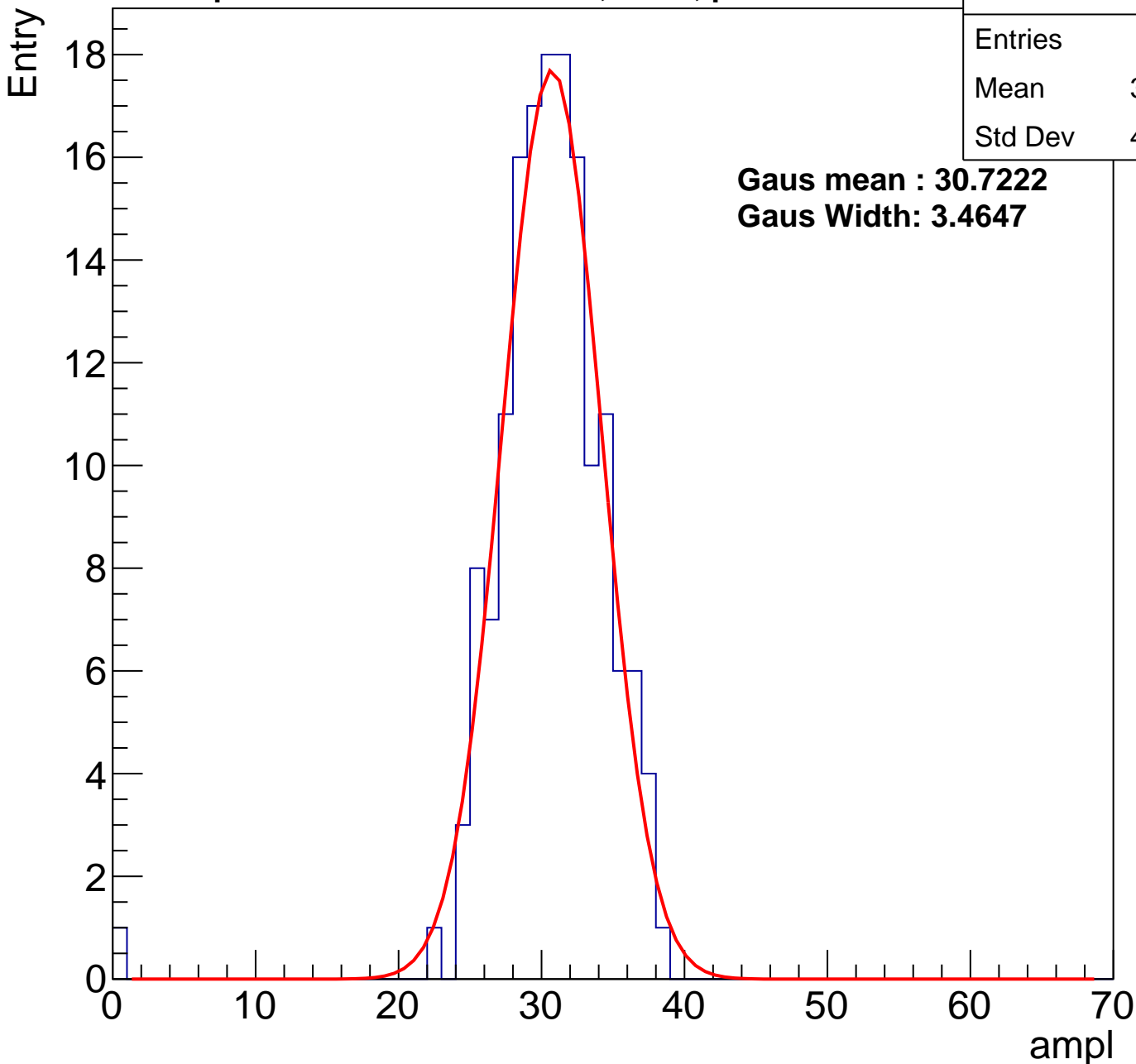
B1L001S, U26-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 154 |
| Mean | 30.13 |
| Std Dev | 4.058 |

Gaus mean : 30.7222

Gaus Width: 3.4647



B1L001S, U26-ch109, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 141 |
| Mean | 37.55 |
| Std Dev | 3.376 |

Gaus mean : 38.1114

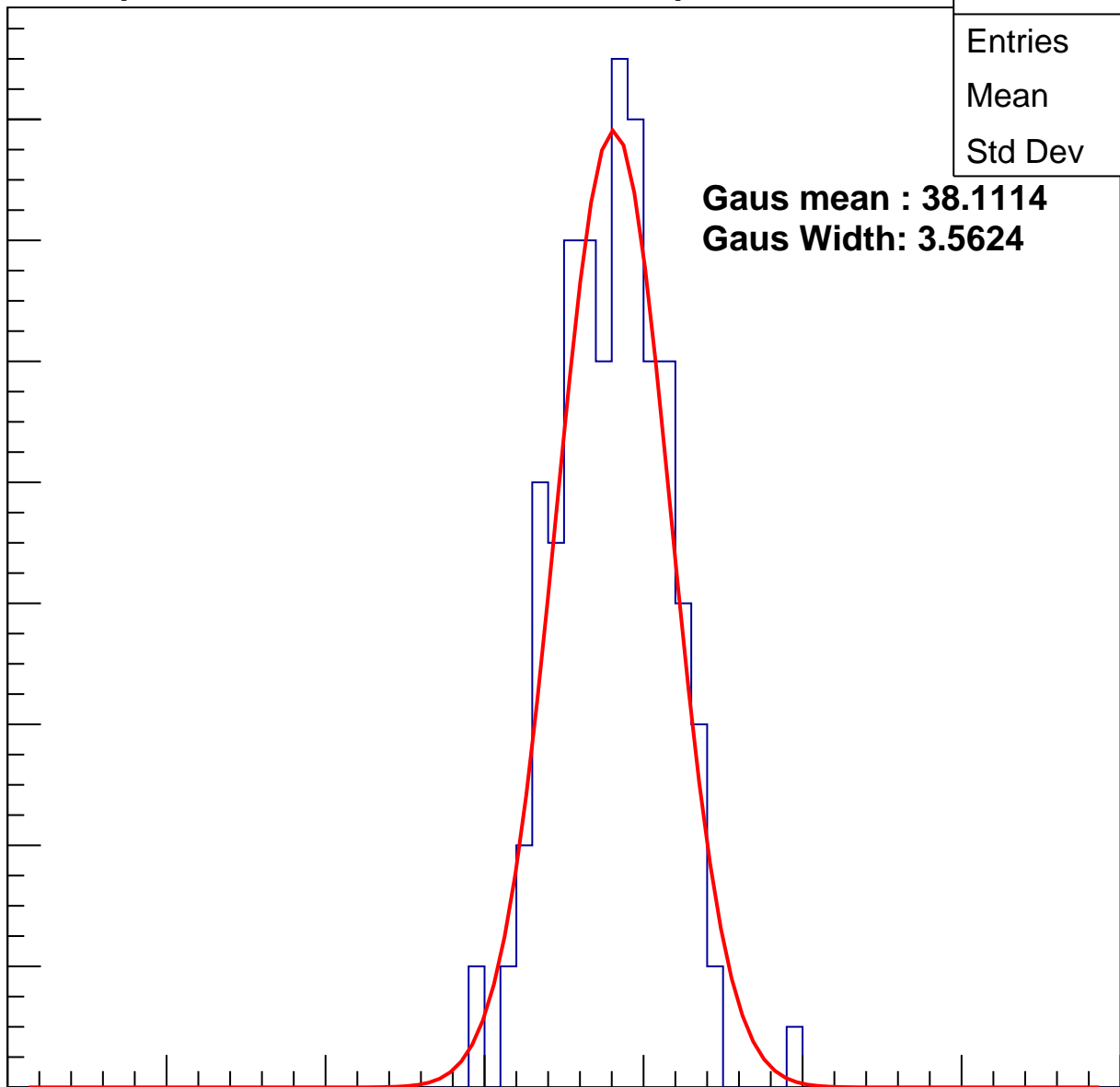
Gaus Width: 3.5624

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch109, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 125 |
| Mean | 44.23 |
| Std Dev | 3.357 |

Gaus mean : 44.6582

Gaus Width: 3.2403

Entry

16
14
12
10
8
6
4
2
0

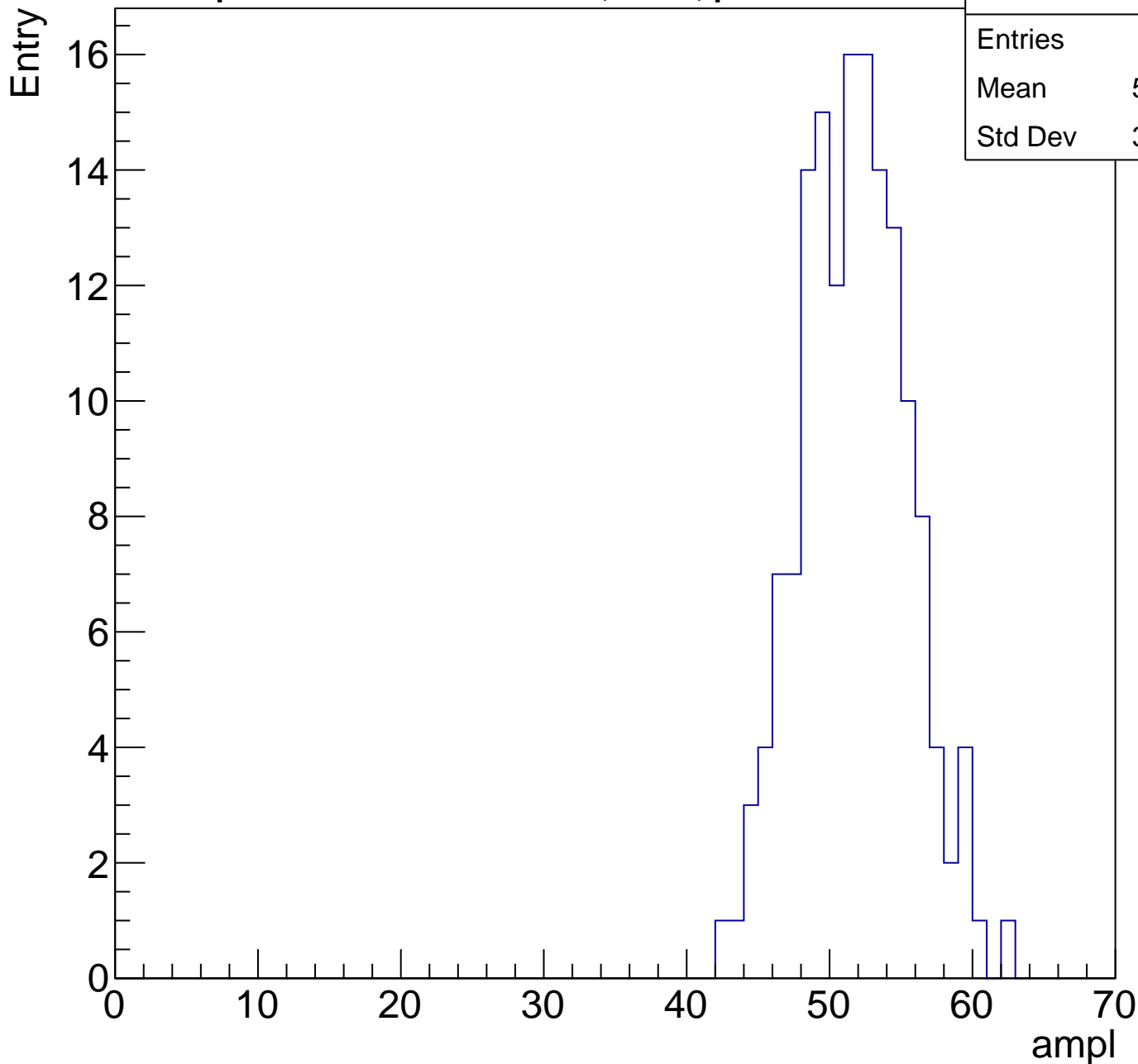
ampl

0 10 20 30 40 50 60 70

B1L001S, U26-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 153 |
| Mean | 51.29 |
| Std Dev | 3.755 |



B1L001S, U26-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

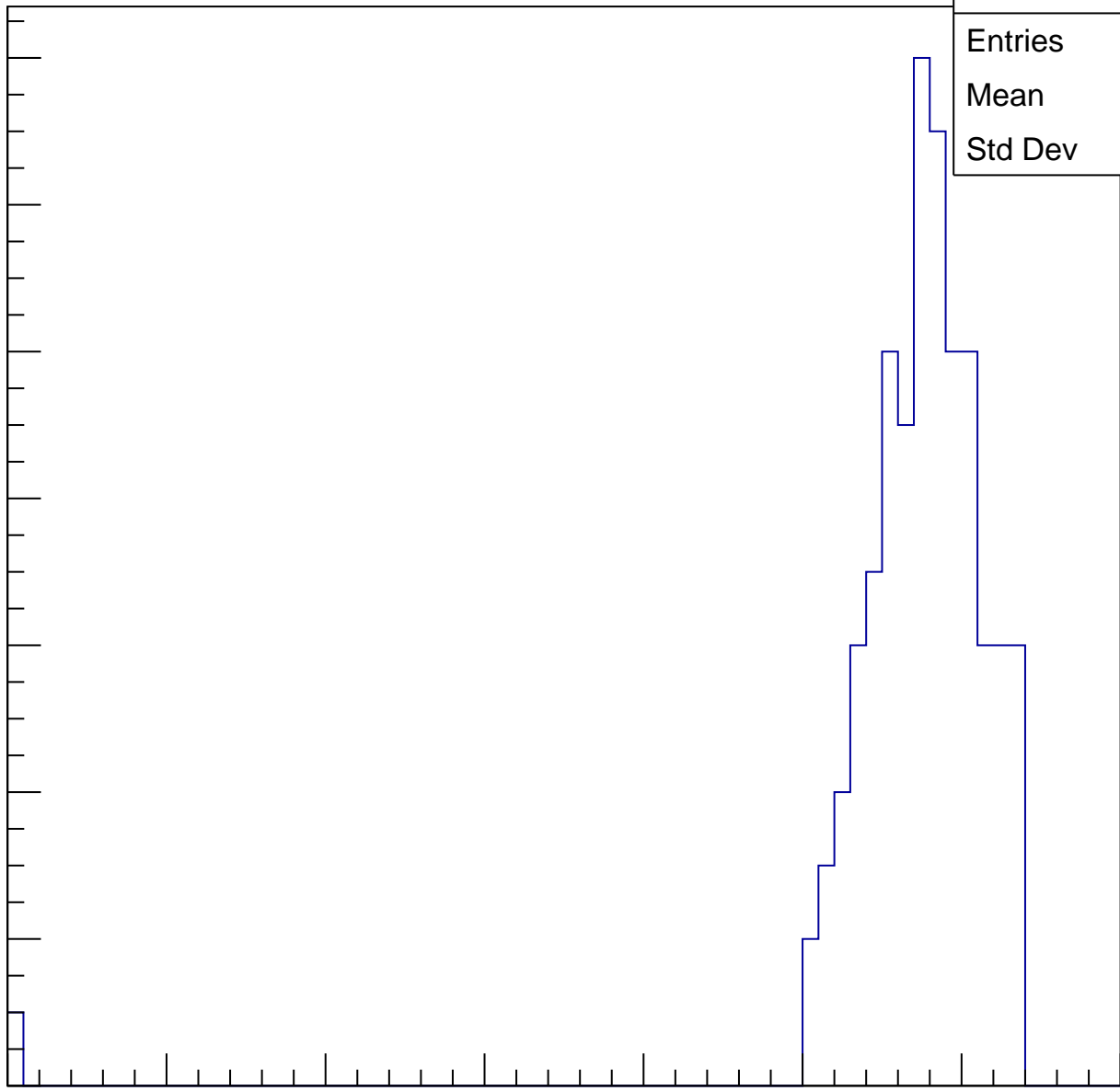
| | |
|---------|-------|
| Entries | 107 |
| Mean | 56.72 |
| Std Dev | 6.383 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch109, adc5

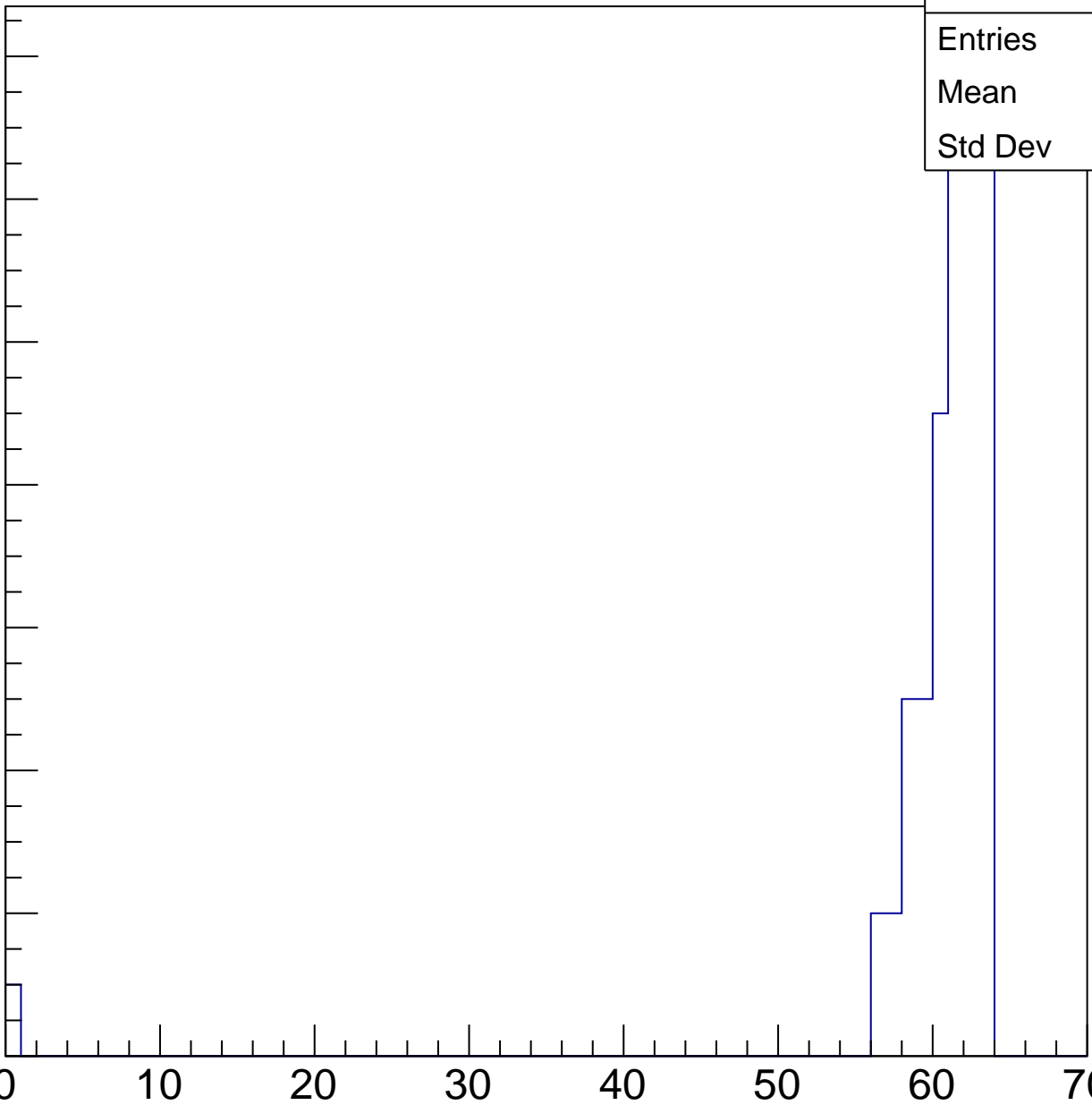
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 59.84 |
| Std Dev | 7.763 |

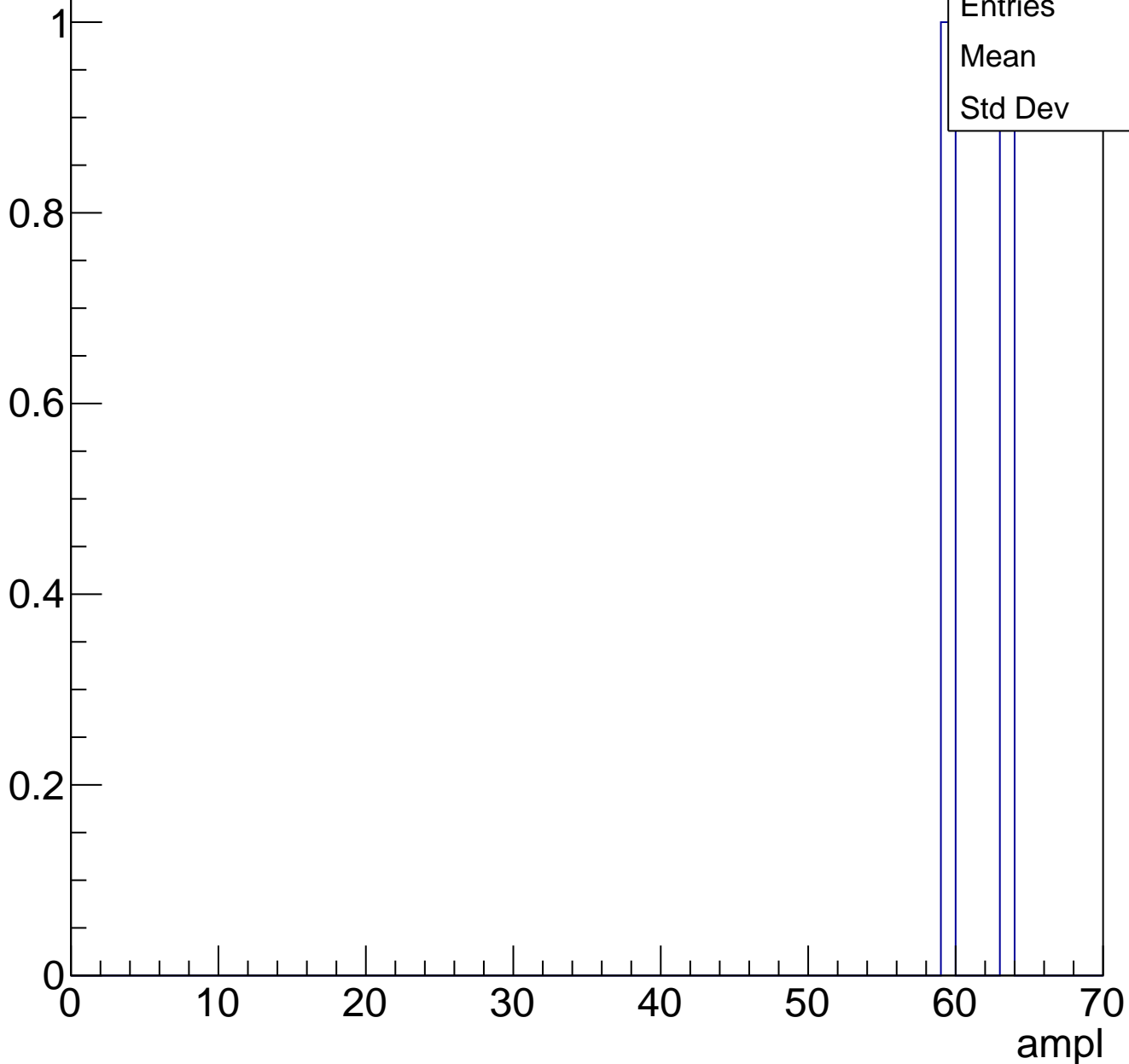
ampl



B1L001S, U26-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

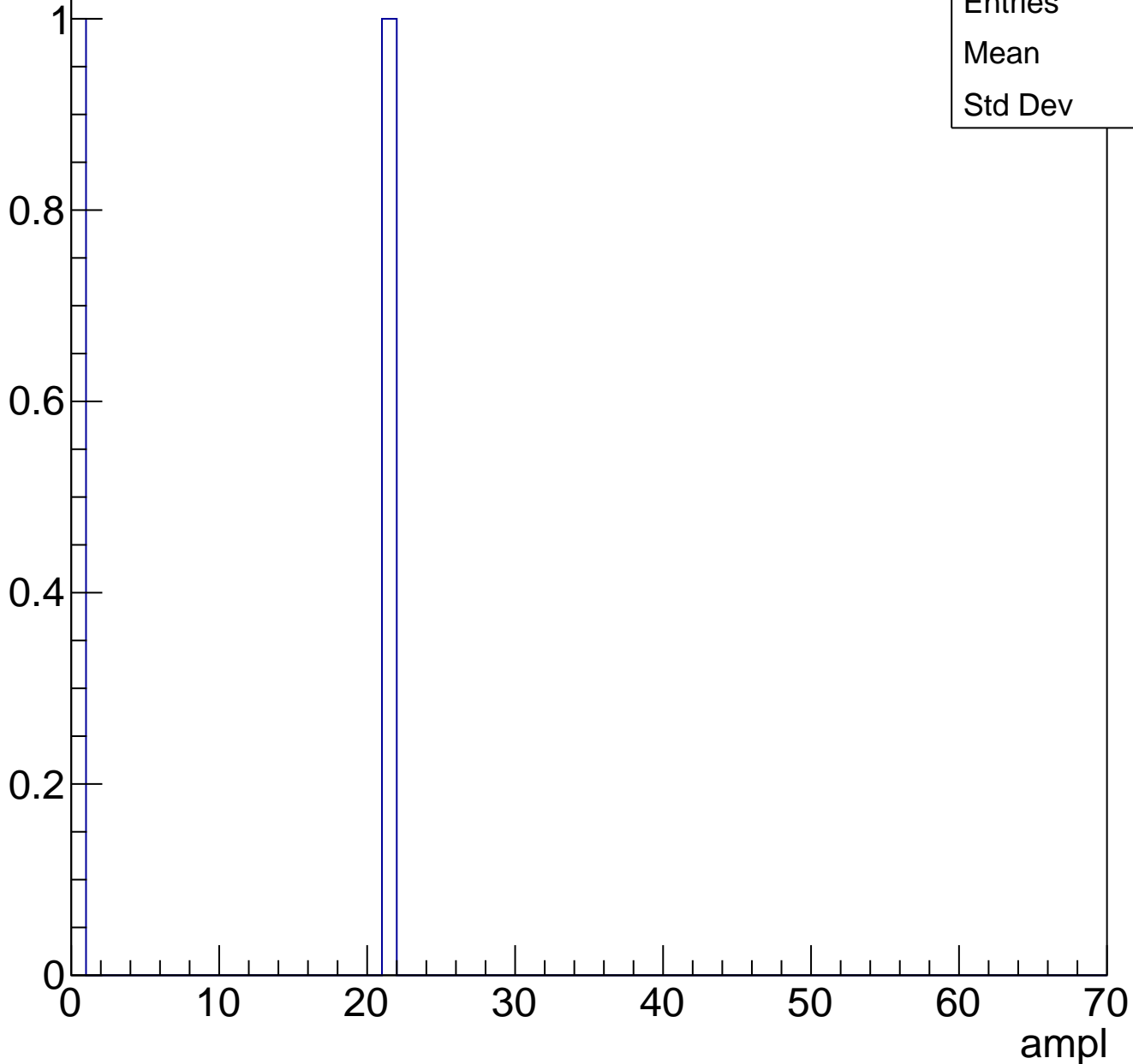
Entry



B1L001S, U26-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



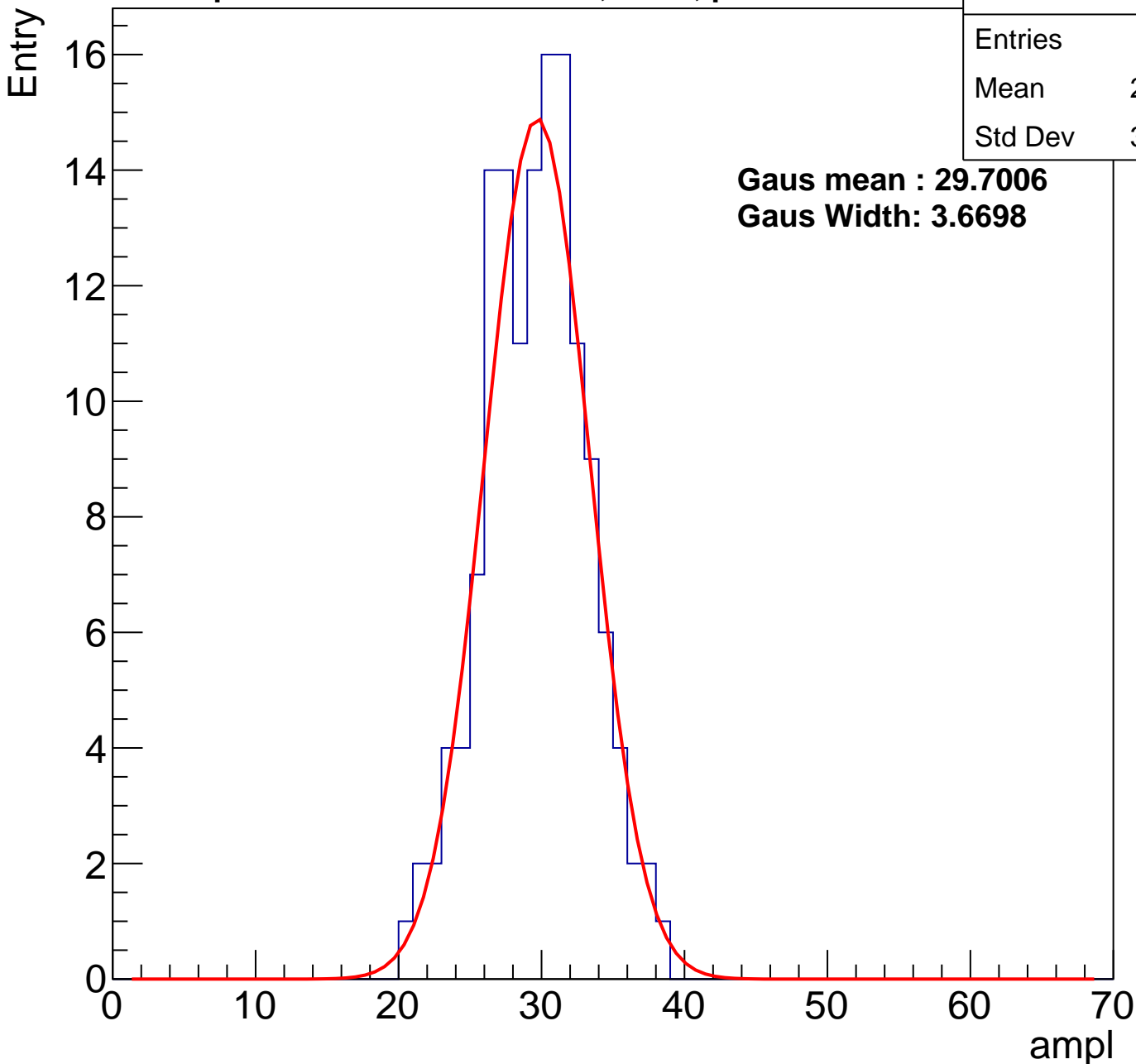
B1L001S, U26-ch110, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 29.13 |
| Std Dev | 3.563 |

Gaus mean : 29.7006

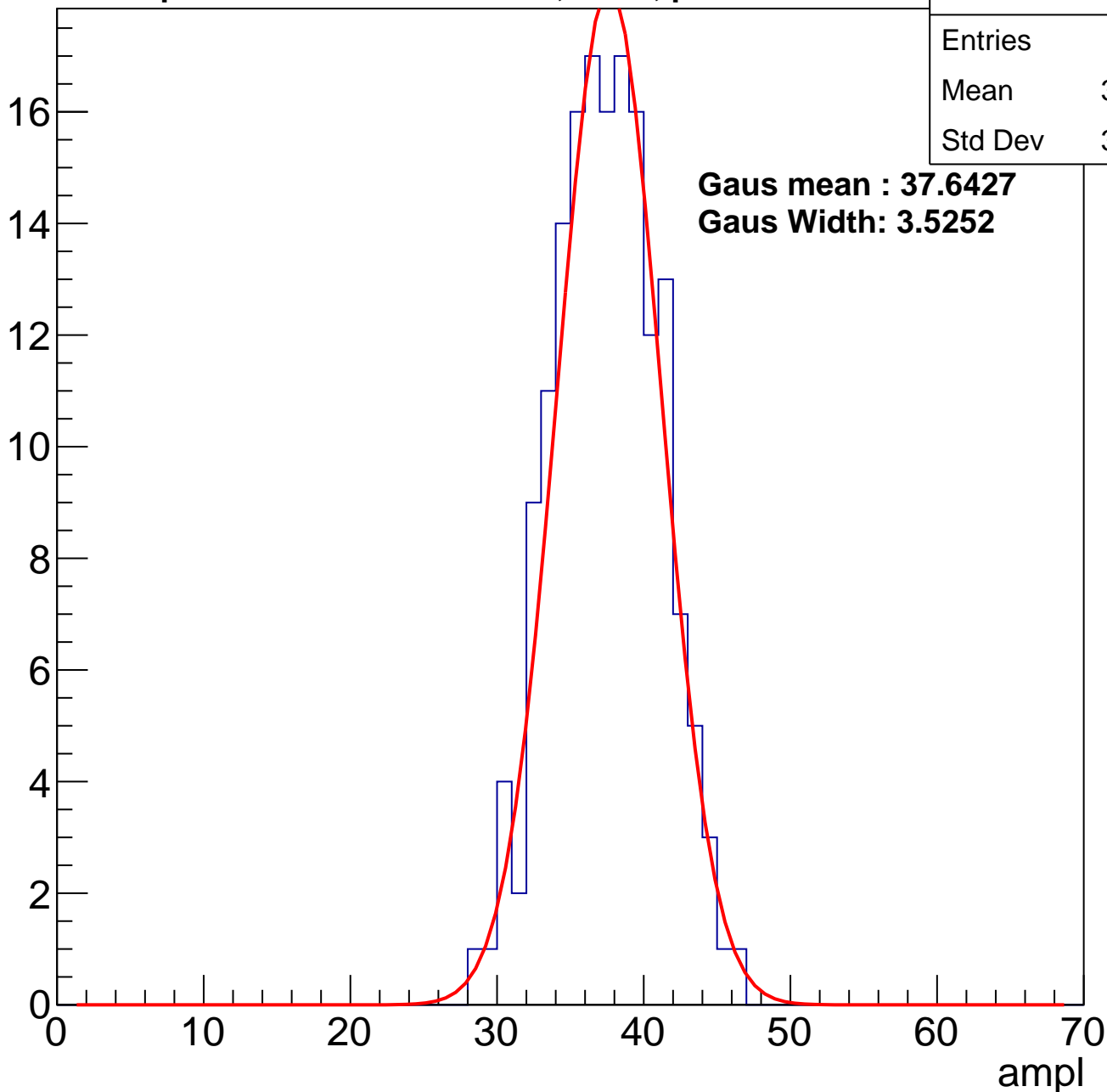
Gaus Width: 3.6698



B1L001S, U26-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 97 |
| Mean | 43.41 |
| Std Dev | 2.921 |

Gaus mean : 44.1127

Gaus Width: 3.2077

0

2

4

6

8

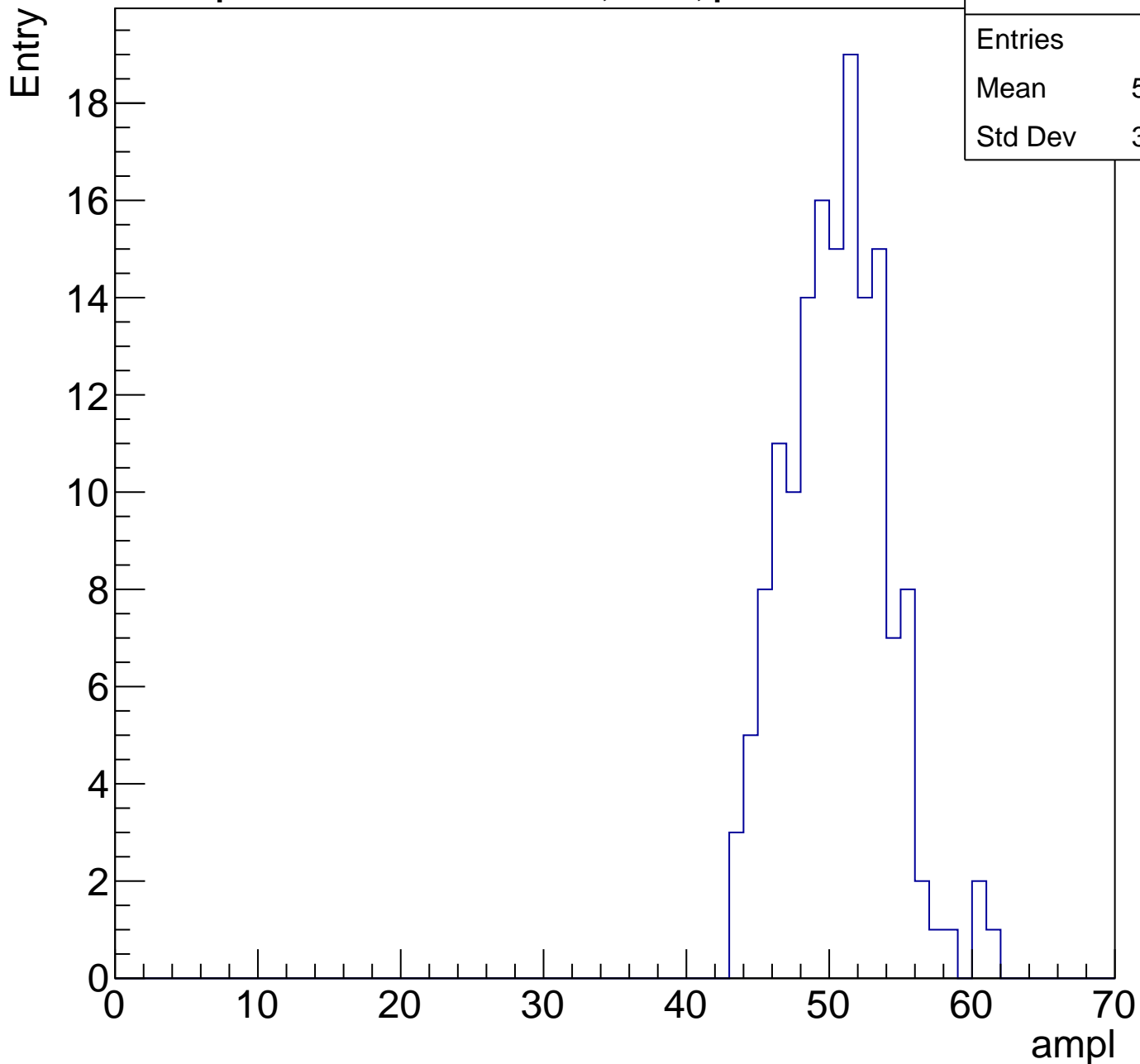
10

12

B1L001S, U26-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 50.06 |
| Std Dev | 3.529 |

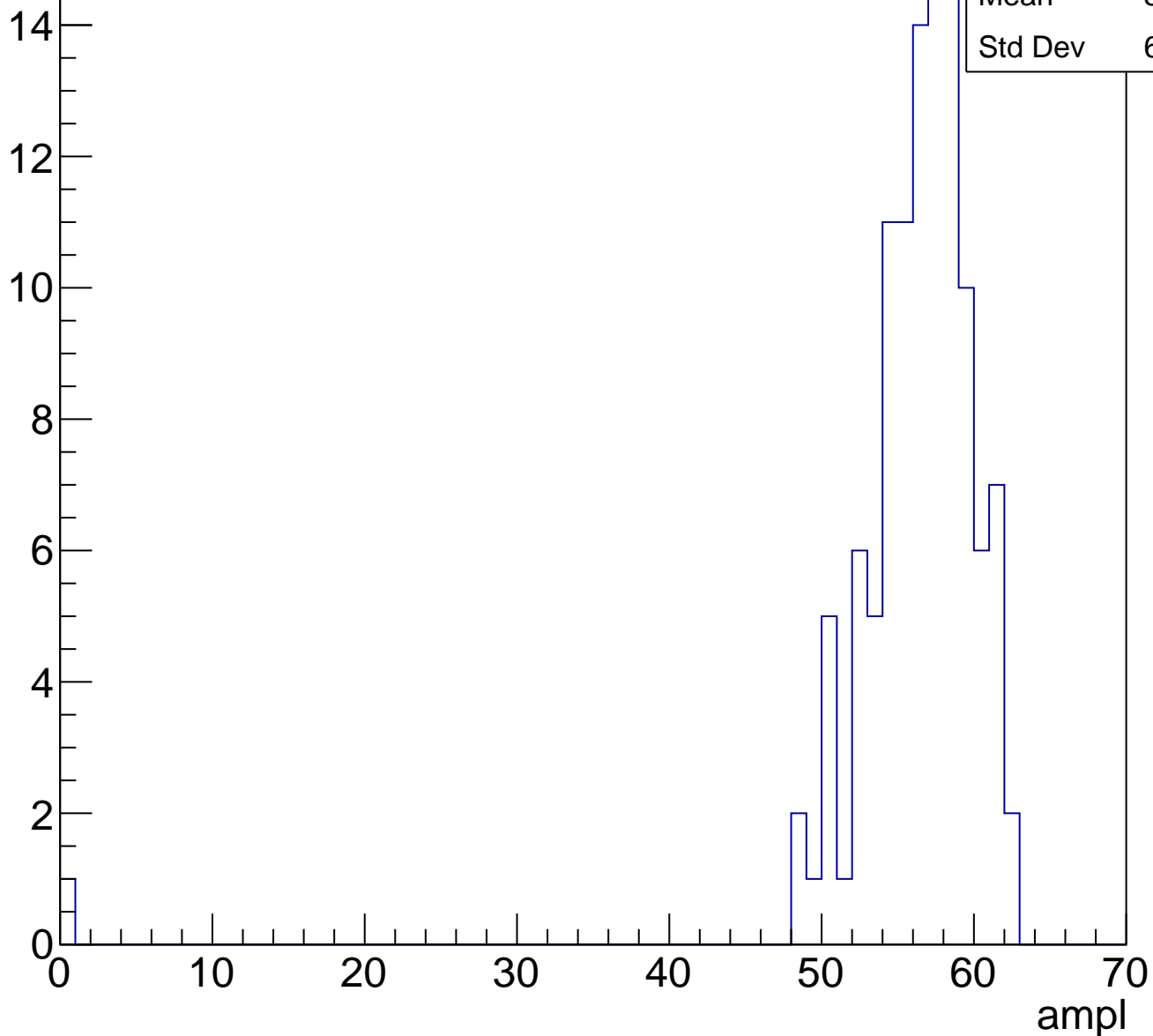


B1L001S, U26-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 55.64 |
| Std Dev | 6.129 |

Entry



B1L001S, U26-ch110, adc5

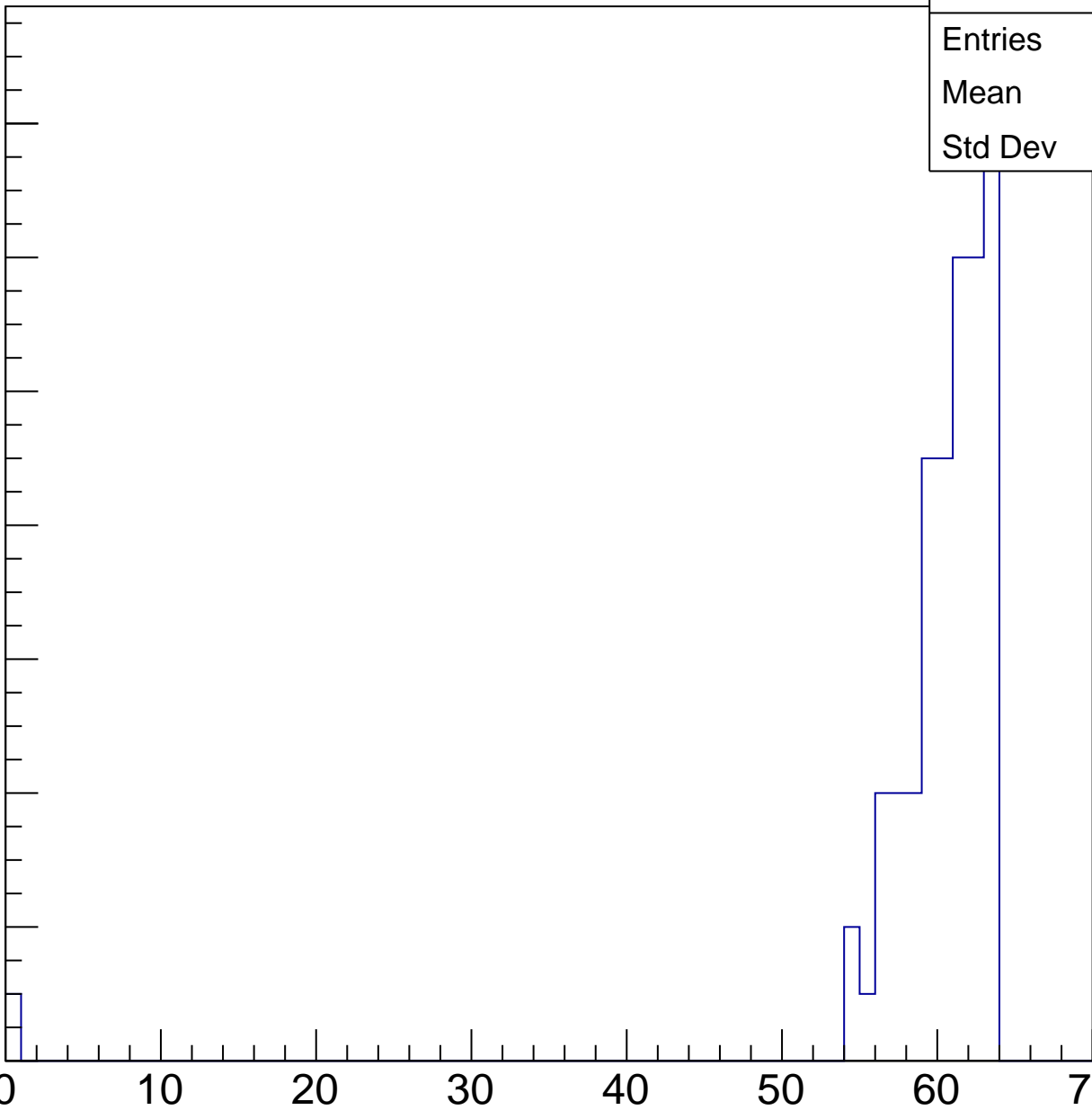
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 59.44 |
| Std Dev | 7.397 |

ampl

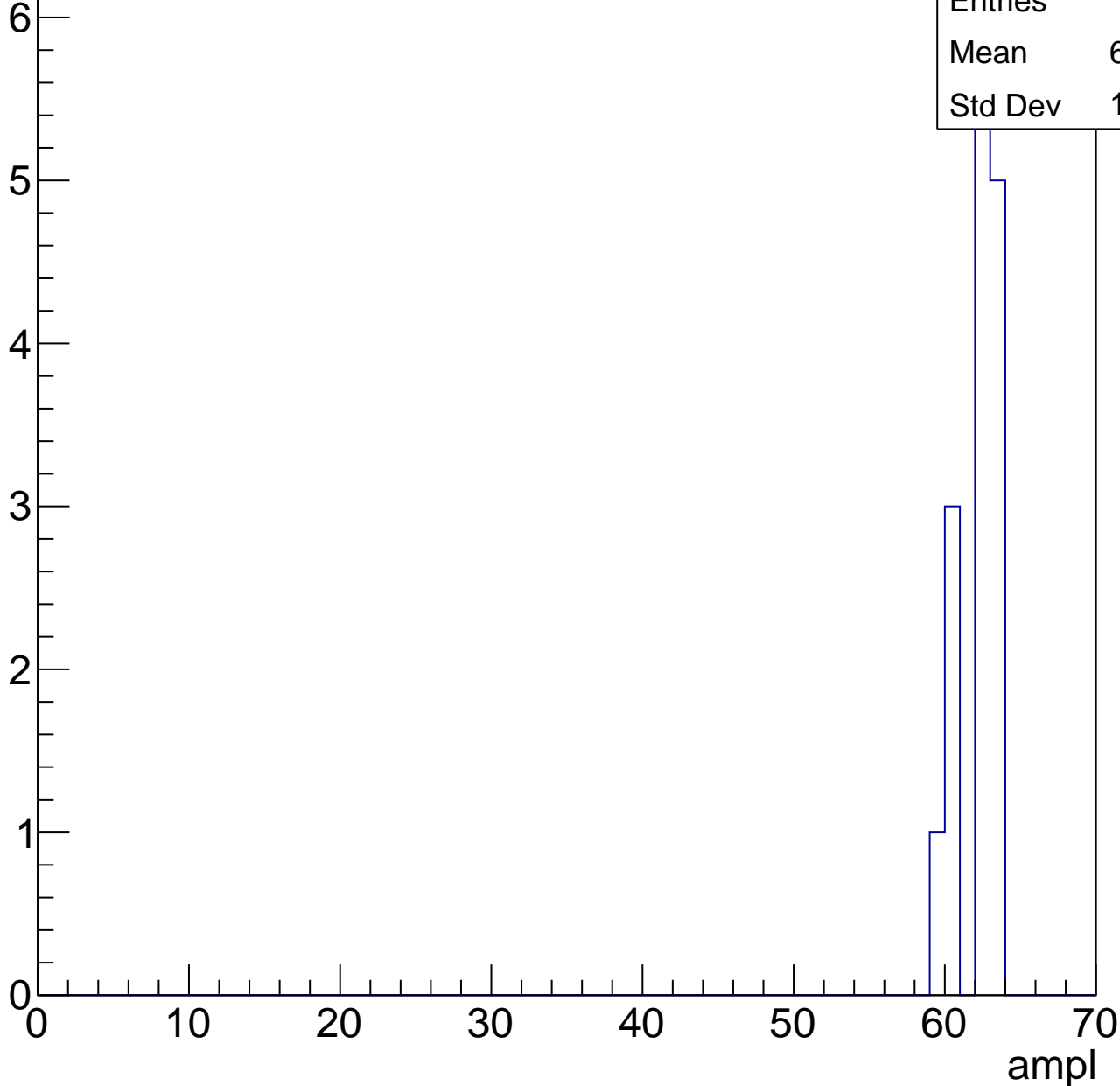


B1L001S, U26-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.73 |
| Std Dev | 1.289 |



B1L001S, U26-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch111, adc0

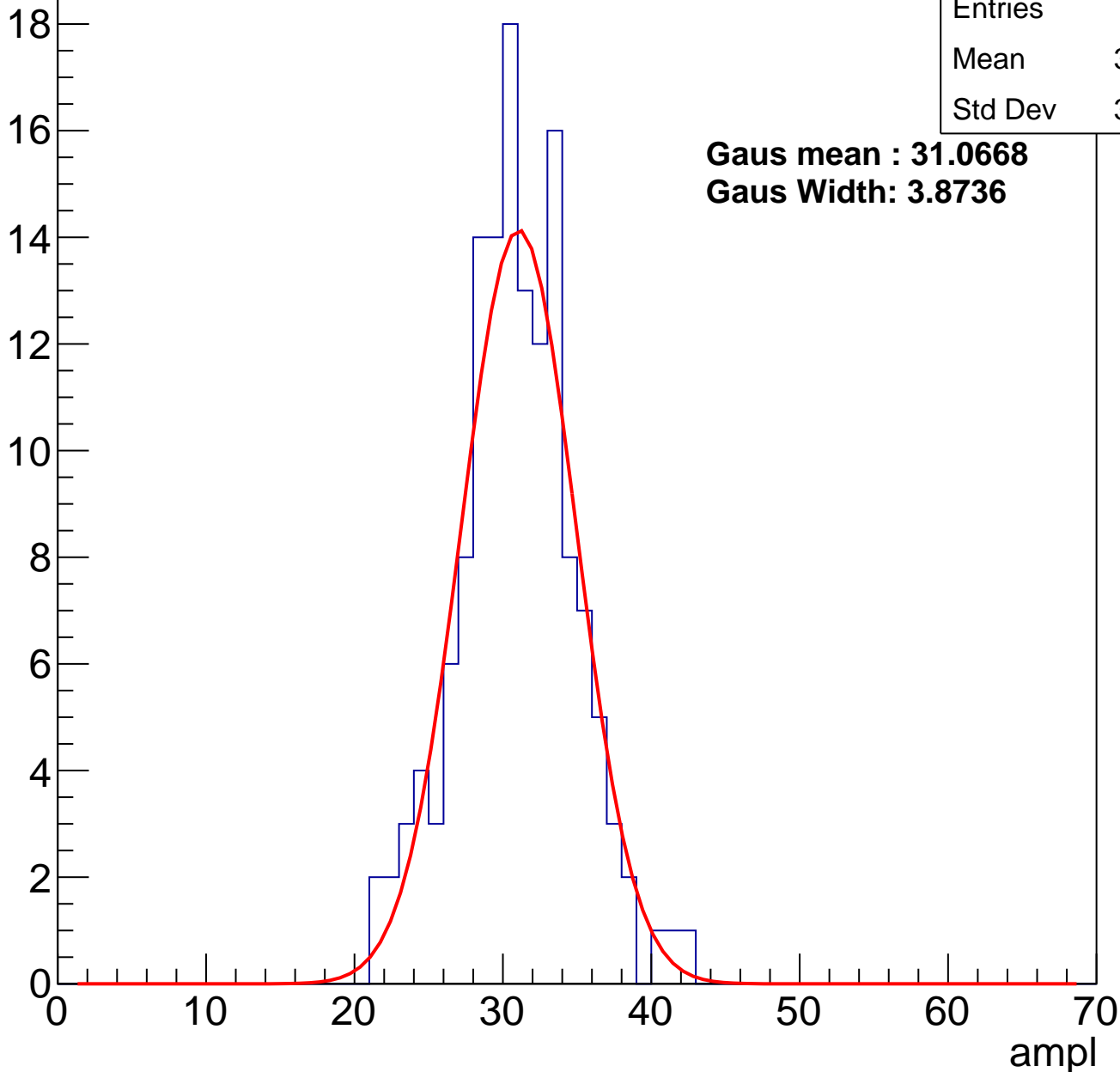
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 30.48 |
| Std Dev | 3.913 |

Gaus mean : 31.0668

Gaus Width: 3.8736

Entry



B1L001S, U26-ch111, adc1

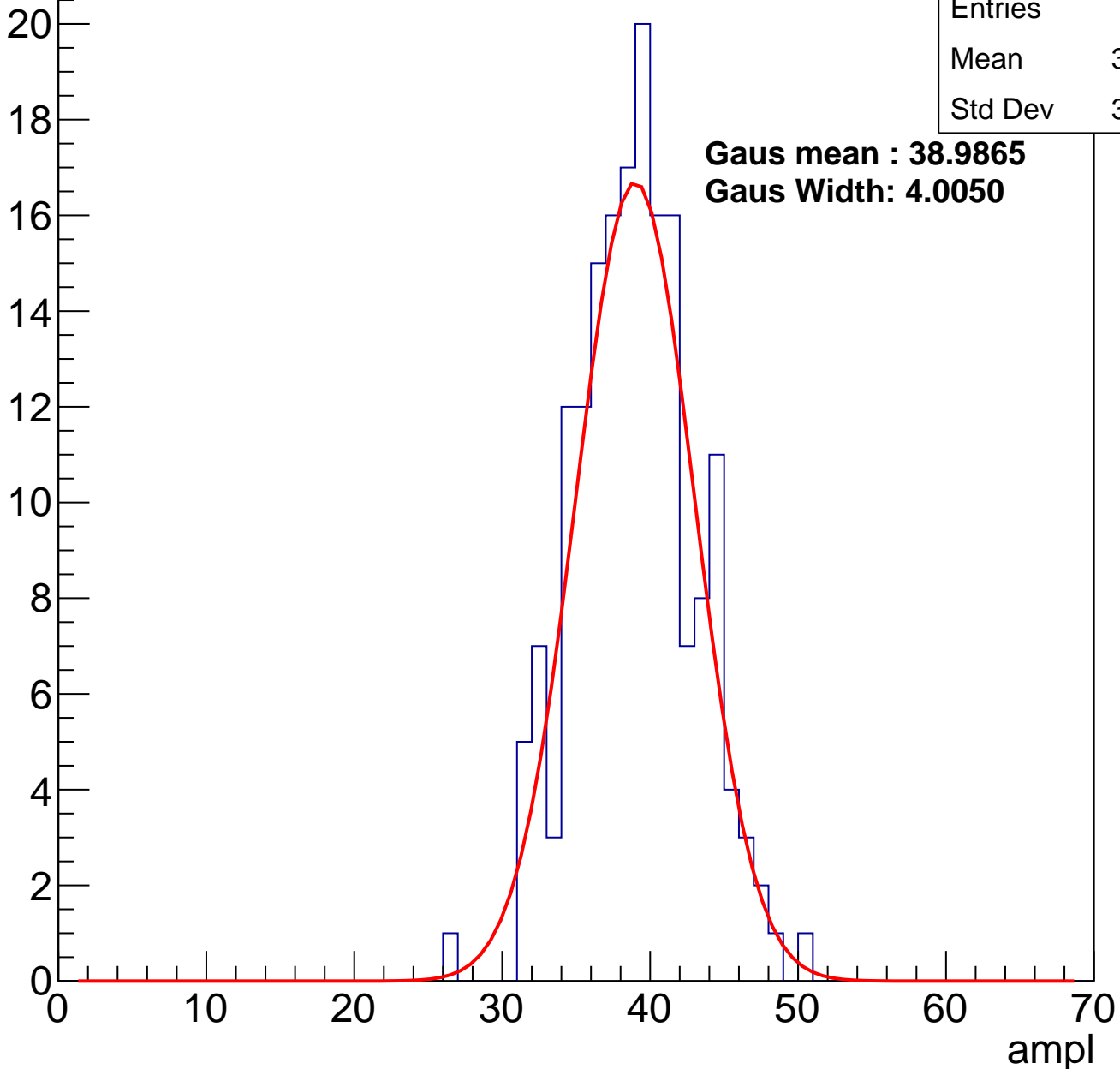
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 177 |
| Mean | 38.52 |
| Std Dev | 3.959 |

Gaus mean : 38.9865

Gaus Width: 4.0050

Entry



B1L001S, U26-ch111, adc2

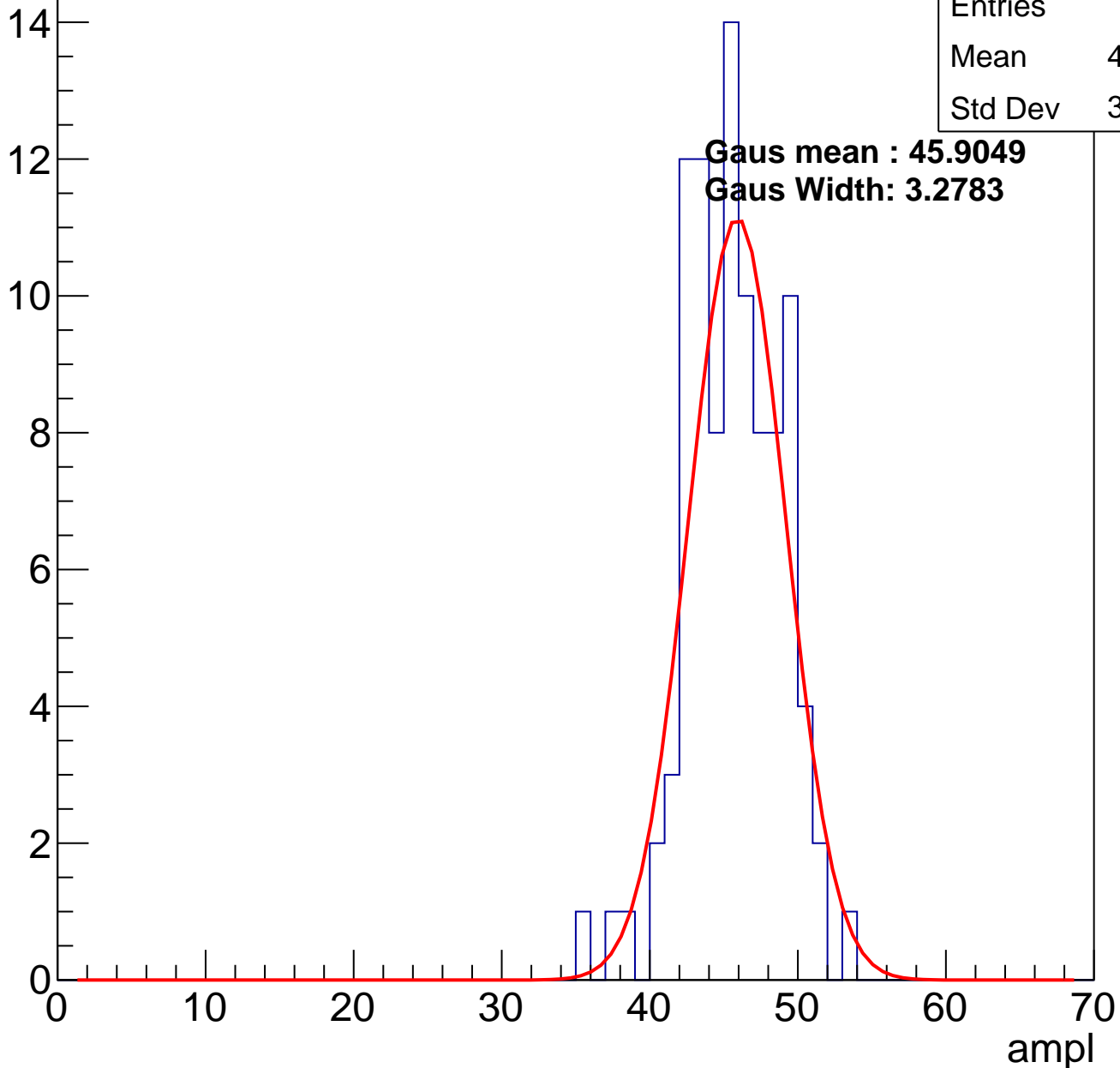
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 97 |
| Mean | 45.15 |
| Std Dev | 3.183 |

Gaus mean : 45.9049

Gaus Width: 3.2783

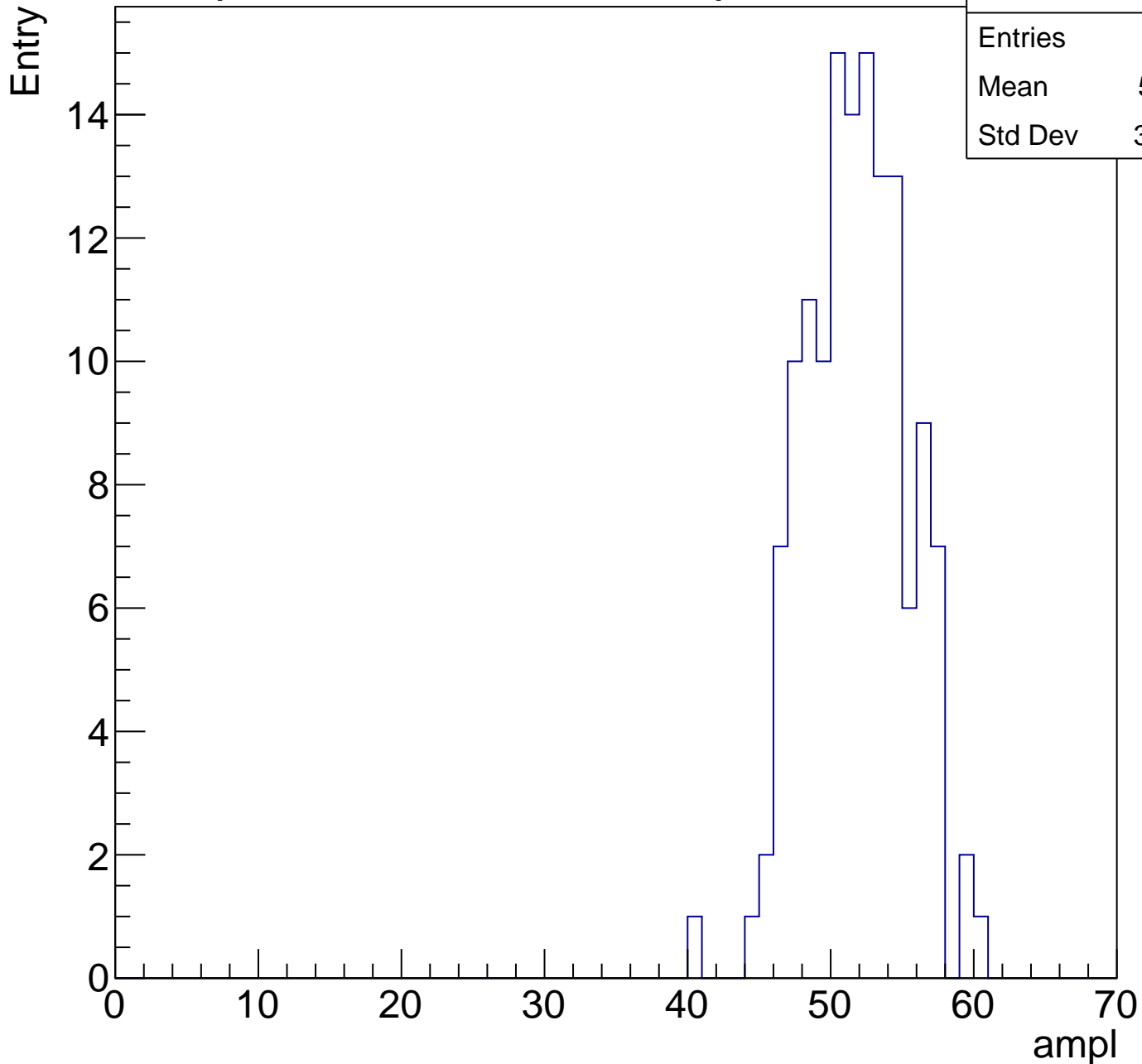
Entry



B1L001S, U26-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 137 |
| Mean | 51.31 |
| Std Dev | 3.505 |



B1L001S, U26-ch111, adc4

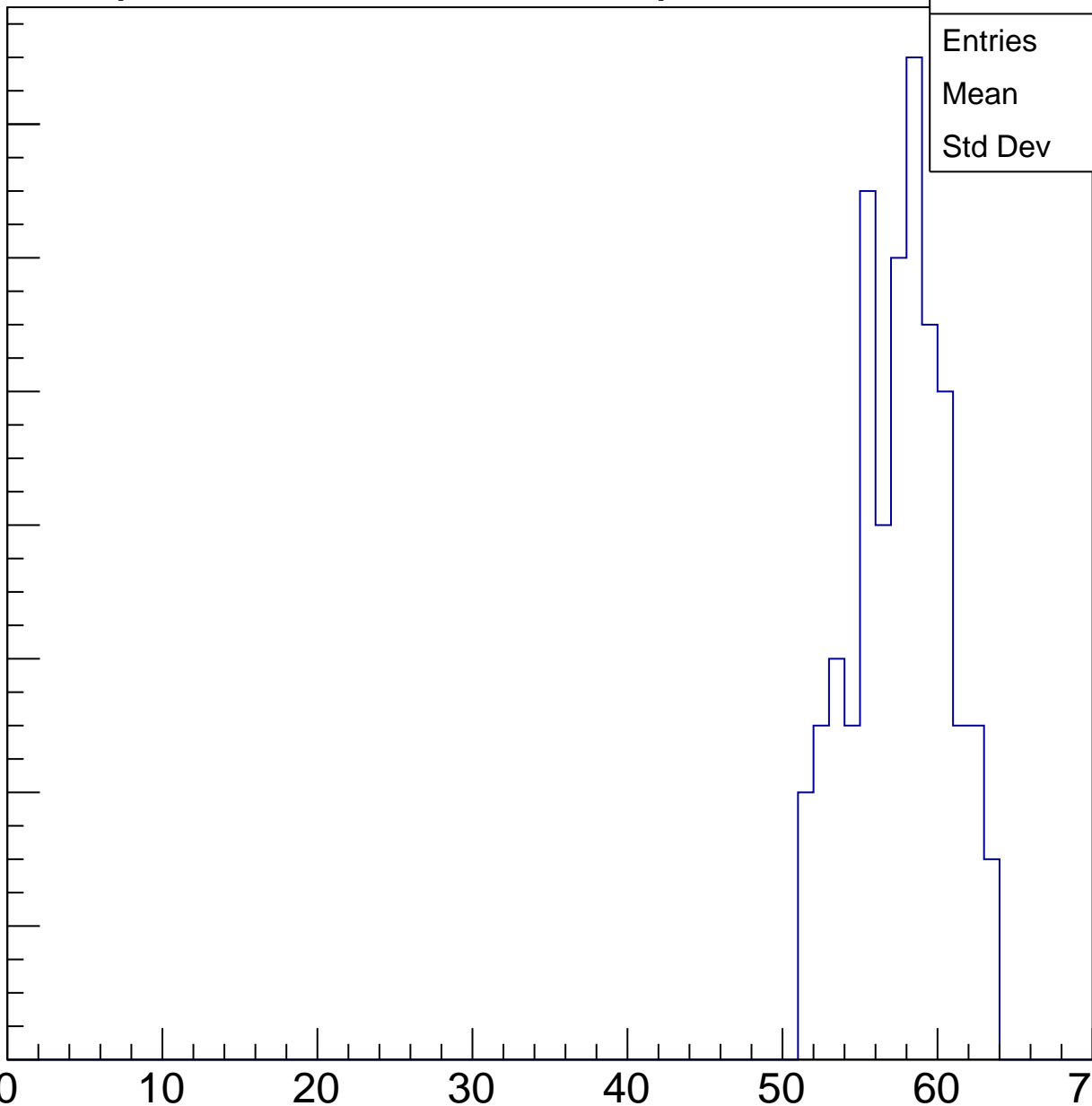
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 102 |
| Mean | 57.08 |
| Std Dev | 3.022 |

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch111, adc5

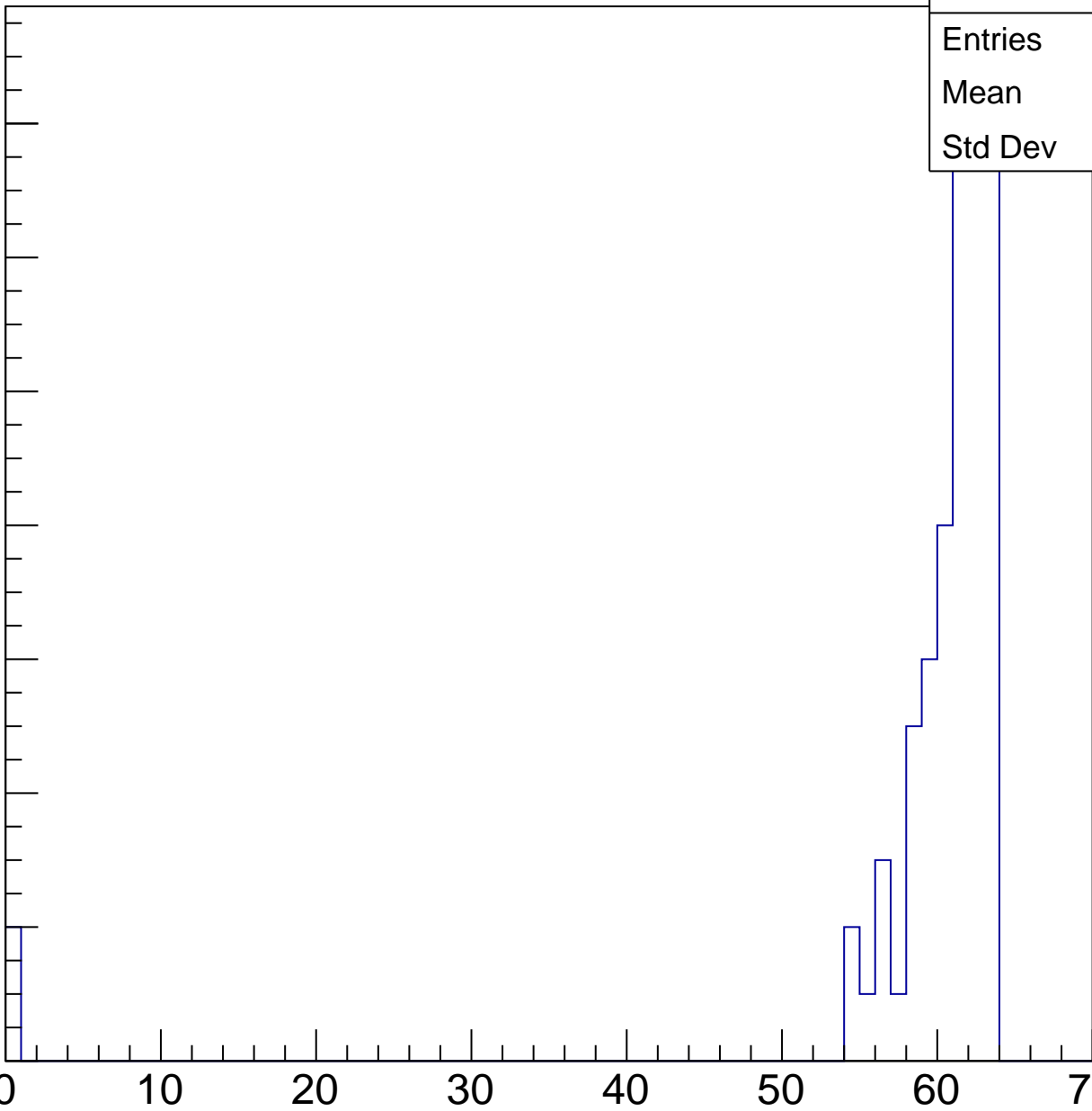
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 58.88 |
| Std Dev | 10.2 |

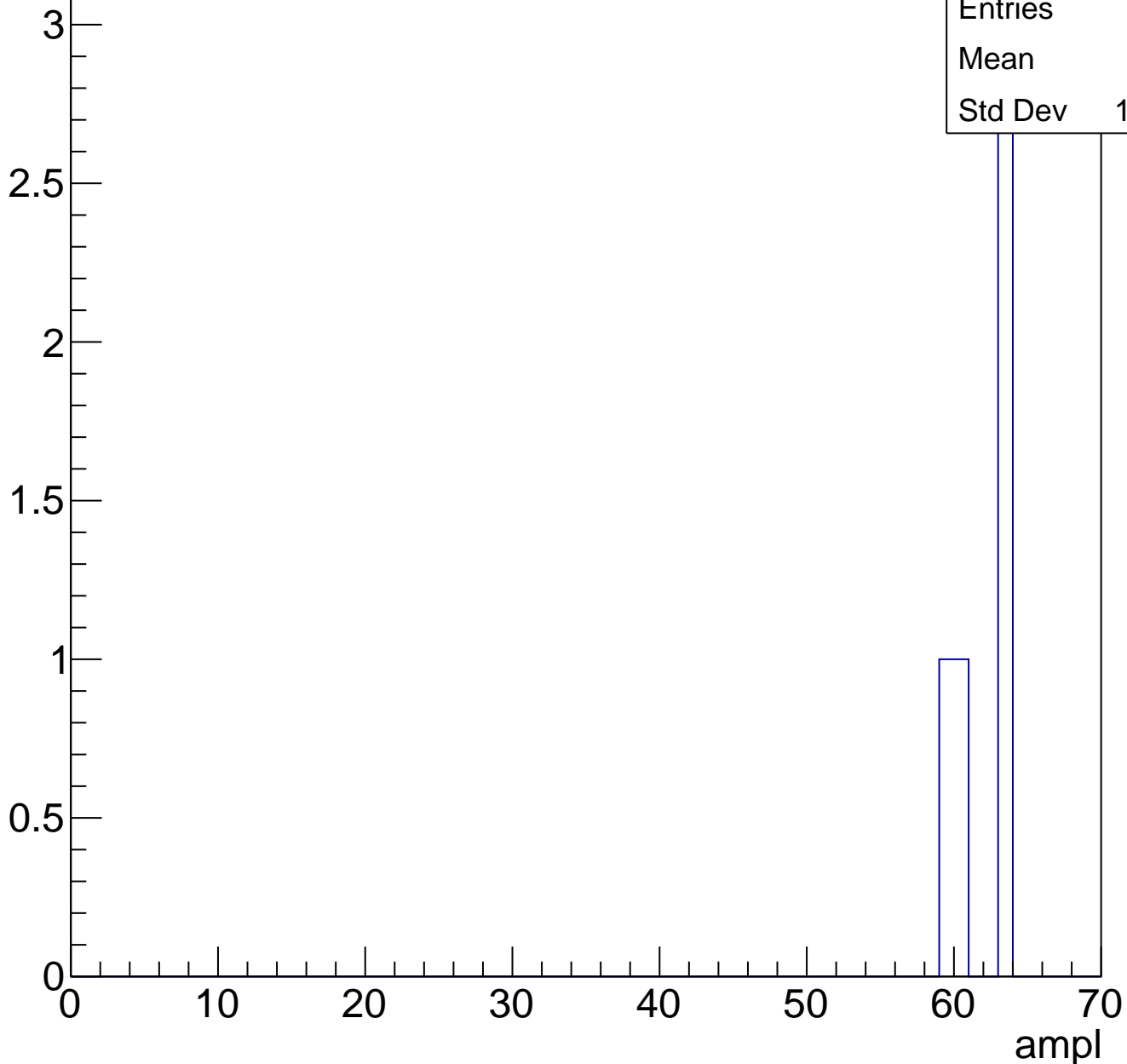
ampl



B1L001S, U26-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

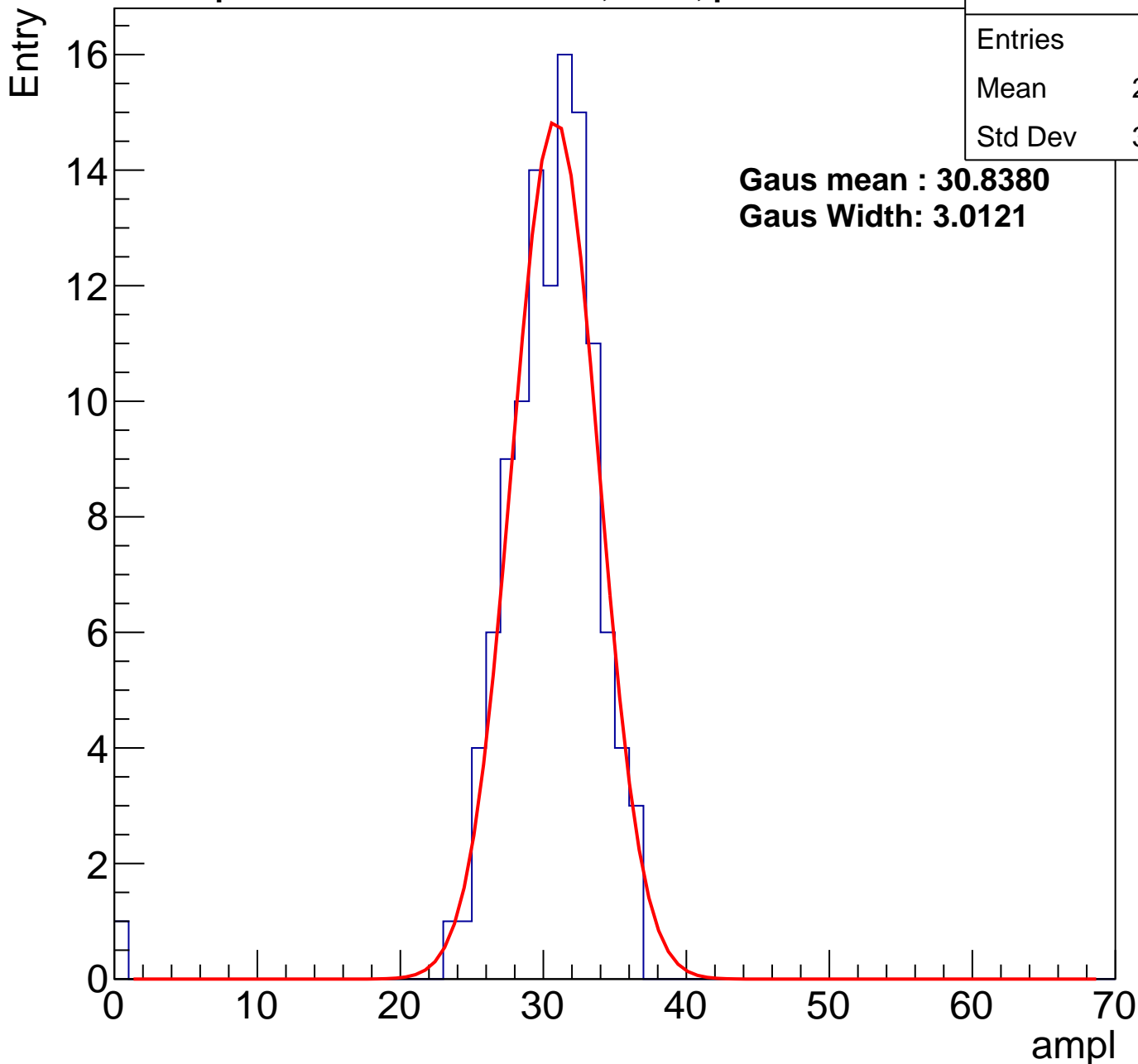
B1L001S, U26-ch112, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 113 |
| Mean | 29.94 |
| Std Dev | 3.985 |

Gaus mean : 30.8380

Gaus Width: 3.0121



B1L001S, U26-ch112, adc1

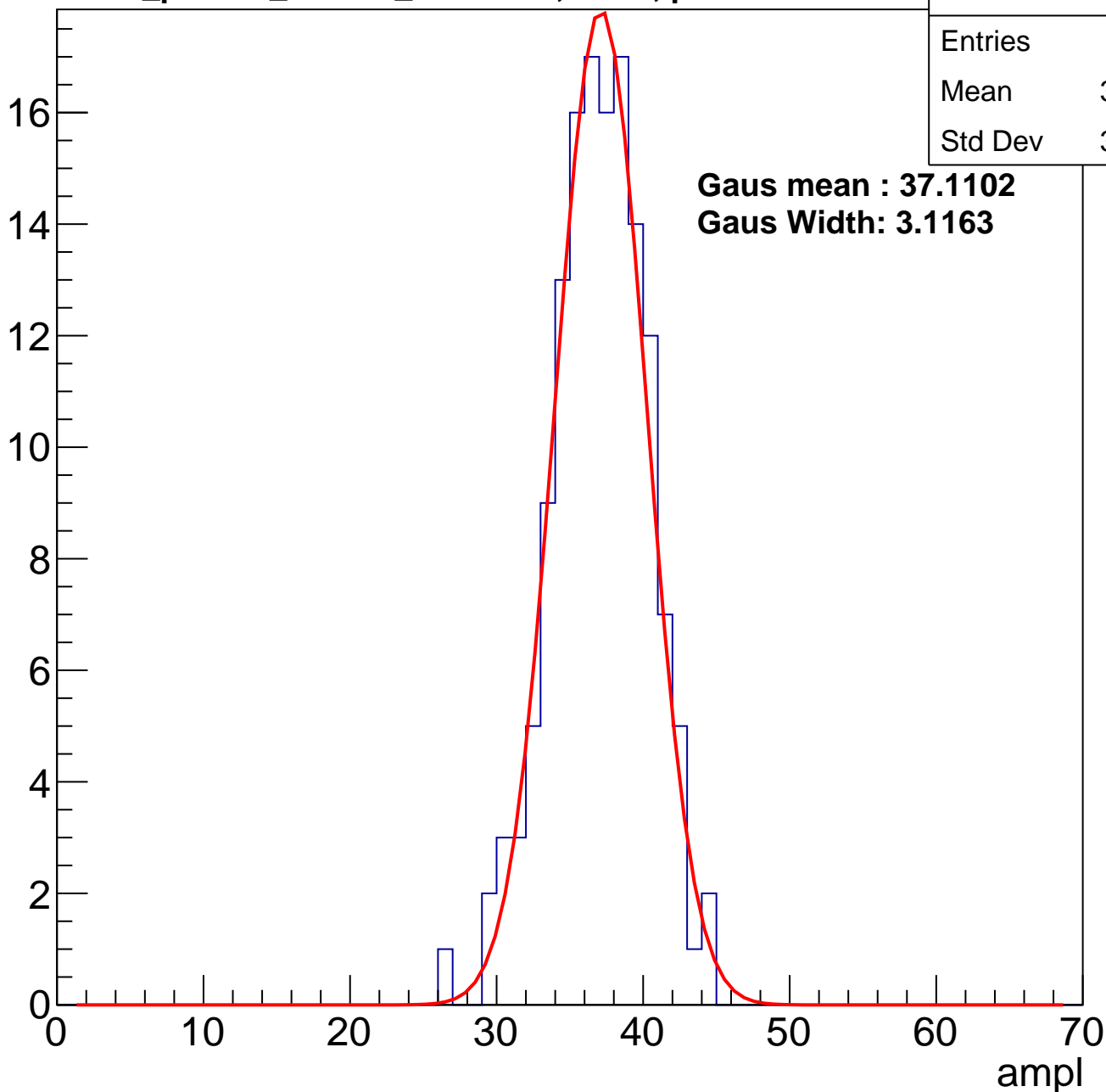
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 36.57 |
| Std Dev | 3.235 |

Gaus mean : 37.1102

Gaus Width: 3.1163

Entry



B1L001S, U26-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 135 |
| Mean | 43.77 |
| Std Dev | 3.136 |

Gaus mean : 44.2768

Gaus Width: 3.3427

Entry

16

14

12

10

8

6

4

2

0

0

10

20

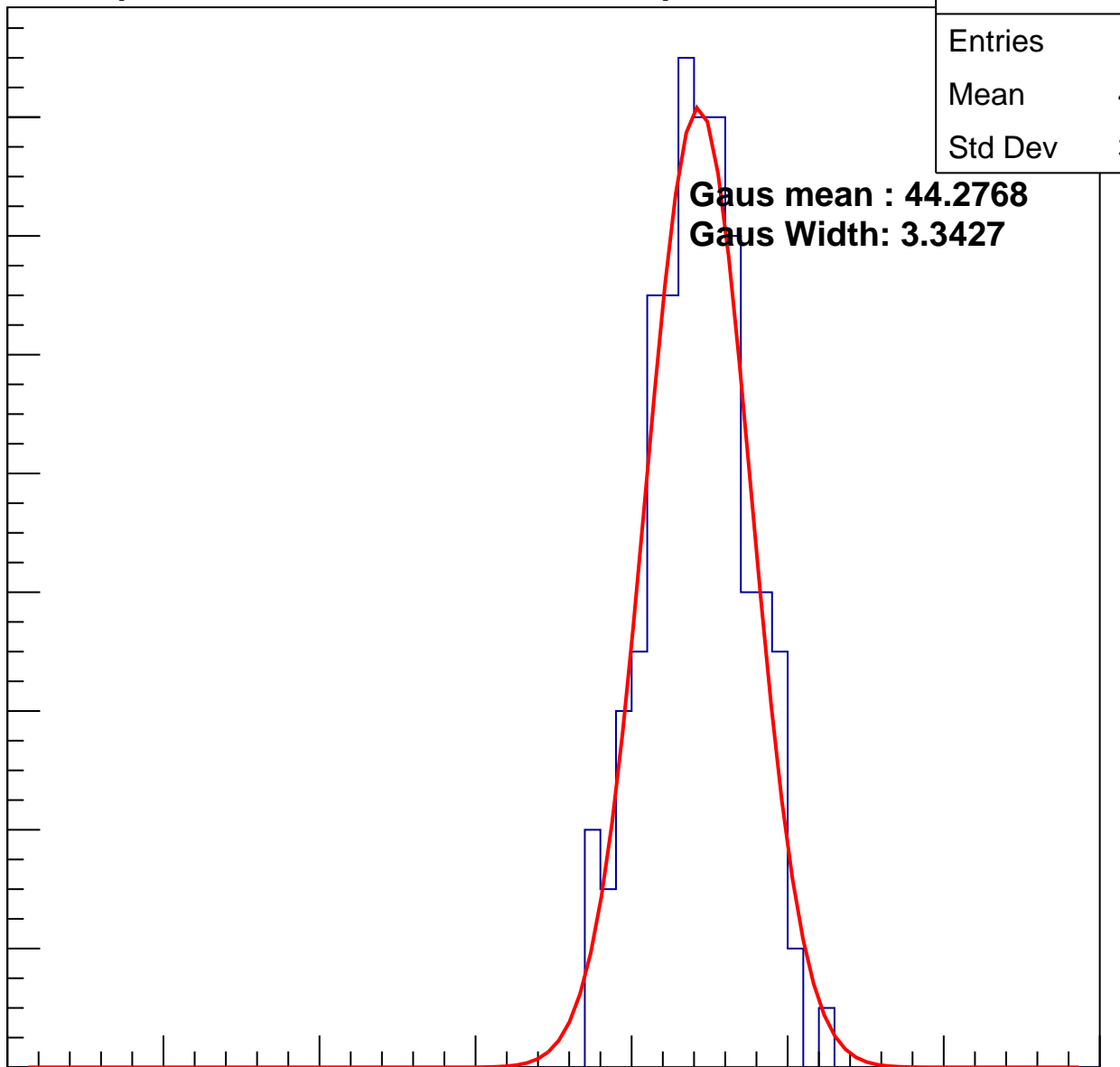
30

40

50

60

ampl



B1L001S, U26-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

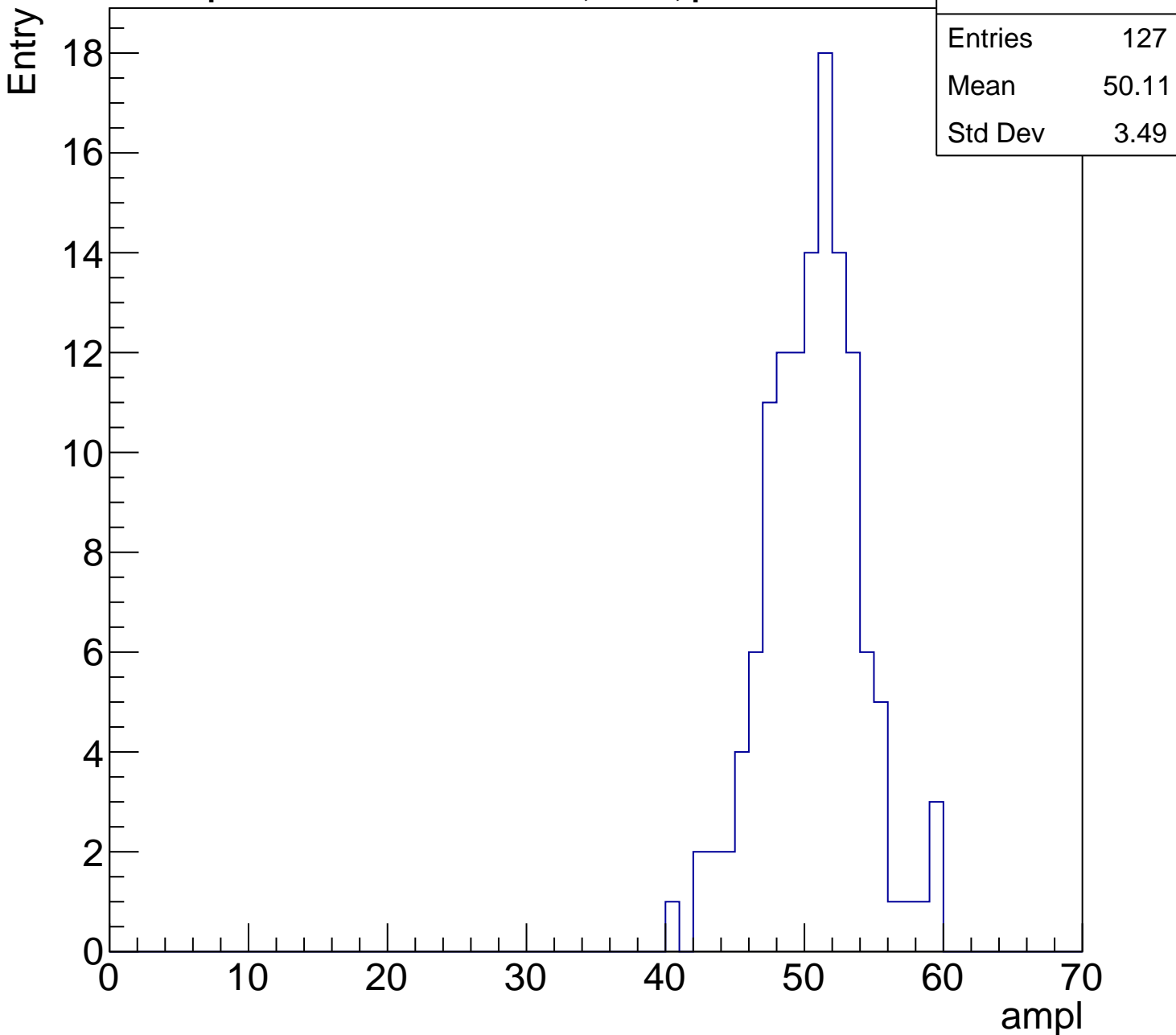
| | |
|---------|-------|
| Entries | 127 |
| Mean | 50.11 |
| Std Dev | 3.49 |

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

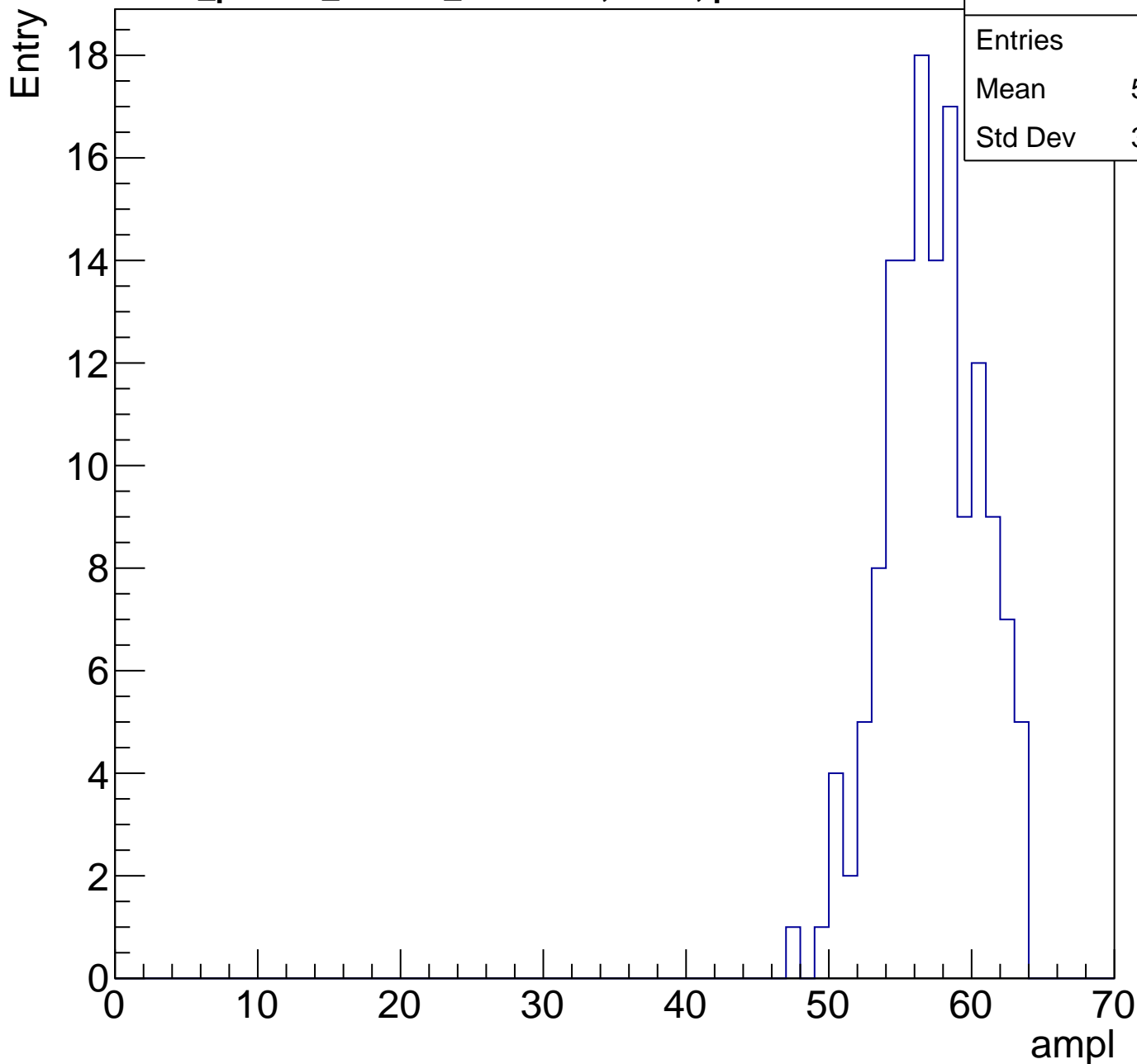
ampl



B1L001S, U26-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 140 |
| Mean | 56.78 |
| Std Dev | 3.315 |



B1L001S, U26-ch112, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 62 |
| Mean | 59.65 |
| Std Dev | 7.901 |

Entry

12

10

8

6

4

2

0

0

10

20

30

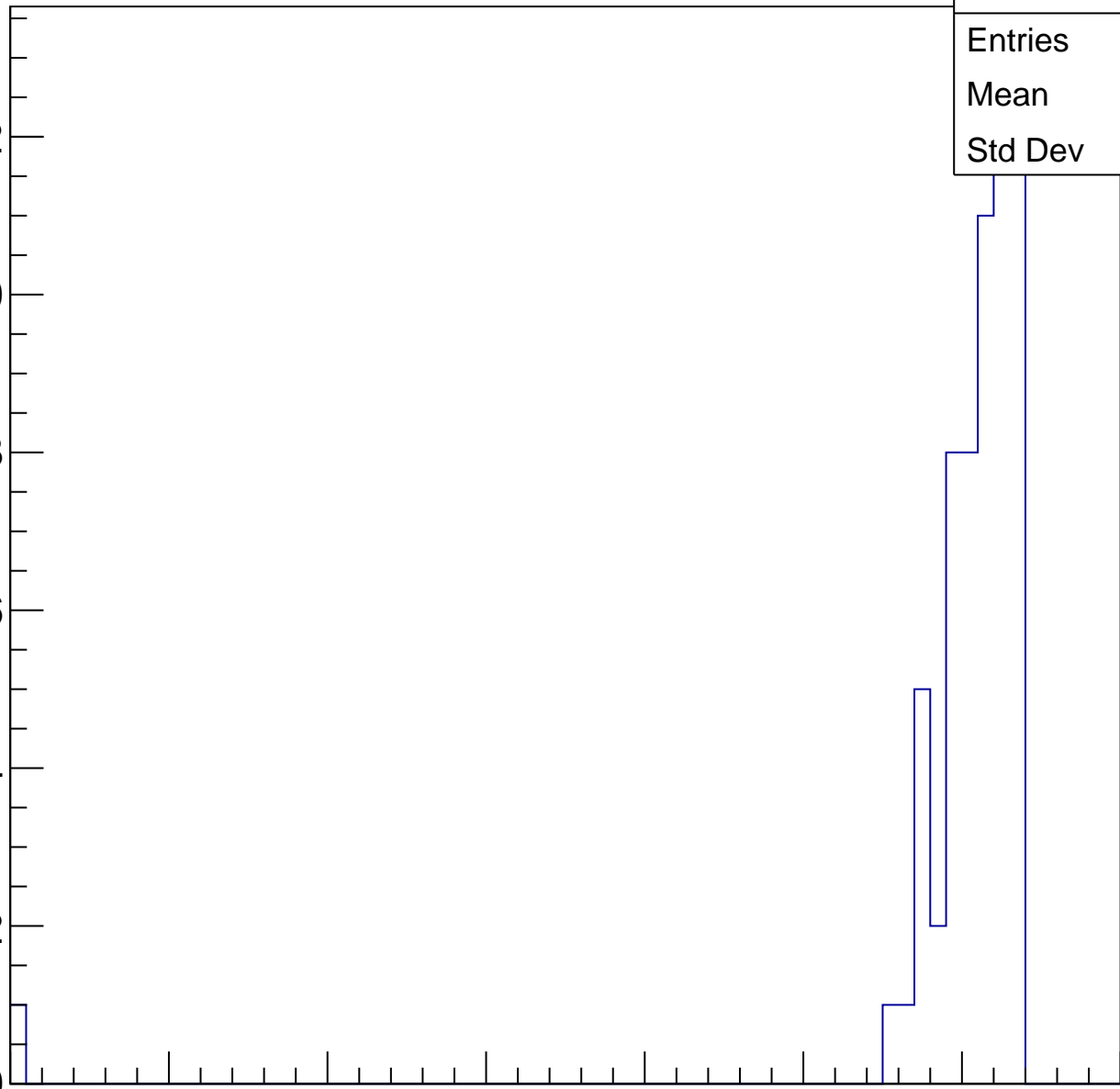
40

50

60

70

ampl



B1L001S, U26-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 47 |
| Std Dev | 27.14 |

ampl

B1L001S, U26-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

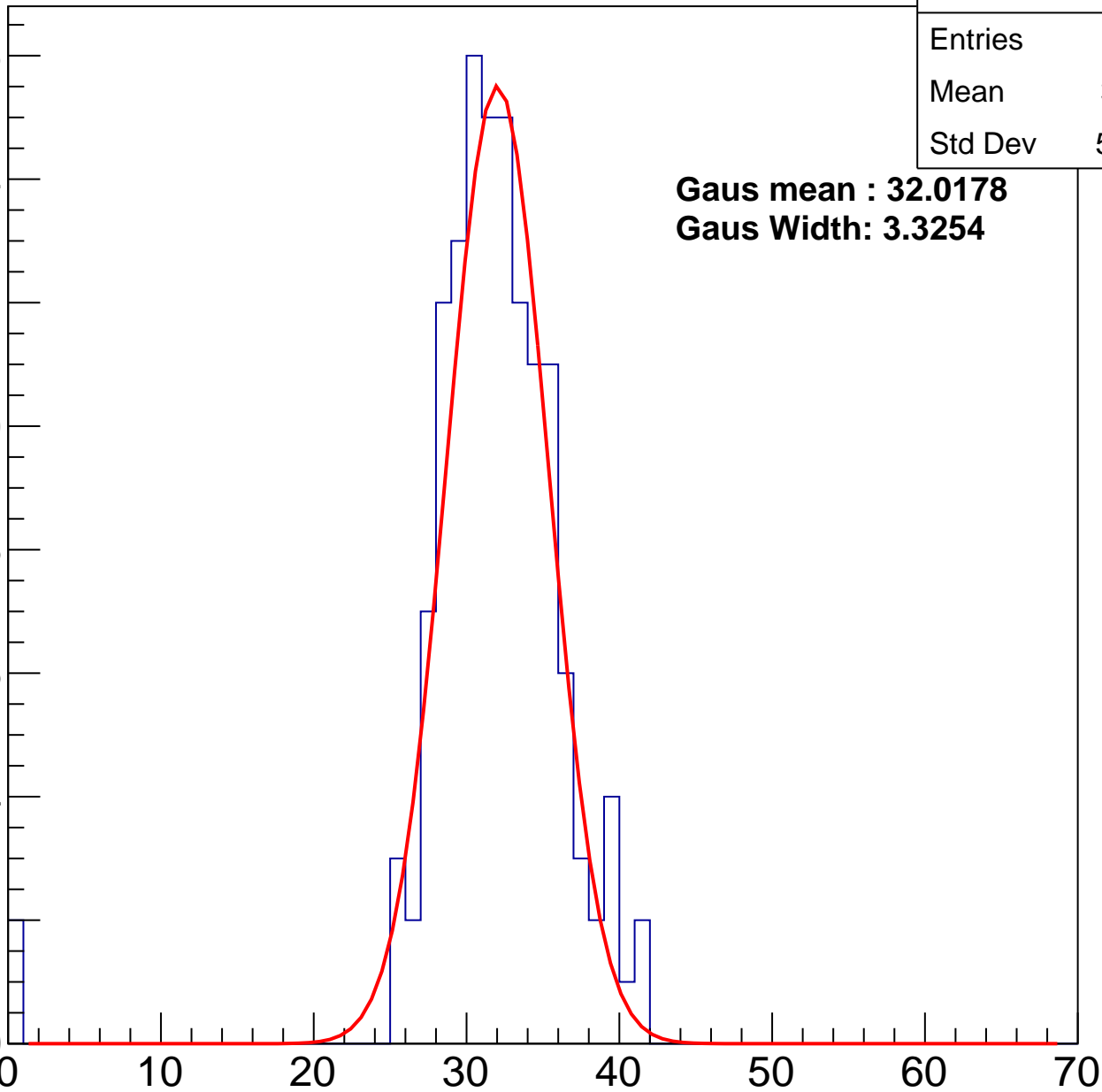
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 137 |
| Mean | 31.31 |
| Std Dev | 5.116 |

Gaus mean : 32.0178

Gaus Width: 3.3254

ampl



B1L001S, U26-ch113, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 38.19 |
| Std Dev | 3.336 |

Gaus mean : 38.2748

Gaus Width: 3.5747

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

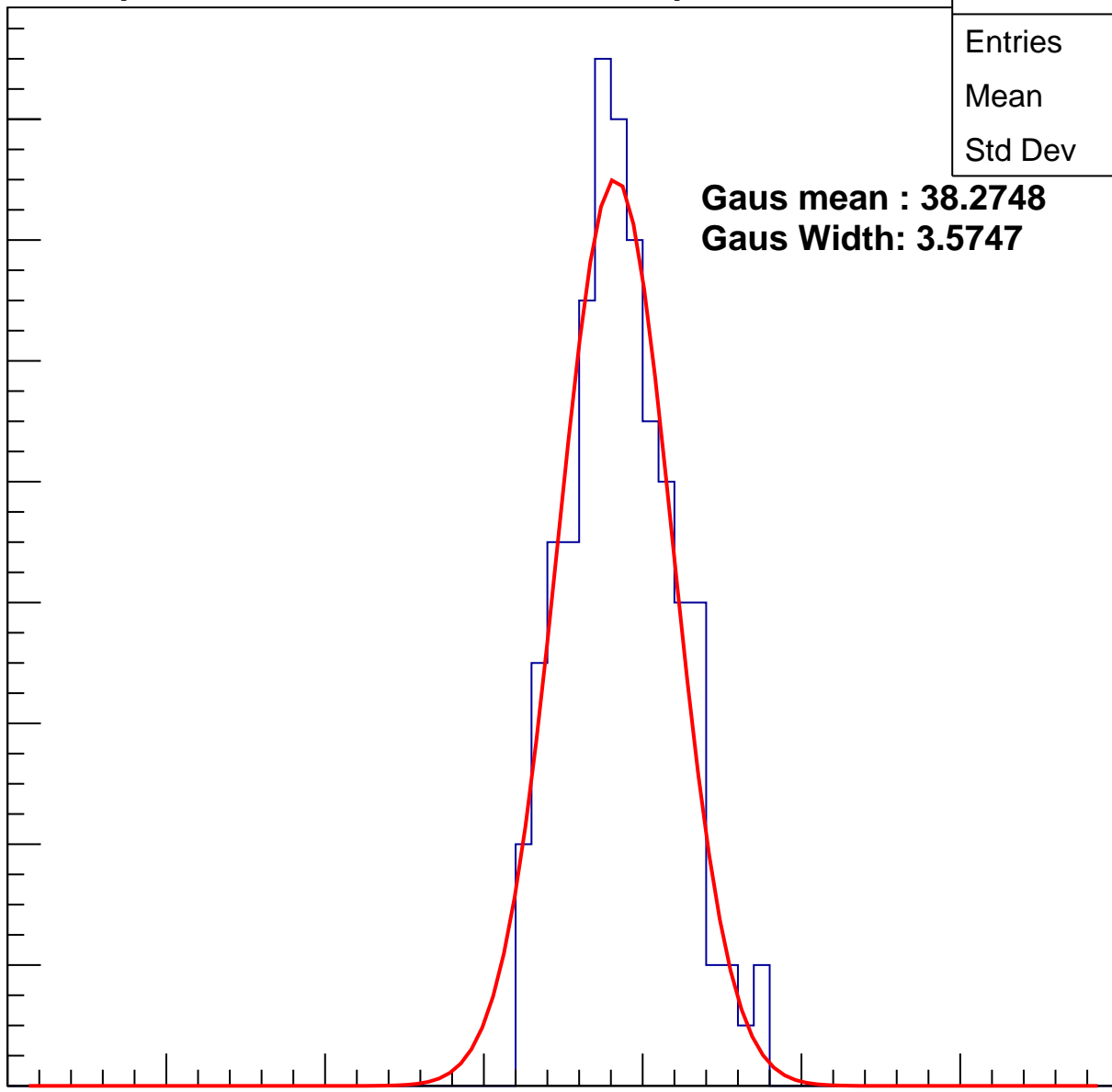
40

50

60

70

ampl



B1L001S, U26-ch113, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

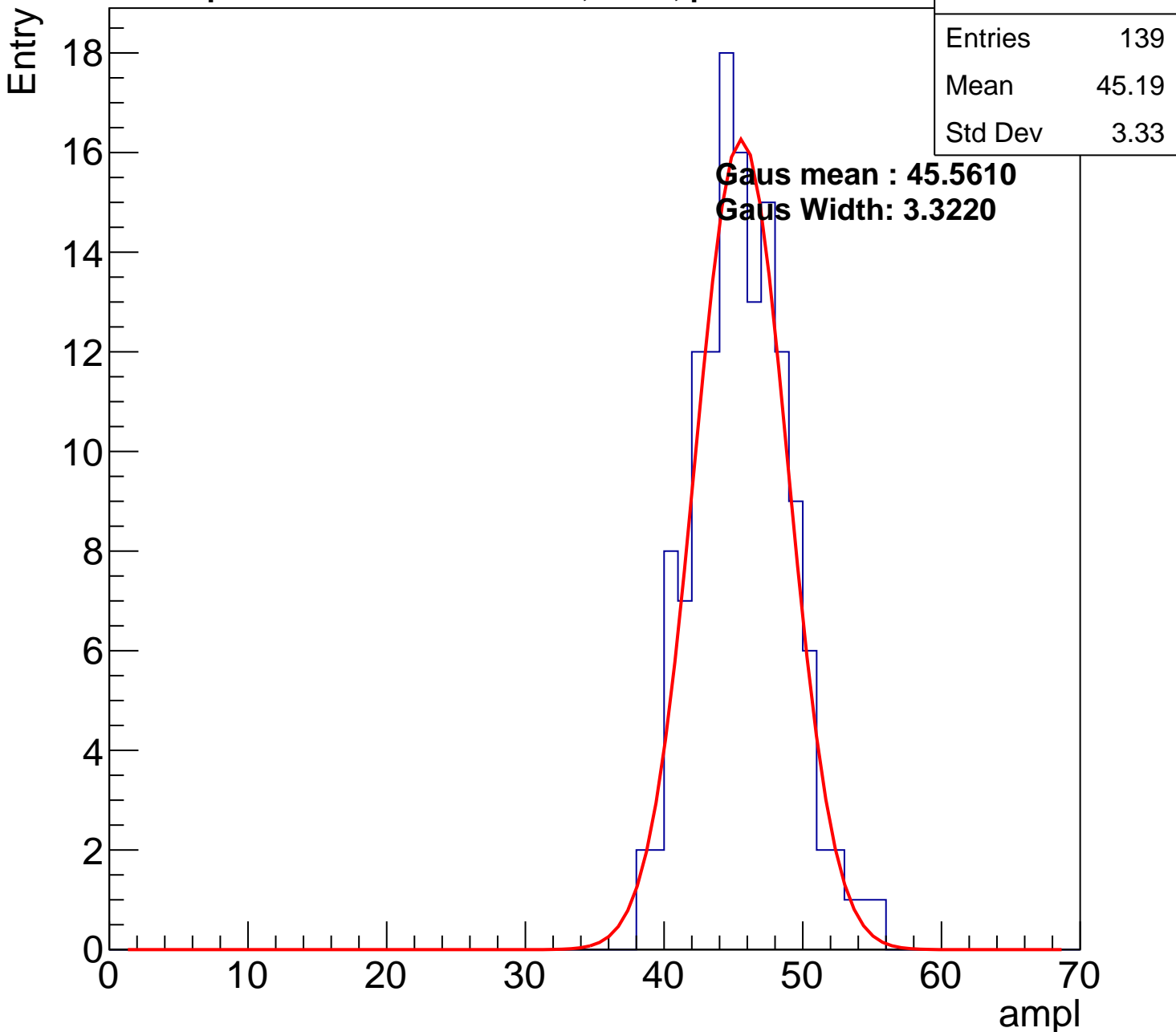
| | |
|---------|-------|
| Entries | 139 |
| Mean | 45.19 |
| Std Dev | 3.33 |

Gaus mean : 45.5610

Gaus Width: 3.3220

ampl

0 10 20 30 40 50 60 70

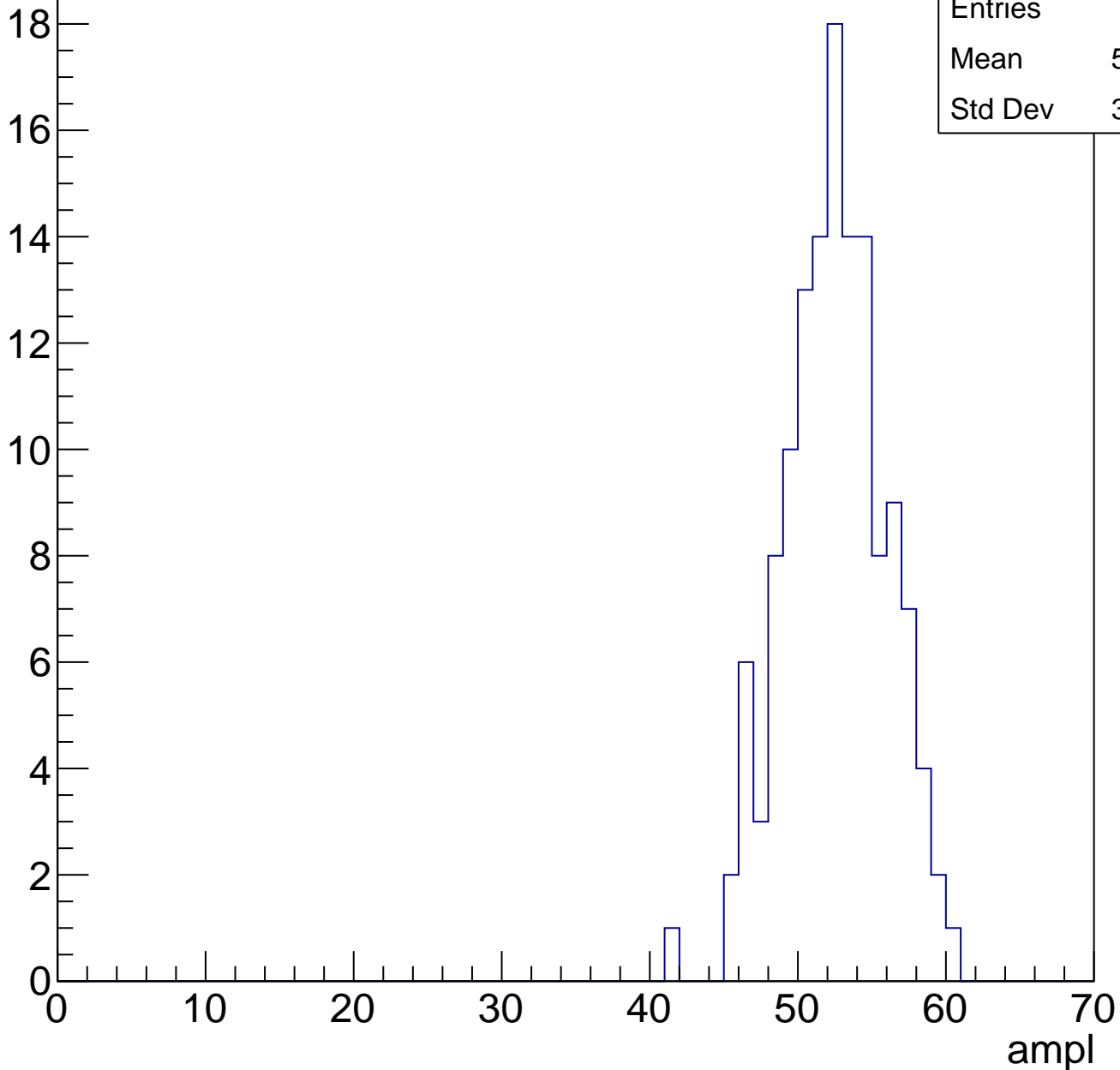


B1L001S, U26-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 134 |
| Mean | 52.04 |
| Std Dev | 3.413 |



B1L001S, U26-ch113, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 92 |
| Mean | 57.39 |
| Std Dev | 3.061 |

Entry

10

8

6

4

2

0

0

10

20

30

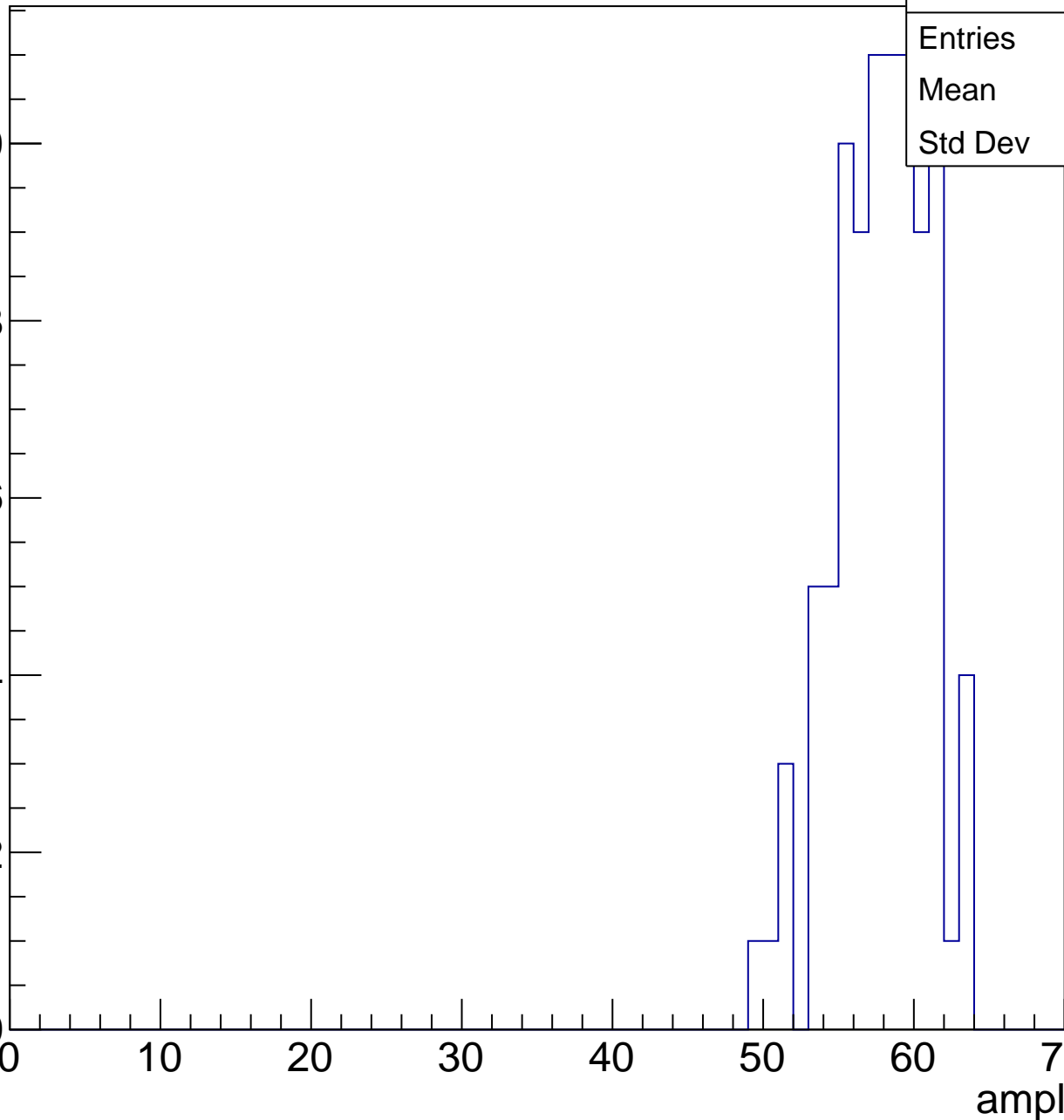
40

50

60

70

ampl

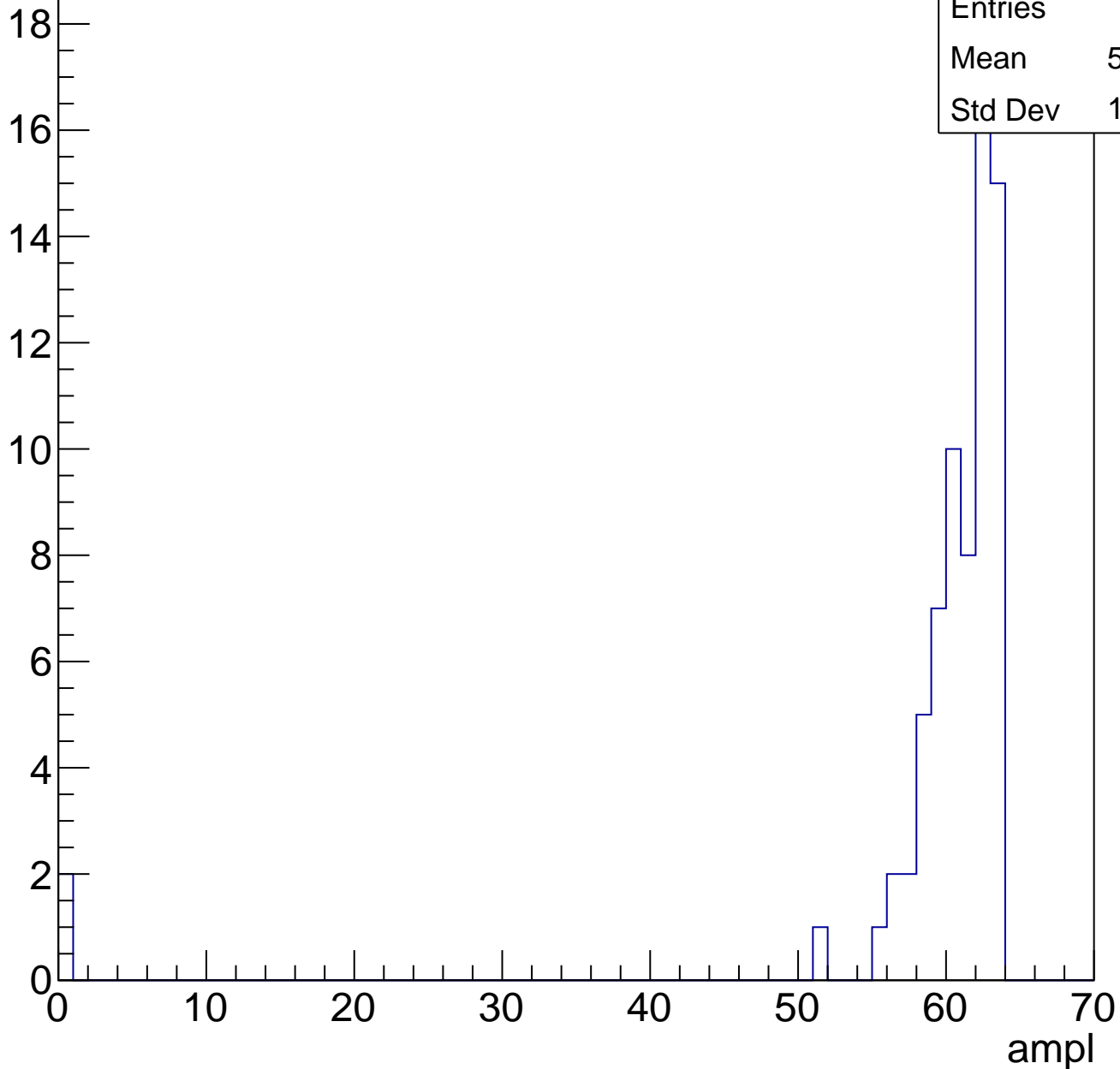


B1L001S, U26-ch113, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 58.93 |
| Std Dev | 10.29 |

Entry



B1L001S, U26-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch114, adc0

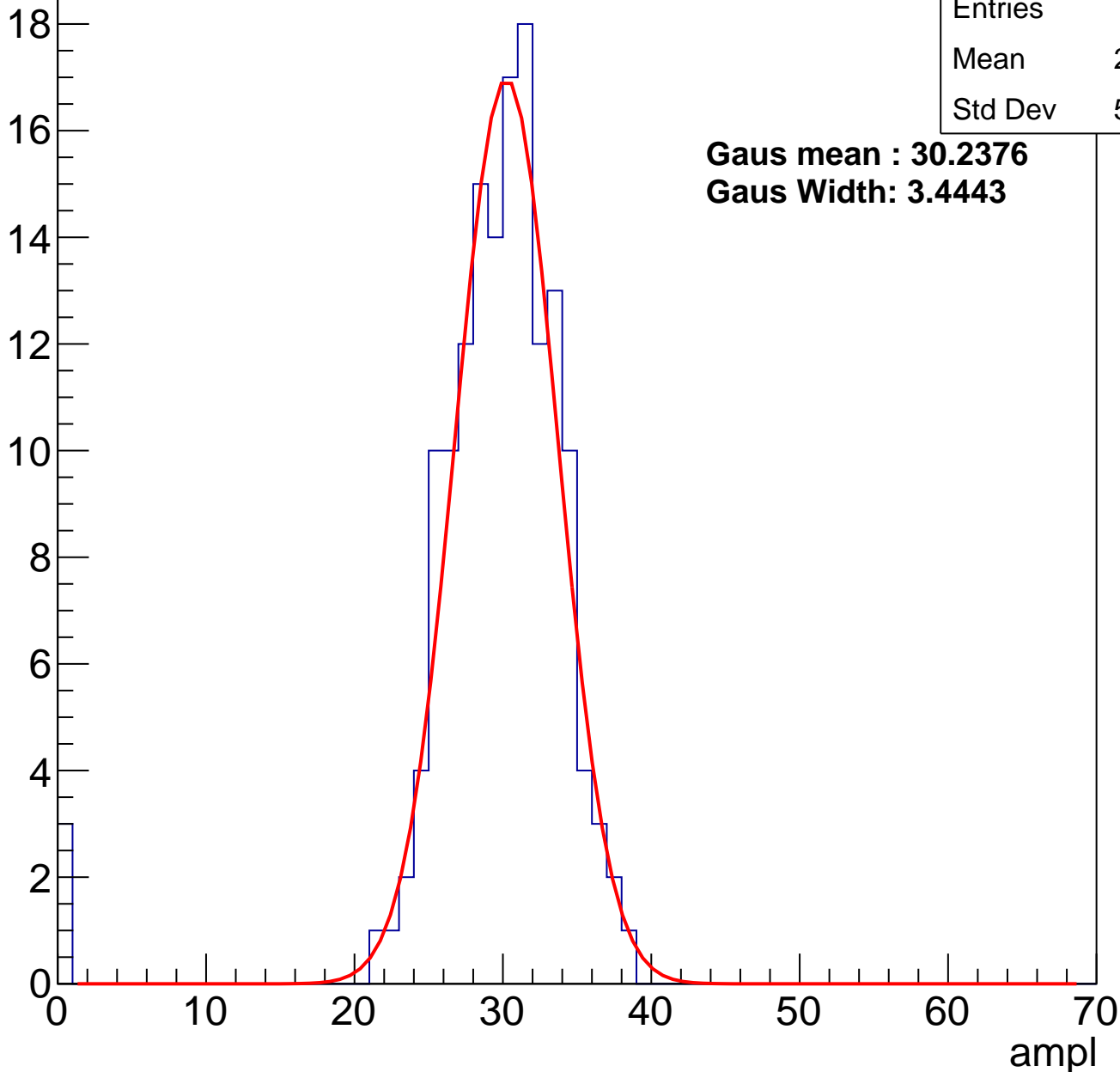
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 29.12 |
| Std Dev | 5.303 |

Gaus mean : 30.2376

Gaus Width: 3.4443

Entry



B1L001S, U26-ch114, adc1

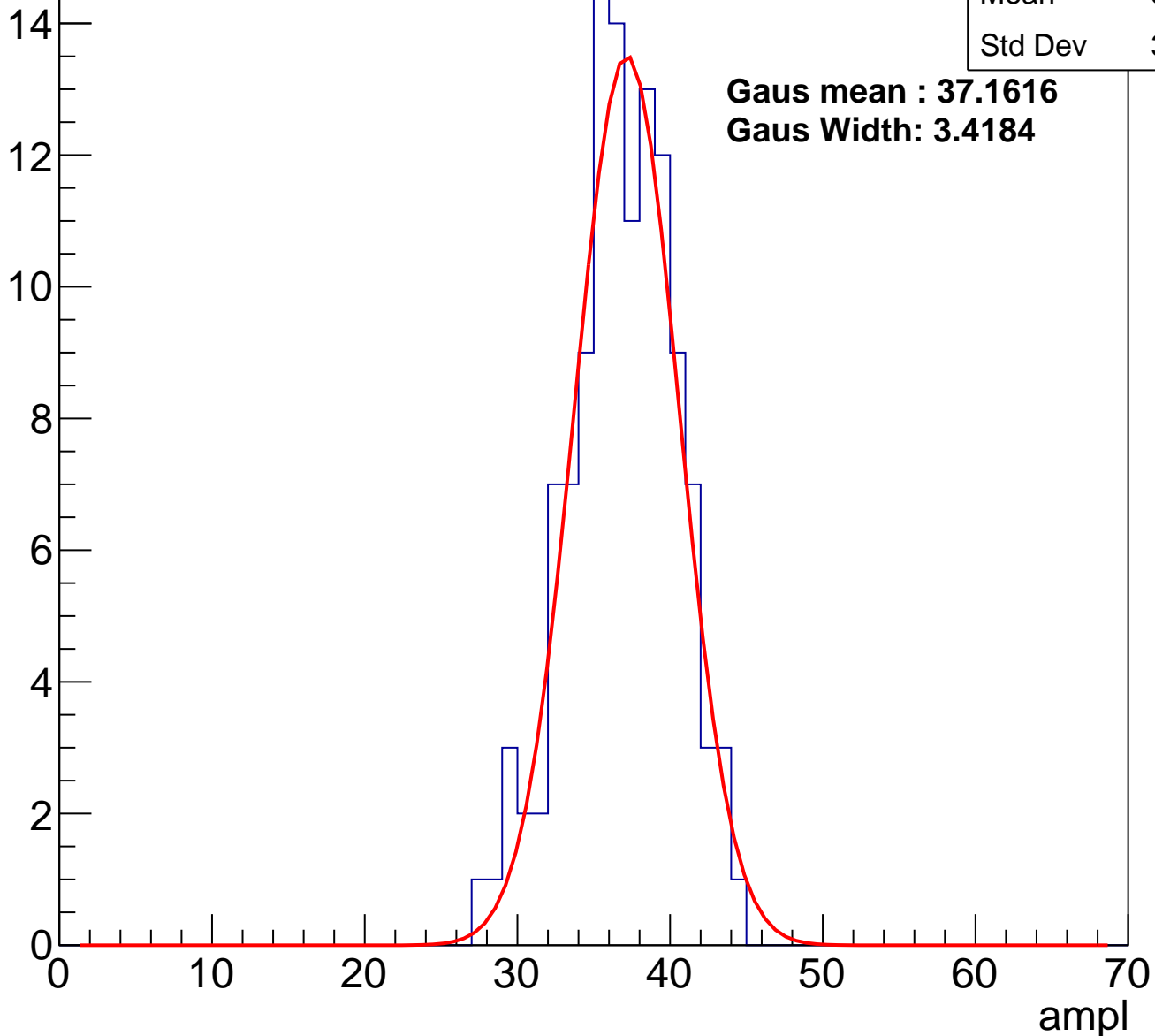
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 120 |
| Mean | 36.41 |
| Std Dev | 3.451 |

Gaus mean : 37.1616

Gaus Width: 3.4184

Entry



B1L001S, U26-ch114, adc2

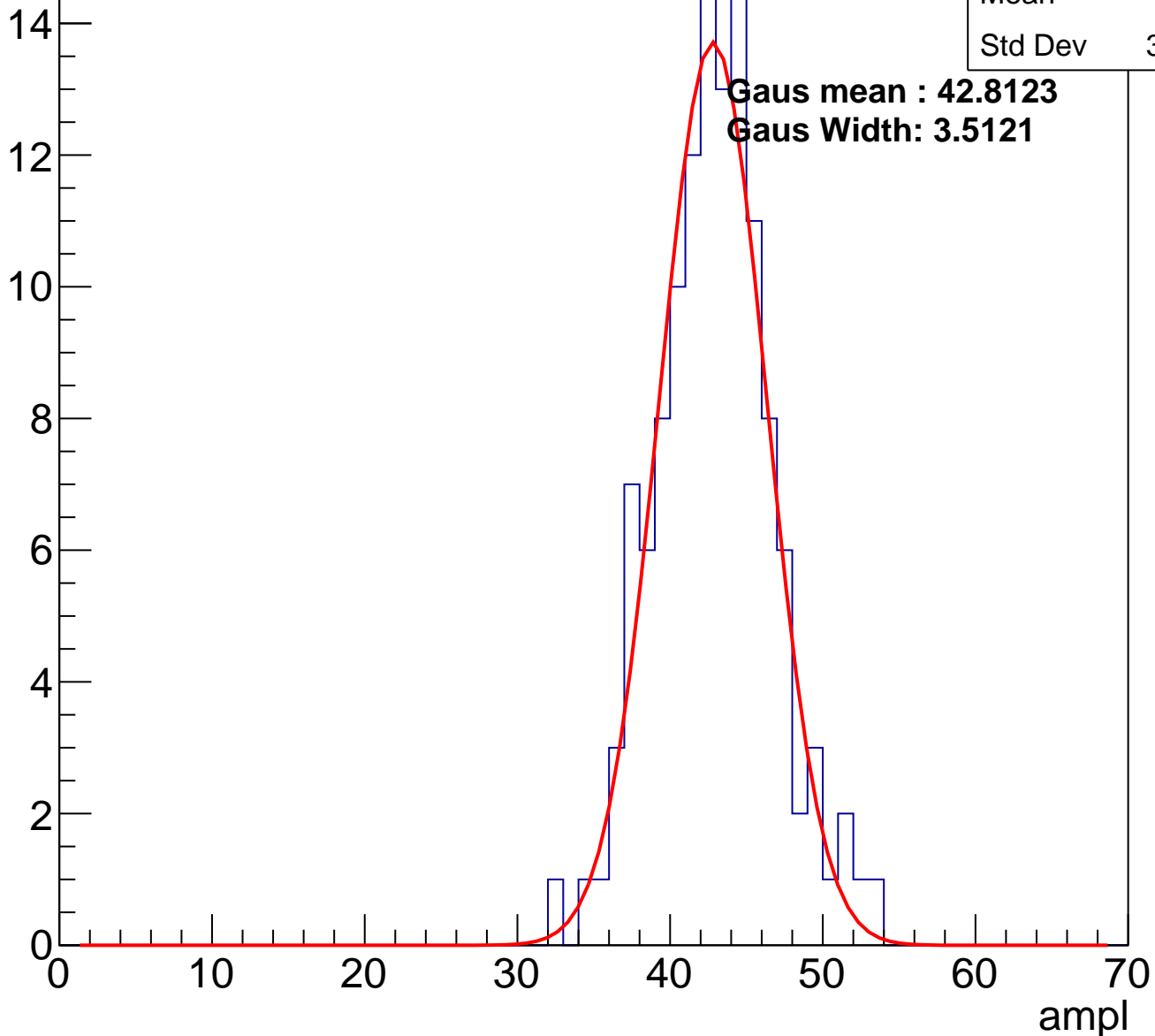
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 42.47 |
| Std Dev | 3.769 |

Gaus mean : 42.8123

Gaus Width: 3.5121

Entry



B1L001S, U26-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 136 |
| Mean | 48.91 |
| Std Dev | 3.394 |

Entry

16

14

12

10

8

6

4

2

0

0

10

20

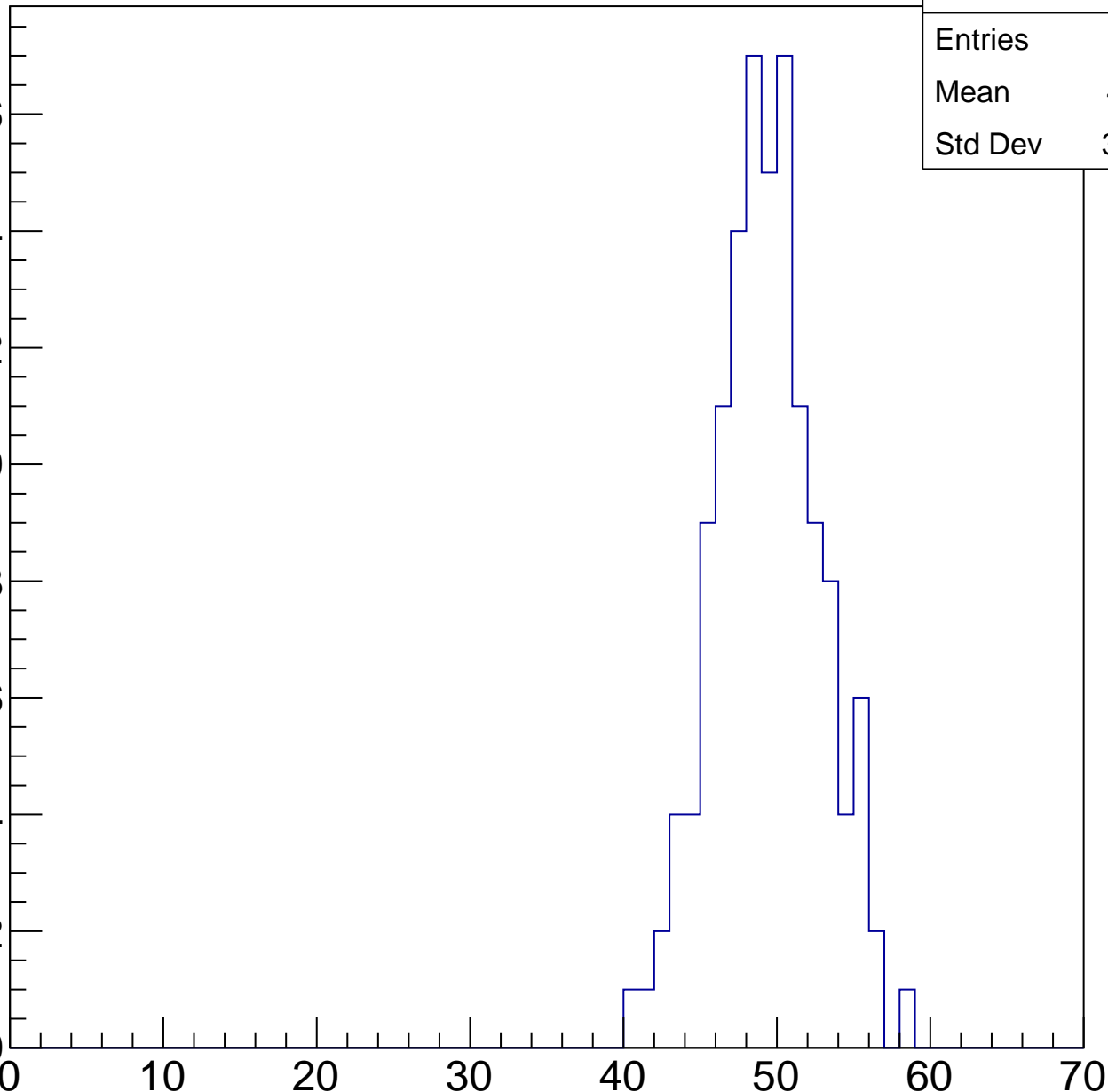
30

40

50

60

ampl



B1L001S, U26-ch114, adc4

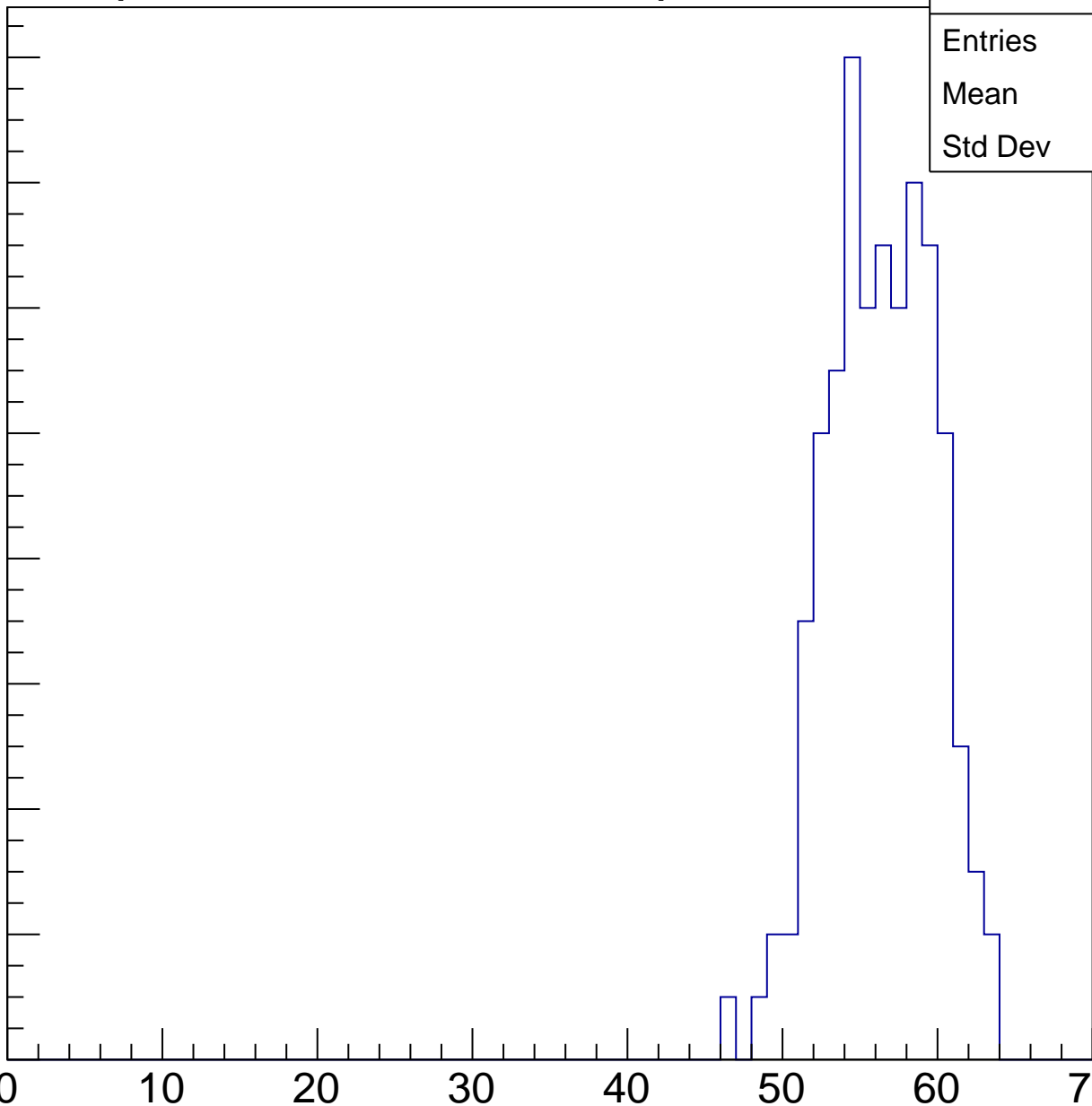
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 134 |
| Mean | 55.85 |
| Std Dev | 3.365 |

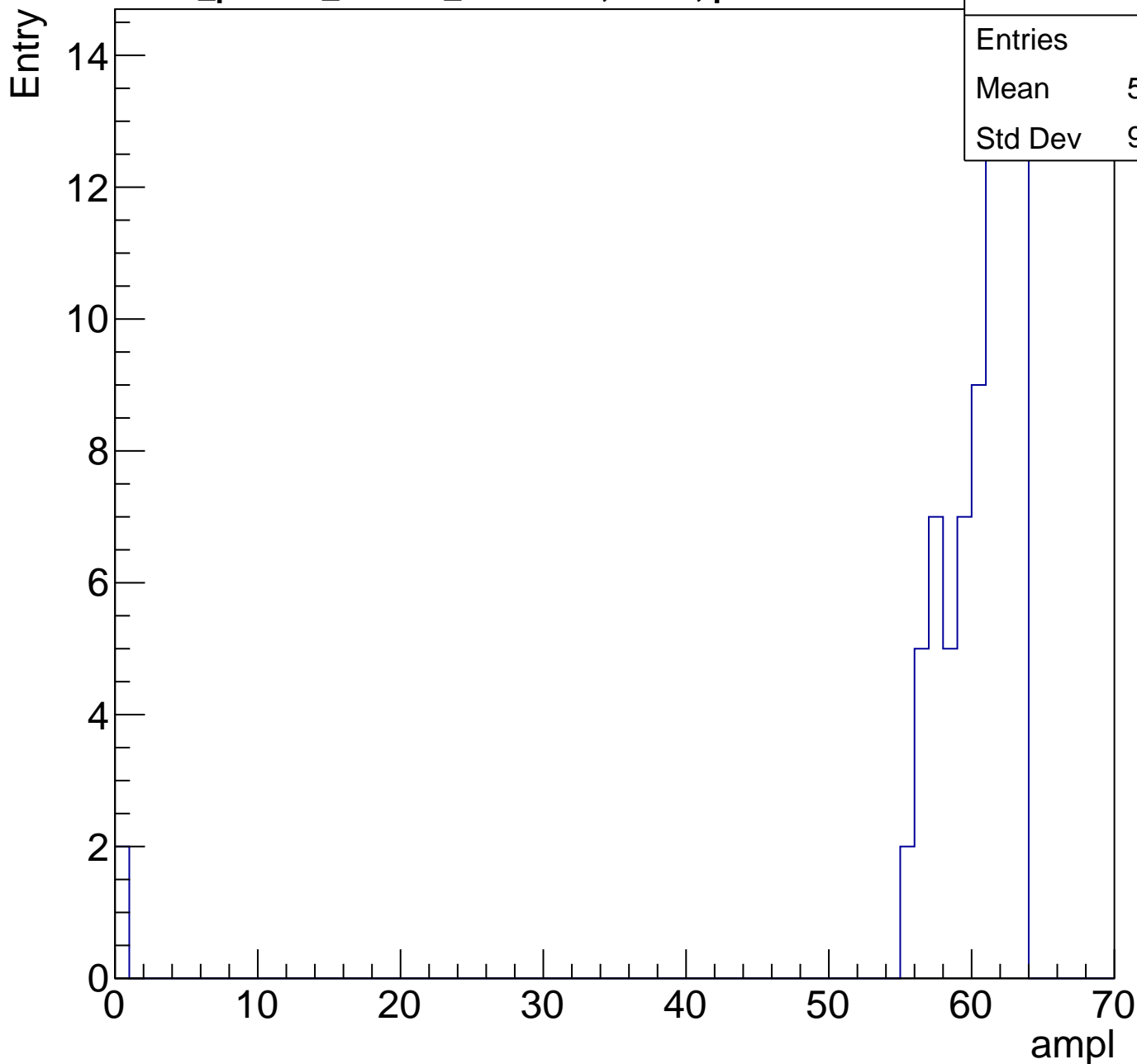
ampl



B1L001S, U26-ch114, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 58.58 |
| Std Dev | 9.835 |

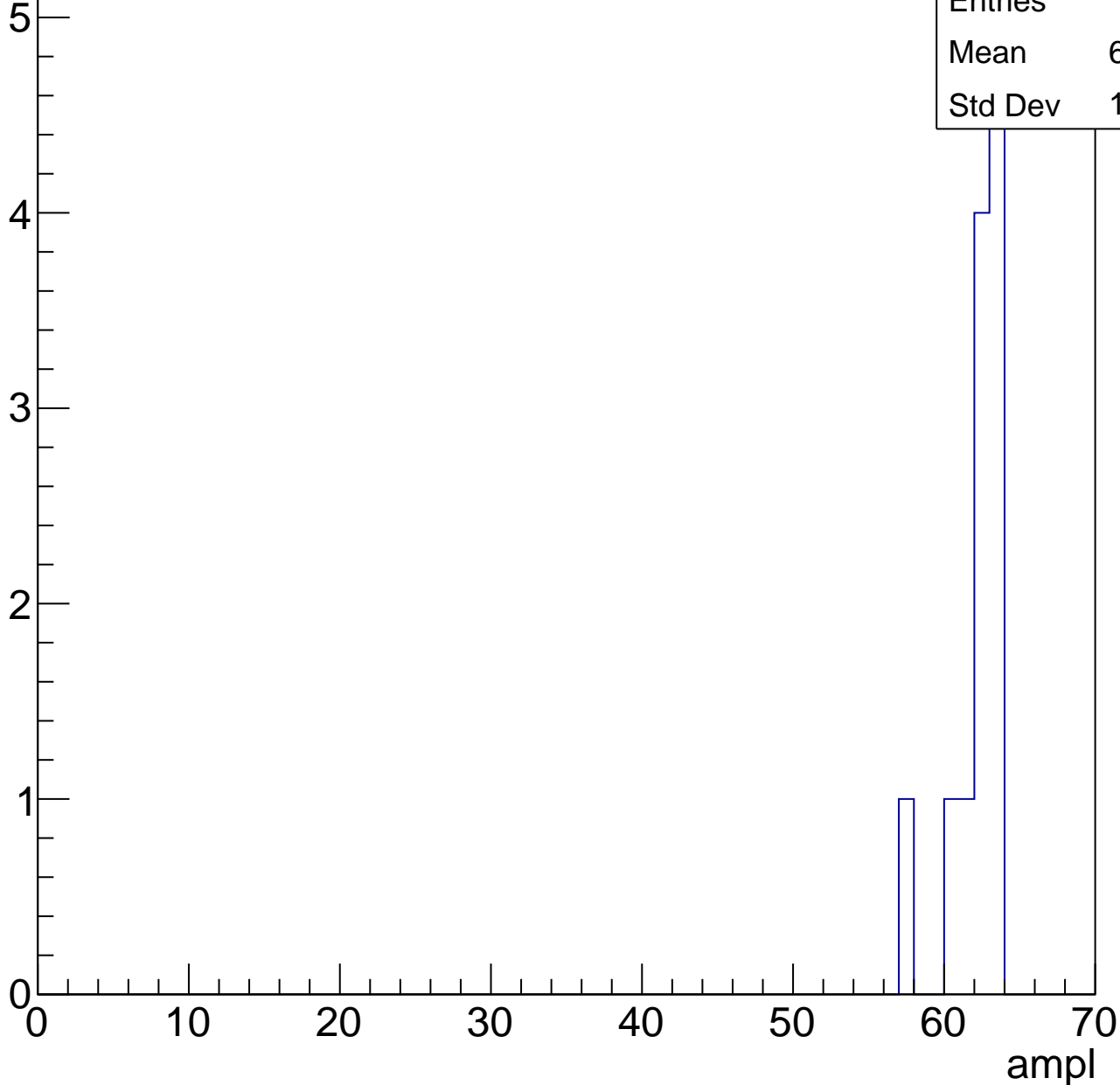


B1L001S, U26-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 61.75 |
| Std Dev | 1.689 |



B1L001S, U26-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 116 |
| Mean | 28.37 |
| Std Dev | 4.772 |

Gaus mean : 29.2839

Gaus Width: 2.9383

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

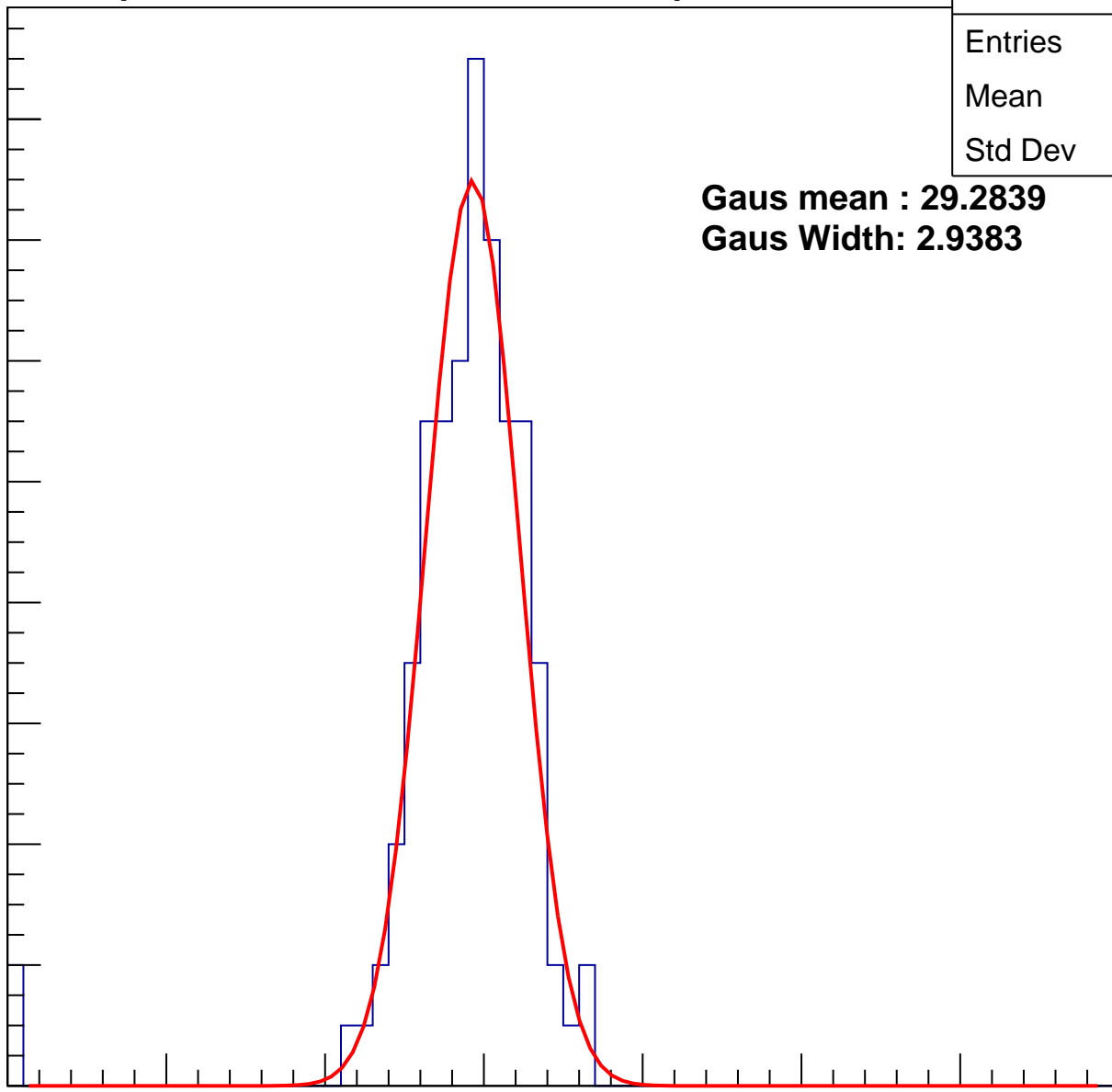
40

50

60

70

ampl



B1L001S, U26-ch115, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 142 |
| Mean | 35.51 |
| Std Dev | 3.394 |

Gaus mean : 35.9814

Gaus Width: 3.7214

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

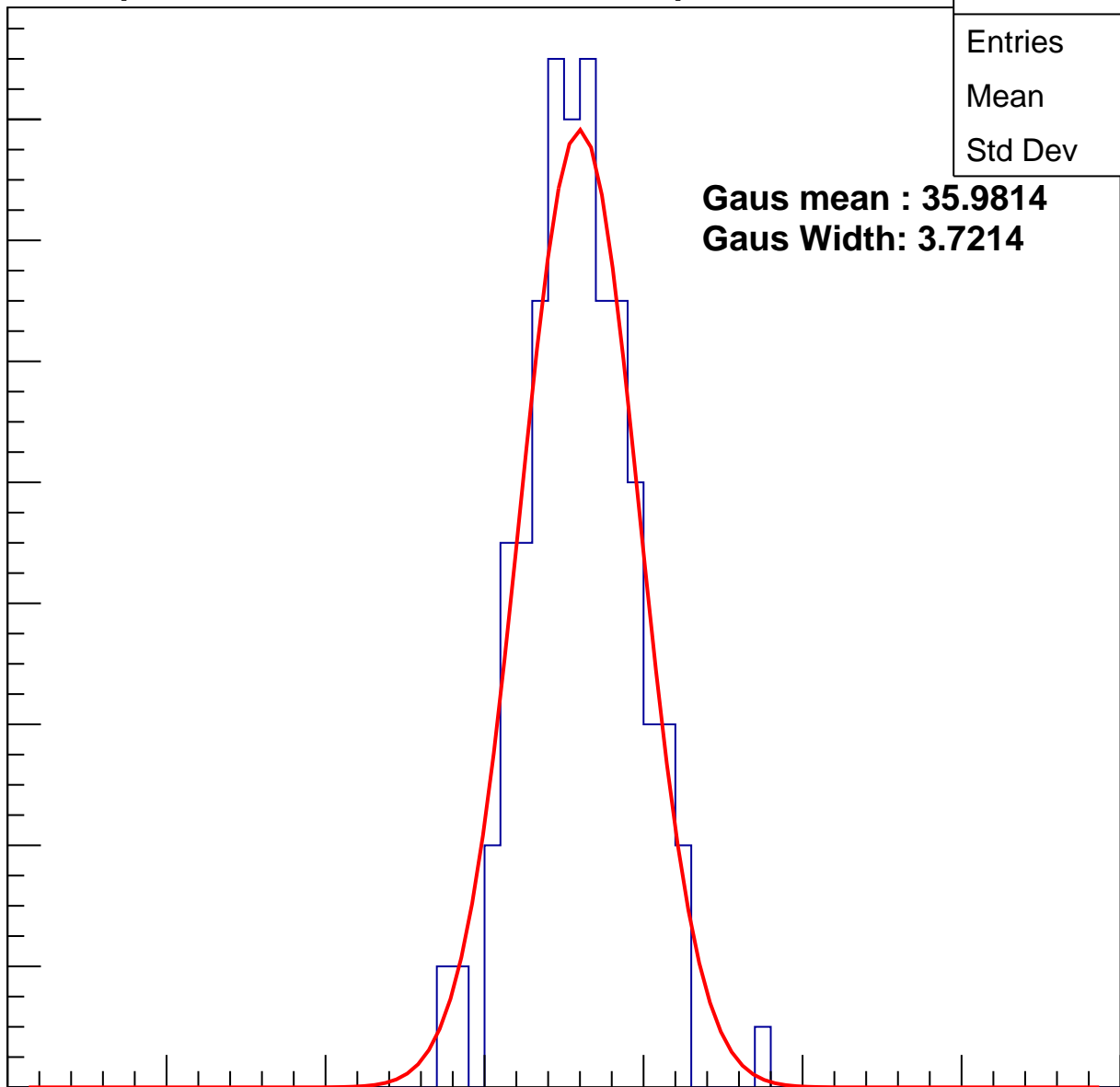
40

50

60

70

ampl



B1L001S, U26-ch115, adc2

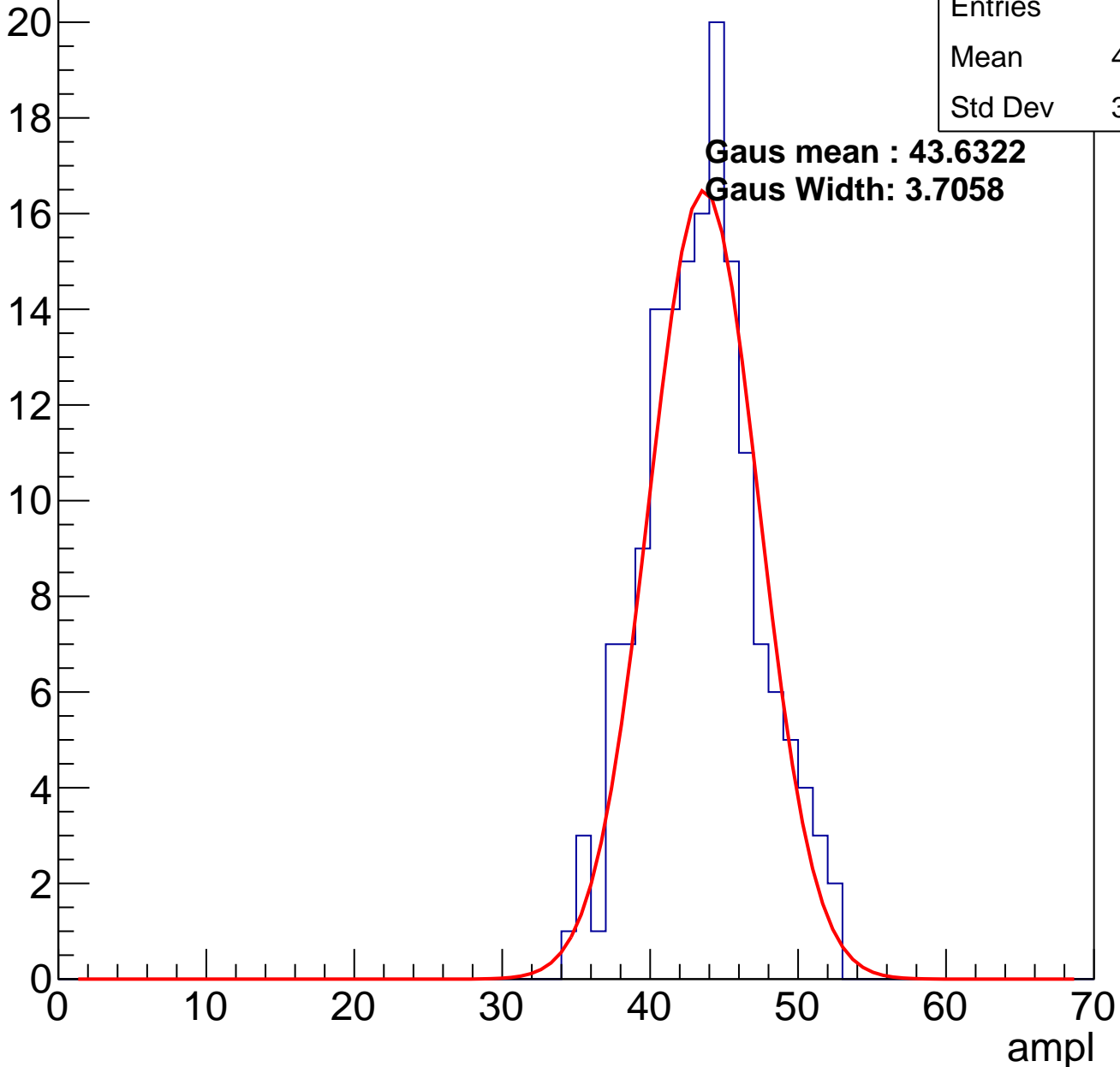
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 160 |
| Mean | 43.02 |
| Std Dev | 3.759 |

Gaus mean : 43.6322

Gaus Width: 3.7058

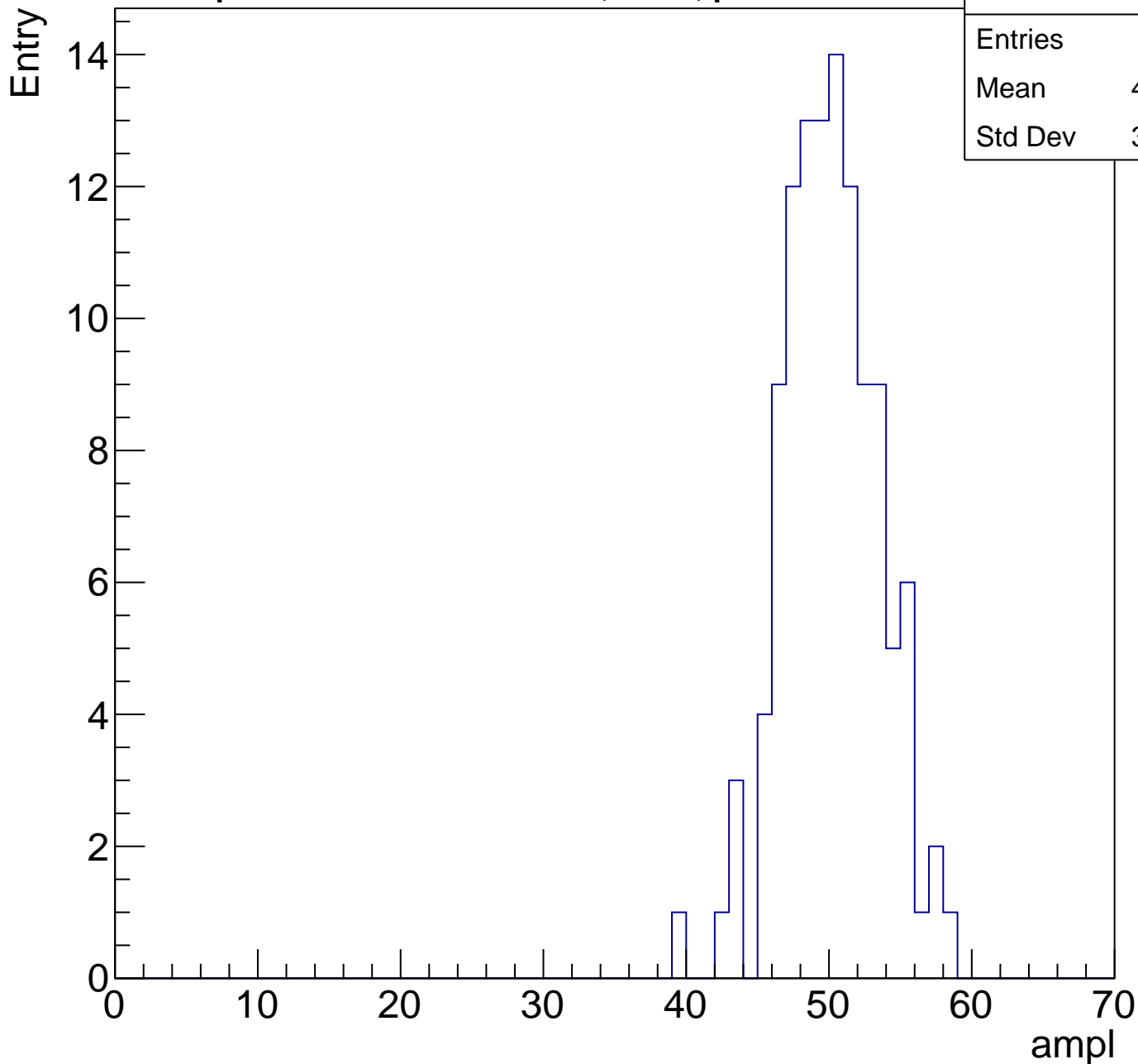
Entry



B1L001S, U26-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 115 |
| Mean | 49.69 |
| Std Dev | 3.355 |



B1L001S, U26-ch115, adc4

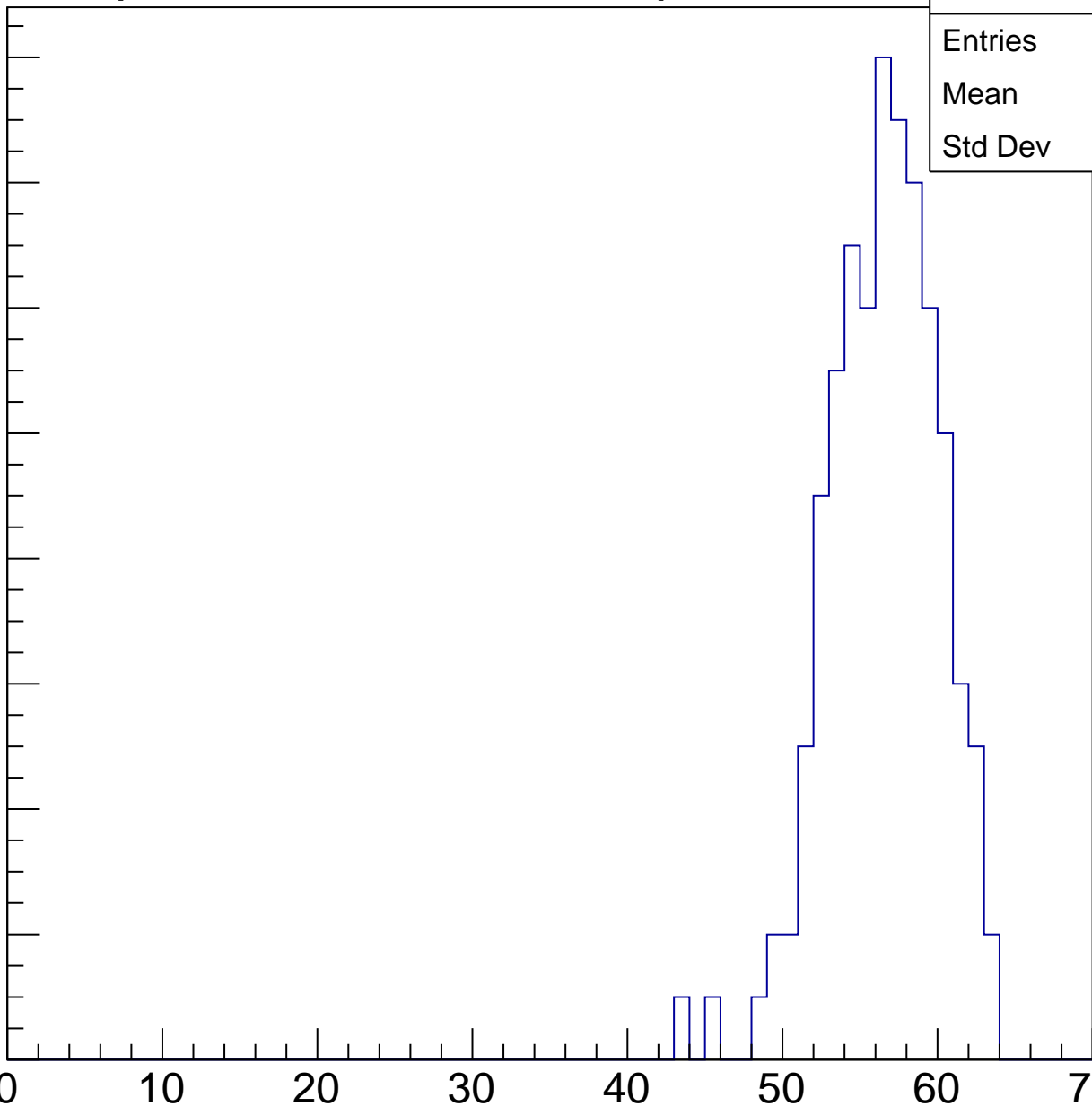
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 137 |
| Mean | 56.02 |
| Std Dev | 3.546 |

ampl



B1L001S, U26-ch115, adc5

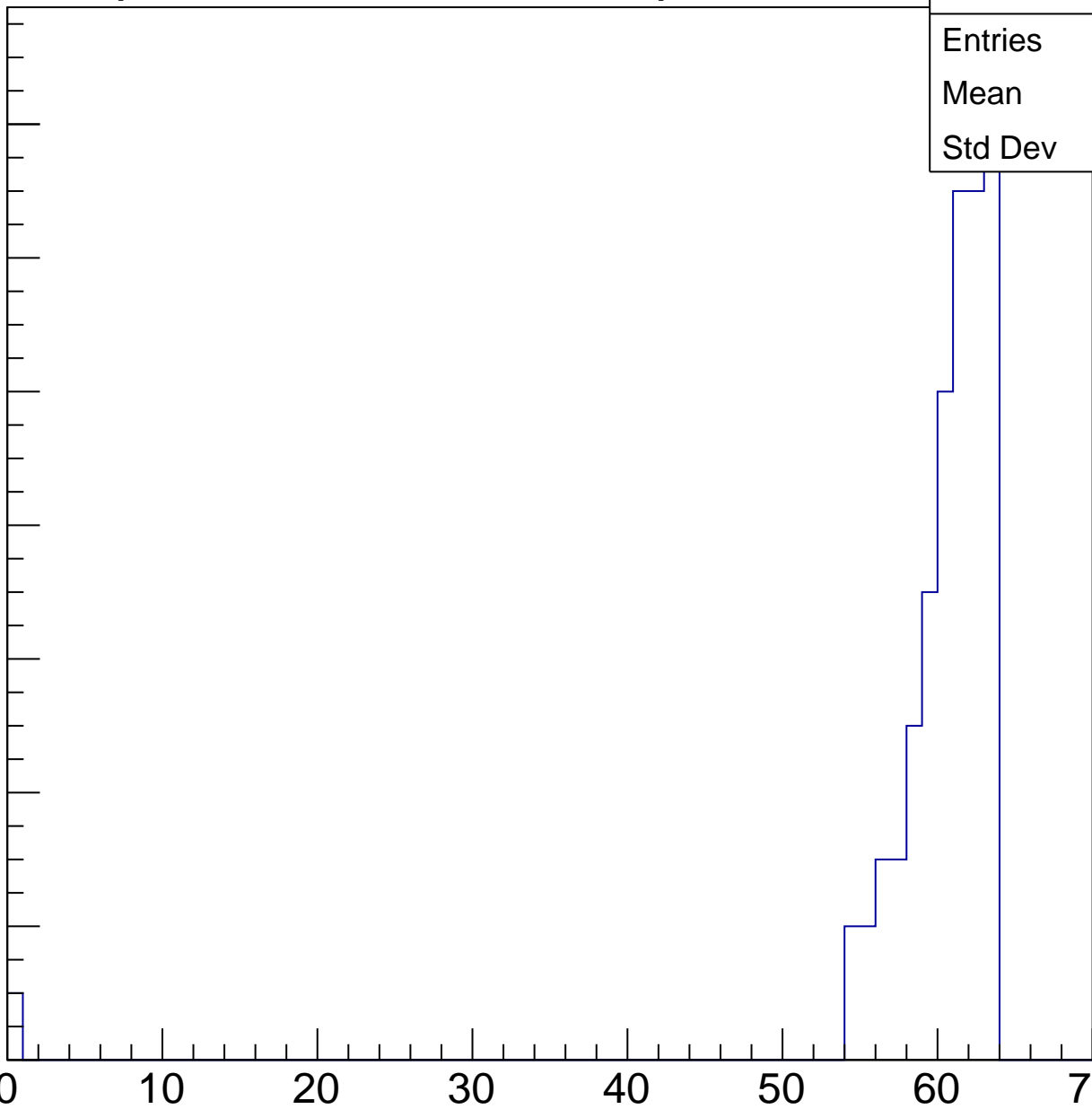
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 74 |
| Mean | 59.51 |
| Std Dev | 7.358 |

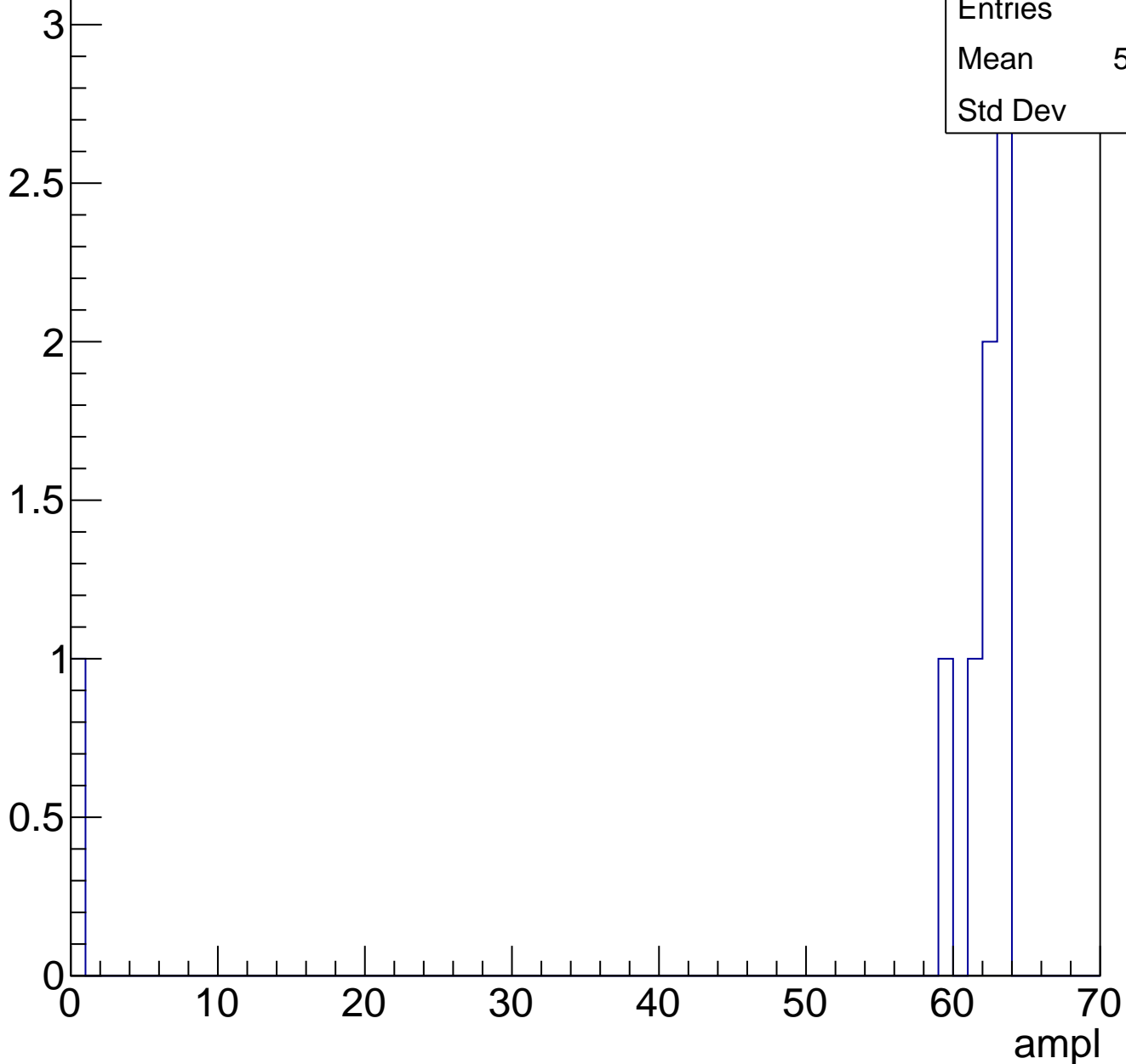
ampl



B1L001S, U26-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

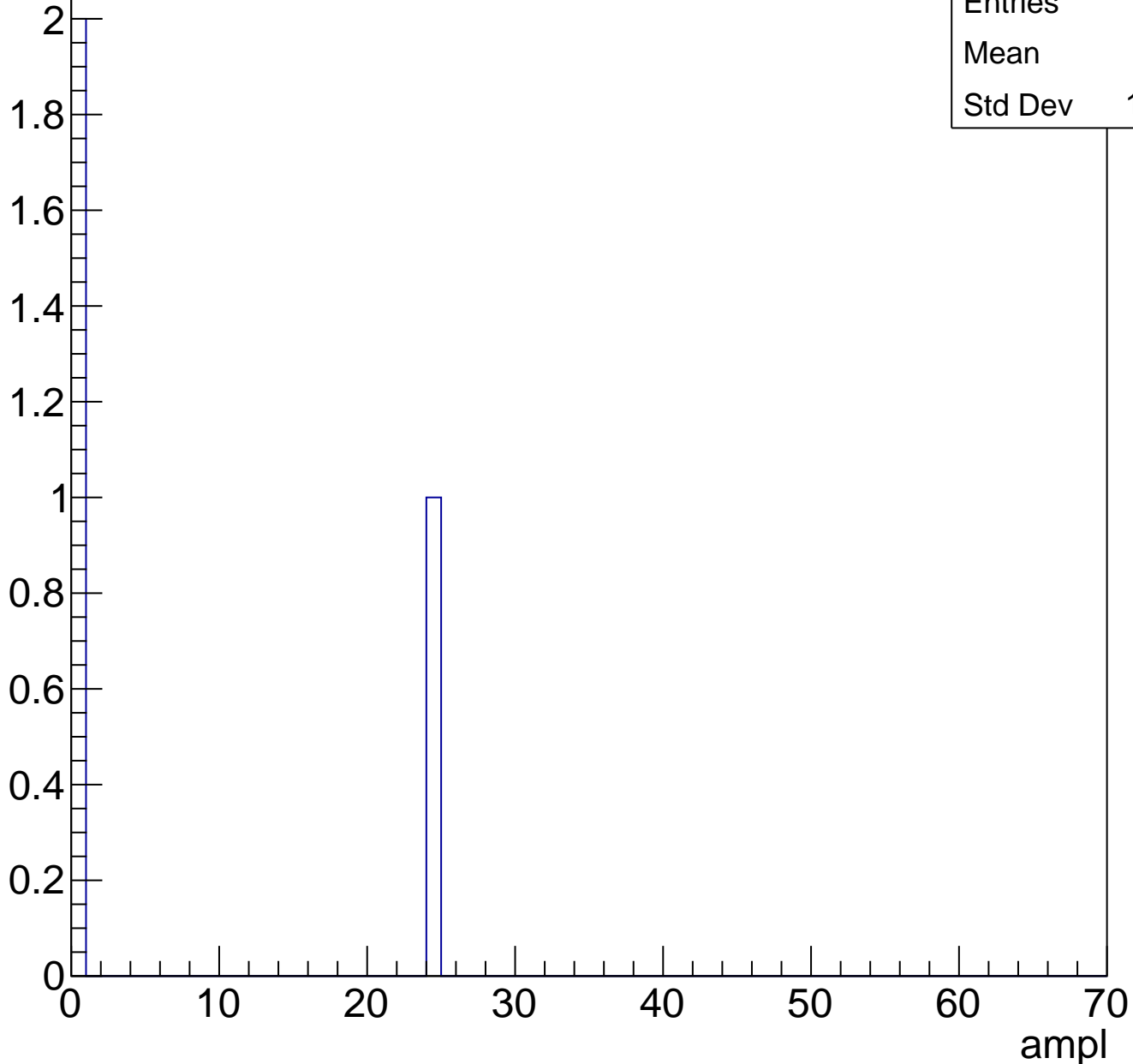
Entry



B1L001S, U26-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 8 |
| Std Dev | 11.31 |

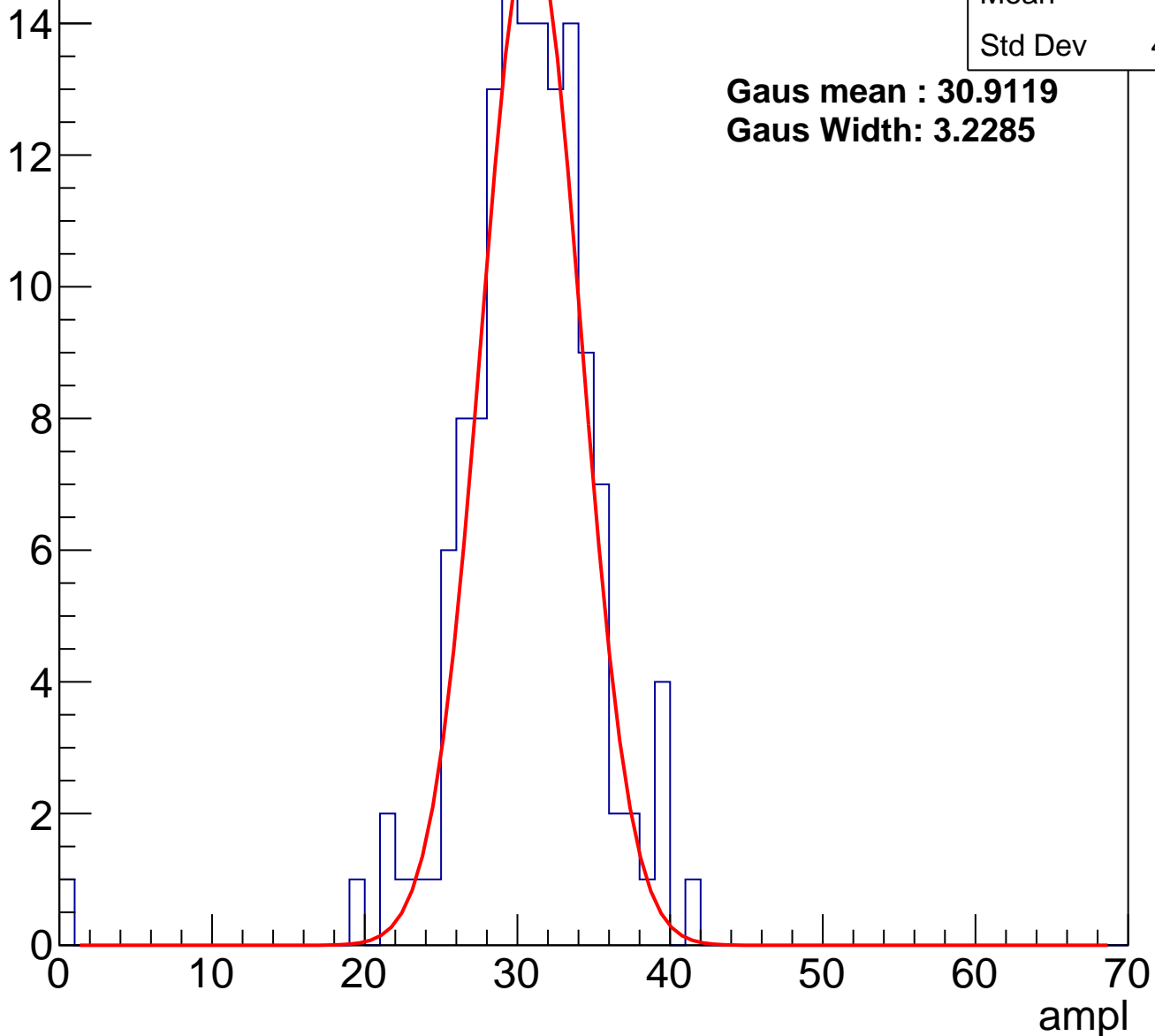
B1L001S, U26-ch116, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 30.2 |
| Std Dev | 4.611 |

Gaus mean : 30.9119
Gaus Width: 3.2285

Entry



B1L001S, U26-ch116, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 143 |
| Mean | 37.02 |
| Std Dev | 3.59 |

Gaus mean : 37.2108

Gaus Width: 3.3367

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

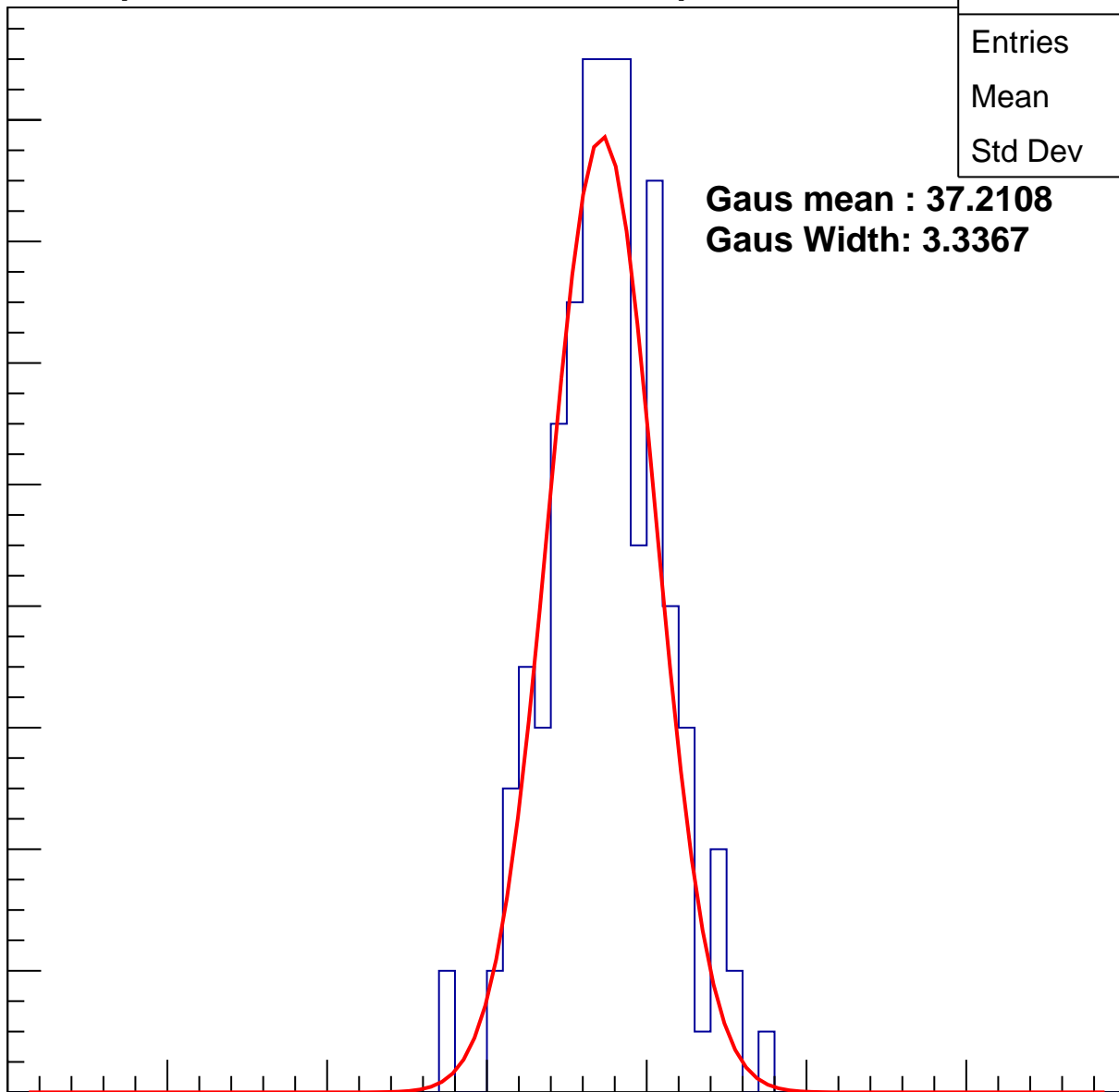
40

50

60

70

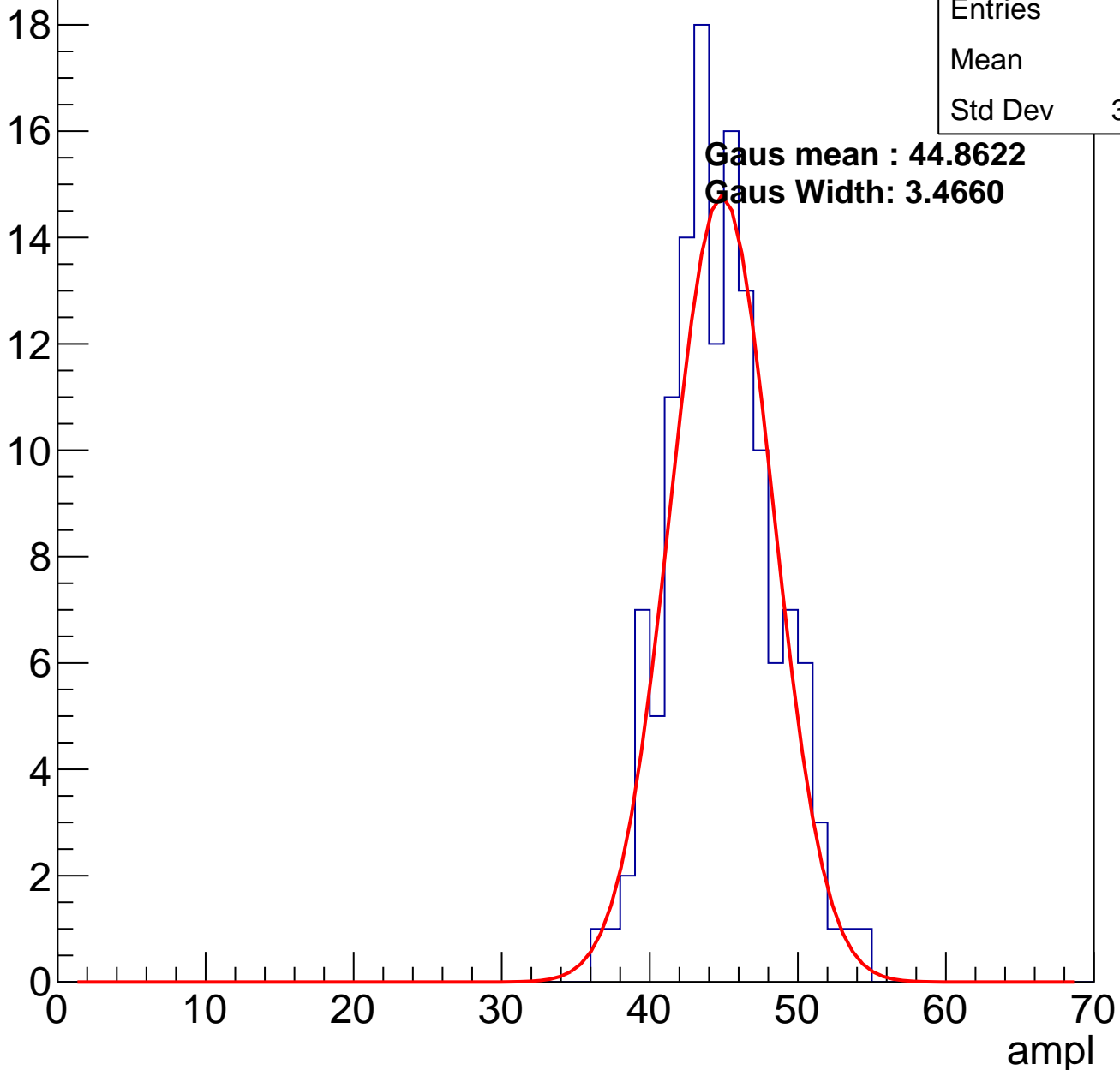
ampl



B1L001S, U26-ch116, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

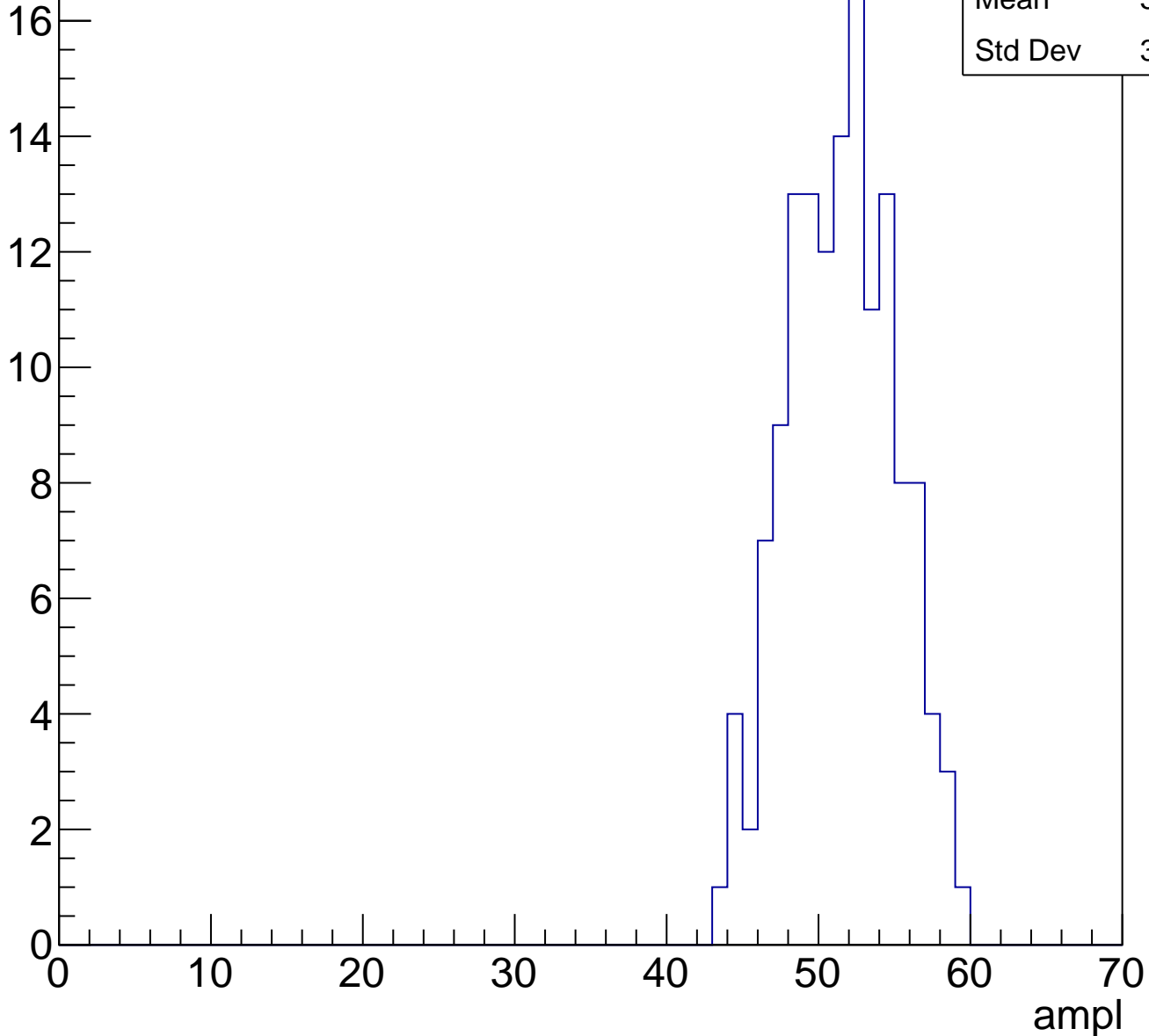


B1L001S, U26-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 140 |
| Mean | 51.05 |
| Std Dev | 3.477 |



B1L001S, U26-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

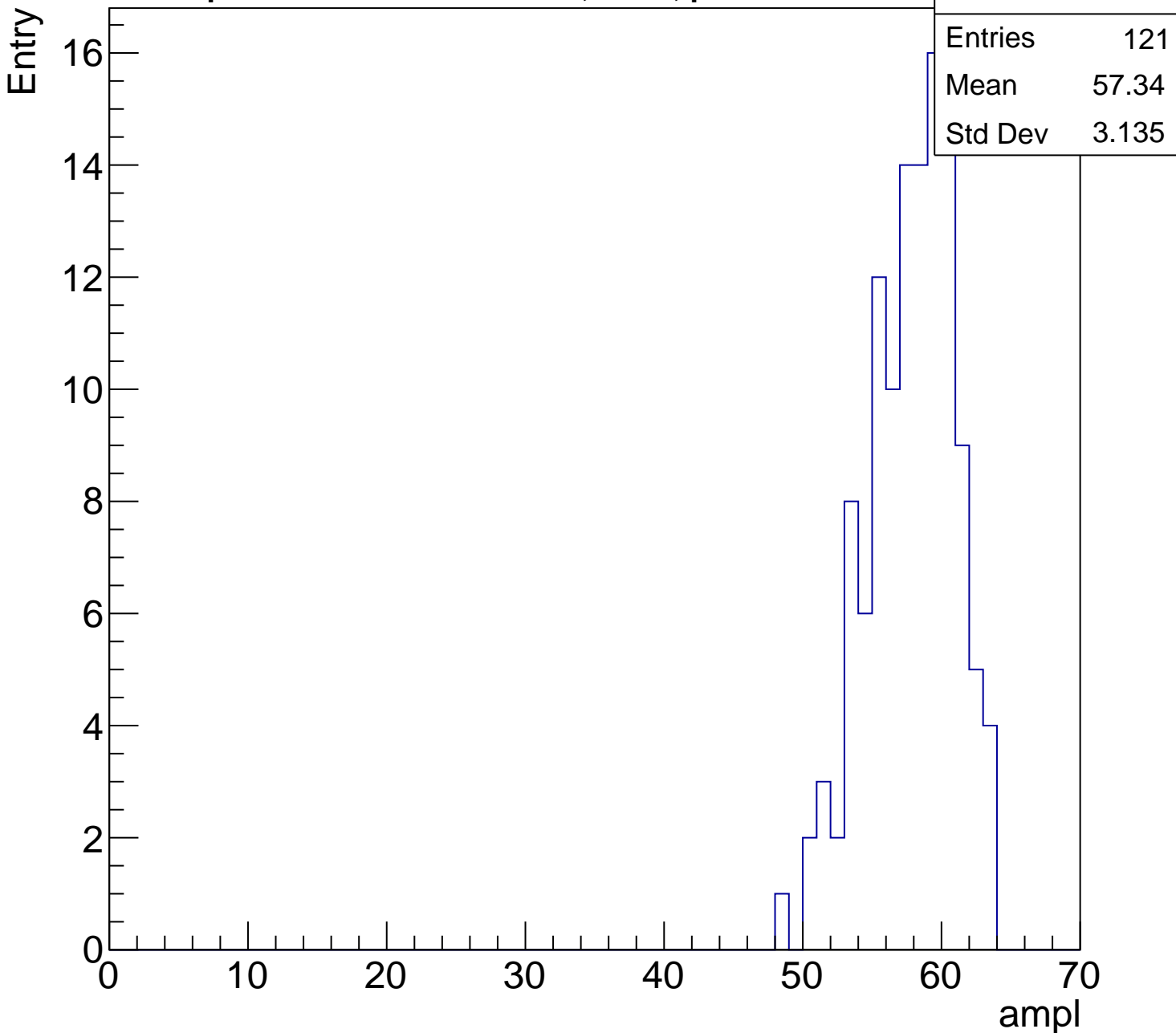
| | |
|---------|-------|
| Entries | 121 |
| Mean | 57.34 |
| Std Dev | 3.135 |

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch116, adc5

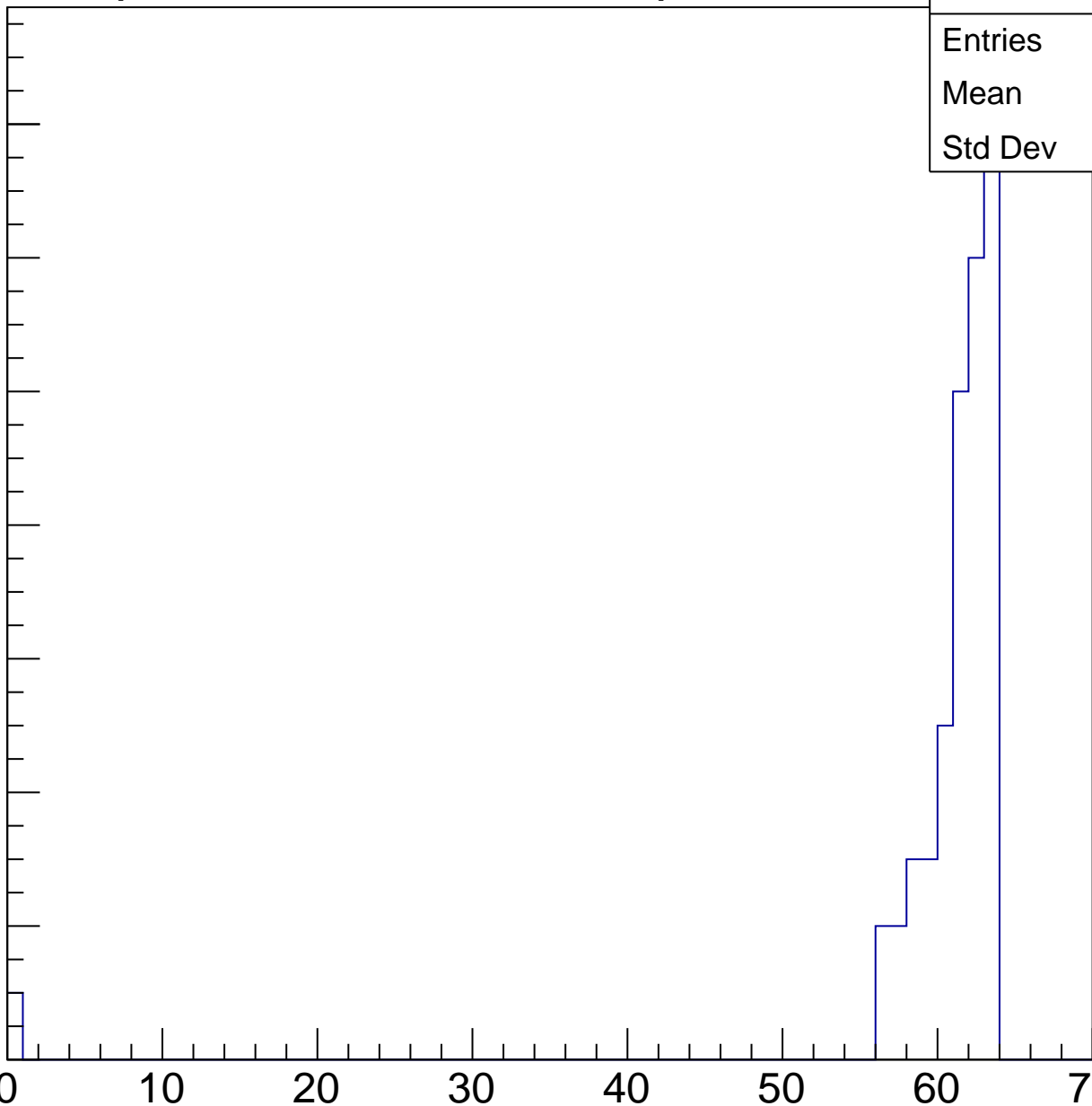
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.92 |
| Std Dev | 8.532 |

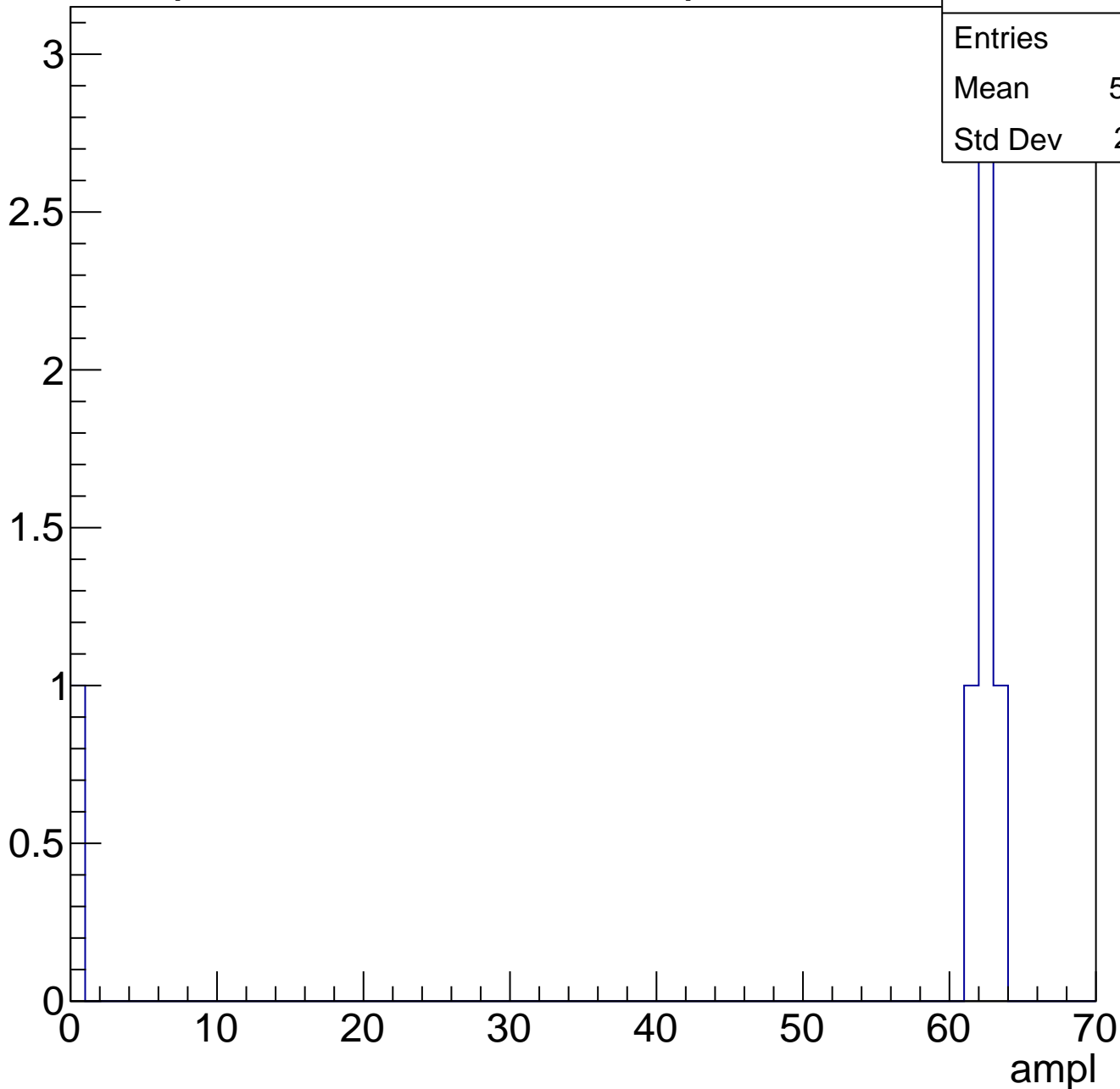
ampl



B1L001S, U26-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch116, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

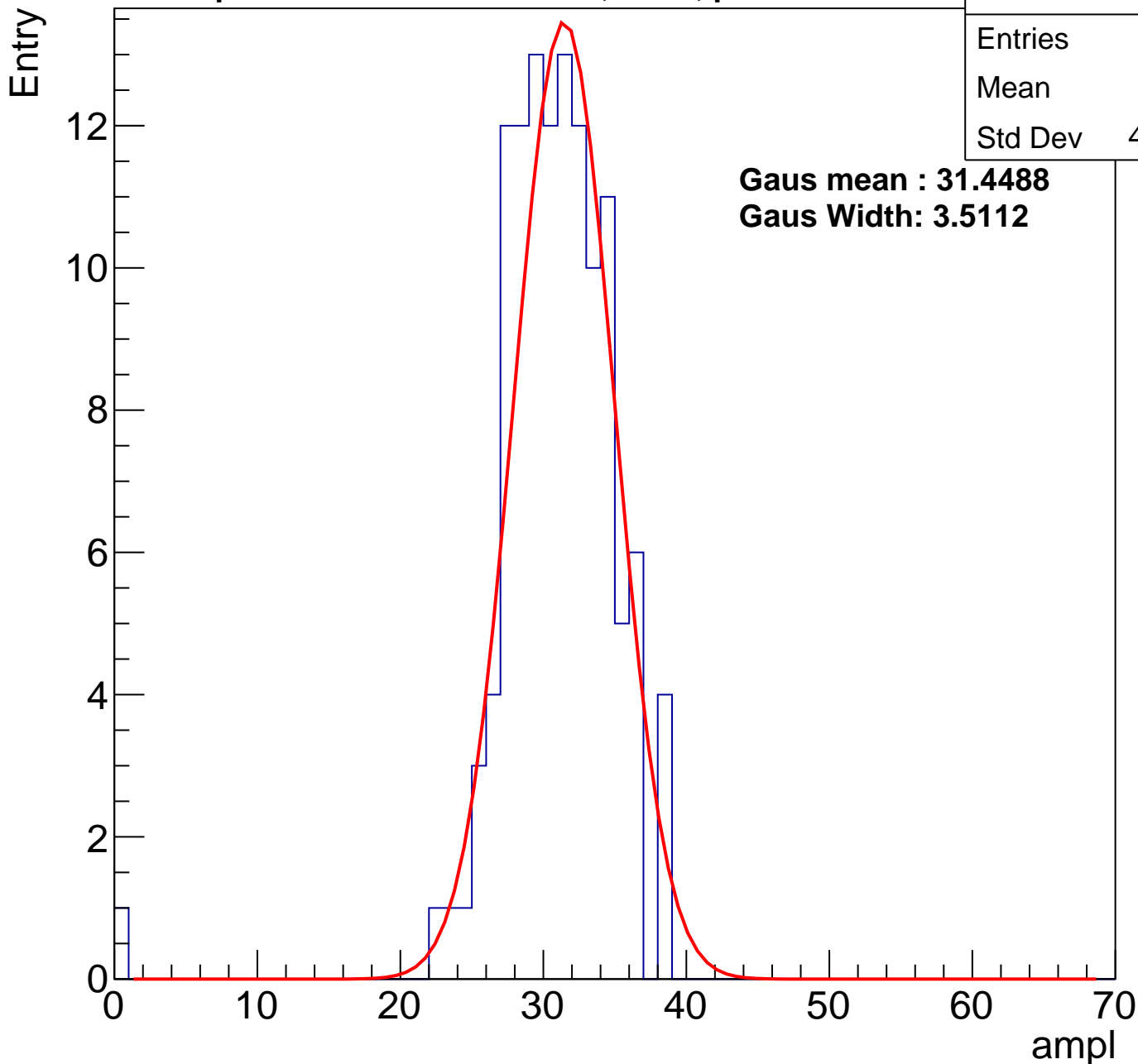
B1L001S, U26-ch117, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 30.4 |
| Std Dev | 4.323 |

Gaus mean : 31.4488

Gaus Width: 3.5112



B1L001S, U26-ch117, adc1

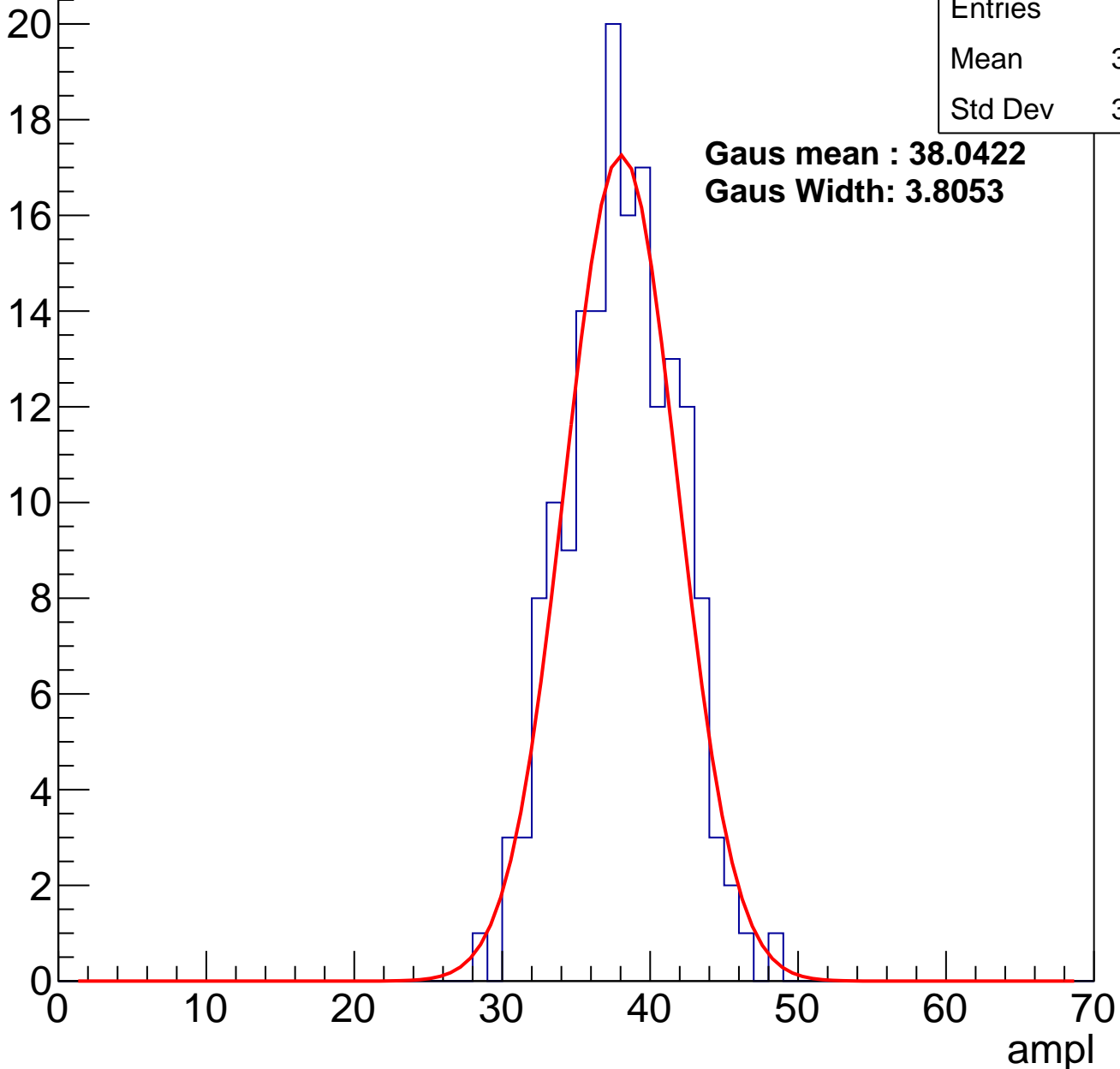
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 167 |
| Mean | 37.63 |
| Std Dev | 3.666 |

Gaus mean : 38.0422

Gaus Width: 3.8053

Entry



B1L001S, U26-ch117, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

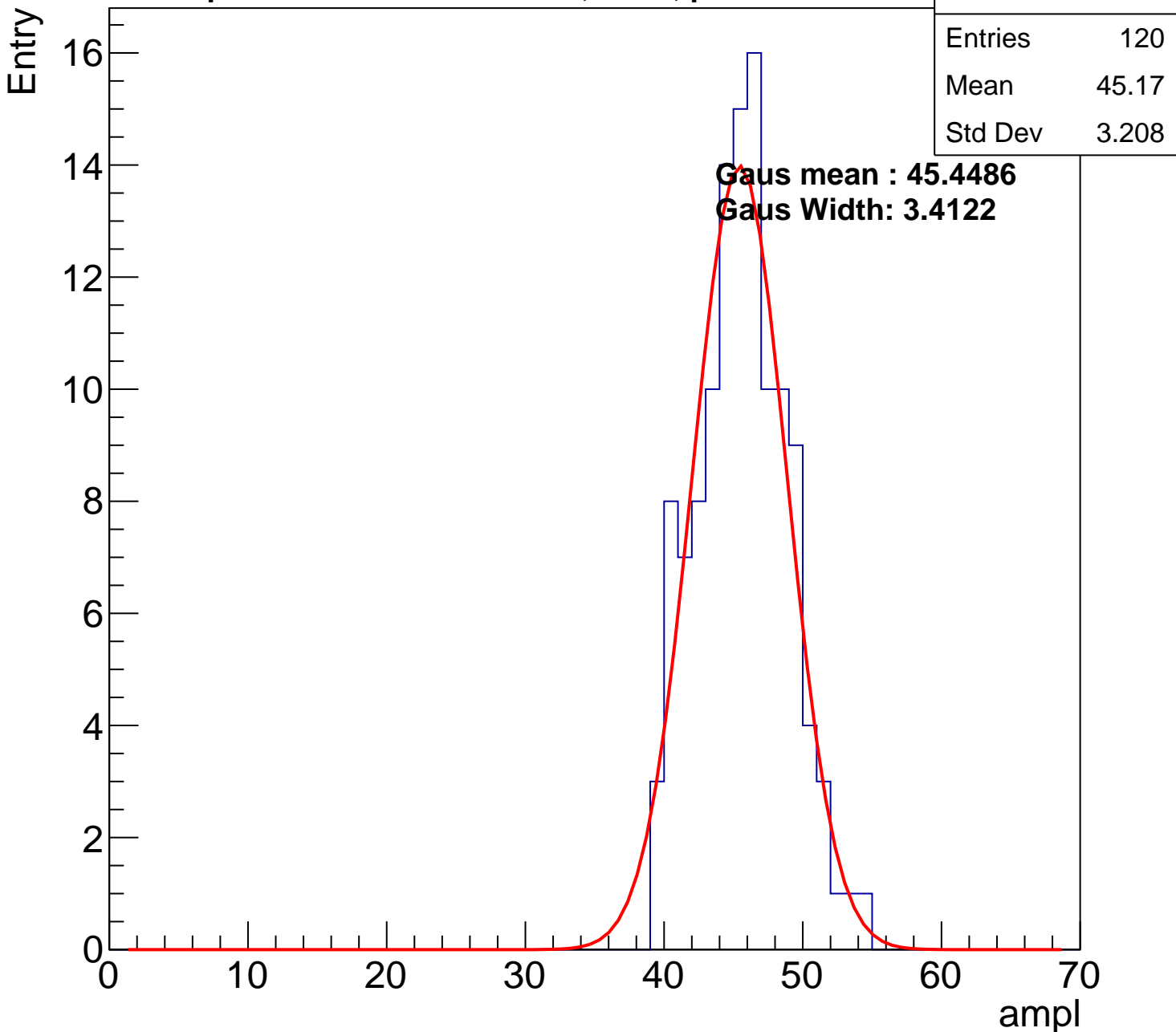
| | |
|---------|-------|
| Entries | 120 |
| Mean | 45.17 |
| Std Dev | 3.208 |

Gaus mean : 45.4486

Gaus Width: 3.4122

ampl

0 10 20 30 40 50 60 70

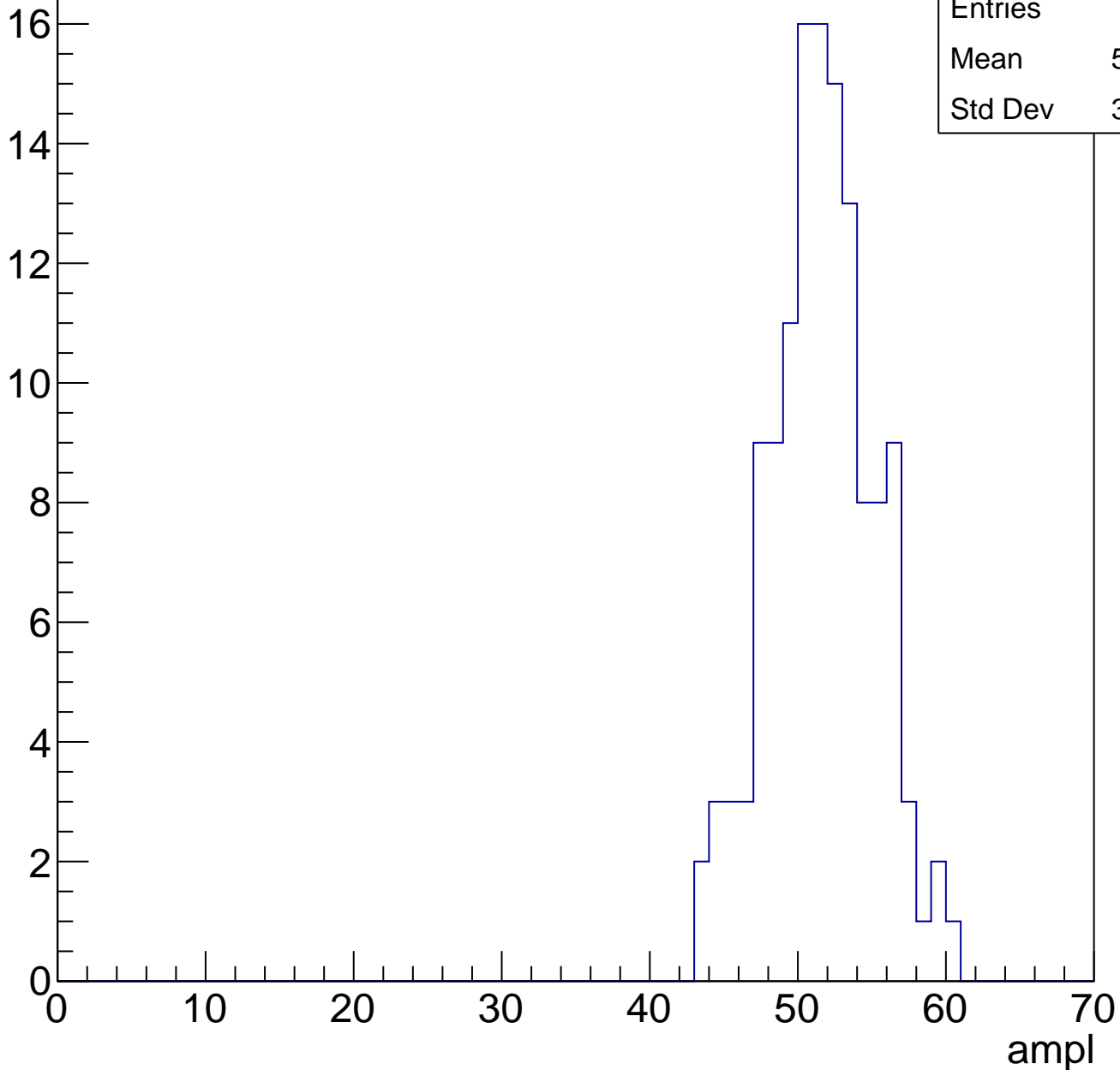


B1L001S, U26-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 132 |
| Mean | 51.16 |
| Std Dev | 3.479 |



B1L001S, U26-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 57.51 |
| Std Dev | 3.177 |

Entry

12

10

8

6

4

2

0

0

10

20

30

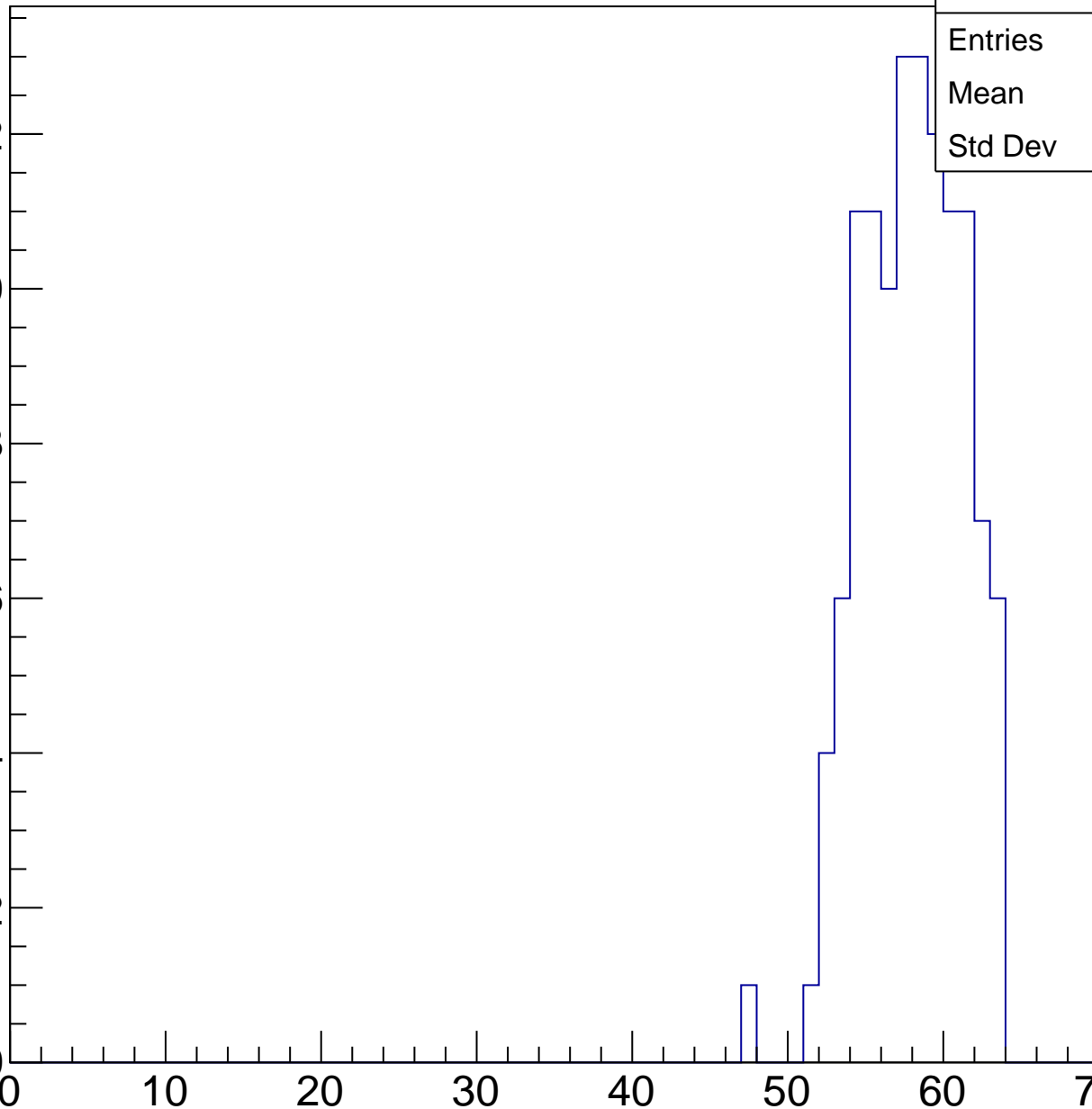
40

50

60

70

ampl



B1L001S, U26-ch117, adc5

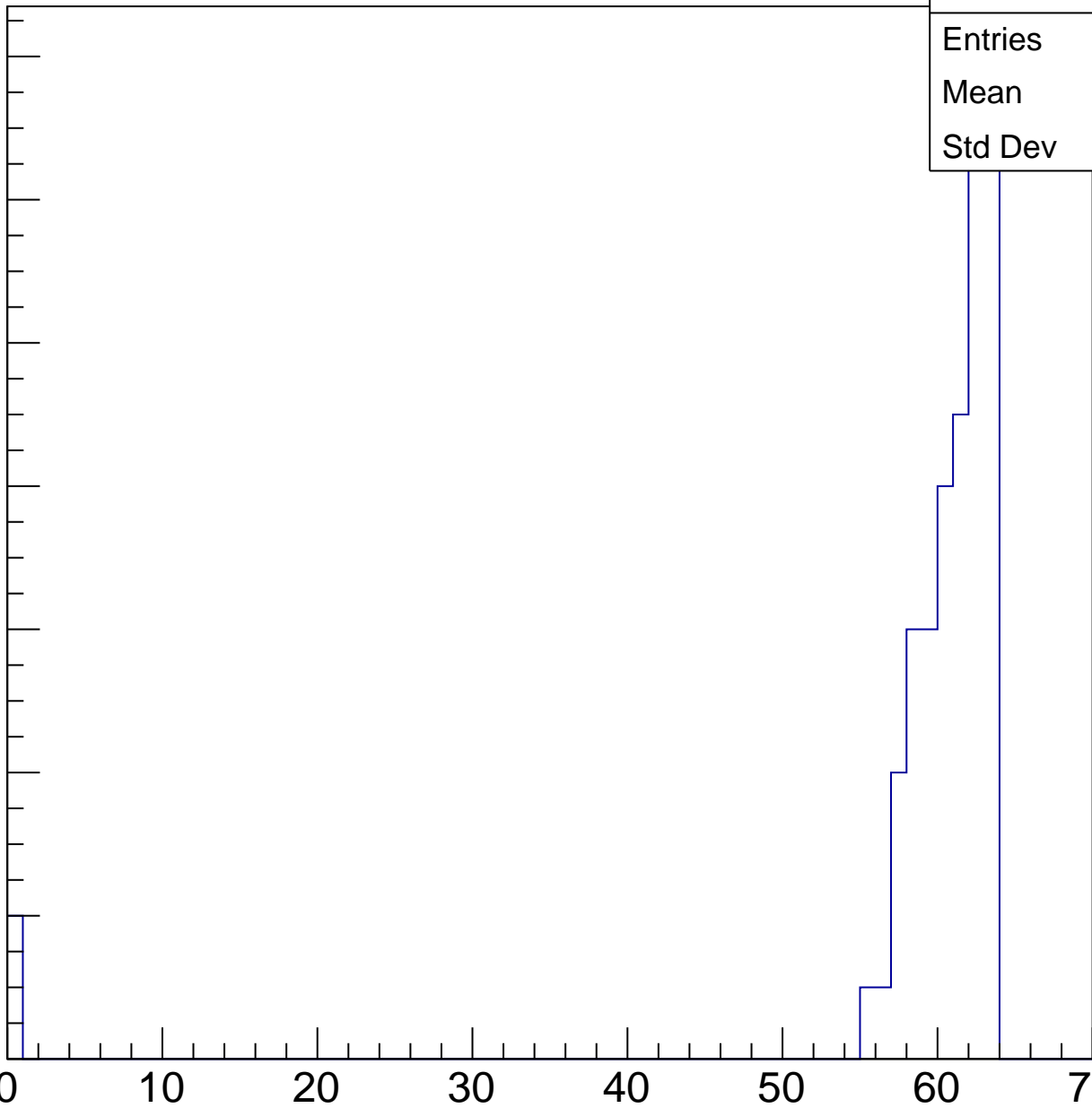
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 58.72 |
| Std Dev | 10.74 |

ampl



B1L001S, U26-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch118, adc0

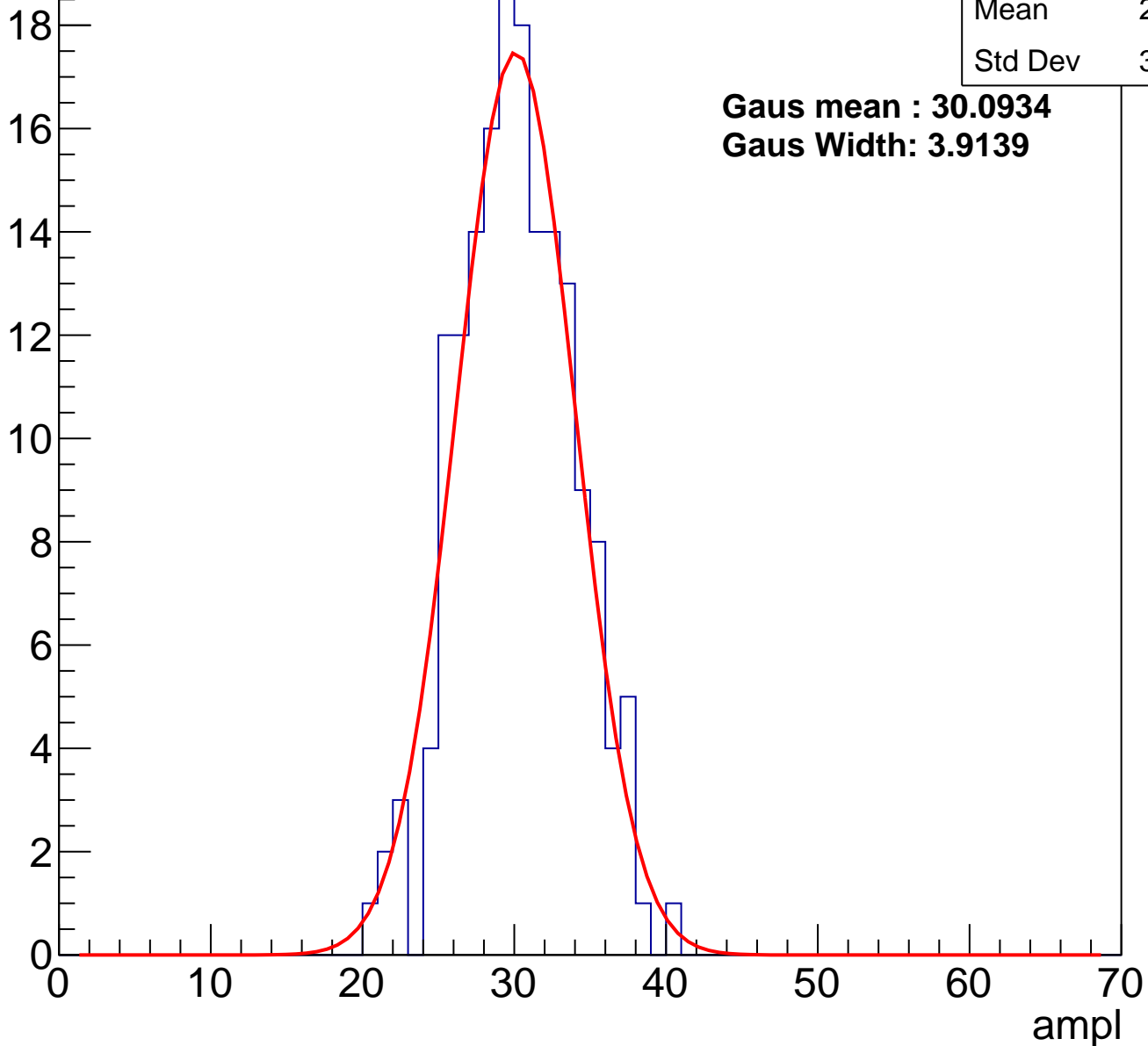
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 170 |
| Mean | 29.75 |
| Std Dev | 3.735 |

Gaus mean : 30.0934

Gaus Width: 3.9139

Entry



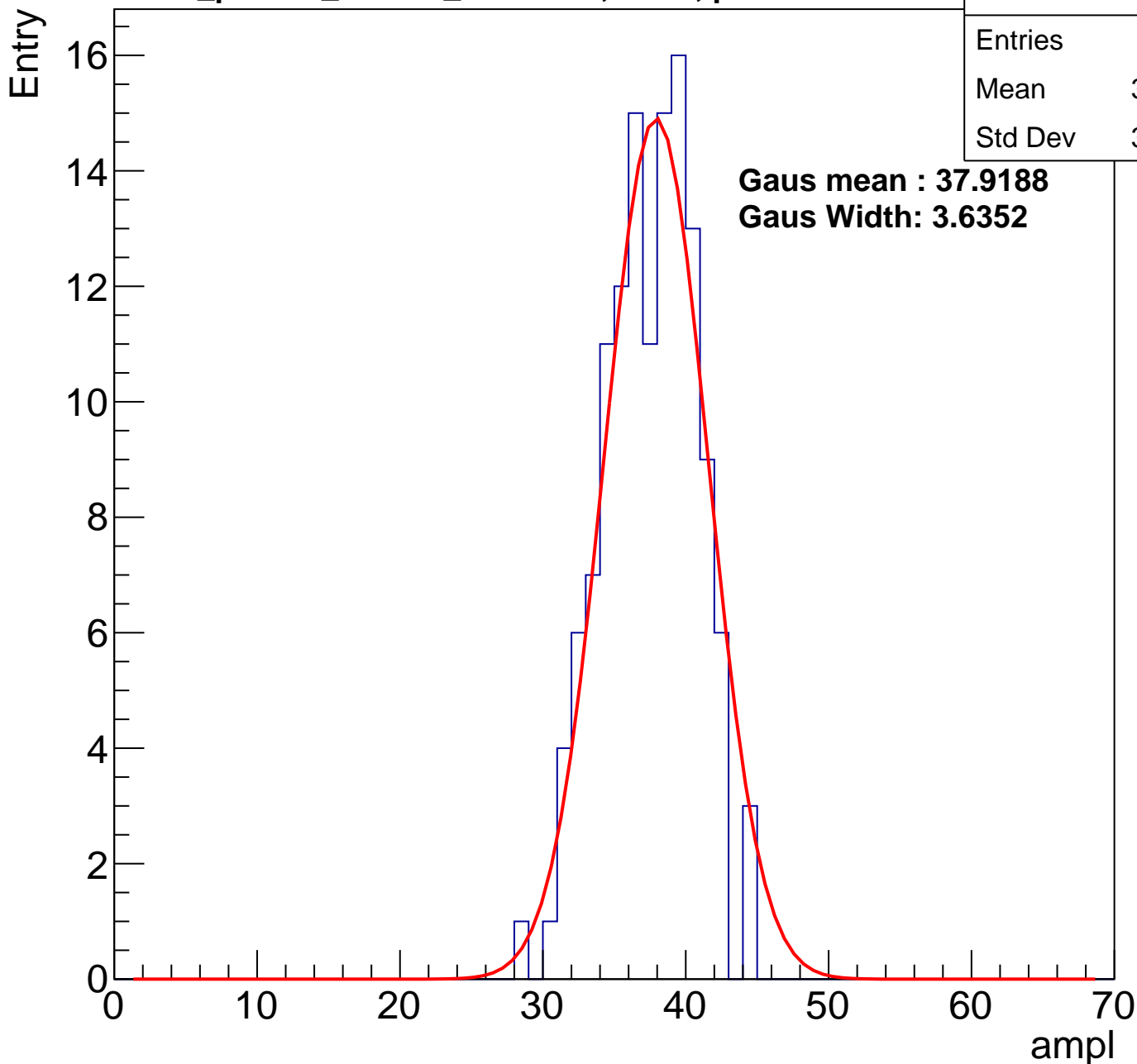
B1L001S, U26-ch118, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 130 |
| Mean | 37.02 |
| Std Dev | 3.204 |

Gaus mean : 37.9188

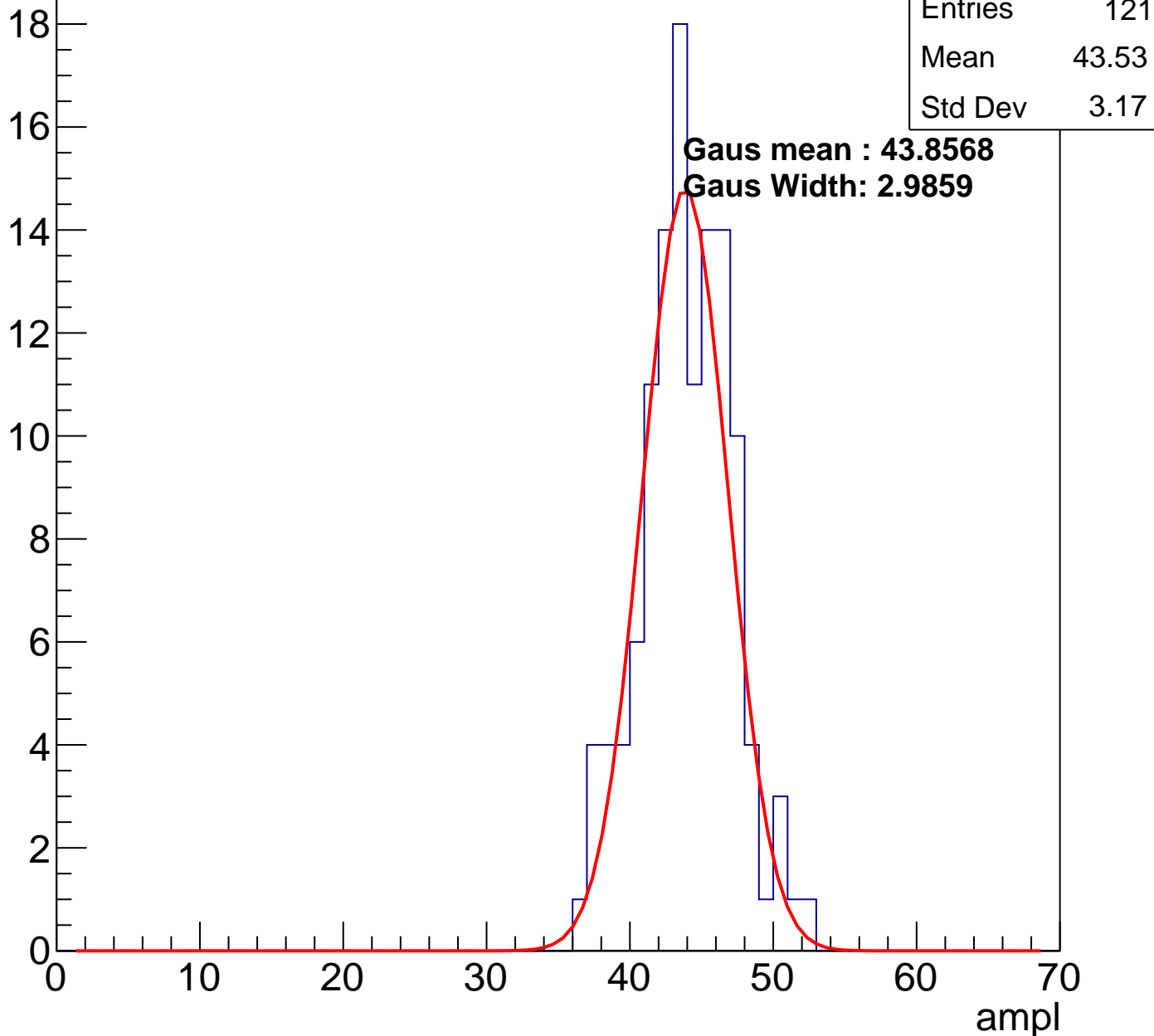
Gaus Width: 3.6352



B1L001S, U26-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

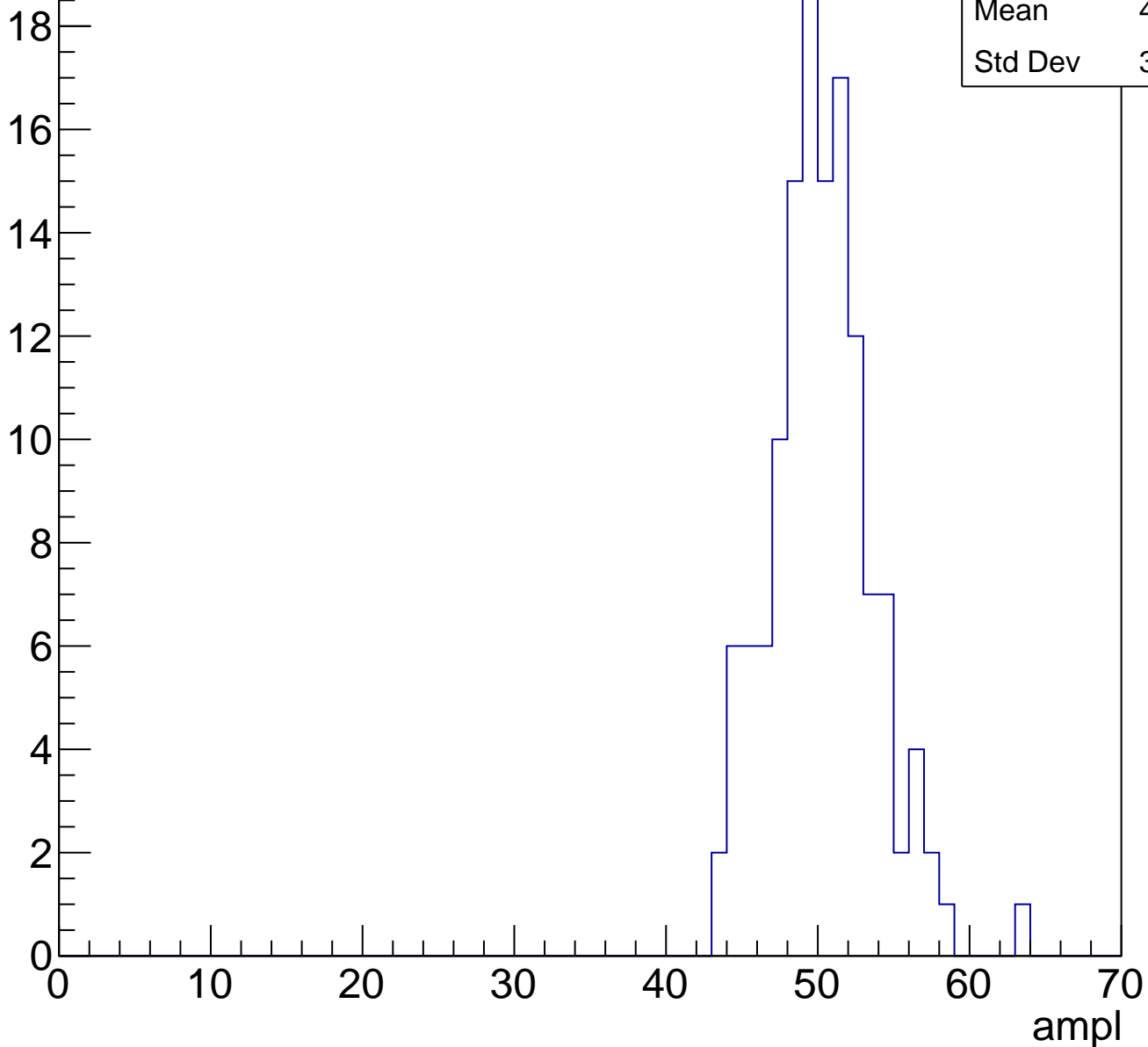


B1L001S, U26-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 49.82 |
| Std Dev | 3.359 |

Entry



B1L001S, U26-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

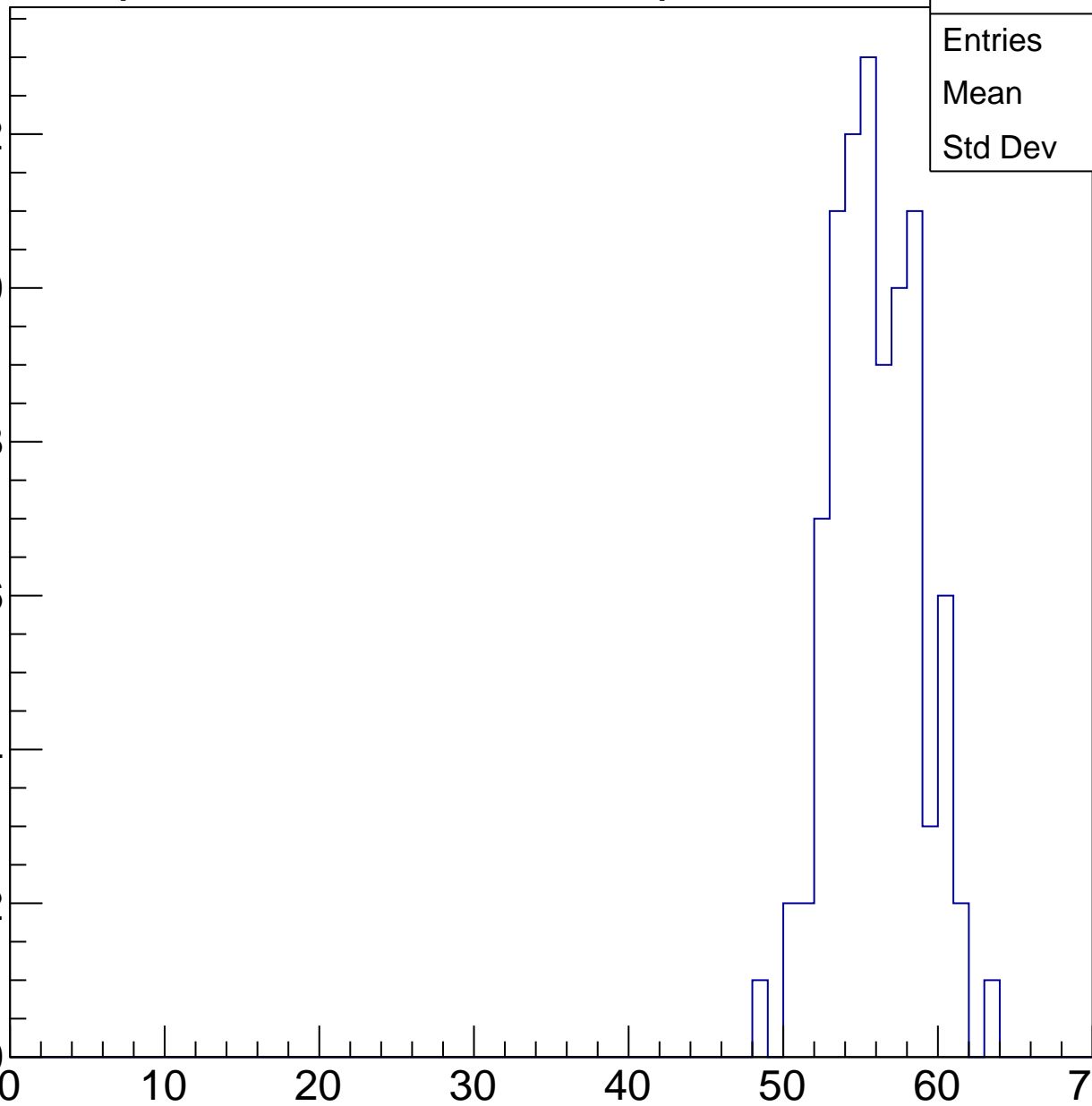
40

50

60

ampl

| | |
|---------|-------|
| Entries | 90 |
| Mean | 55.49 |
| Std Dev | 2.81 |



B1L001S, U26-ch118, adc5

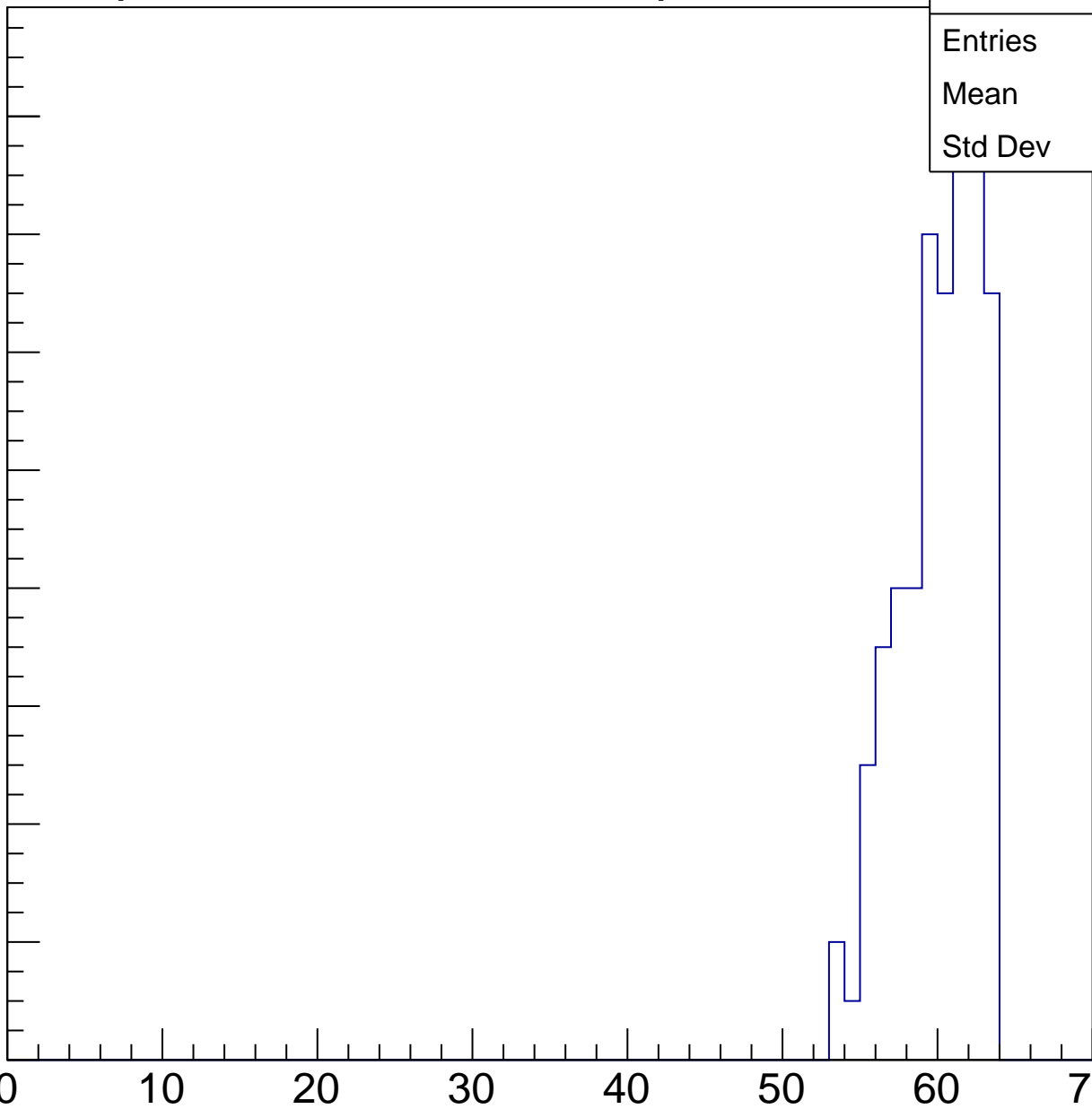
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 105 |
| Mean | 59.65 |
| Std Dev | 2.541 |

ampl

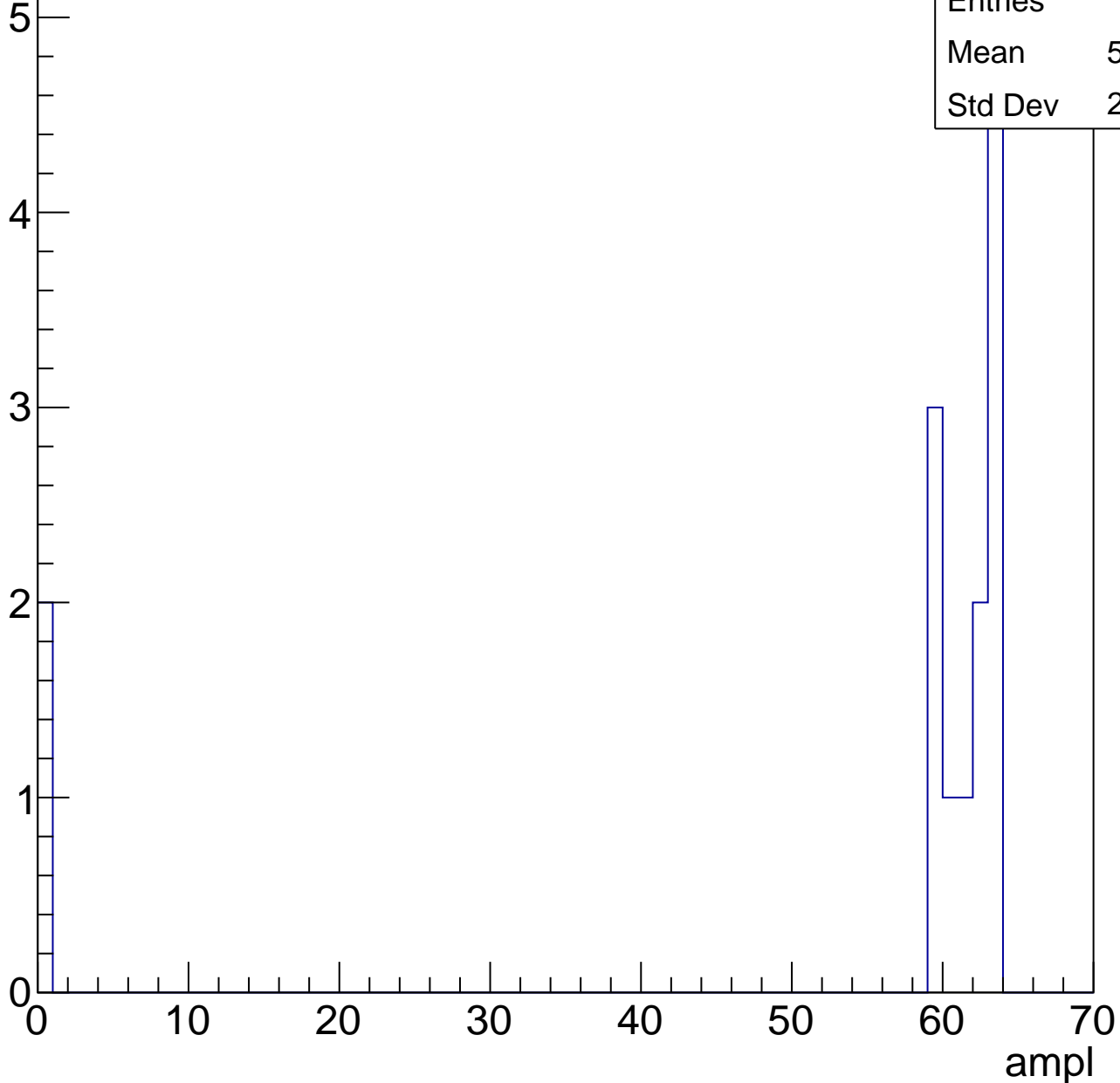


B1L001S, U26-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

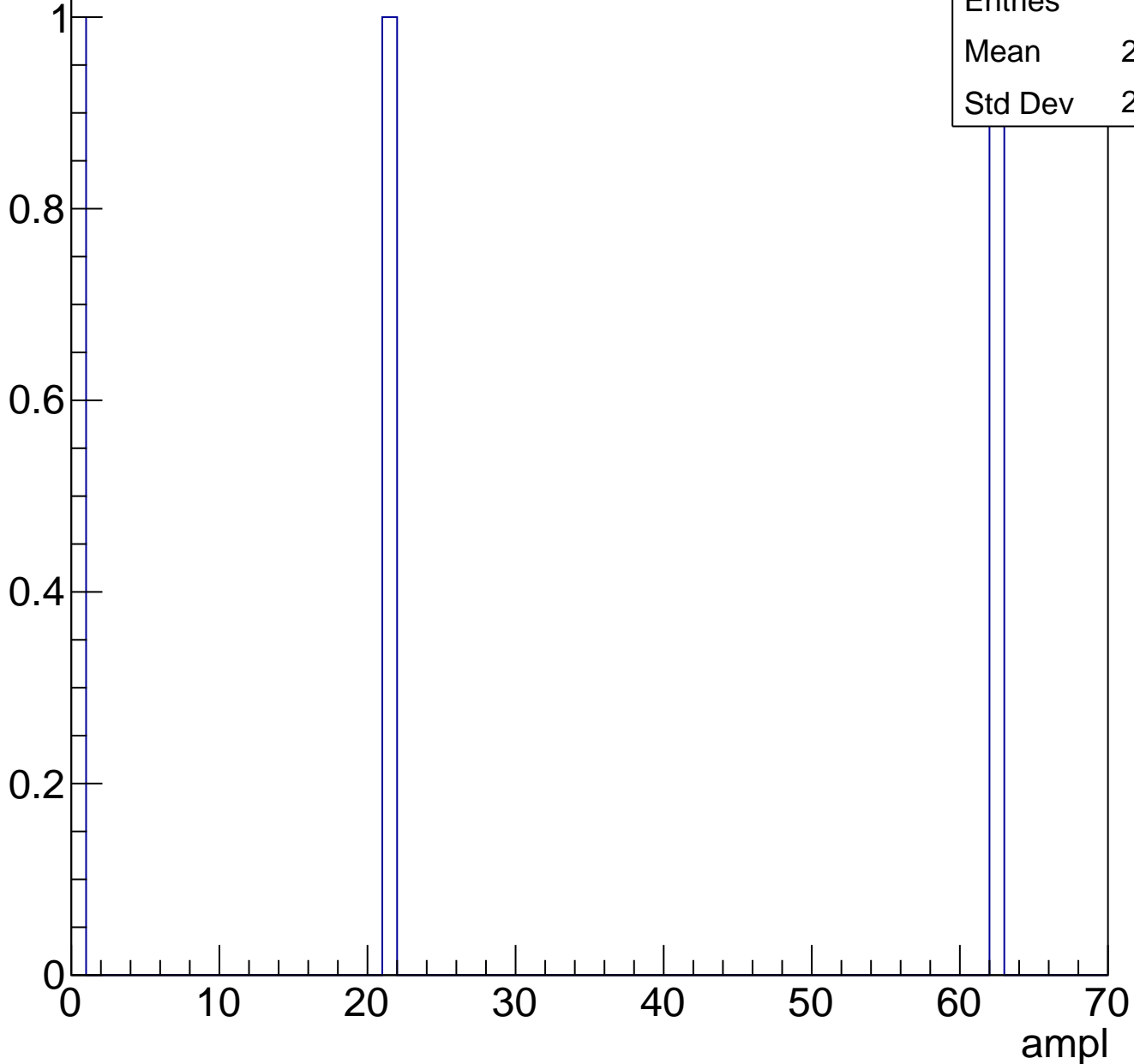
| | |
|---------|-------|
| Entries | 14 |
| Mean | 52.64 |
| Std Dev | 21.55 |



B1L001S, U26-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 27.67 |
| Std Dev | 25.75 |

B1L001S, U26-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 29 |
| Std Dev | 3.764 |

Gaus mean : 29.4509

Gaus Width: 3.8574

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

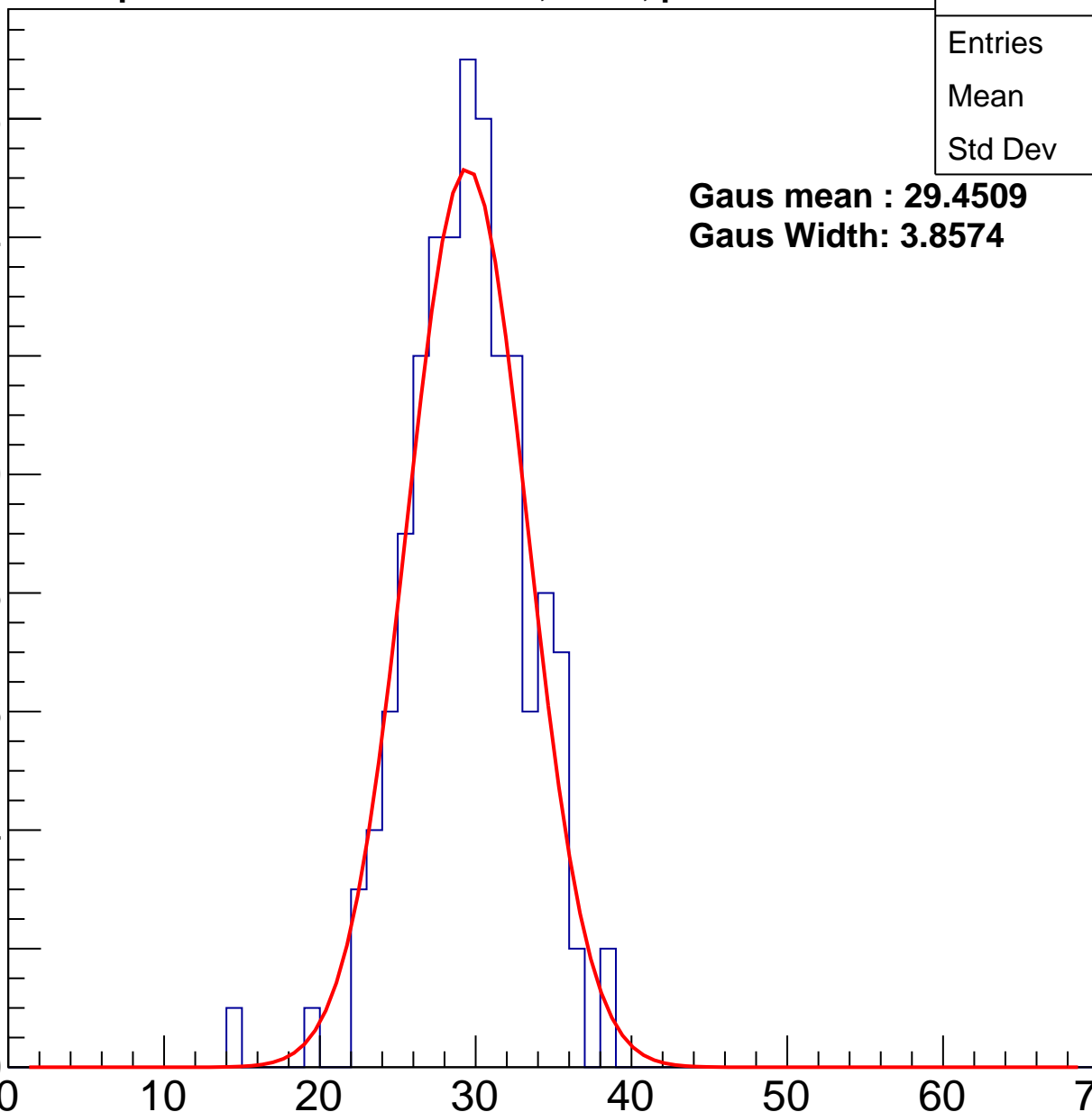
40

50

60

70

ampl



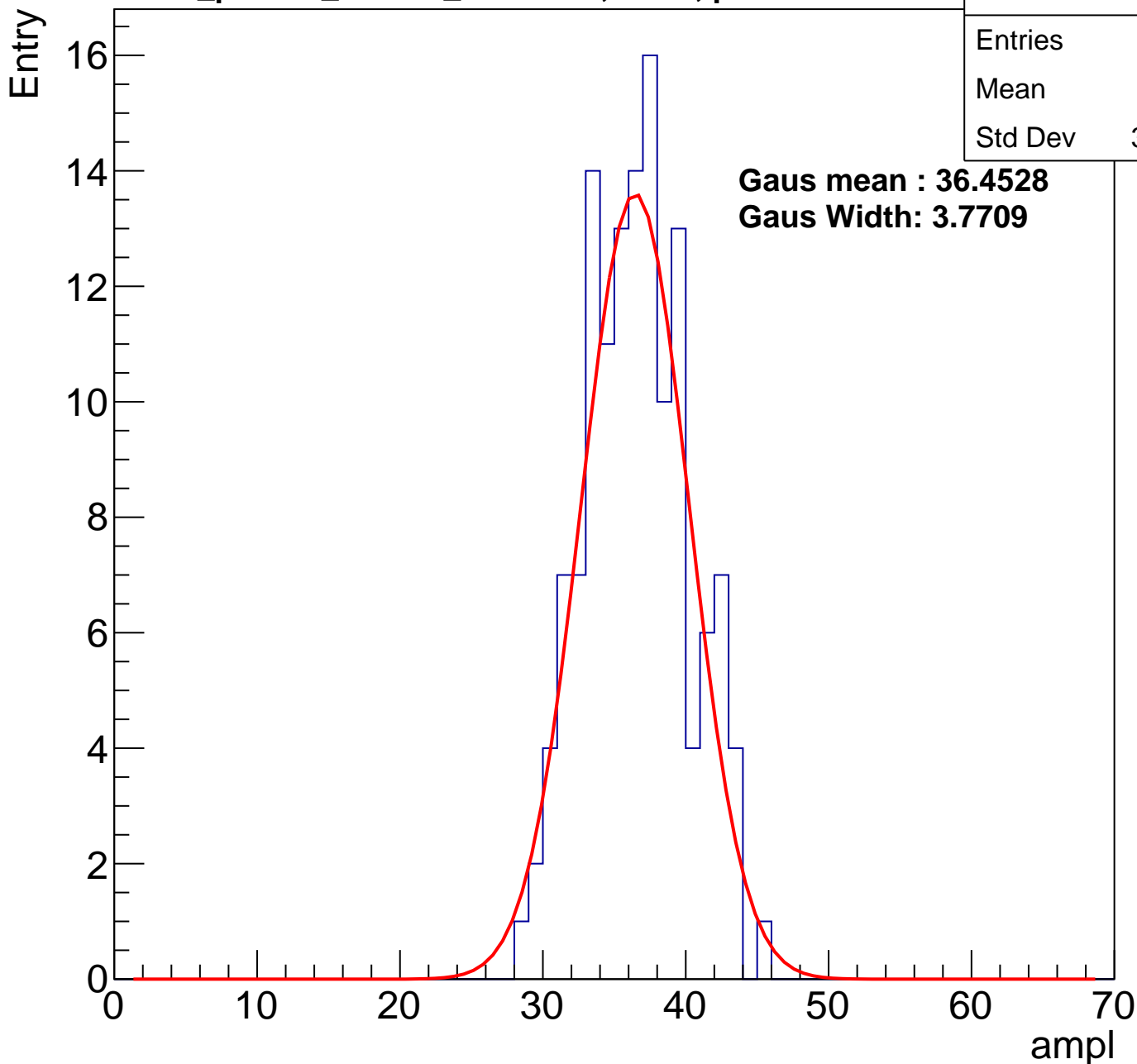
B1L001S, U26-ch119, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 134 |
| Mean | 36.1 |
| Std Dev | 3.552 |

Gaus mean : 36.4528

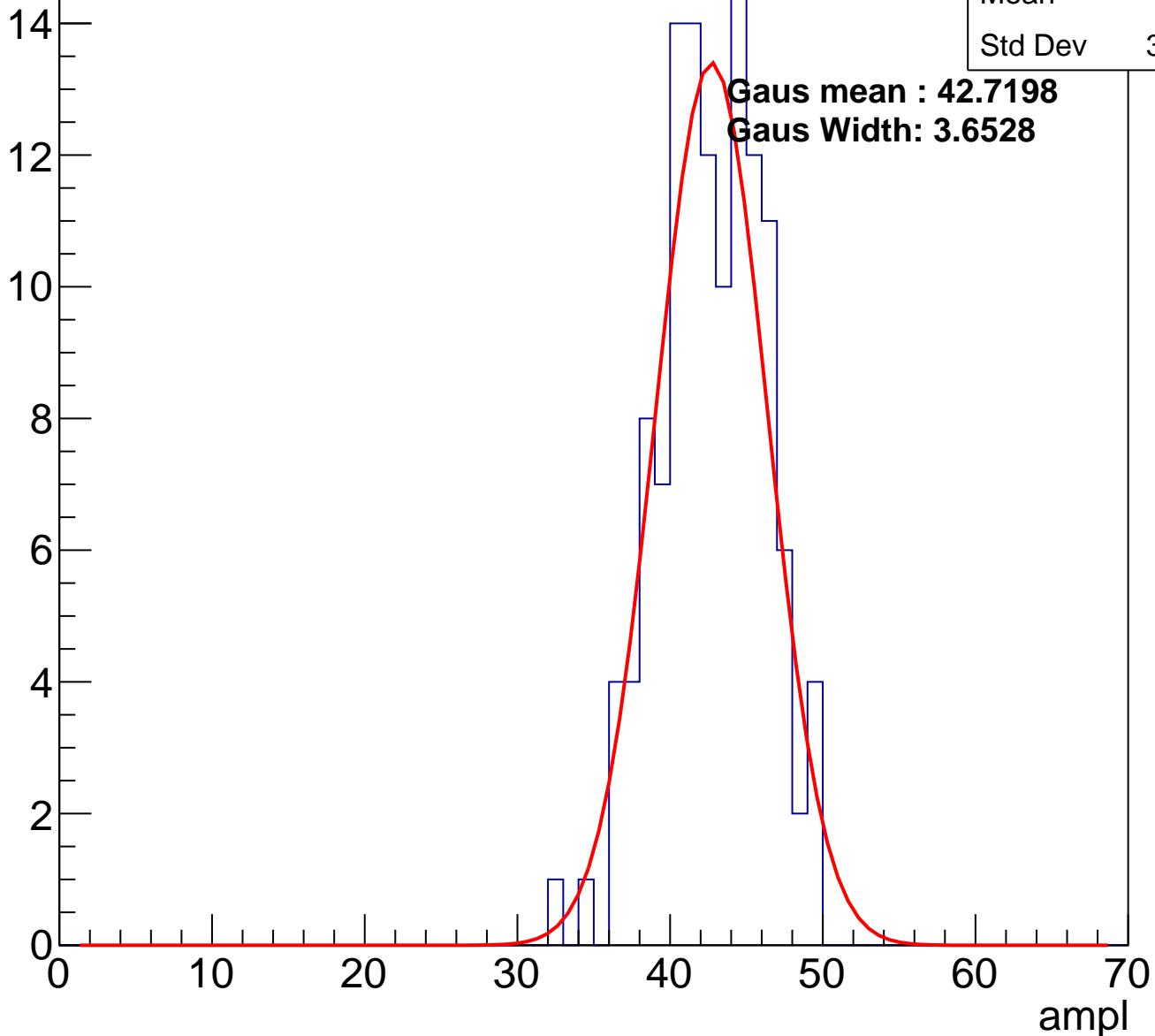
Gaus Width: 3.7709



B1L001S, U26-ch119, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 127 |
| Mean | 49.08 |
| Std Dev | 3.601 |

Entry

14

12

10

8

6

4

2

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0

10

20

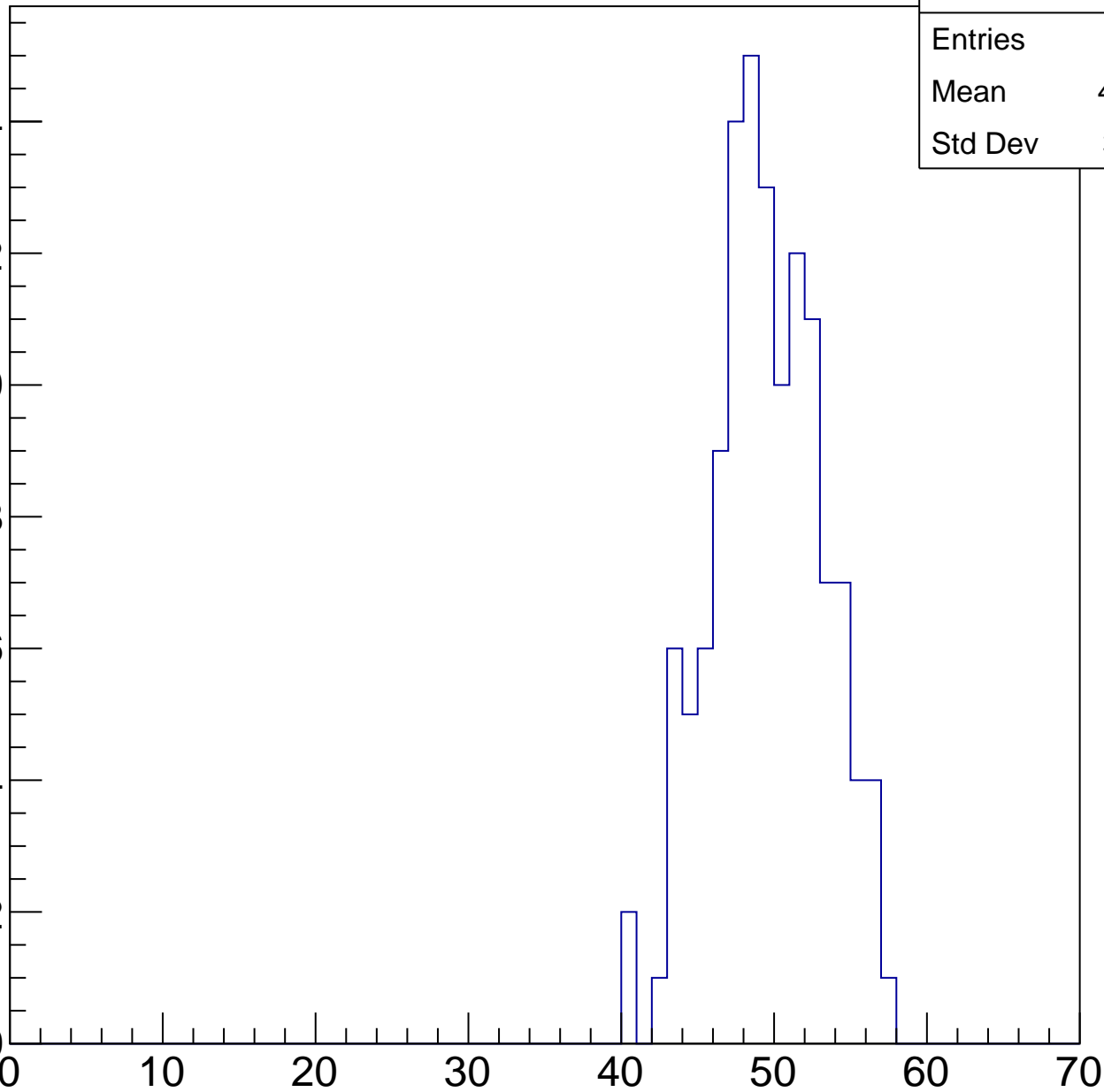
30

40

50

60

ampl



B1L001S, U26-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

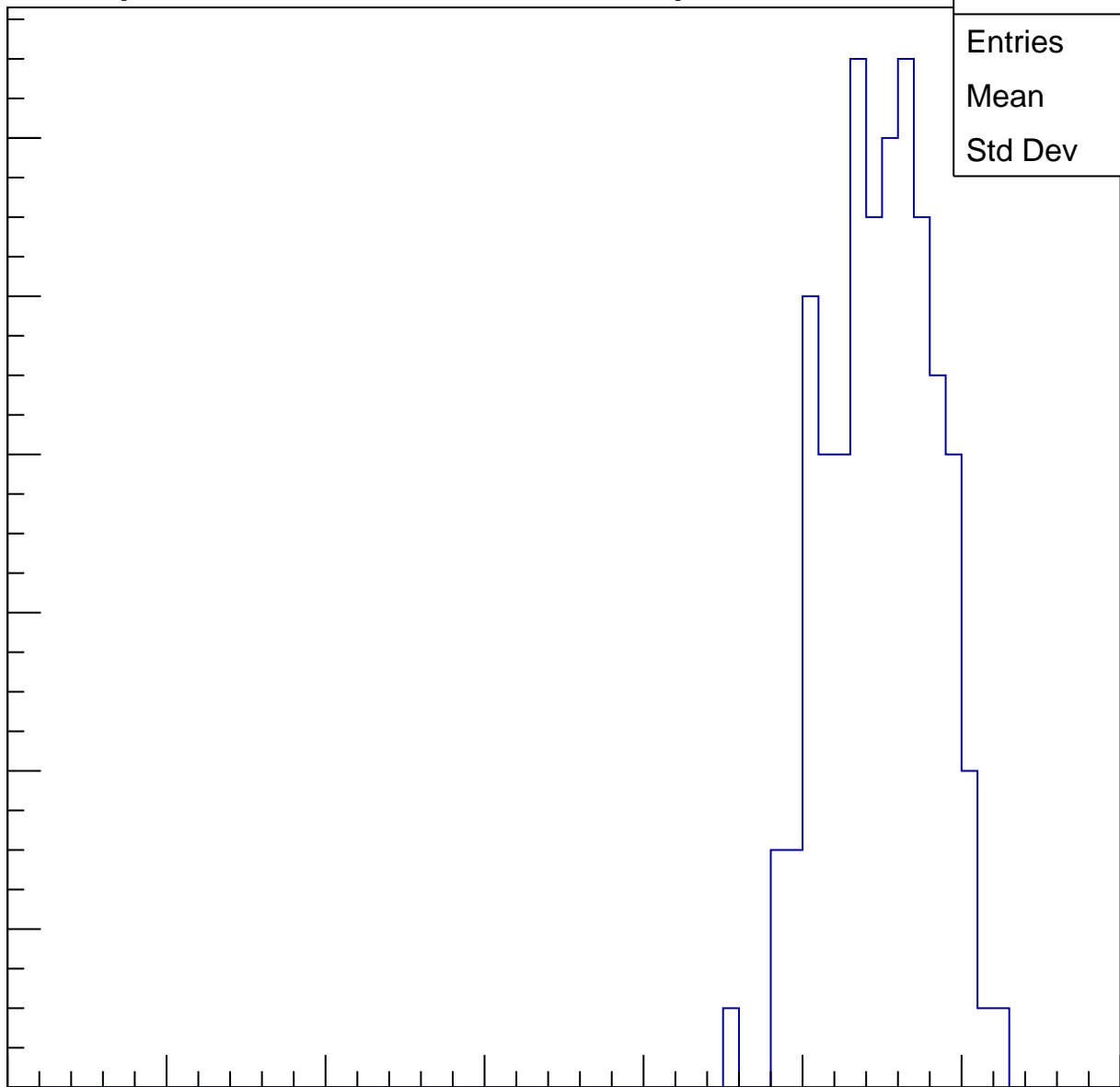
116

Mean

54.44

Std Dev

3.325

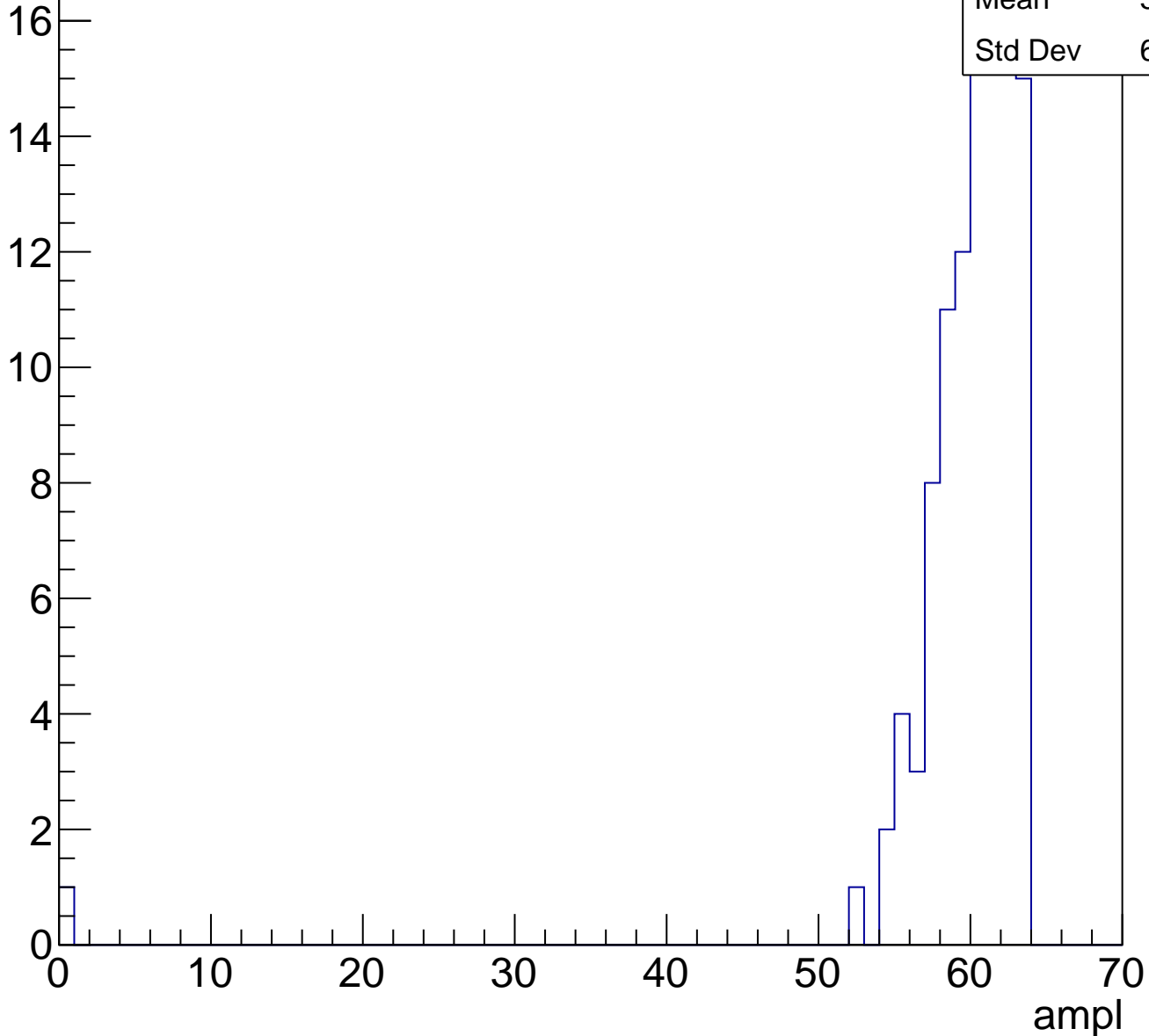


B1L001S, U26-ch119, adc5

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 106 |
| Mean | 59.29 |
| Std Dev | 6.278 |

Entry

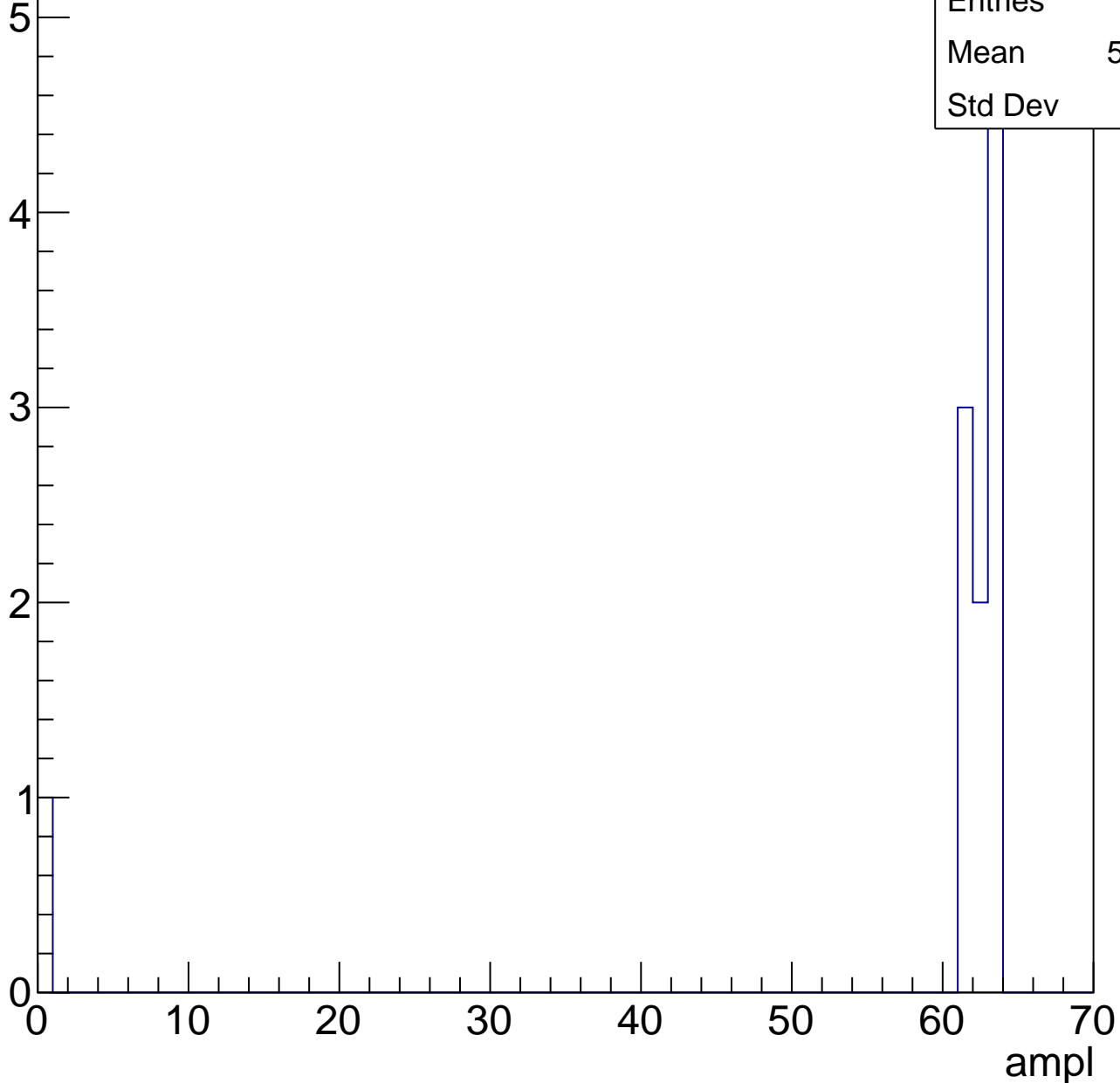


B1L001S, U26-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.55 |
| Std Dev | 17.9 |



B1L001S, U26-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



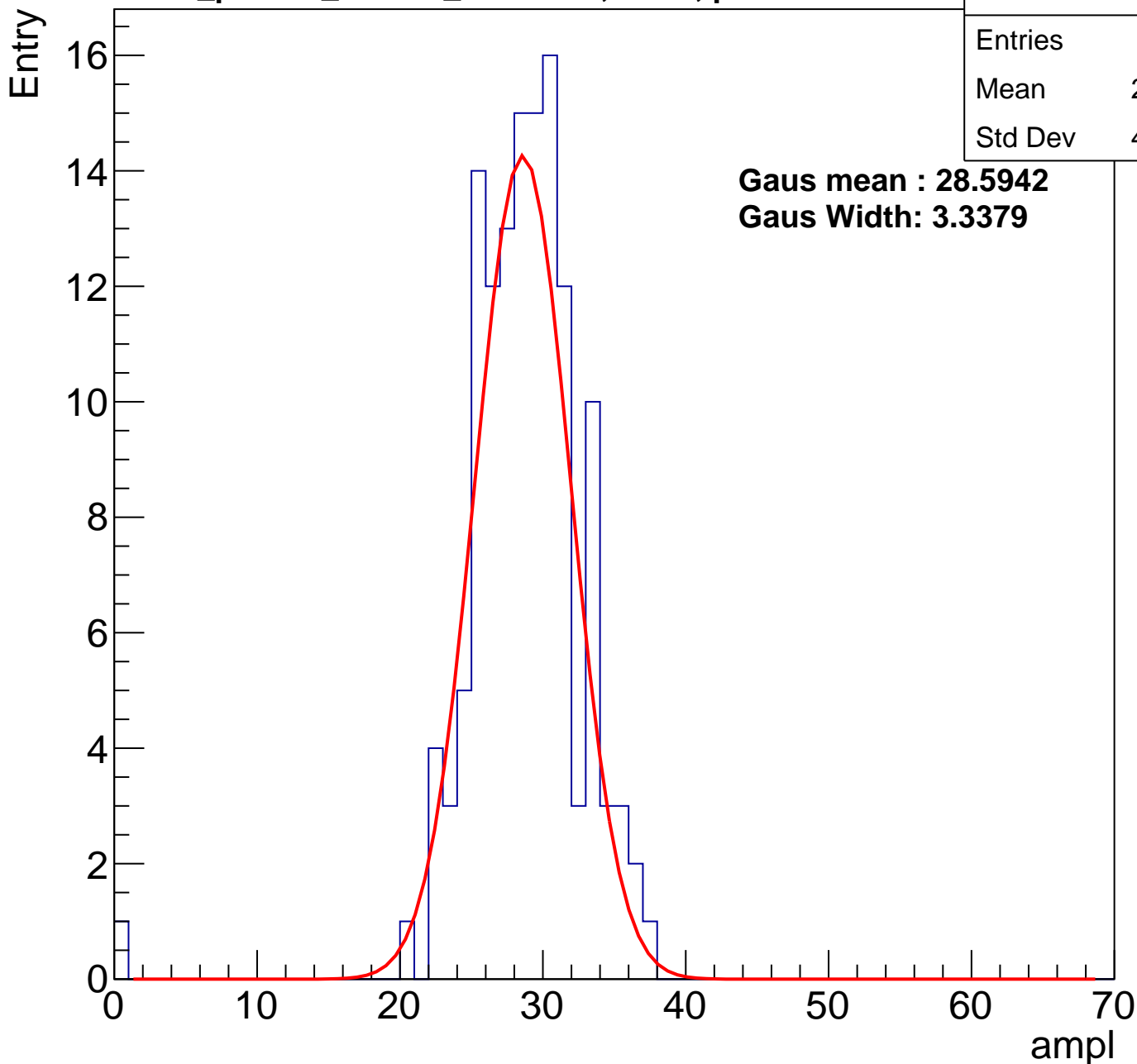
B1L001S, U26-ch120, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 133 |
| Mean | 28.26 |
| Std Dev | 4.135 |

Gaus mean : 28.5942

Gaus Width: 3.3379



B1L001S, U26-ch120, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 172 |
| Mean | 35.72 |
| Std Dev | 4.111 |

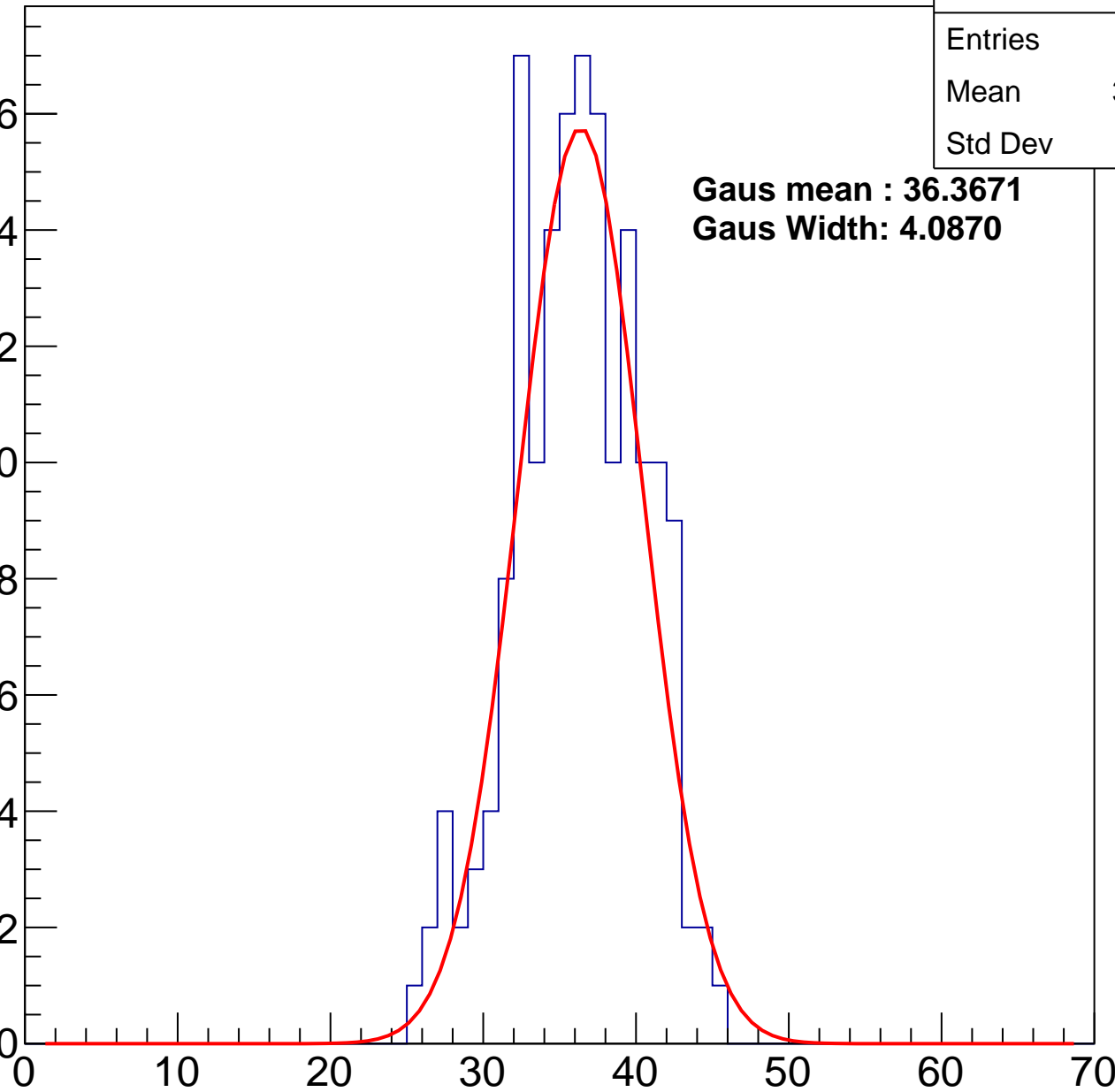
Gaus mean : 36.3671

Gaus Width: 4.0870

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch120, adc2

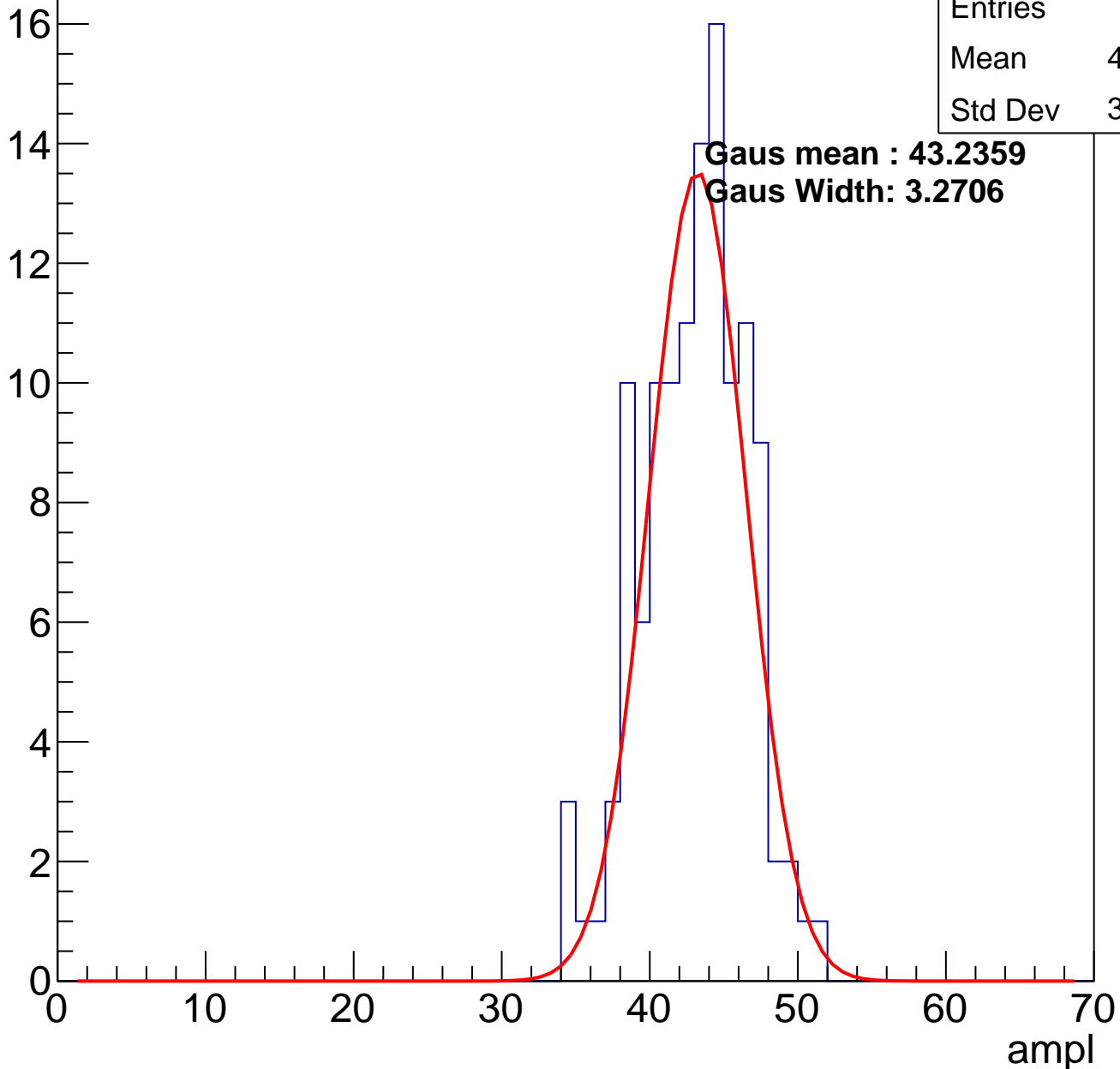
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 121 |
| Mean | 42.56 |
| Std Dev | 3.476 |

Gaus mean : 43.2359

Gaus Width: 3.2706

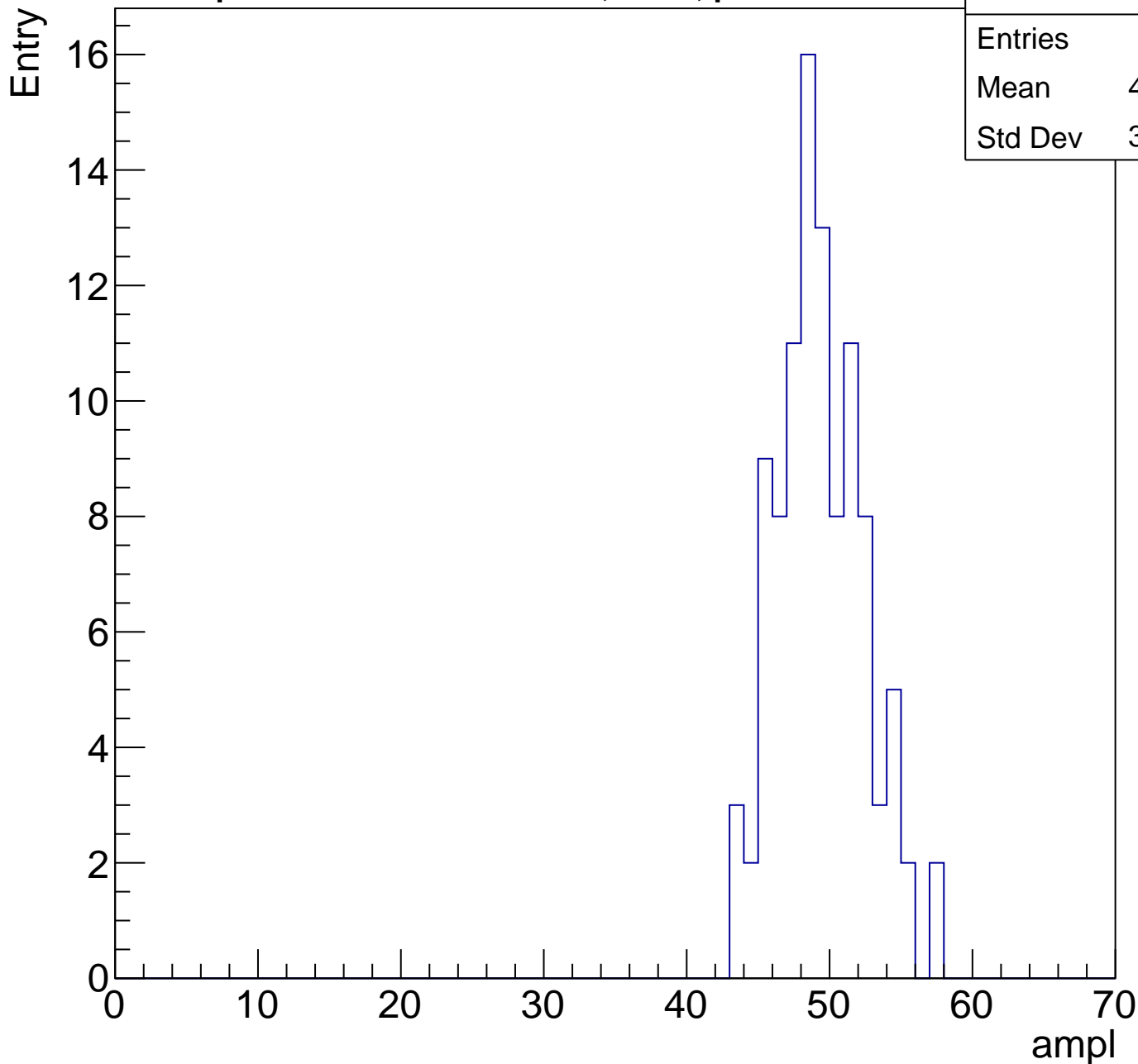
Entry



B1L001S, U26-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 101 |
| Mean | 48.93 |
| Std Dev | 3.035 |



B1L001S, U26-ch120, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 150 |
| Mean | 54.72 |
| Std Dev | 3.408 |

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

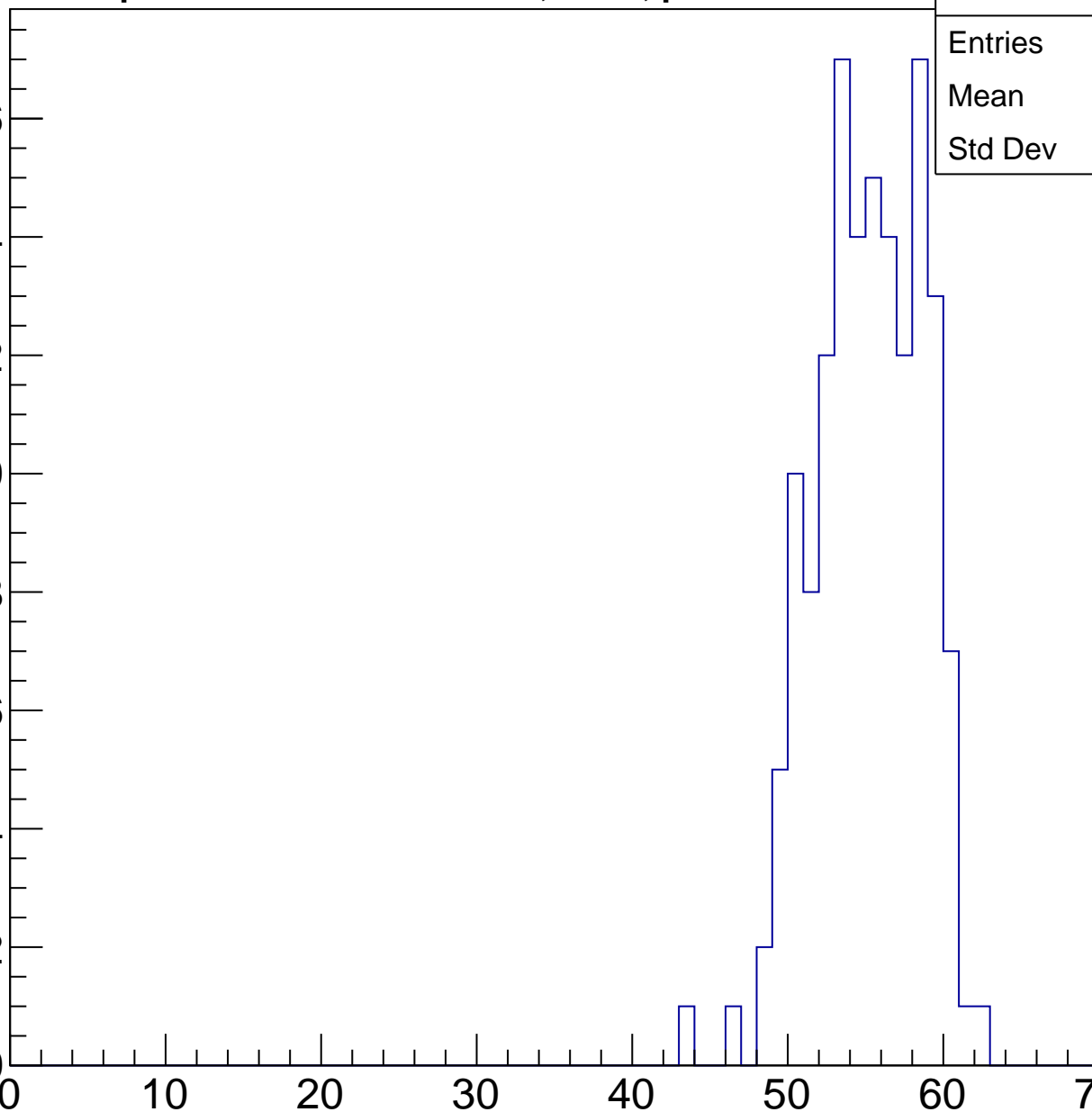
40

50

60

70

ampl



B1L001S, U26-ch120, adc5

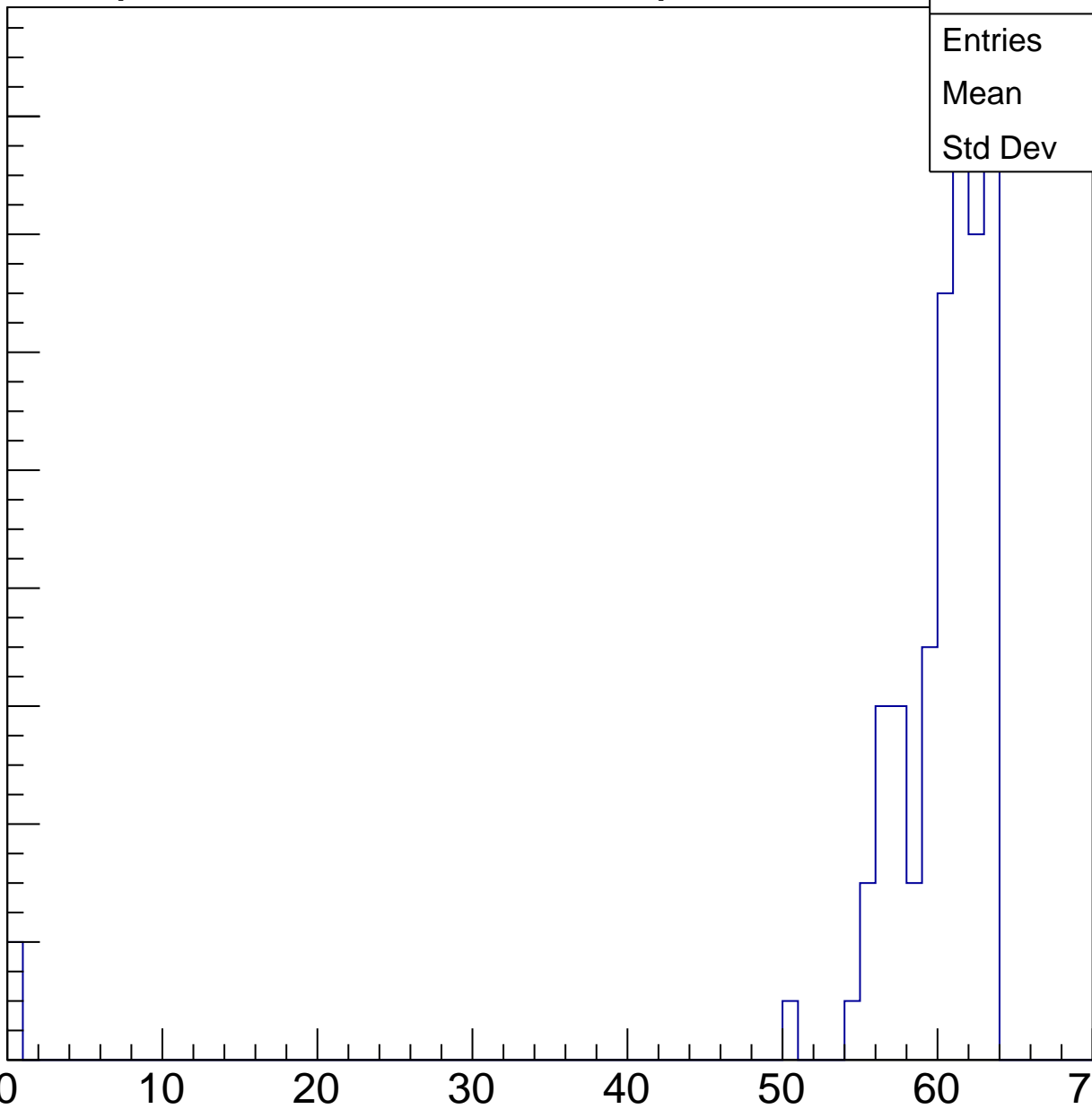
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 58.73 |
| Std Dev | 9.273 |

ampl

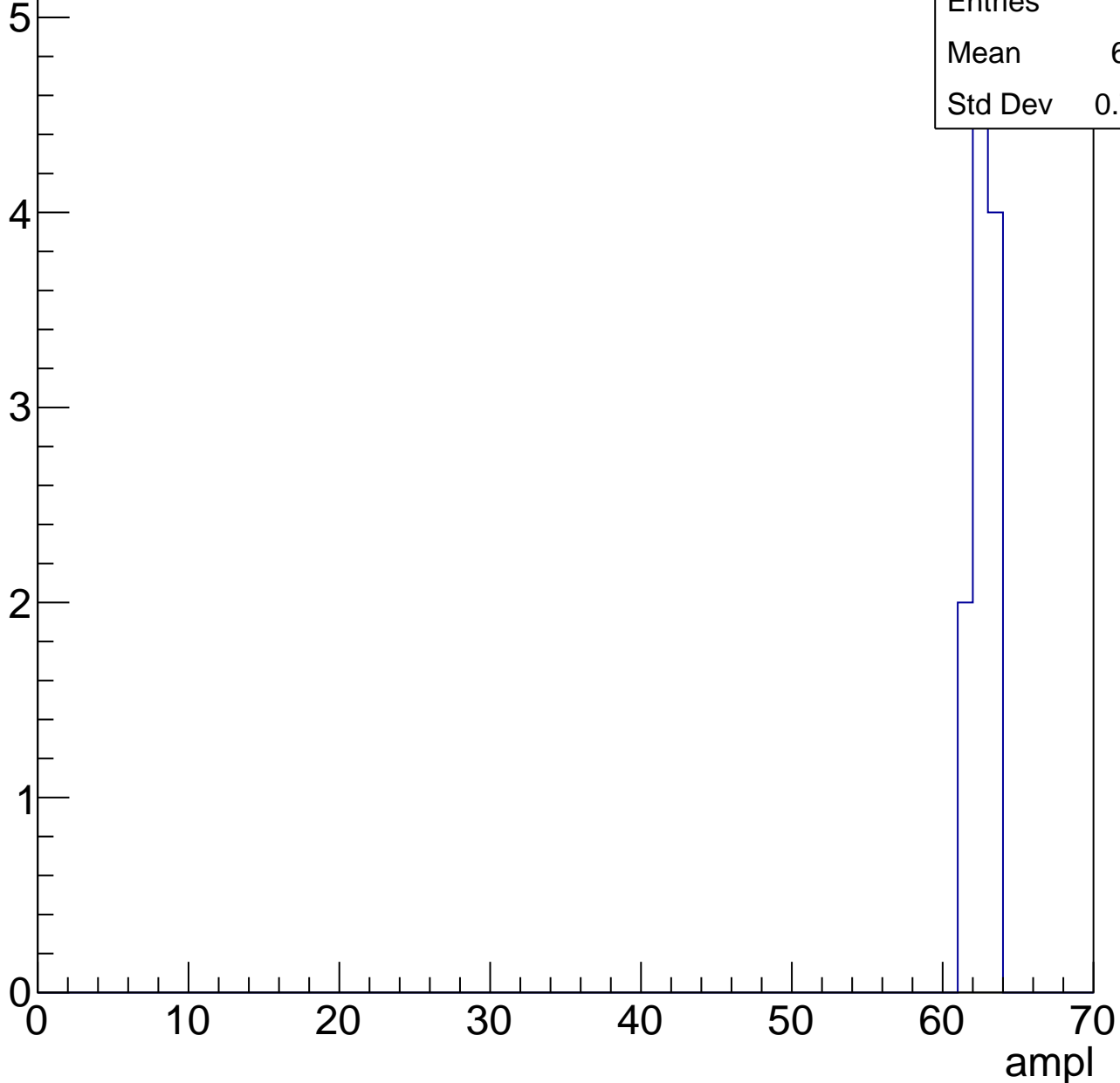


B1L001S, U26-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

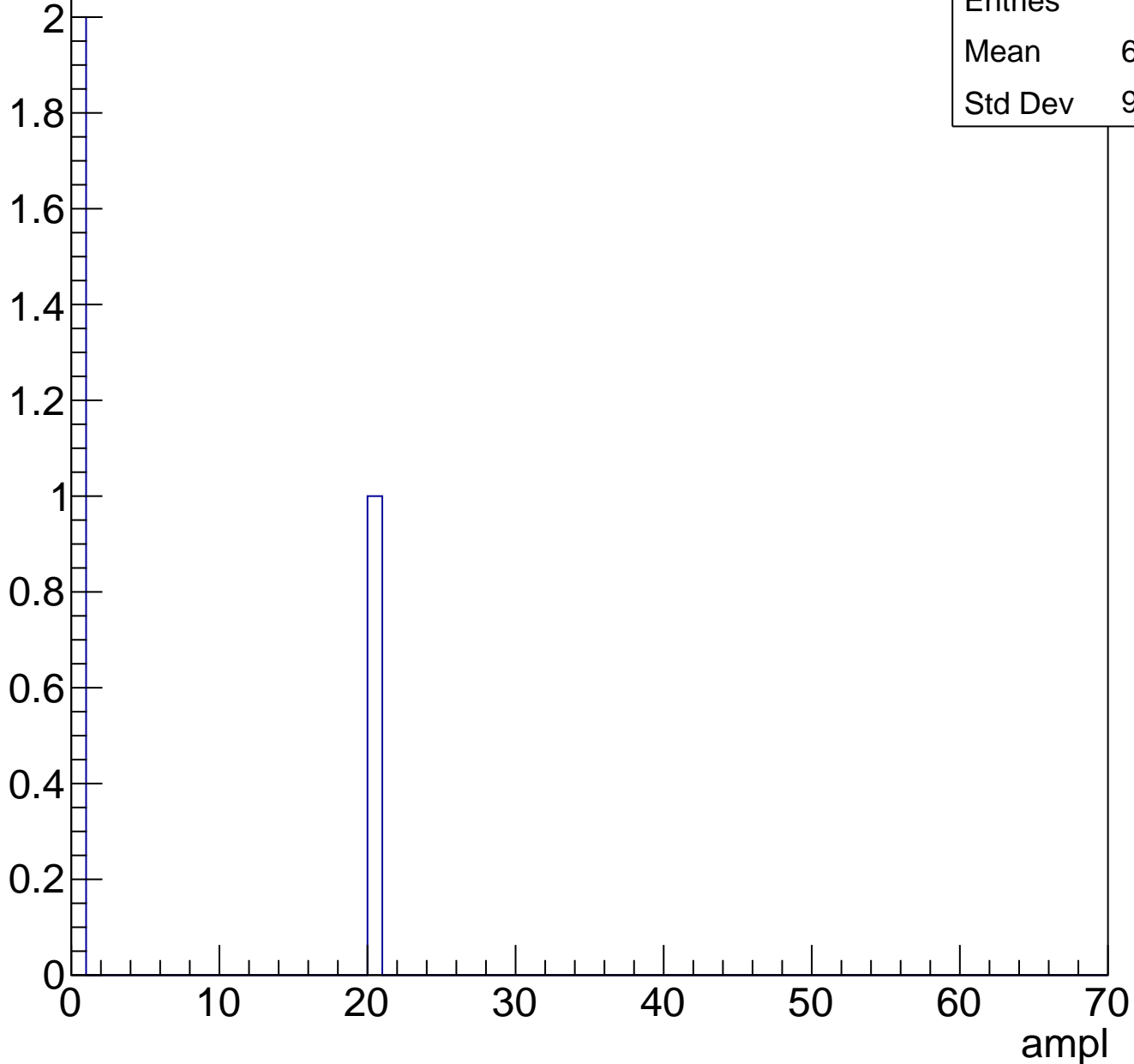
| | |
|---------|--------|
| Entries | 11 |
| Mean | 62.18 |
| Std Dev | 0.7158 |



B1L001S, U26-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

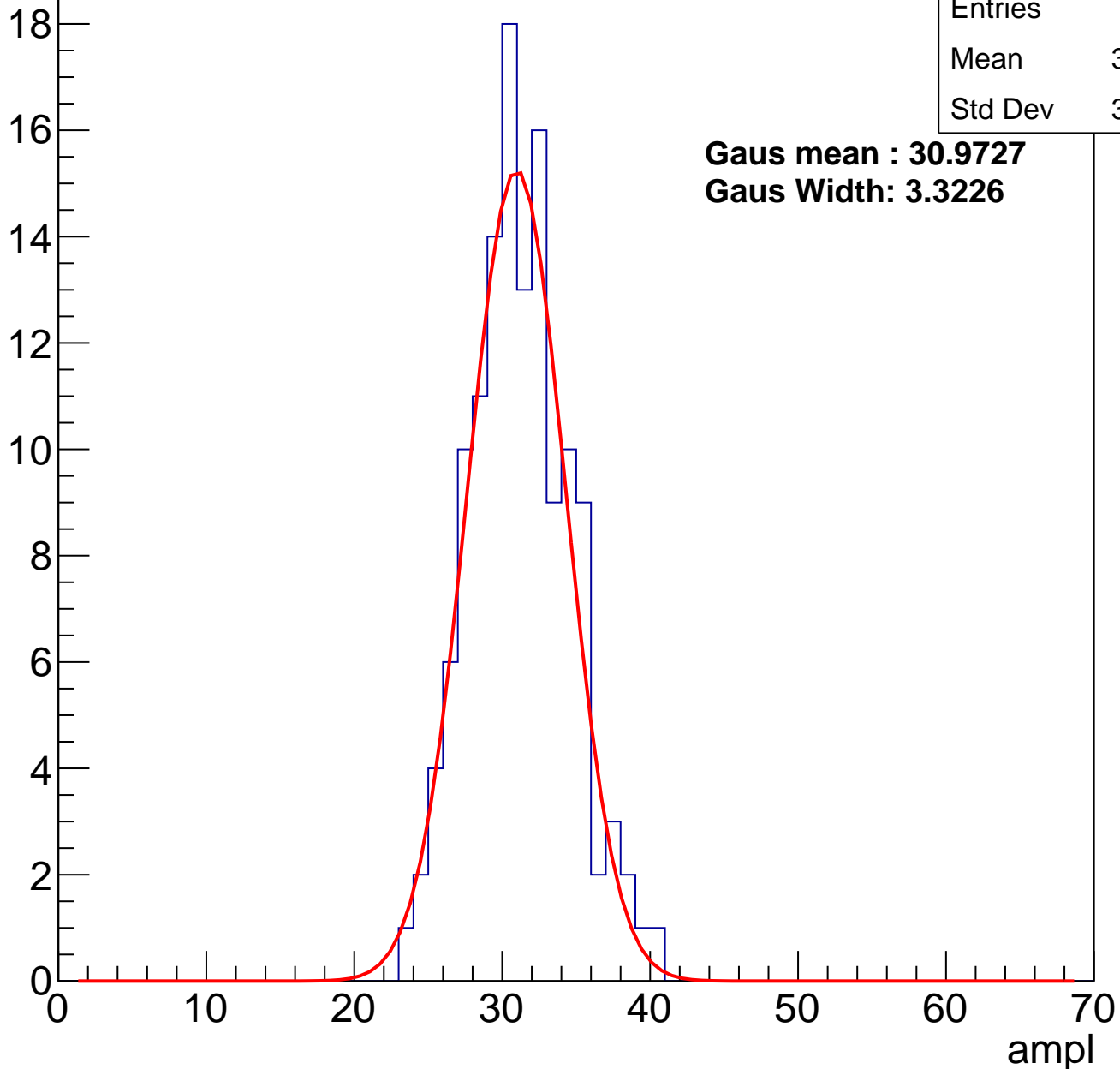


| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.667 |
| Std Dev | 9.428 |

B1L001S, U26-ch121, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 132 |
| Mean | 37.35 |
| Std Dev | 3.606 |

Gaus mean : 37.8082

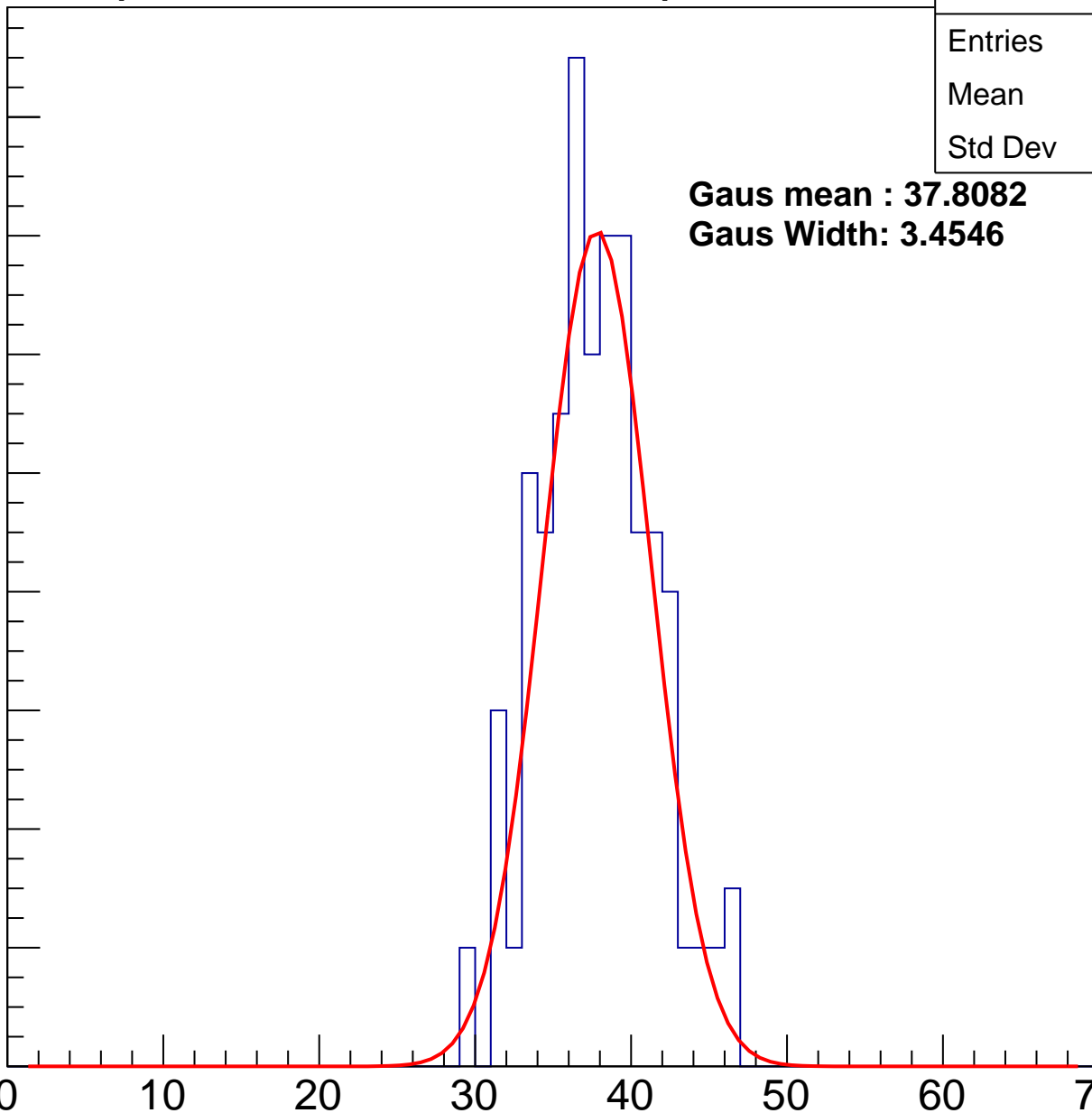
Gaus Width: 3.4546

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

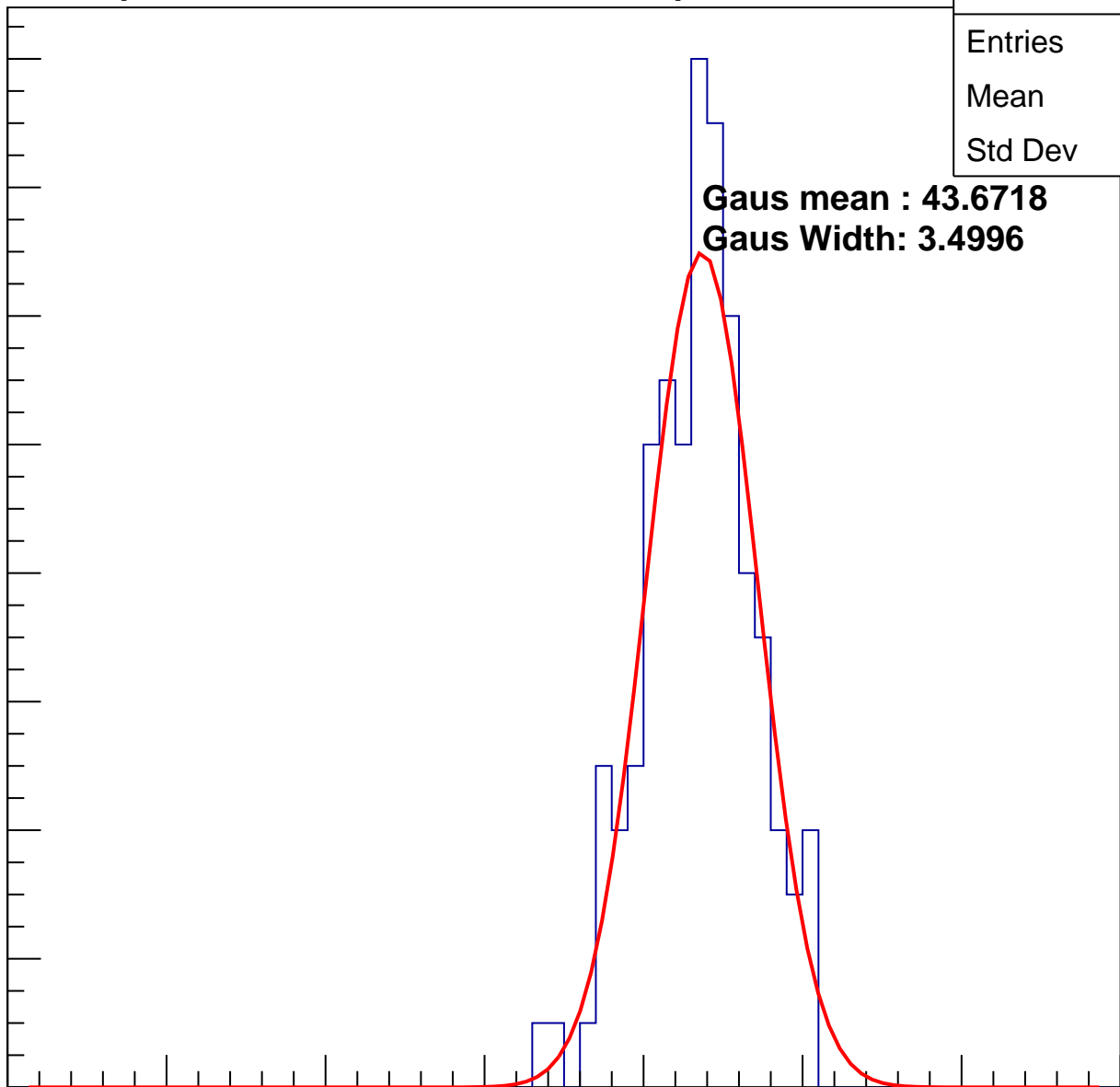
| | |
|---------|-------|
| Entries | 117 |
| Mean | 42.99 |
| Std Dev | 3.441 |

Gaus mean : 43.6718

Gaus Width: 3.4996

0 10 20 30 40 50 60 70

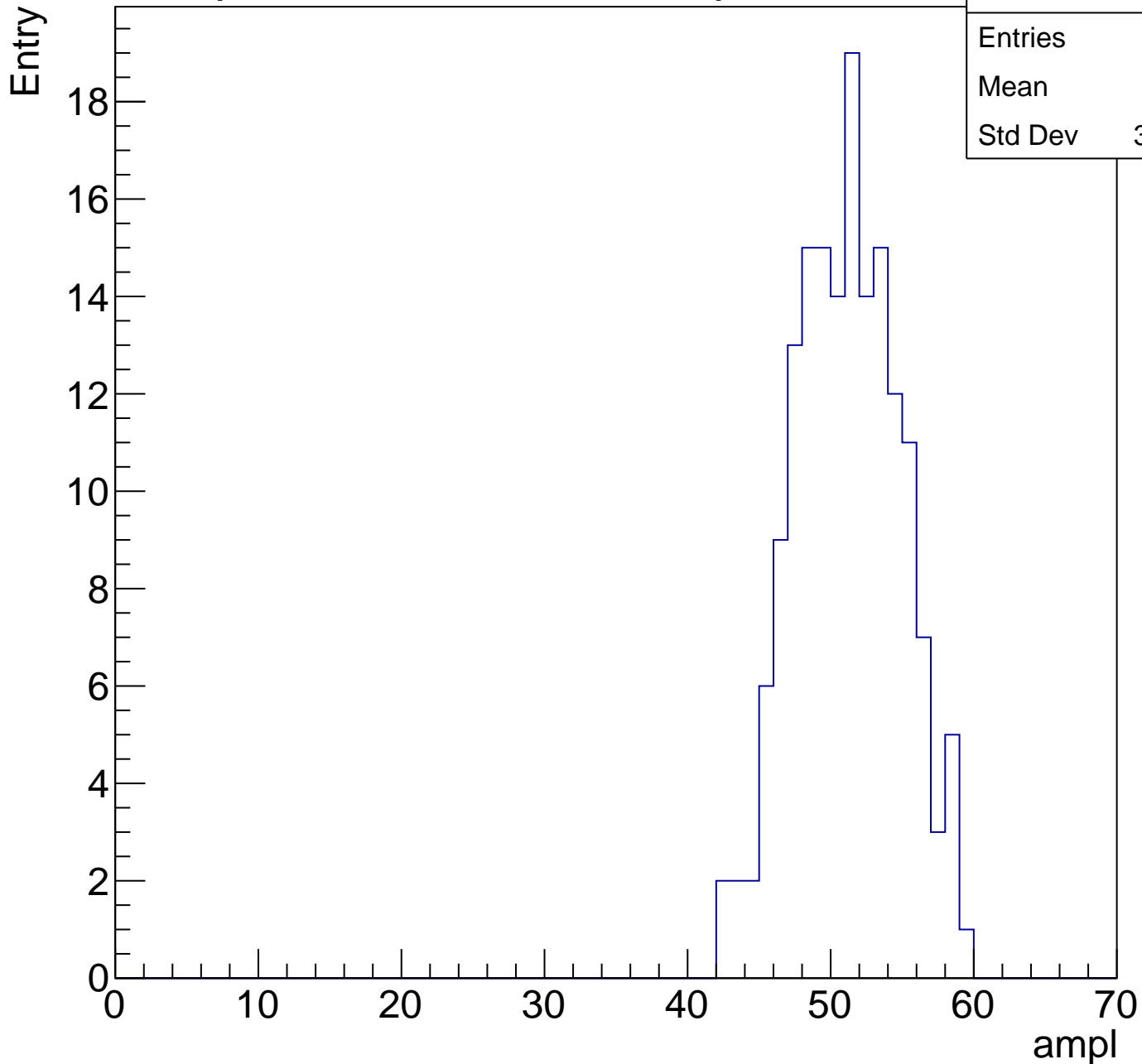
ampl



B1L001S, U26-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 165 |
| Mean | 50.7 |
| Std Dev | 3.636 |



B1L001S, U26-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 89 |
| Mean | 56.22 |
| Std Dev | 3.496 |

Entry

12

10

8

6

4

2

0

0

10

20

30

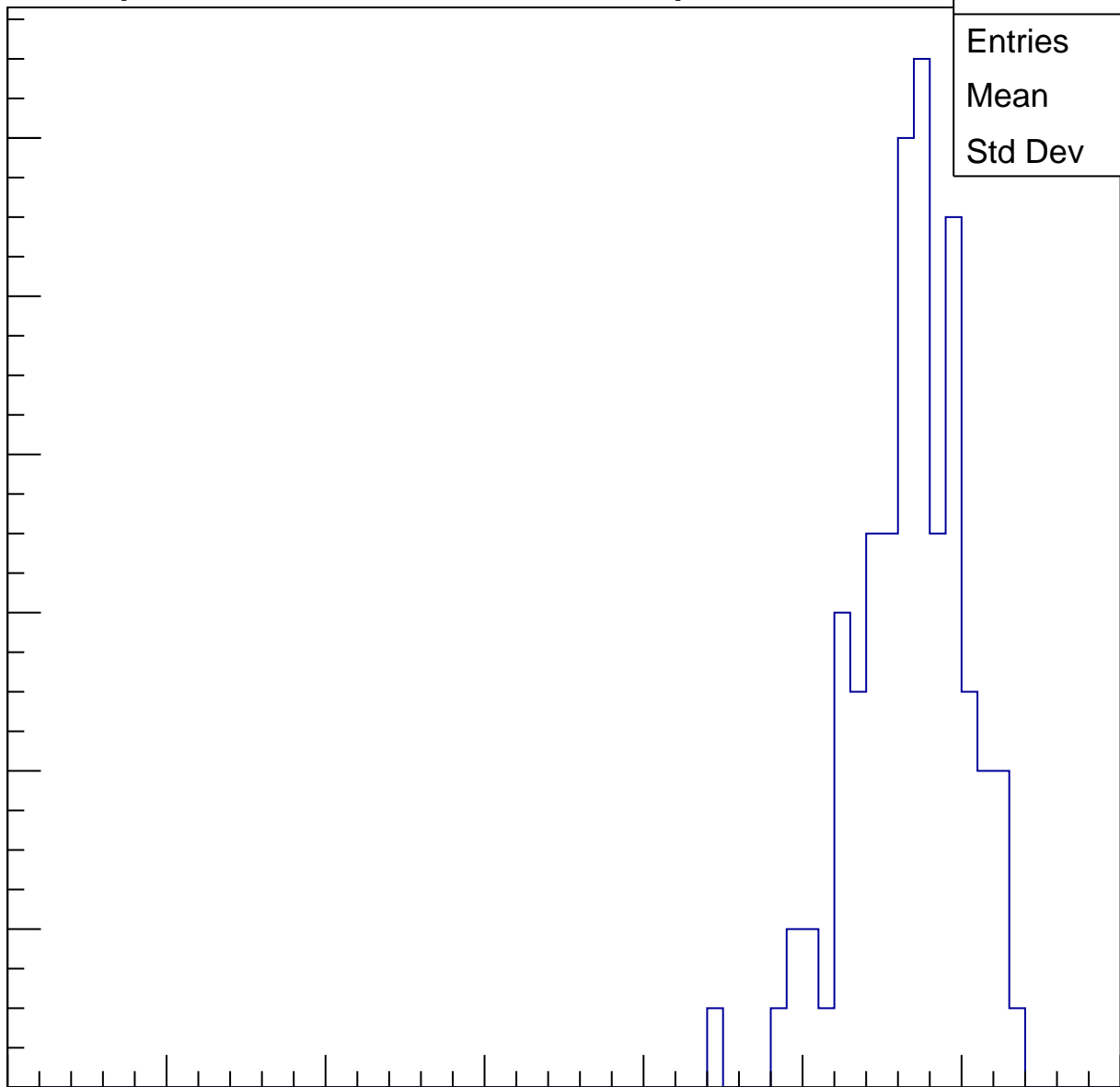
40

50

60

70

ampl



B1L001S, U26-ch121, adc5

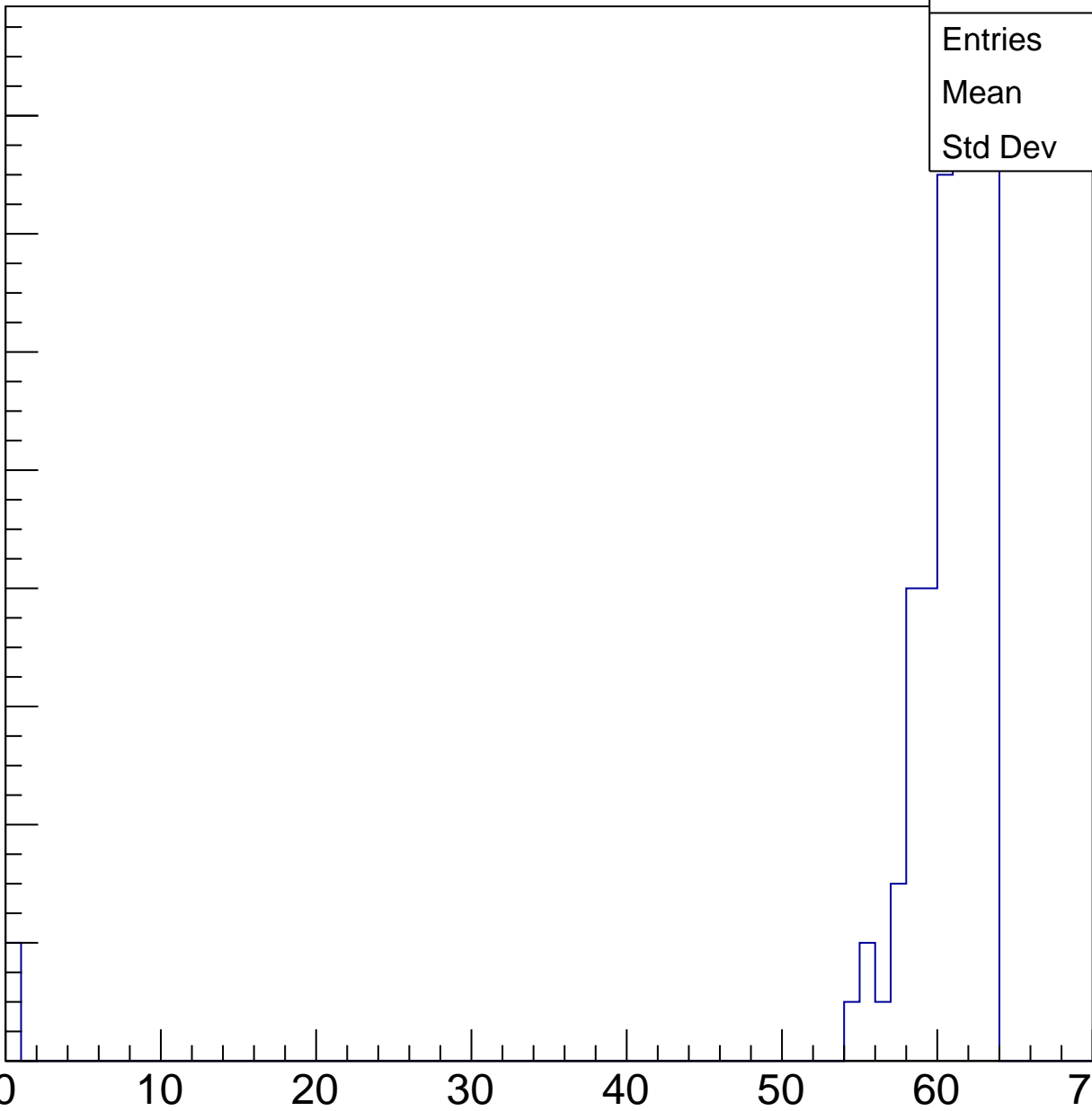
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 89 |
| Mean | 59.17 |
| Std Dev | 9.205 |

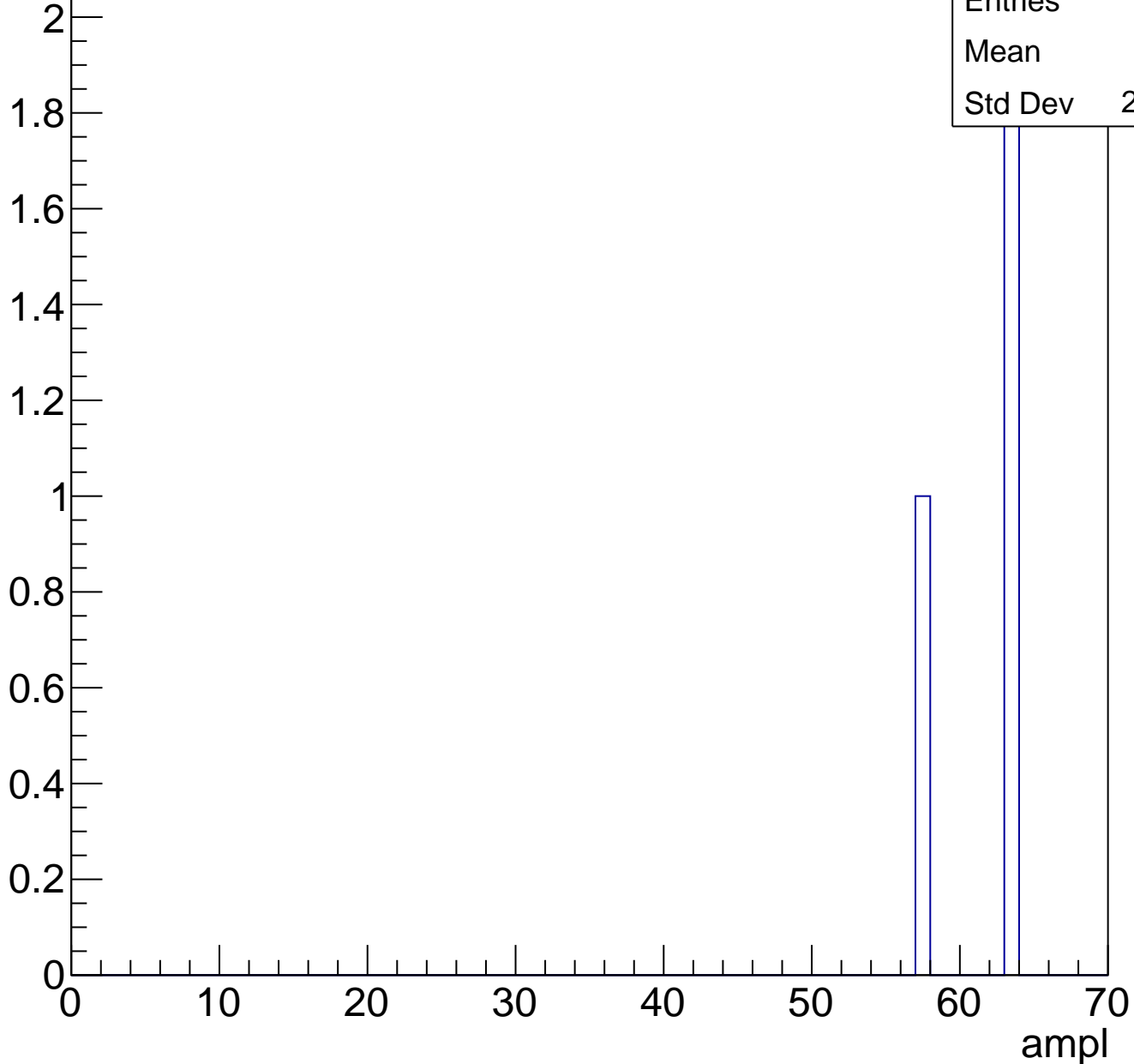
ampl



B1L001S, U26-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

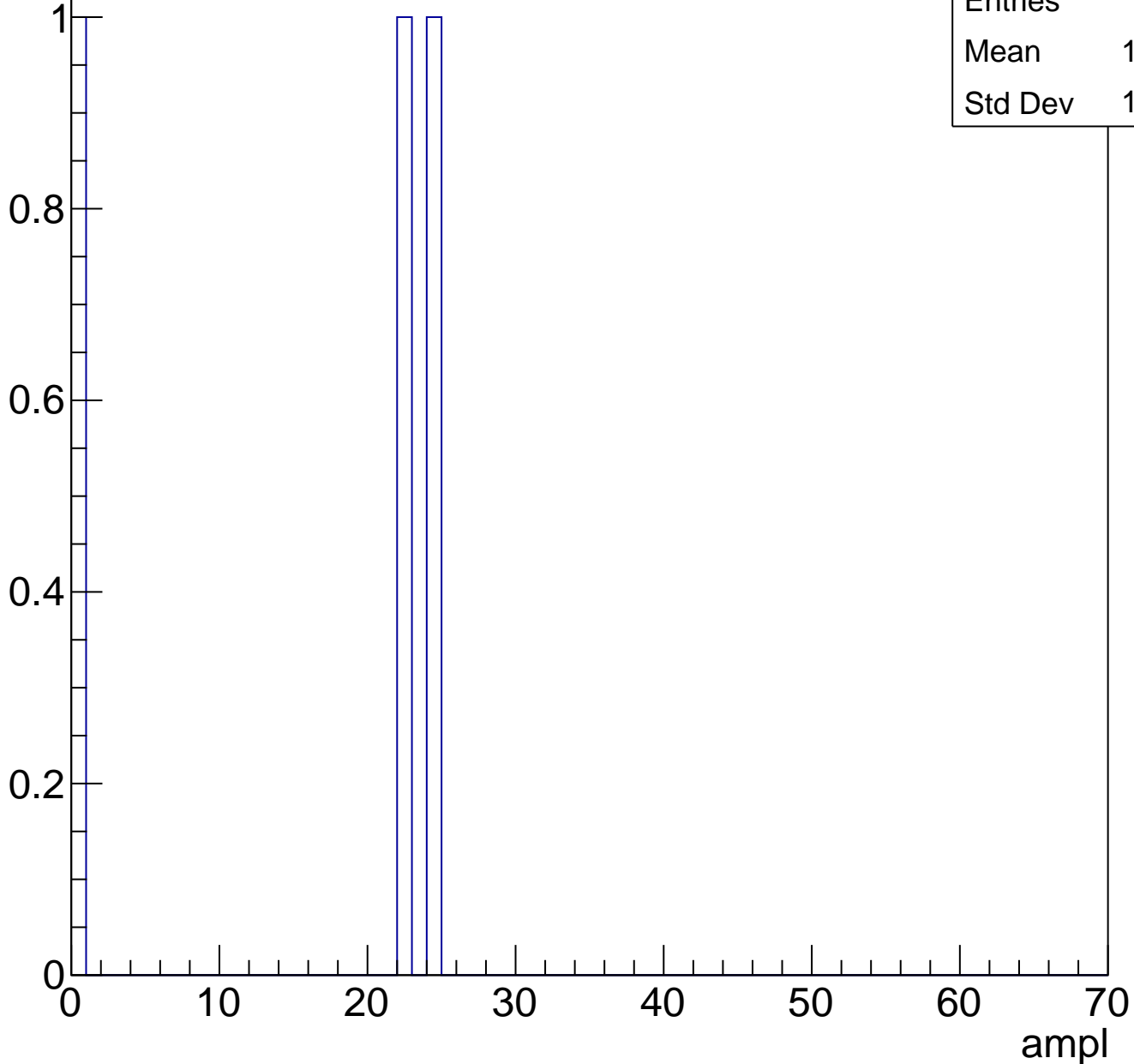
Entry



B1L001S, U26-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch122, adc0

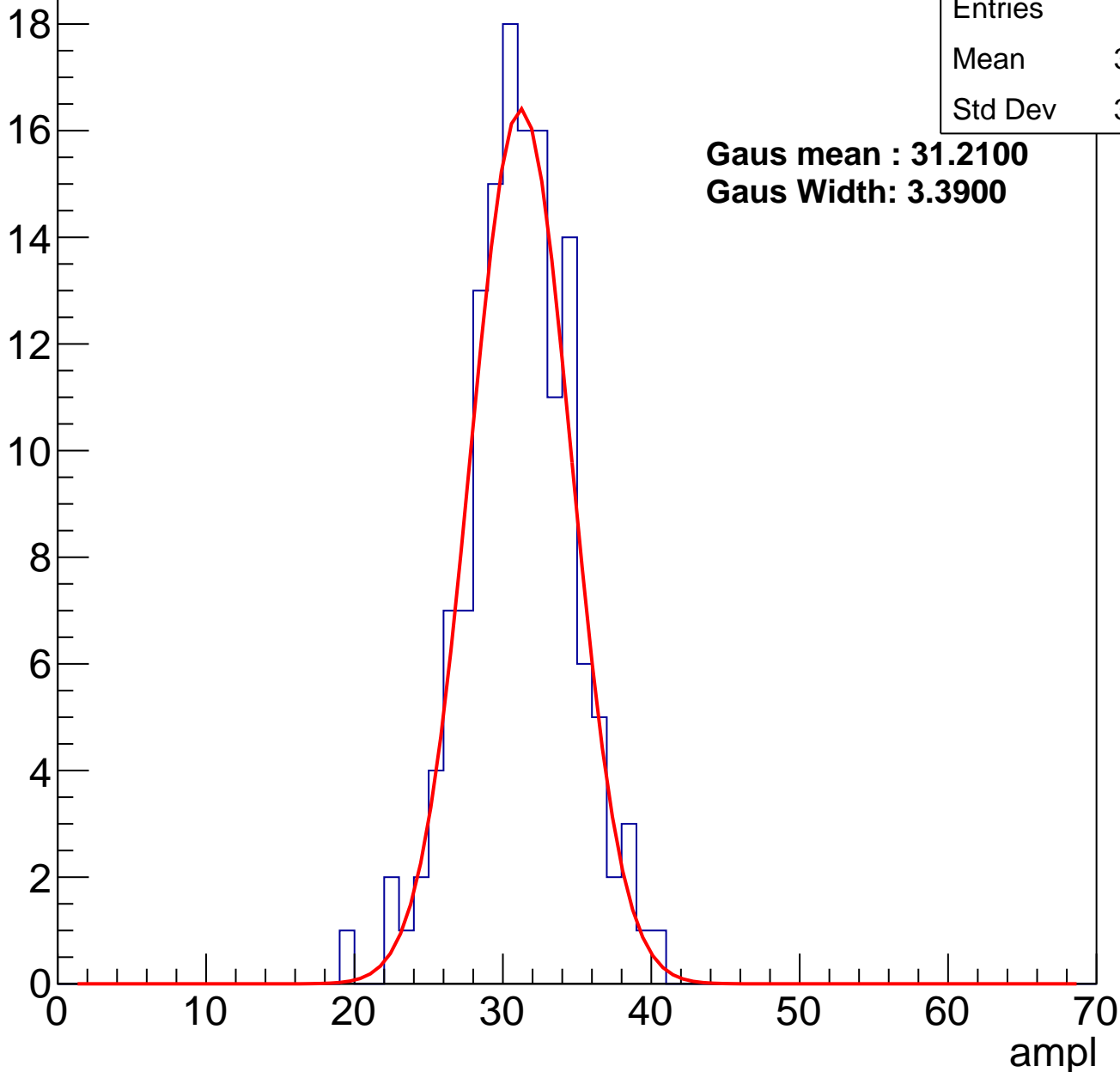
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 145 |
| Mean | 30.68 |
| Std Dev | 3.572 |

Gaus mean : 31.2100

Gaus Width: 3.3900

Entry



B1L001S, U26-ch122, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 117 |
| Mean | 37.34 |
| Std Dev | 3.311 |

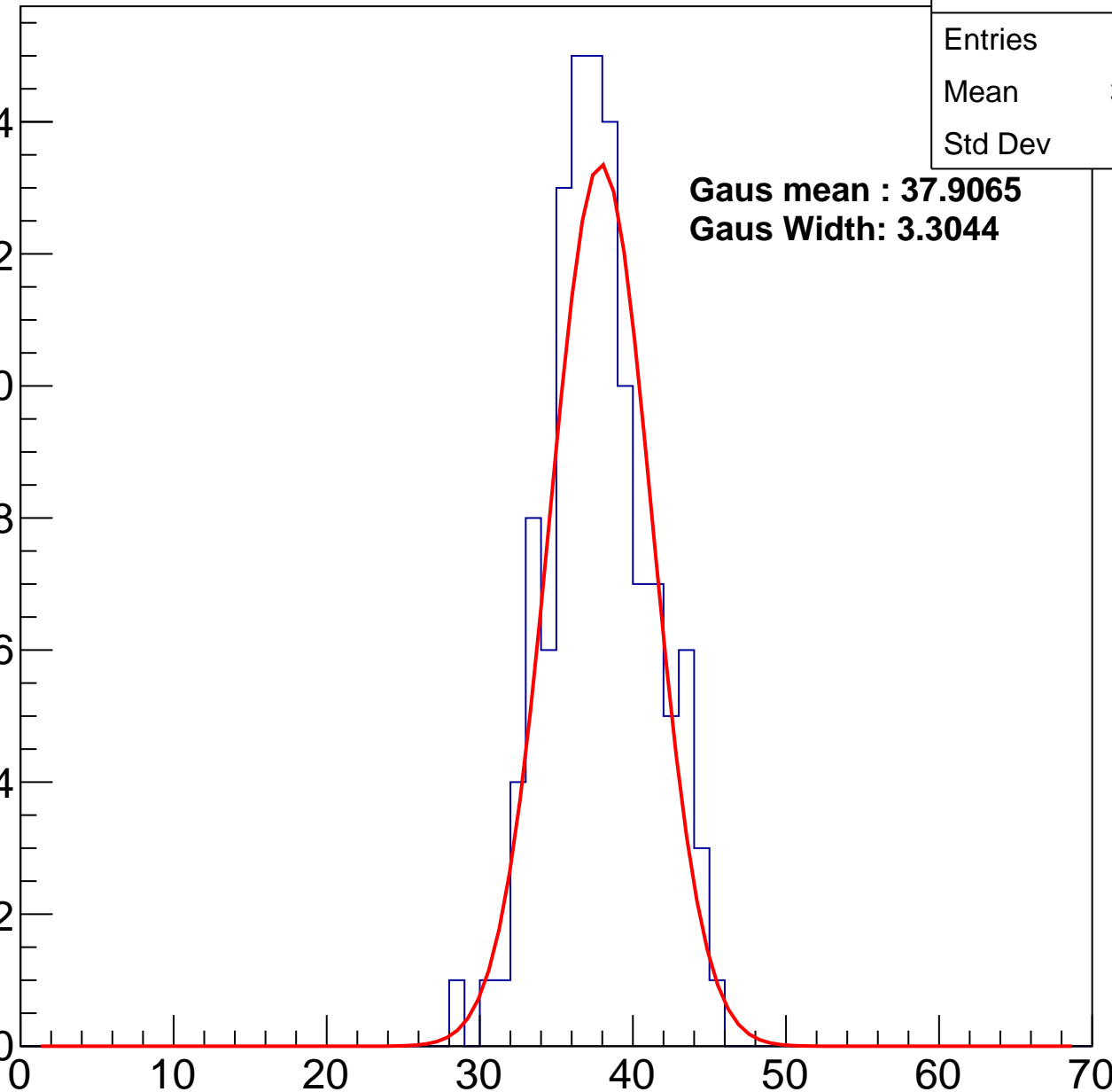
Gaus mean : 37.9065

Gaus Width: 3.3044

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

138

Mean

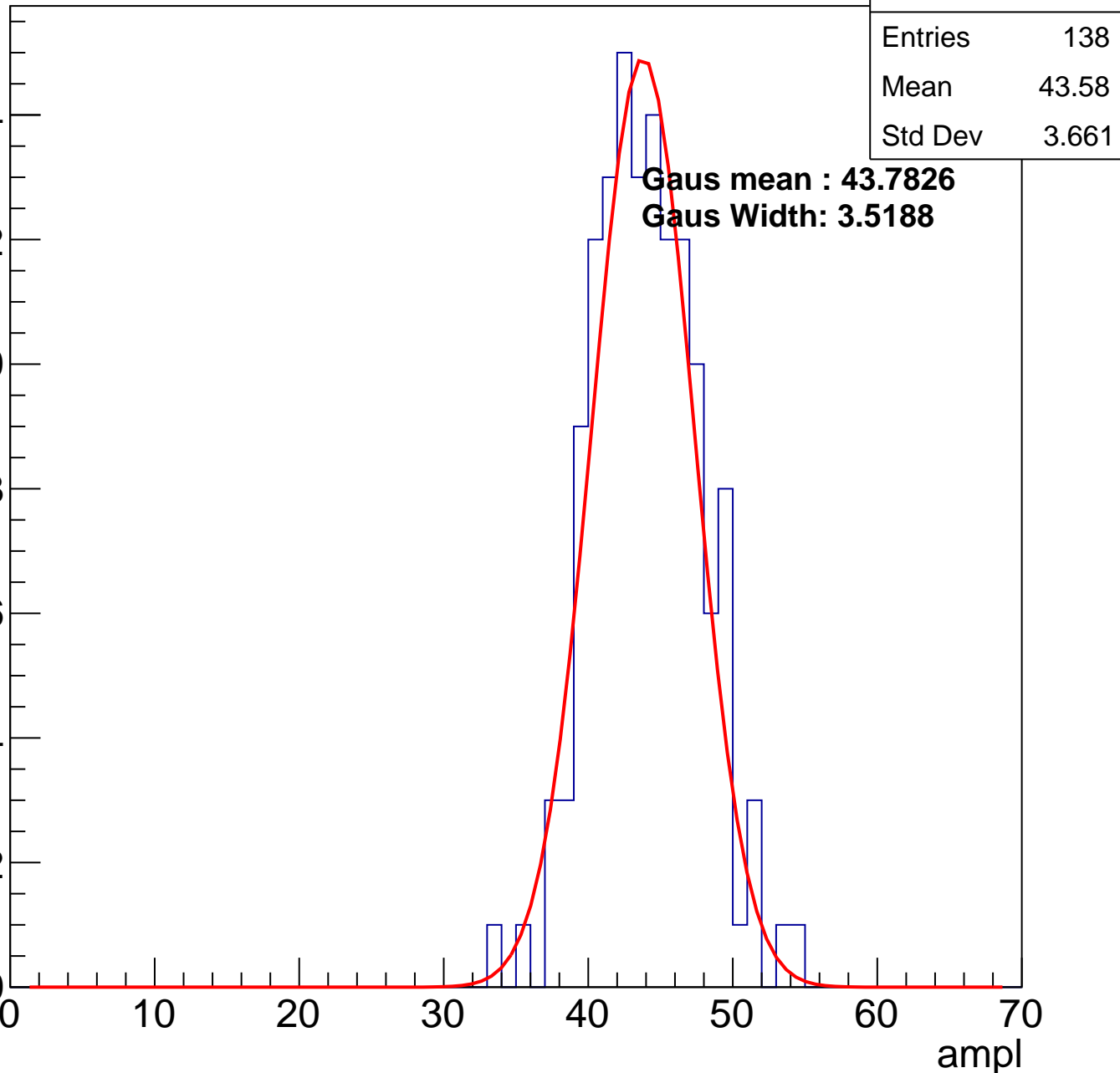
43.58

Std Dev

3.661

Gaus mean : 43.7826

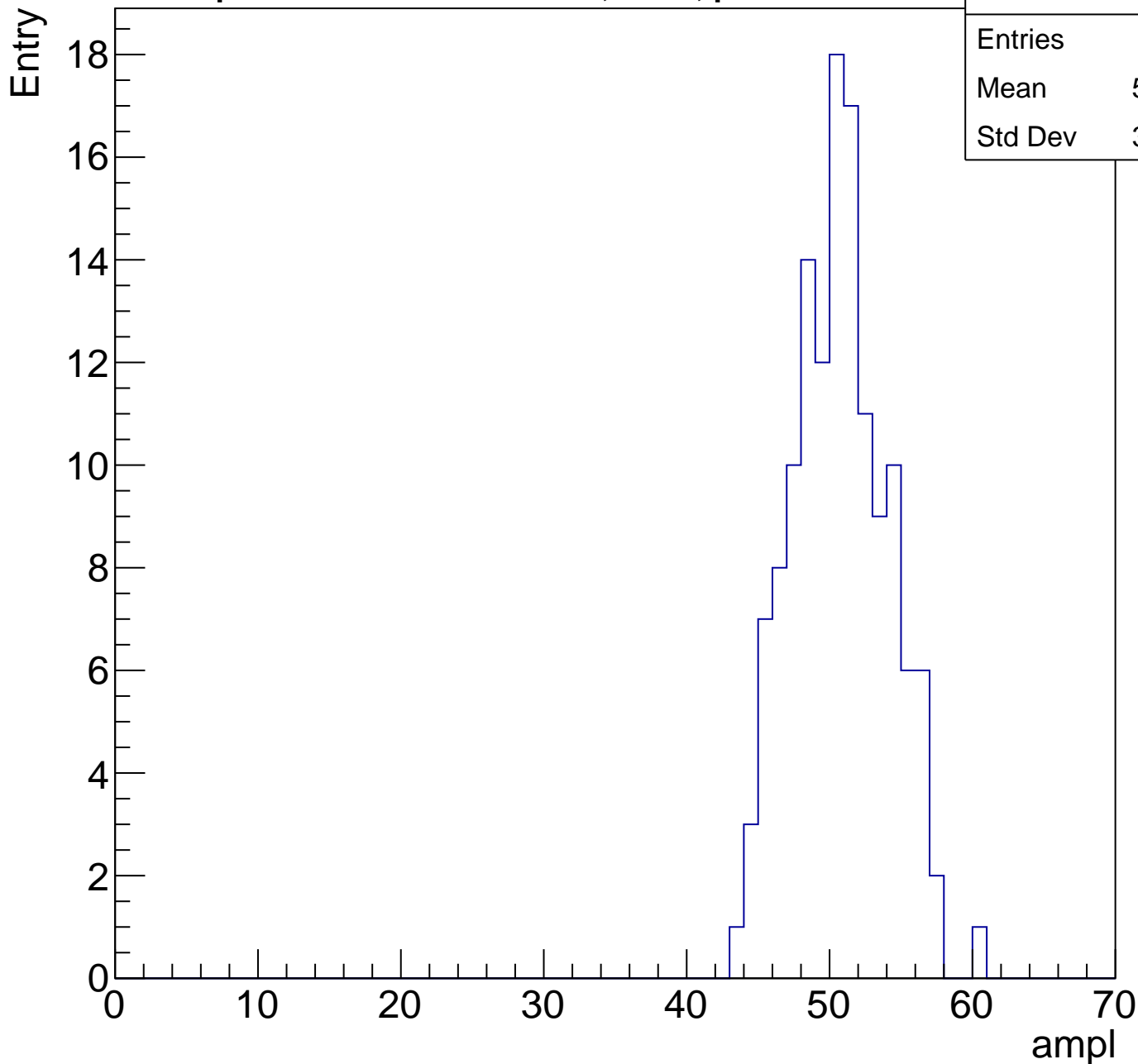
Gaus Width: 3.5188



B1L001S, U26-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

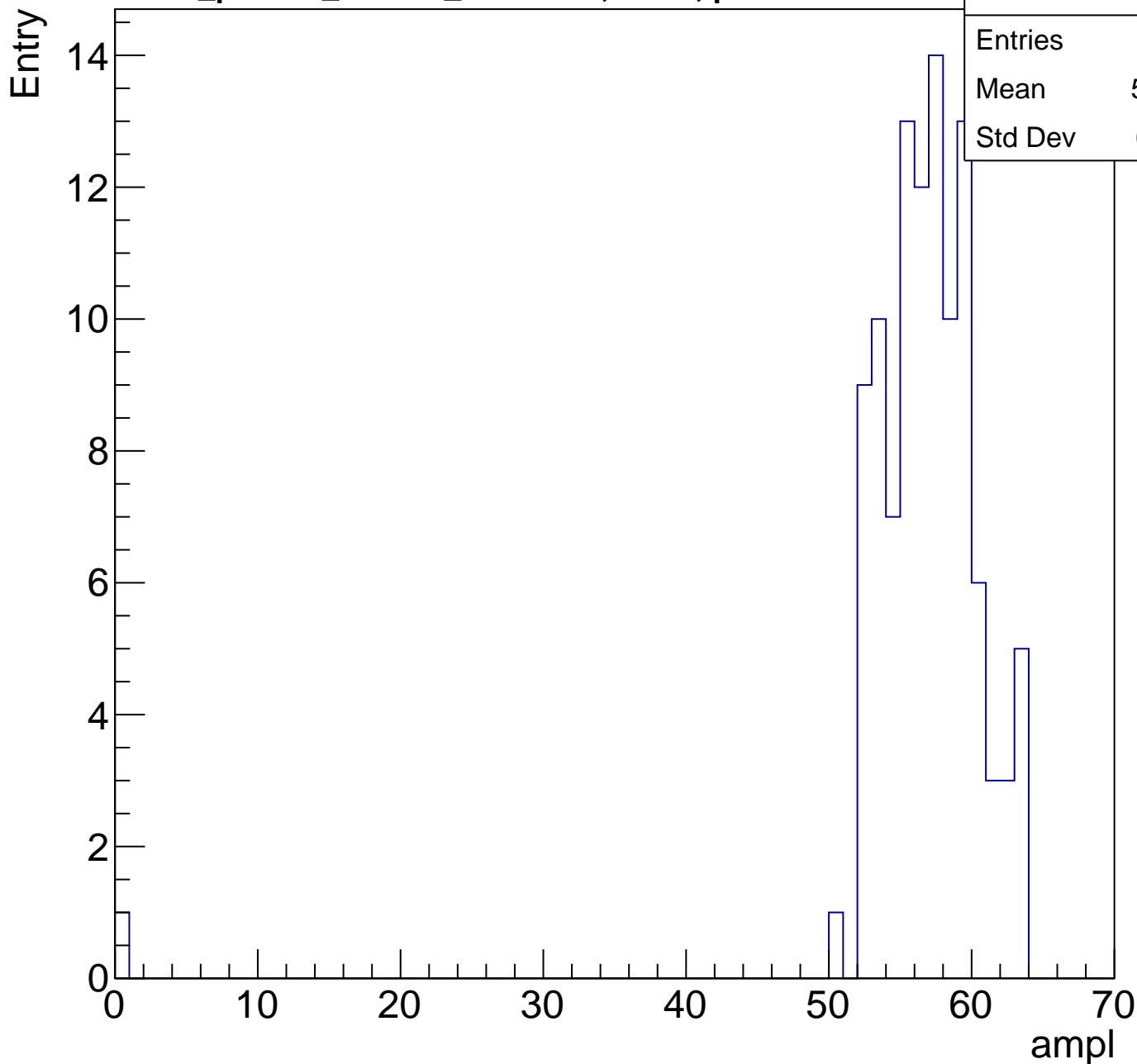
| | |
|---------|-------|
| Entries | 135 |
| Mean | 50.25 |
| Std Dev | 3.306 |



B1L001S, U26-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 107 |
| Mean | 56.09 |
| Std Dev | 6.221 |



B1L001S, U26-ch122, adc5

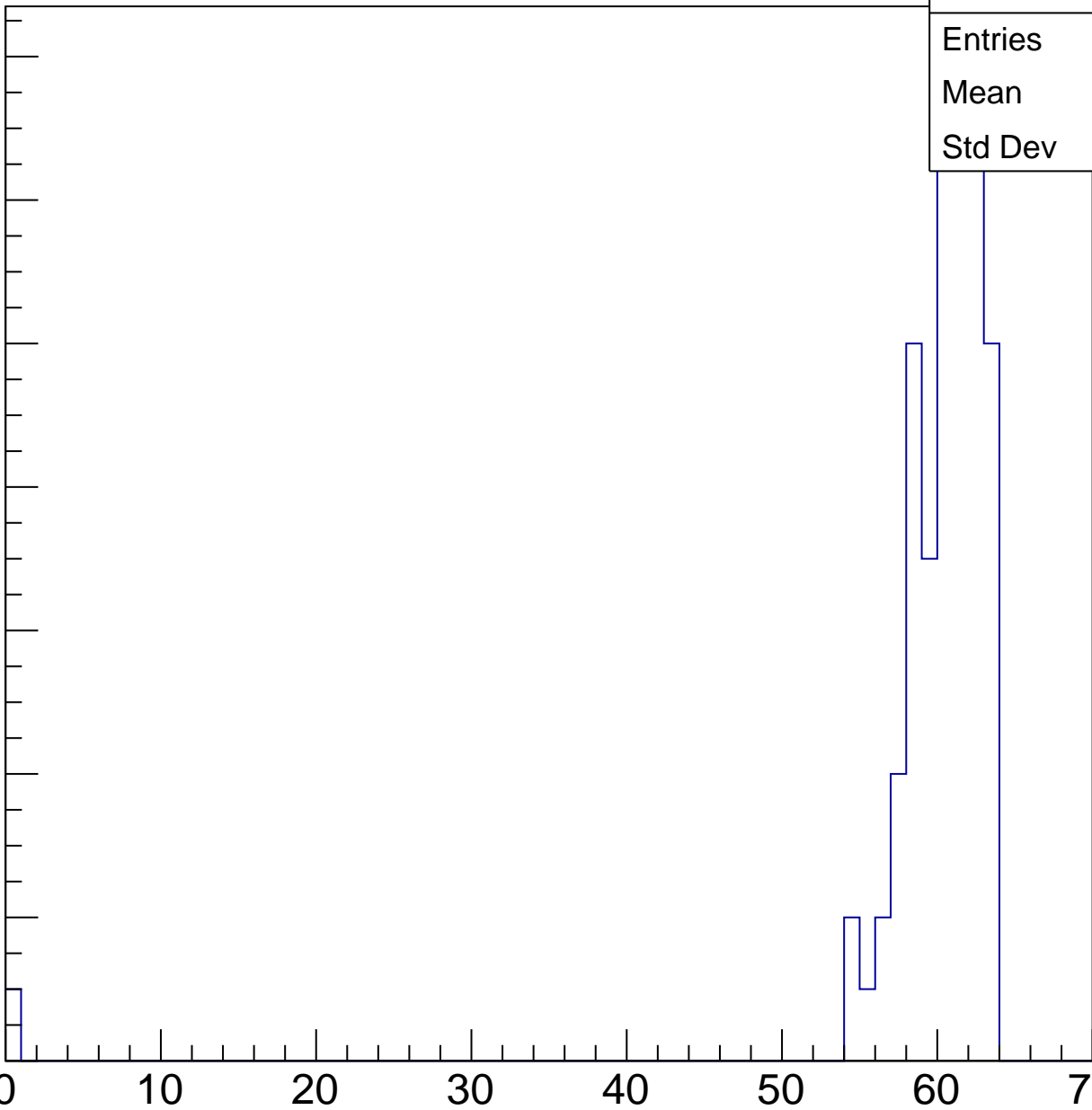
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 77 |
| Mean | 59.3 |
| Std Dev | 7.144 |

ampl

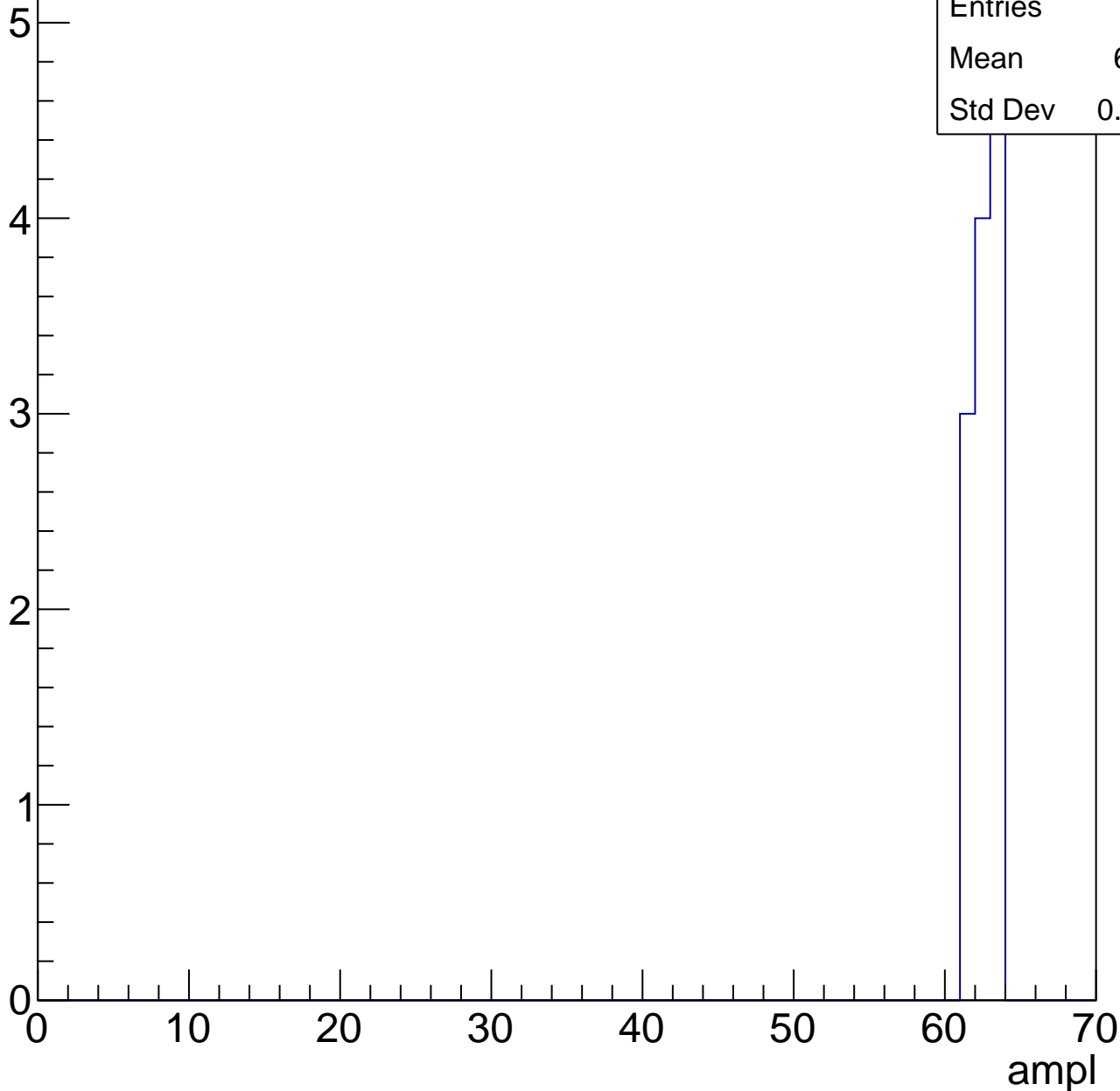


B1L001S, U26-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|--------|
| Entries | 12 |
| Mean | 62.17 |
| Std Dev | 0.7993 |



B1L001S, U26-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch123, adc0

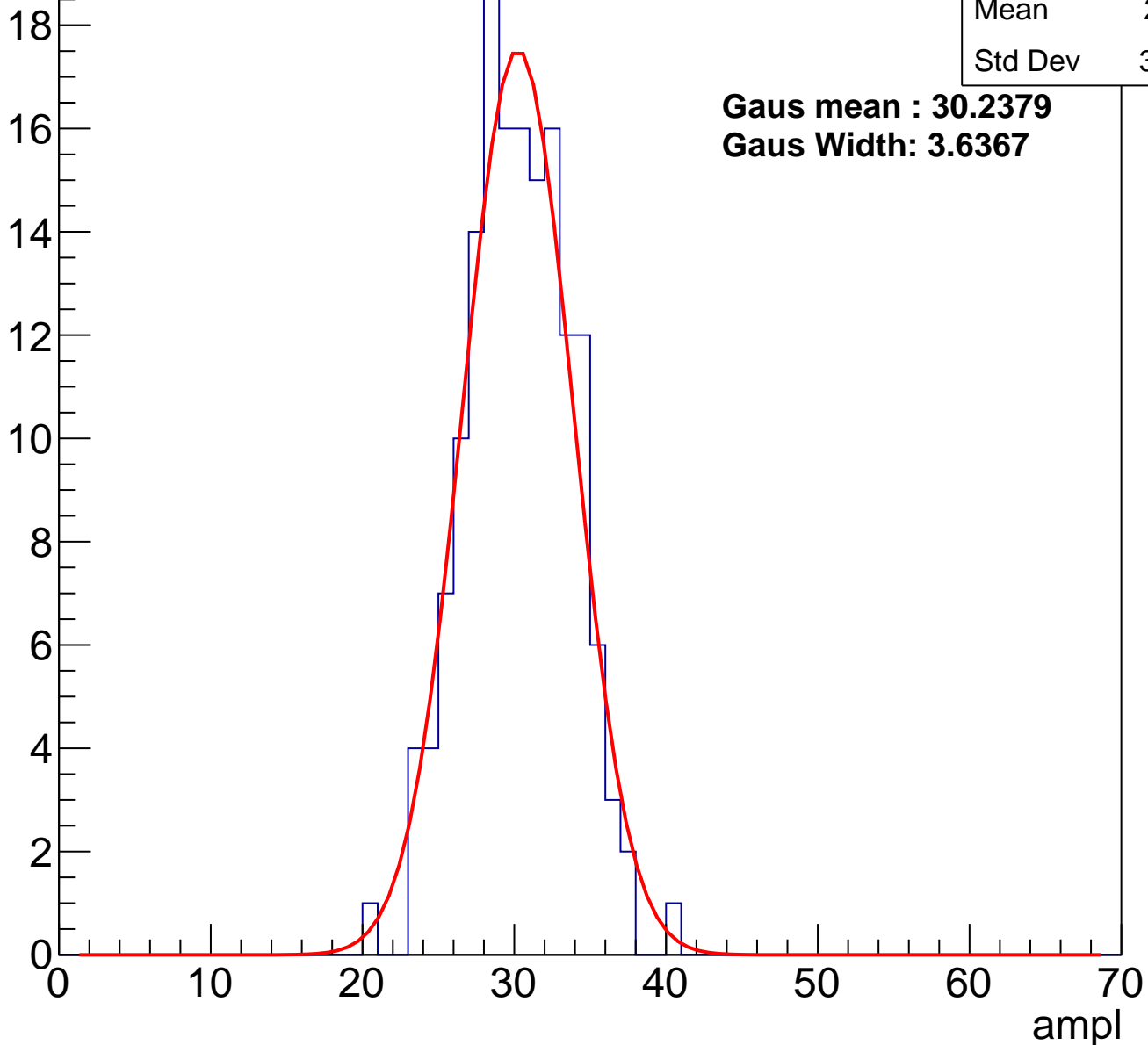
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 29.81 |
| Std Dev | 3.396 |

Gaus mean : 30.2379

Gaus Width: 3.6367

Entry



B1L001S, U26-ch123, adc1

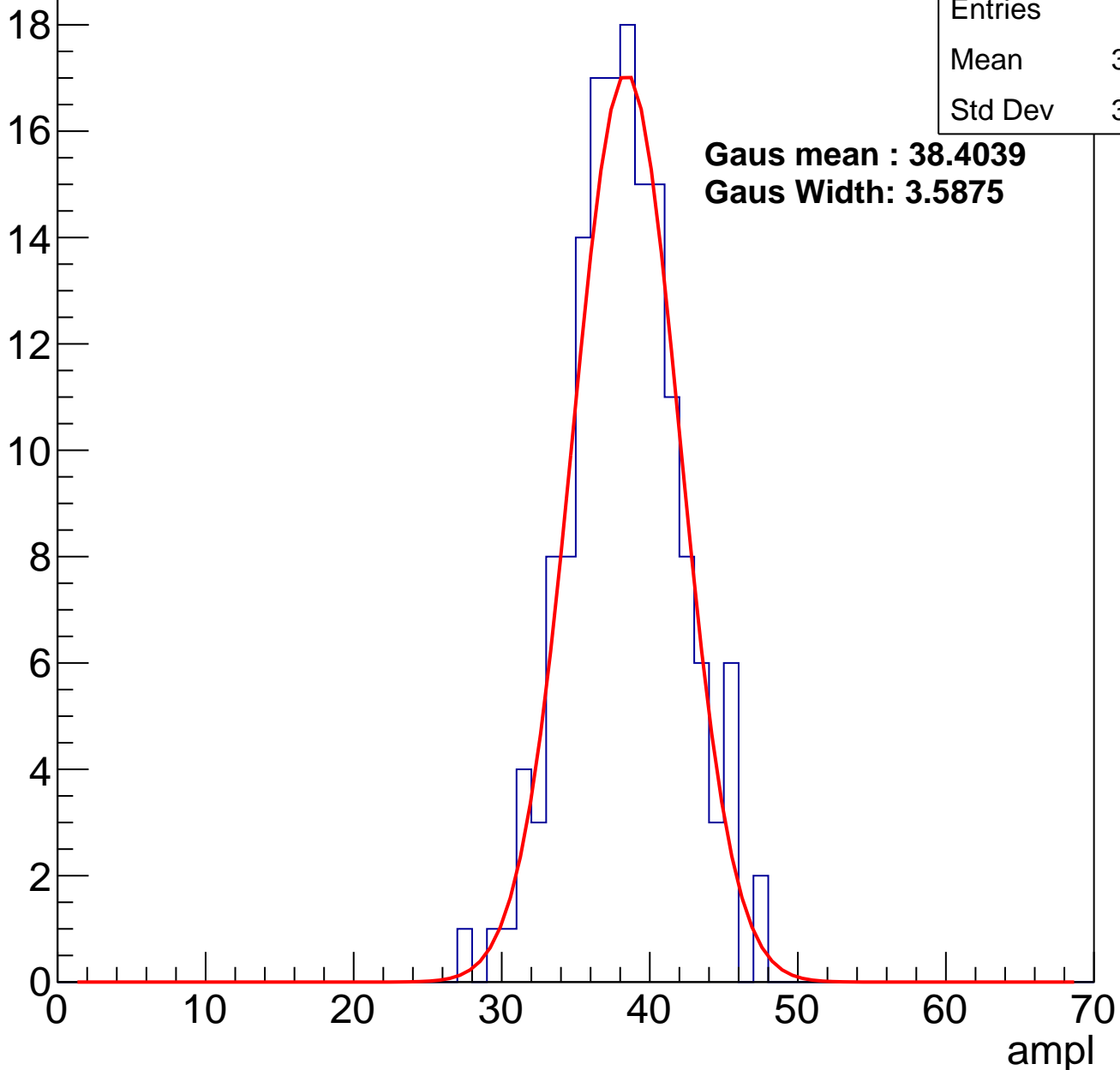
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 158 |
| Mean | 37.87 |
| Std Dev | 3.665 |

Gaus mean : 38.4039

Gaus Width: 3.5875

Entry



B1L001S, U26-ch123, adc2

calib_packv5_042523_0143.root, FC#2, port C2

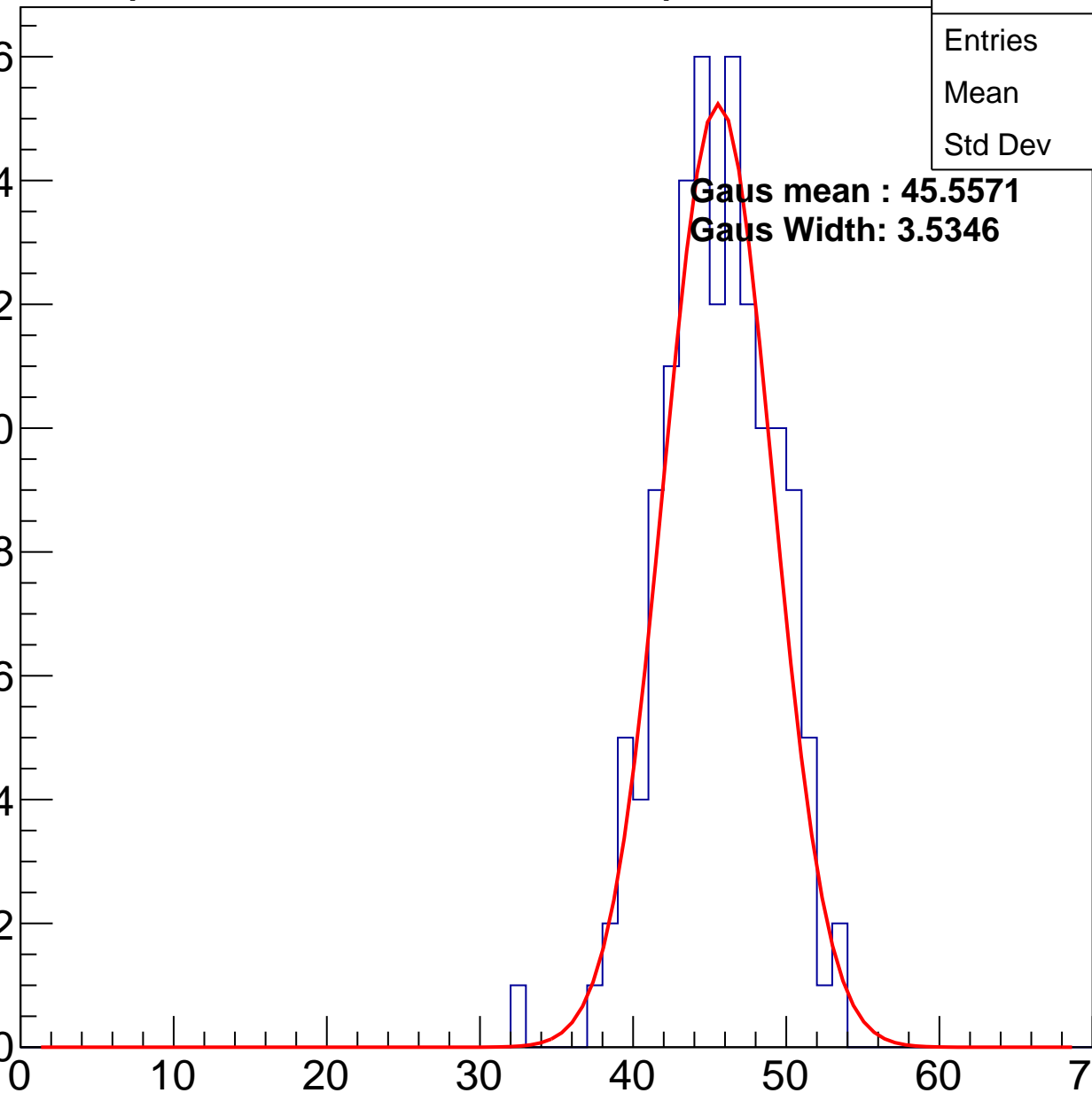
Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 140 |
| Mean | 45.07 |
| Std Dev | 3.607 |

Gaus mean : 45.5571
Gaus Width: 3.5346

ampl

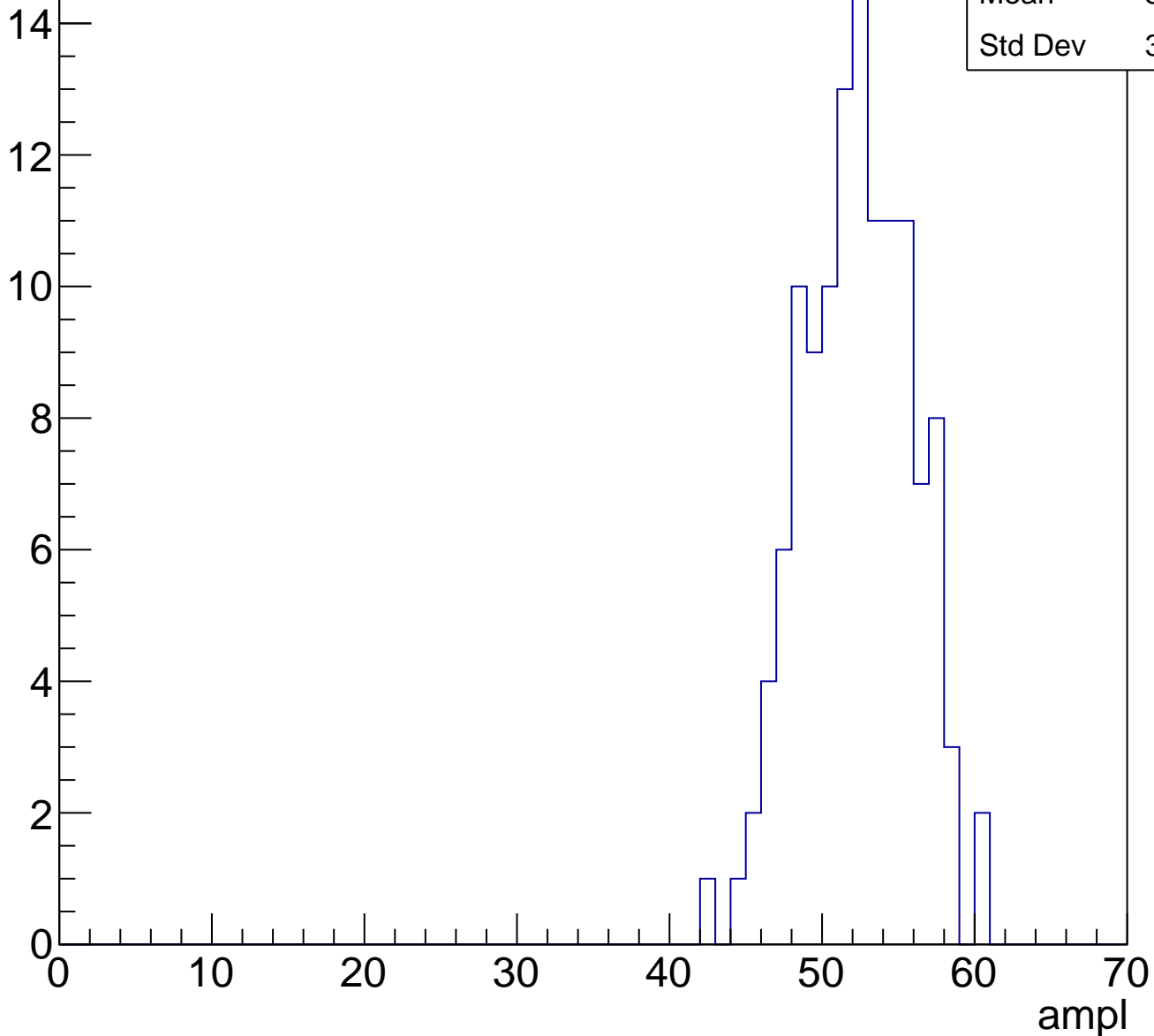


B1L001S, U26-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 124 |
| Mean | 51.85 |
| Std Dev | 3.535 |

Entry



B1L001S, U26-ch123, adc4

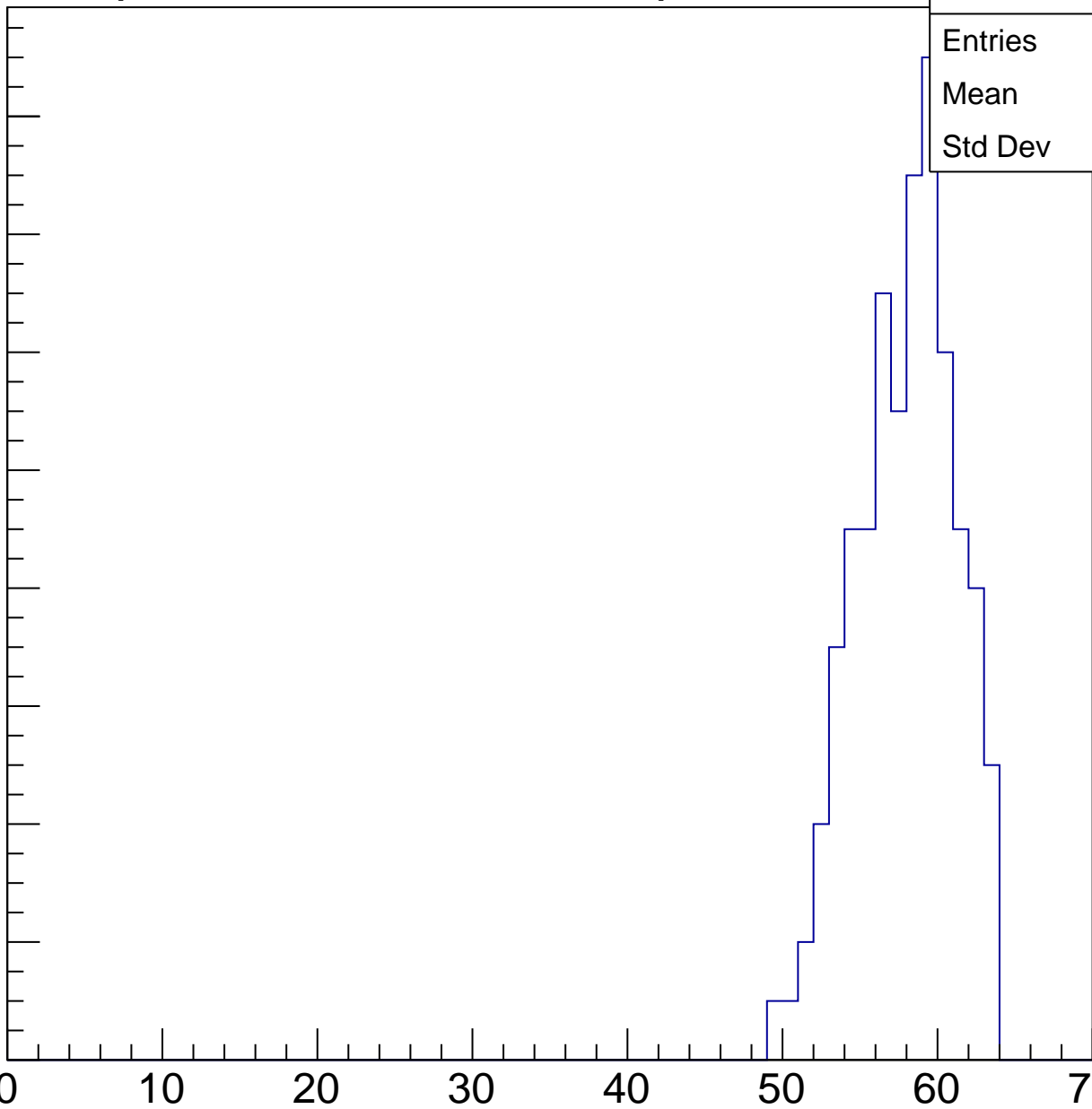
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 123 |
| Mean | 57.47 |
| Std Dev | 3.142 |

ampl



B1L001S, U26-ch123, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

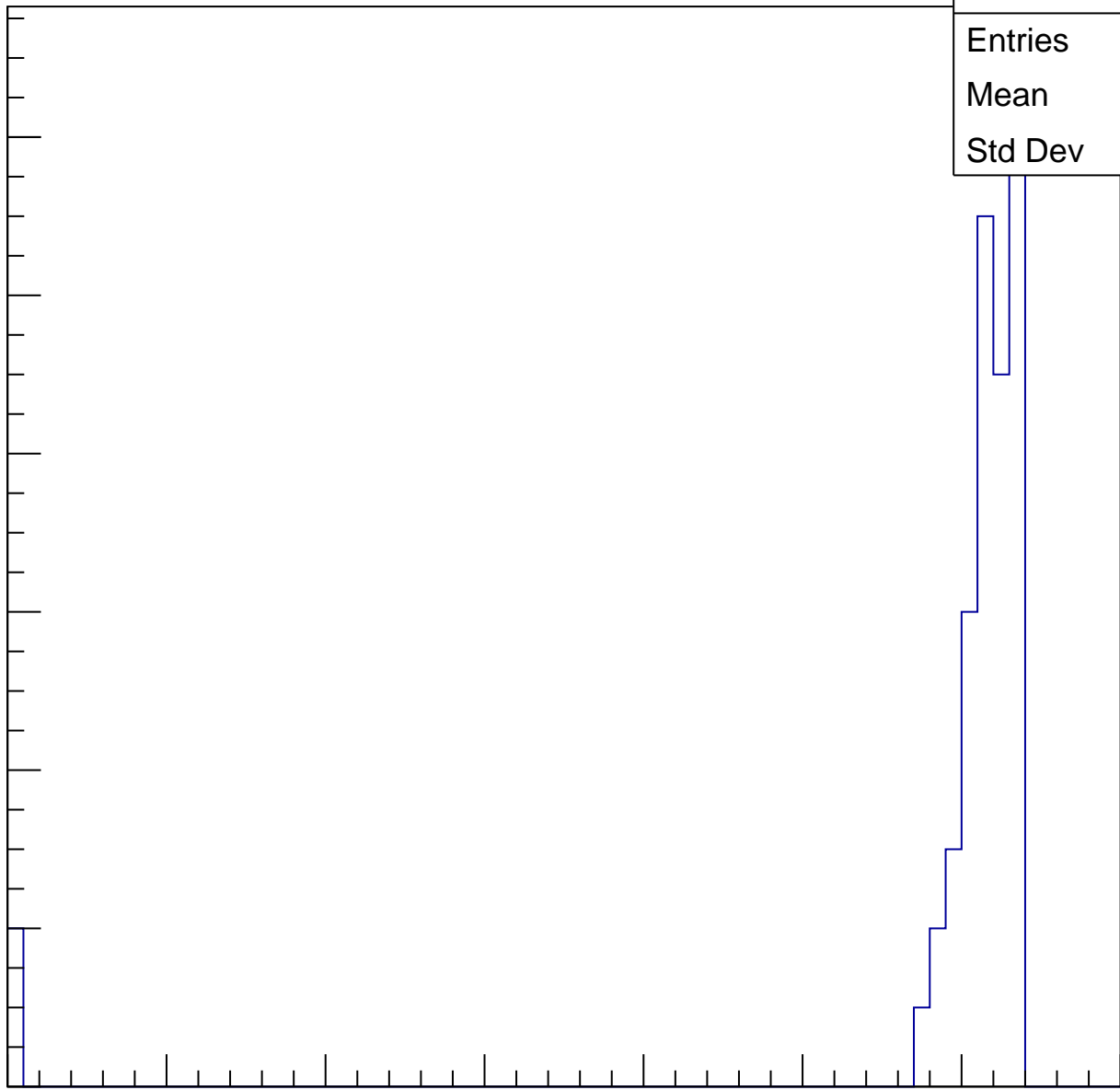
2

0

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.68 |
| Std Dev | 12.46 |

ampl

0 10 20 30 40 50 60 70



B1L001S, U26-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch124, adc0

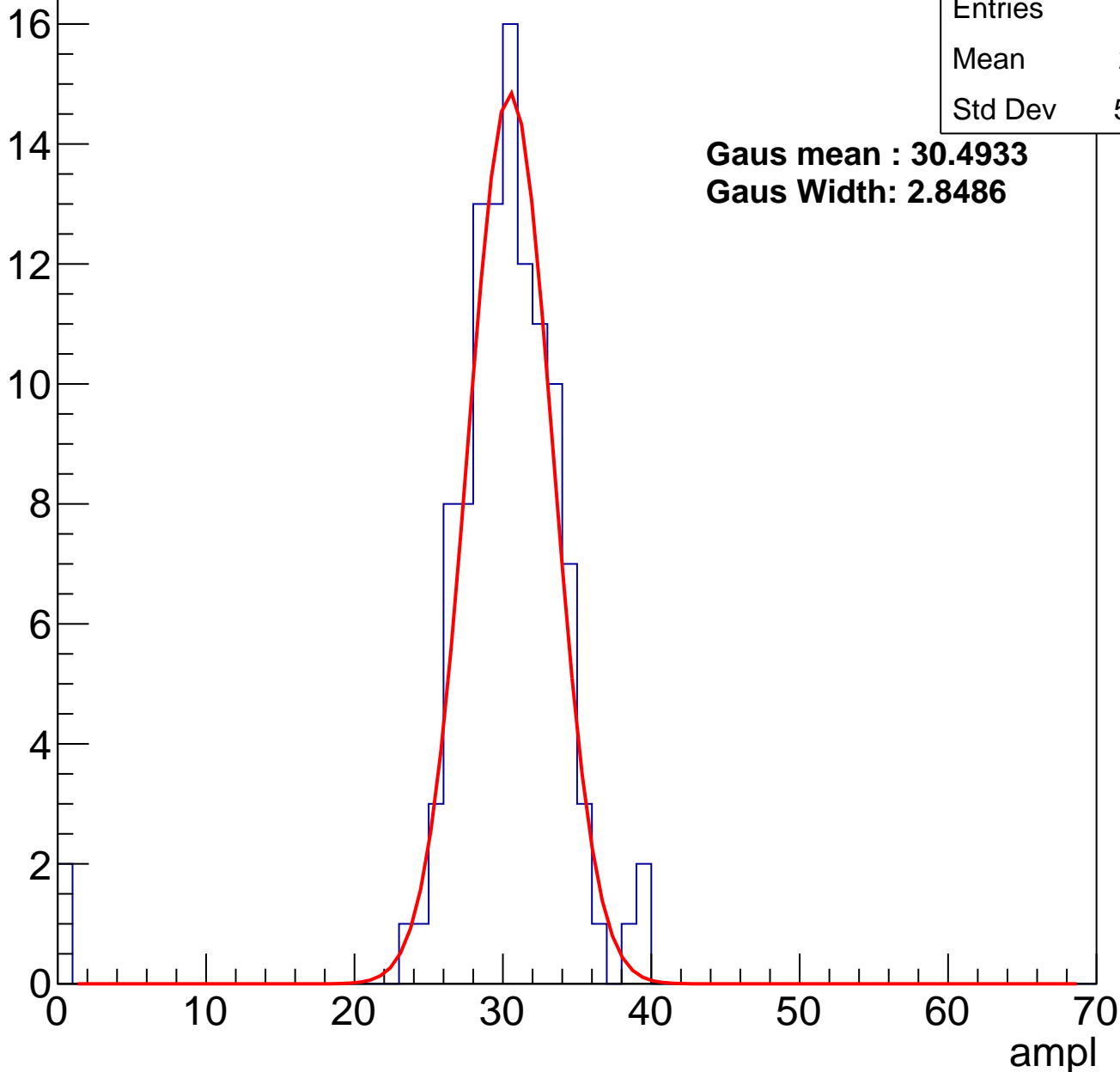
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 112 |
| Mean | 29.61 |
| Std Dev | 5.002 |

Gaus mean : 30.4933

Gaus Width: 2.8486

Entry



B1L001S, U26-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 128 |
| Mean | 36.48 |
| Std Dev | 3.507 |

Gaus mean : 36.4649

Gaus Width: 3.5456

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

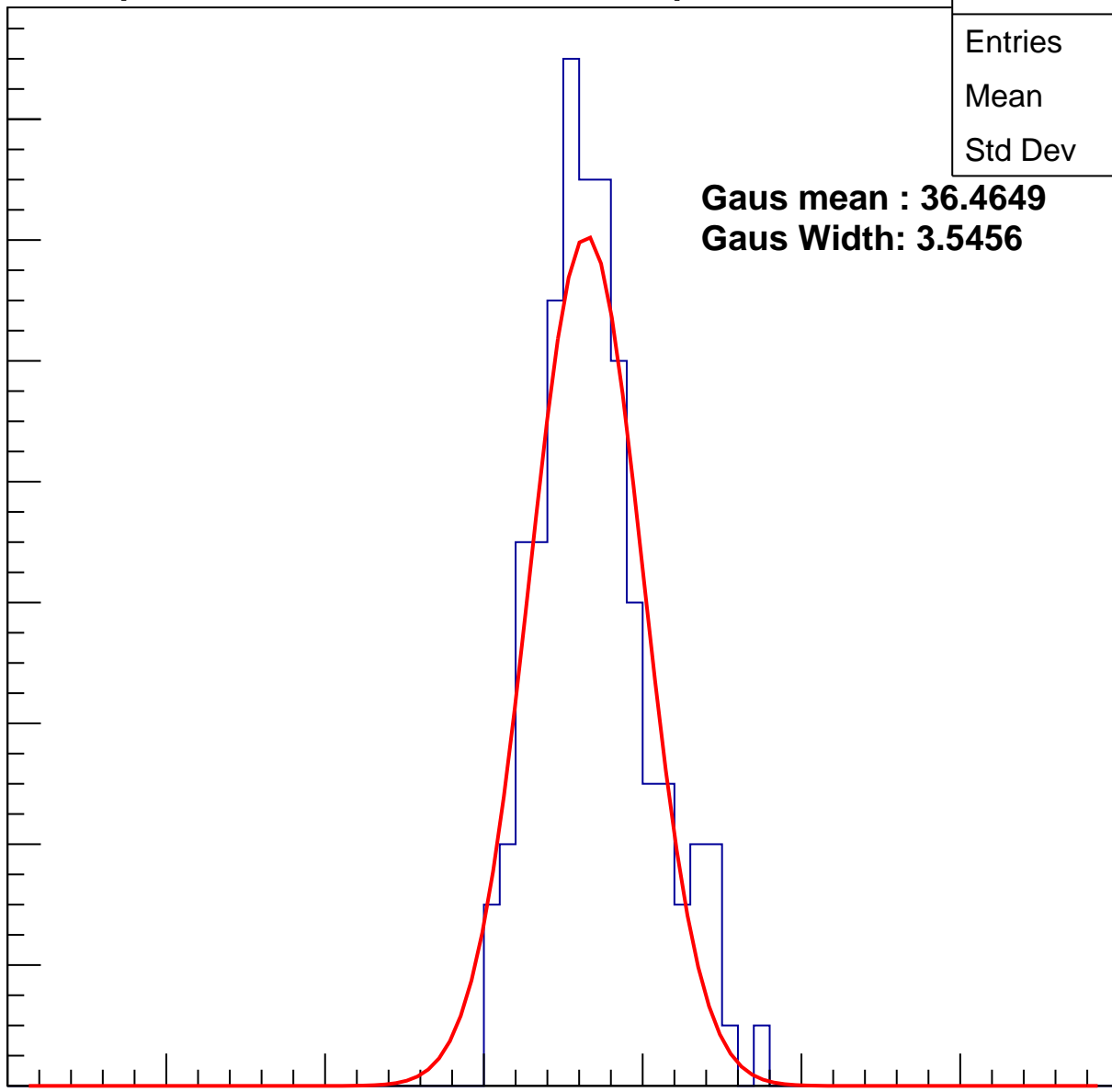
40

50

60

70

ampl



B1L001S, U26-ch124, adc2

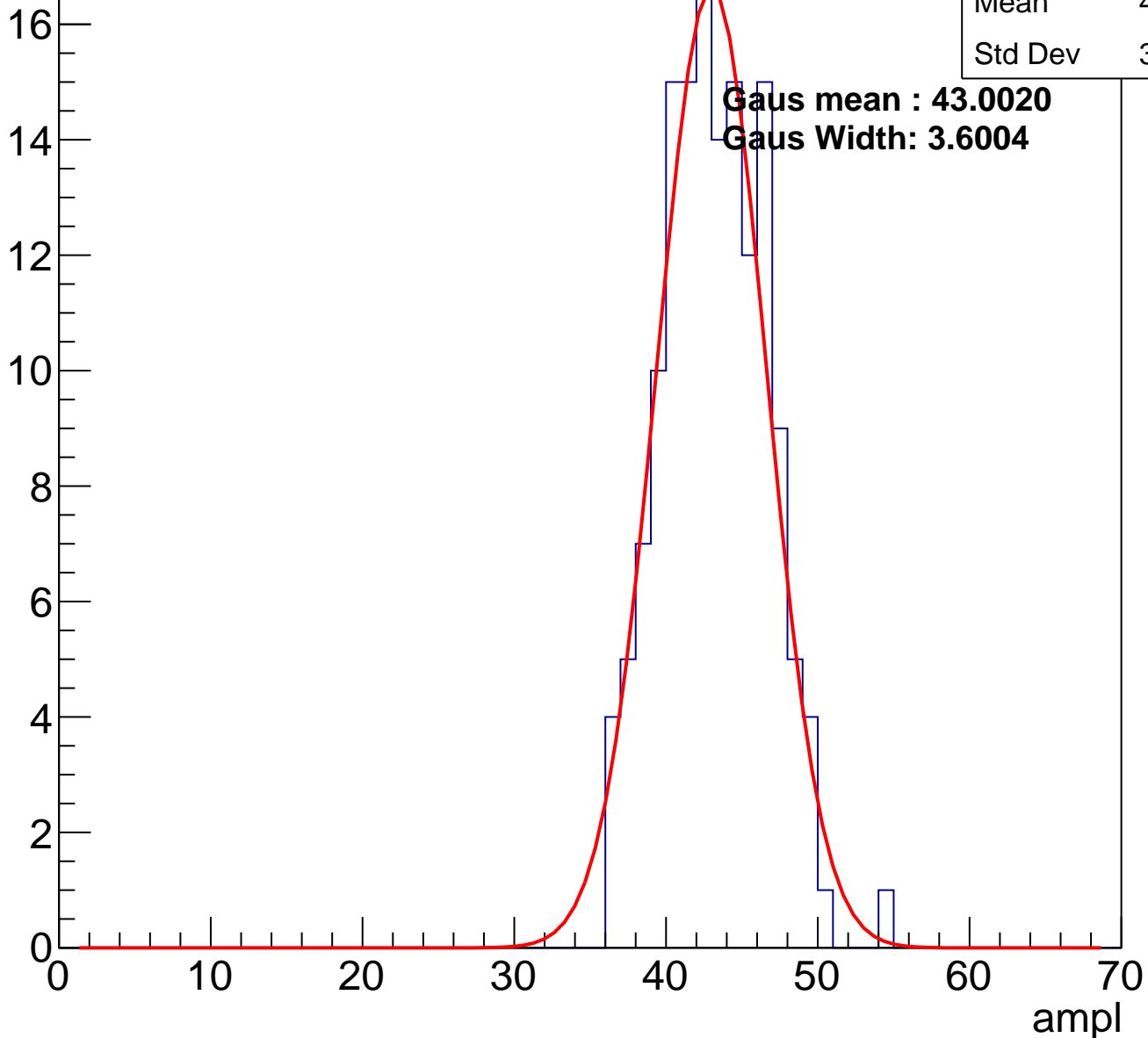
calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 149 |
| Mean | 42.74 |
| Std Dev | 3.372 |

Gaus mean : 43.0020

Gaus Width: 3.6004

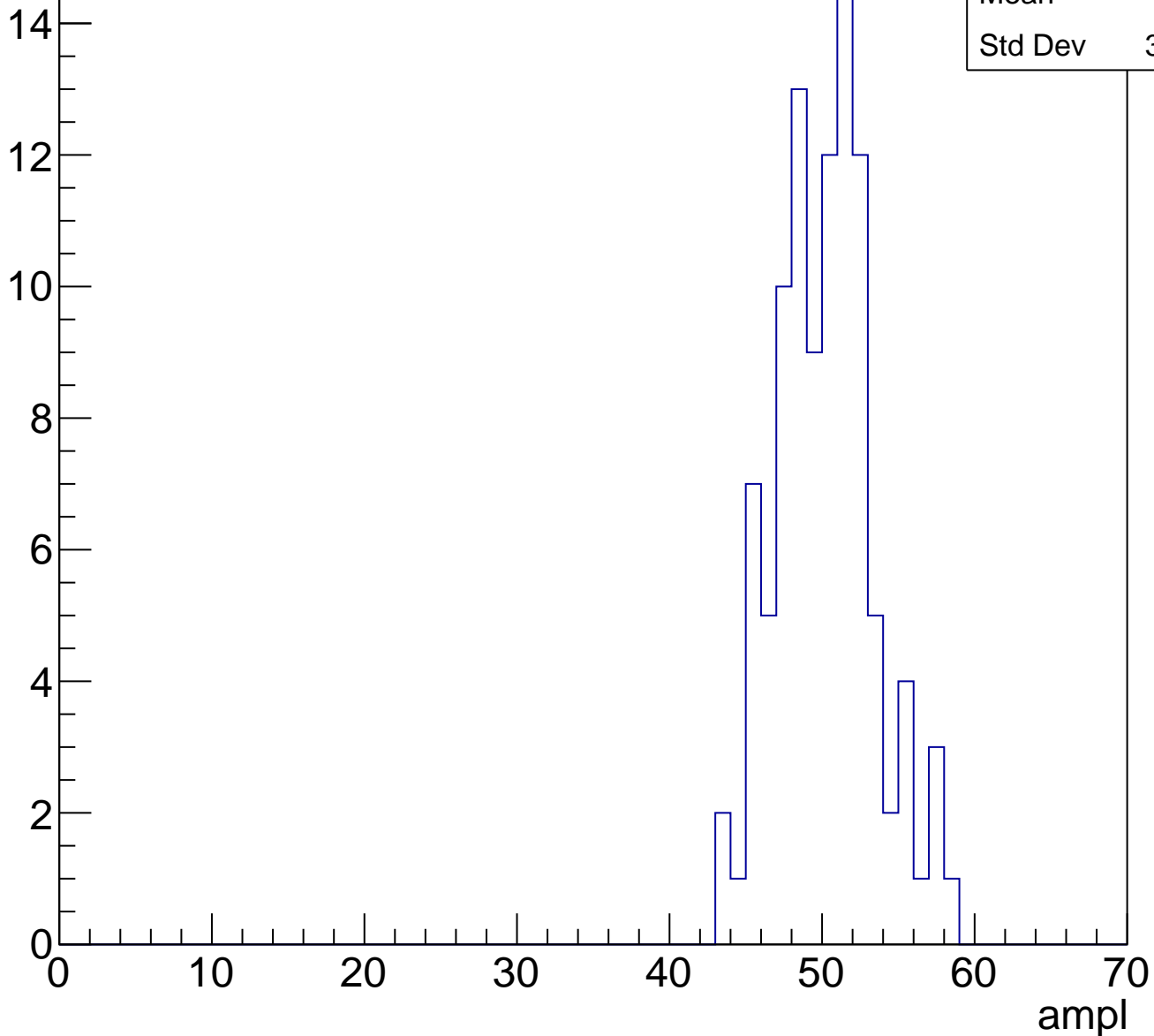


B1L001S, U26-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 102 |
| Mean | 49.77 |
| Std Dev | 3.165 |

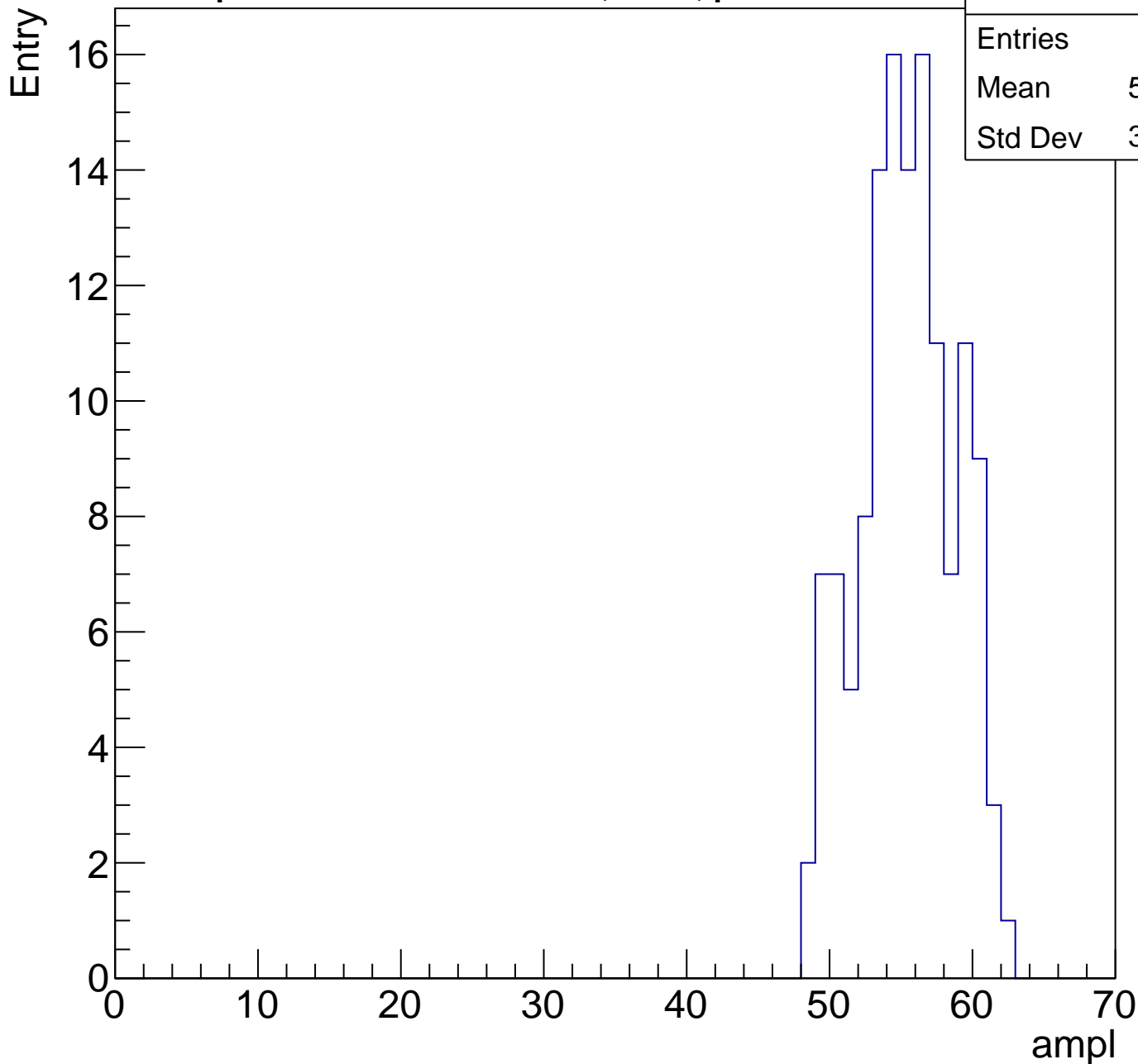
Entry



B1L001S, U26-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 131 |
| Mean | 54.95 |
| Std Dev | 3.304 |



B1L001S, U26-ch124, adc5

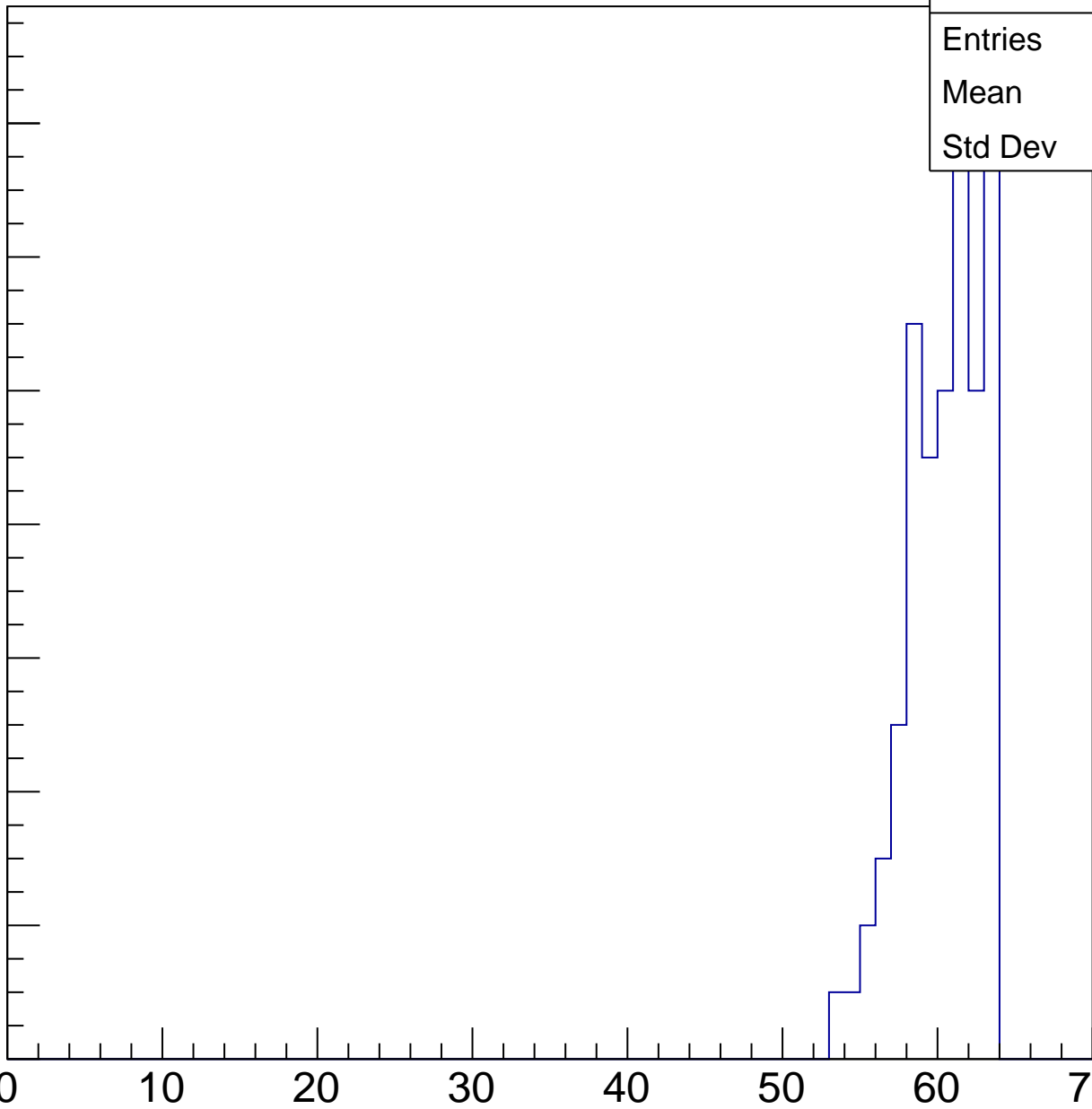
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 81 |
| Mean | 59.95 |
| Std Dev | 2.388 |

ampl

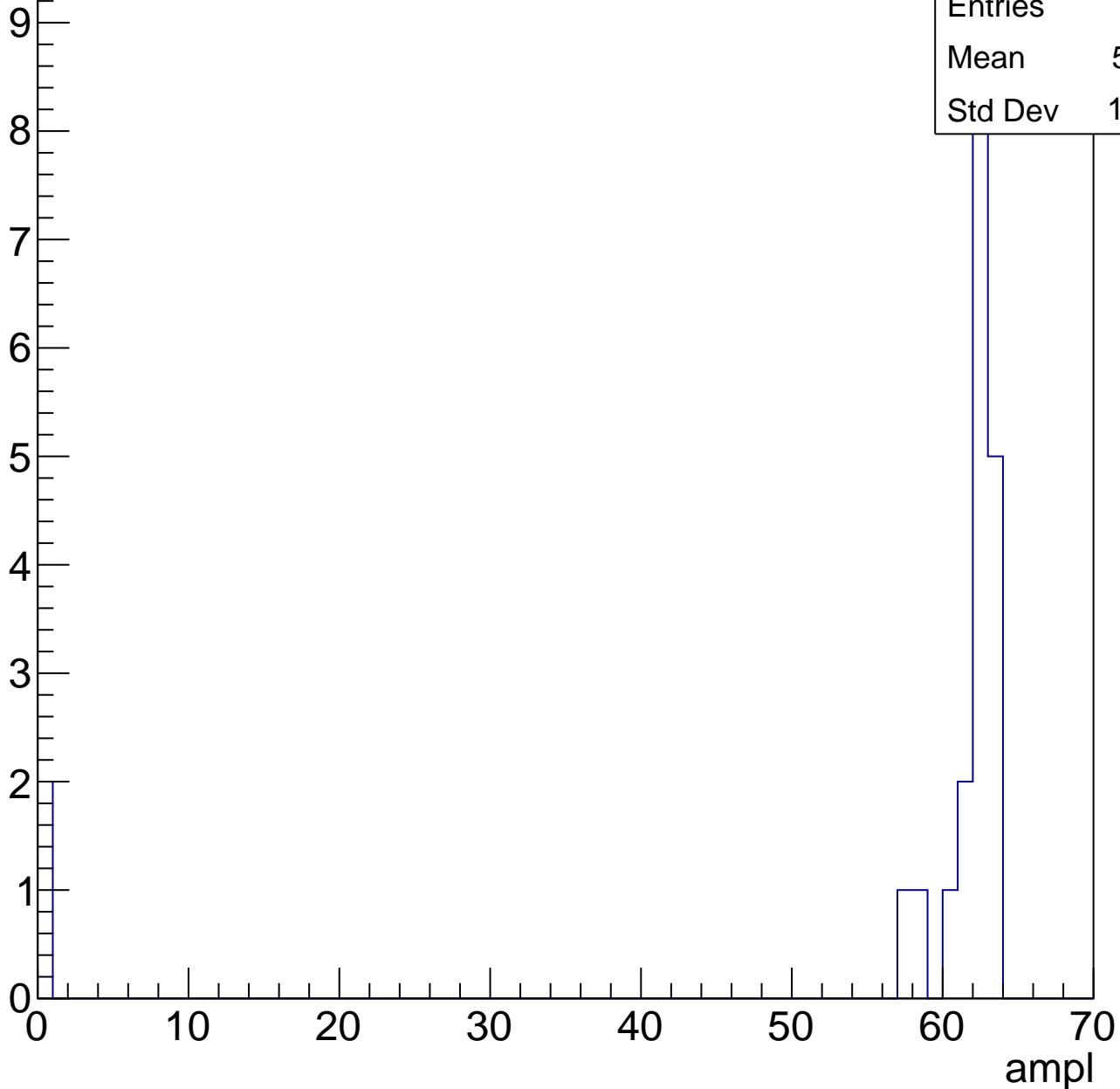


B1L001S, U26-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

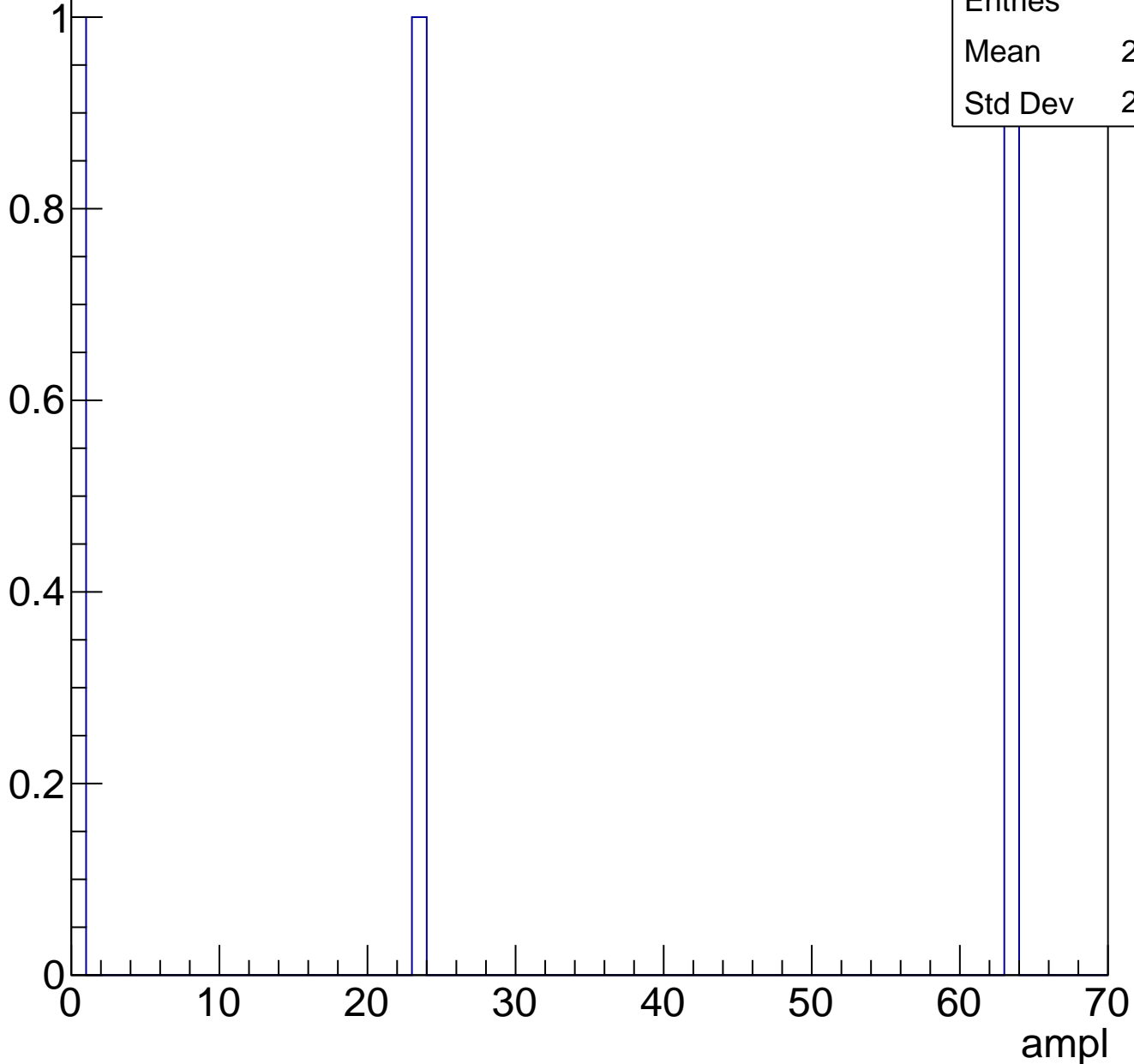
| | |
|---------|-------|
| Entries | 21 |
| Mean | 55.71 |
| Std Dev | 18.14 |



B1L001S, U26-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

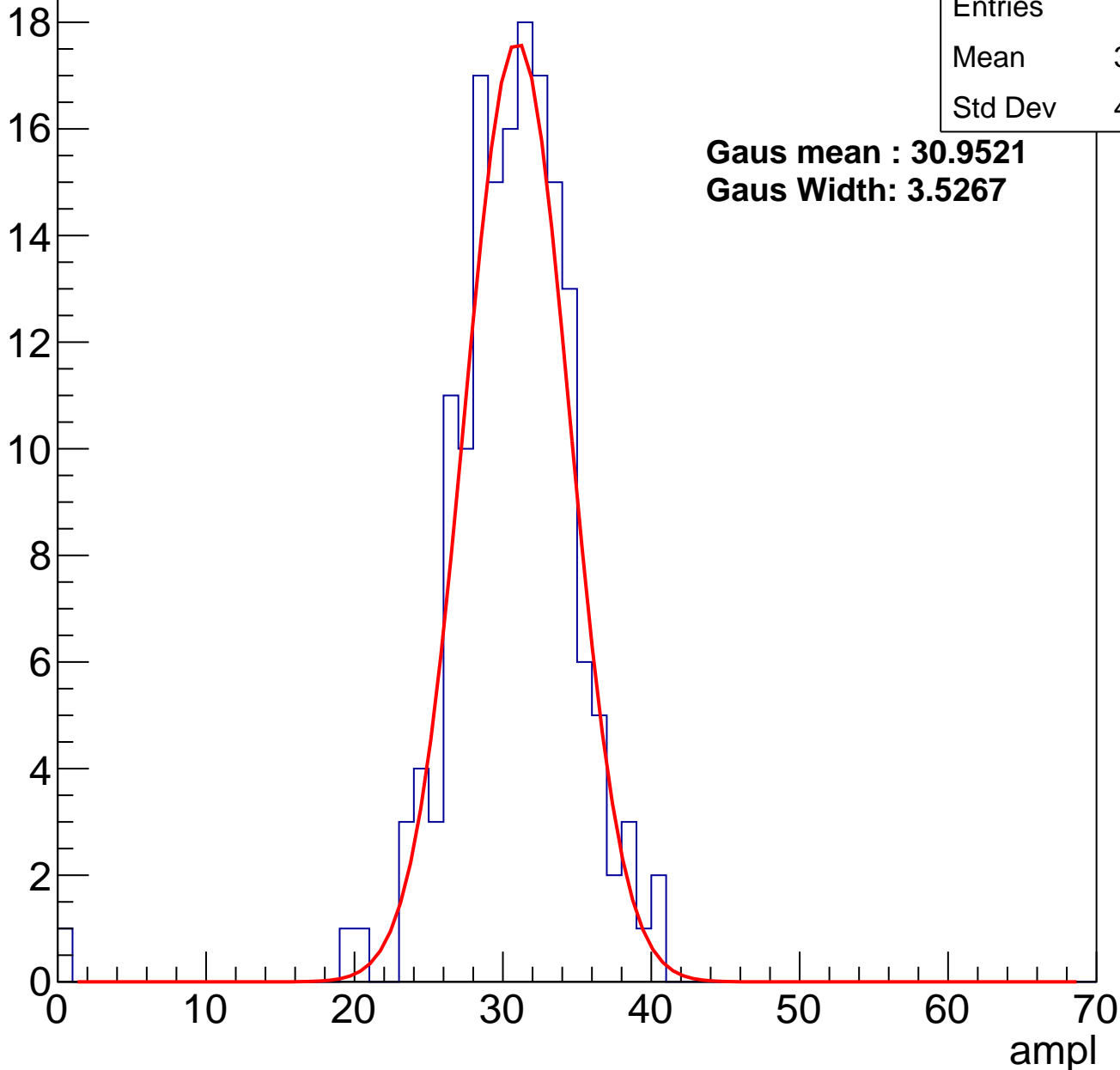


| | |
|---------|-------|
| Entries | 3 |
| Mean | 28.67 |
| Std Dev | 26.03 |

B1L001S, U26-ch125, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch125, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 138 |
| Mean | 38.36 |
| Std Dev | 3.518 |

Gaus mean : 39.1119

Gaus Width: 3.5325

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

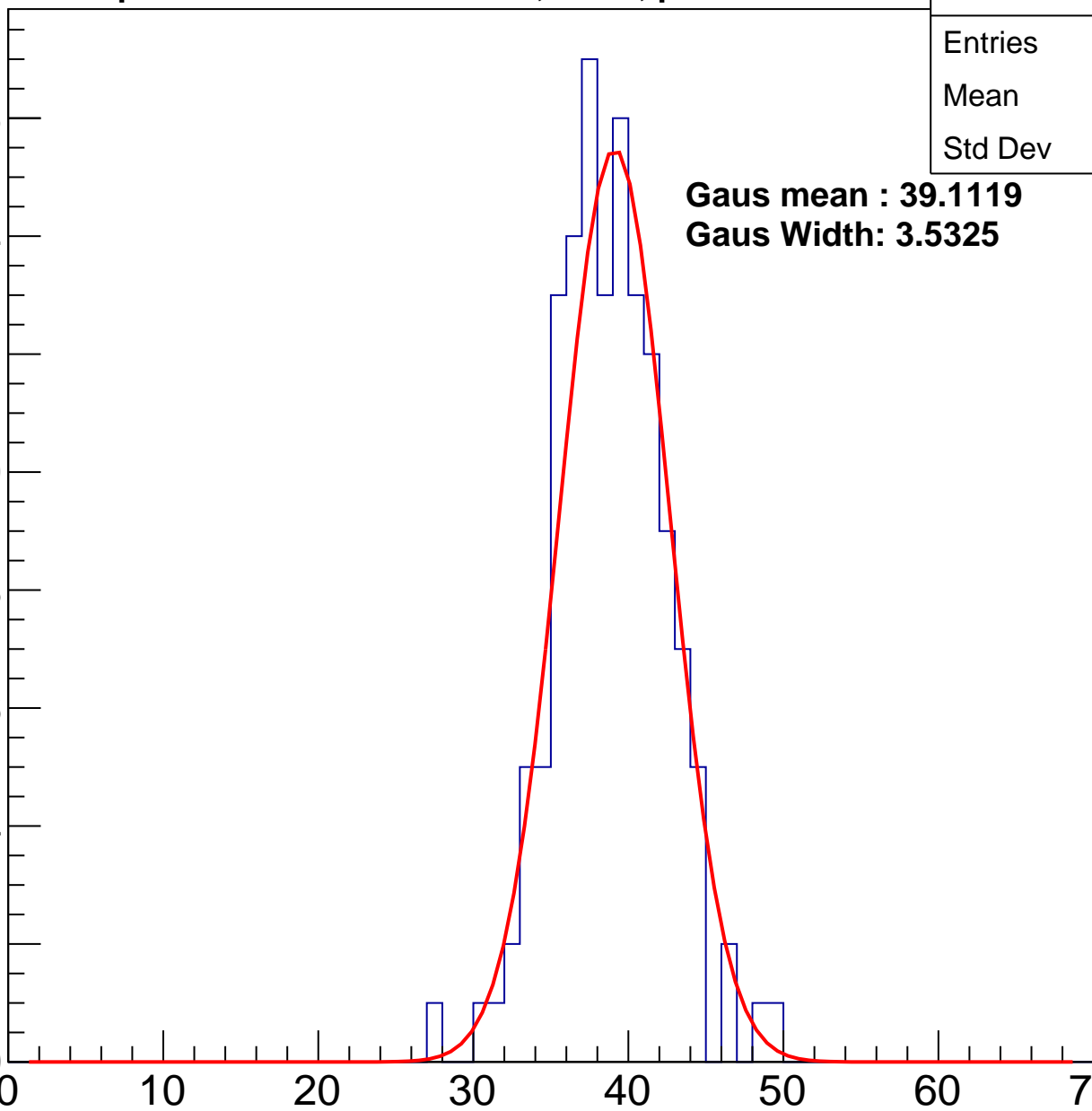
40

50

60

70

ampl



B1L001S, U26-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

103

Mean

44.18

Std Dev

3.757

Gaus mean : 44.7256

Gaus Width: 3.6668

0

10

20

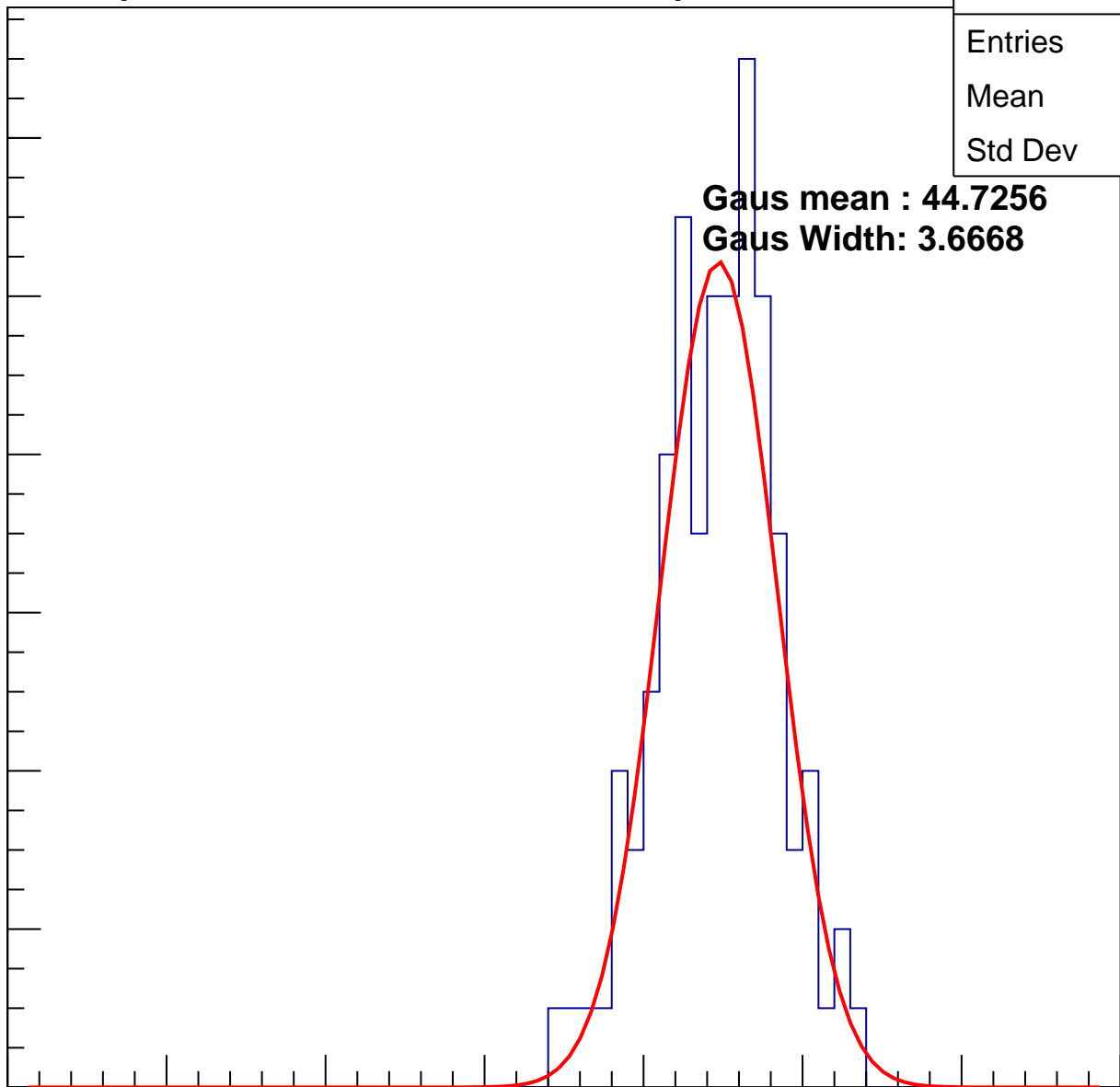
30

40

50

60

ampl

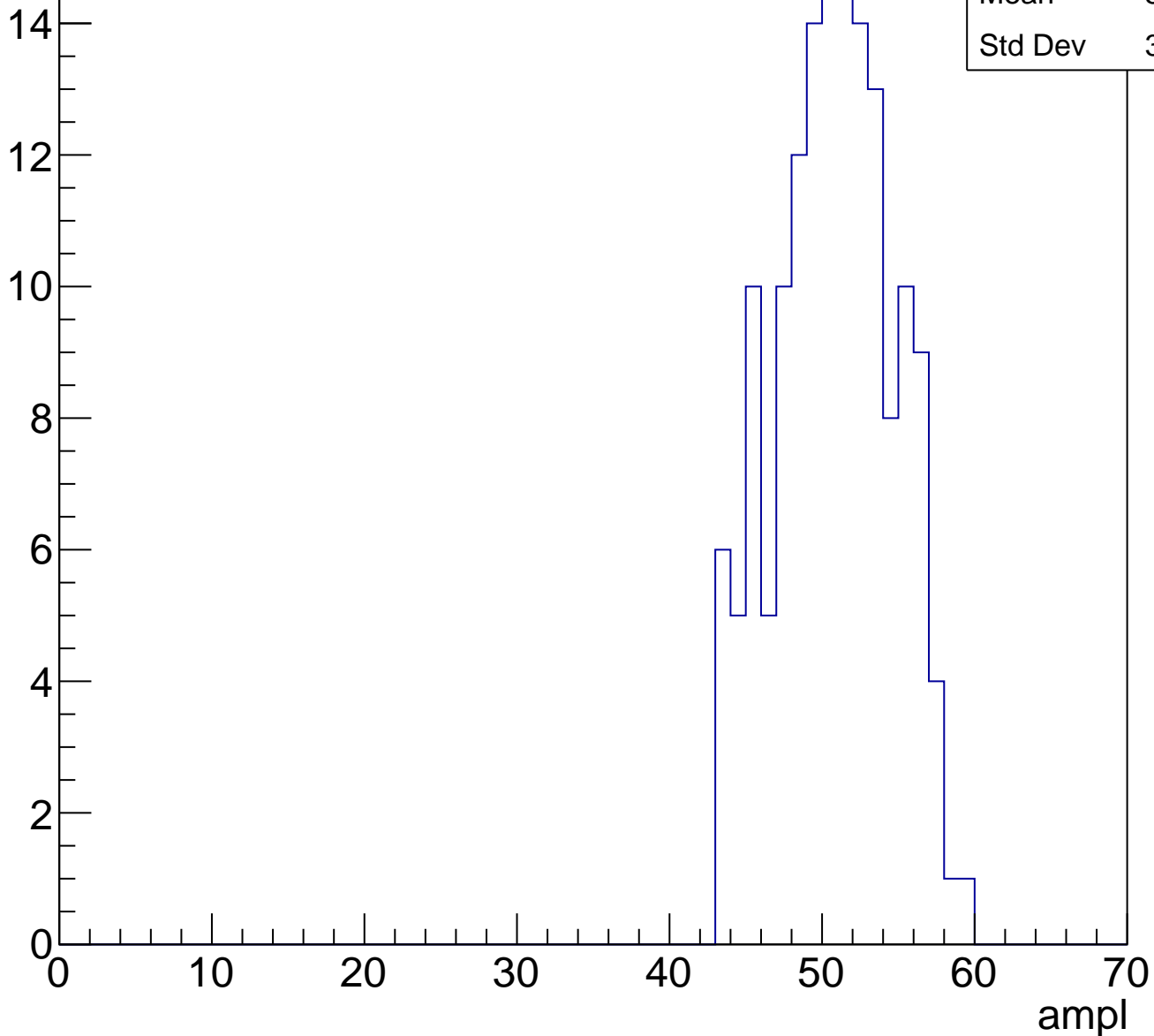


B1L001S, U26-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 152 |
| Mean | 50.35 |
| Std Dev | 3.777 |

Entry



B1L001S, U26-ch125, adc4

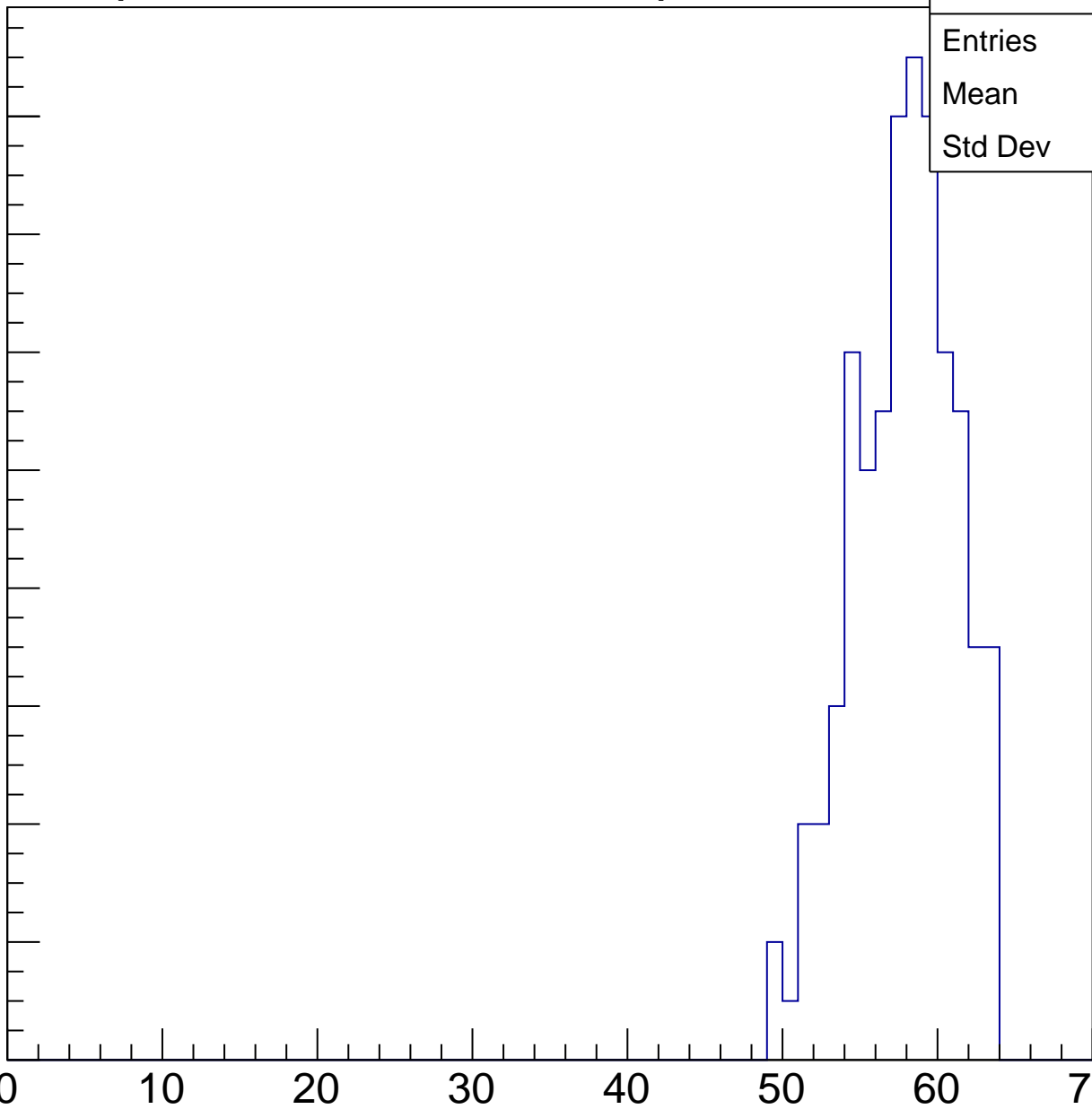
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 136 |
| Mean | 57.35 |
| Std Dev | 3.266 |

ampl



B1L001S, U26-ch125, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

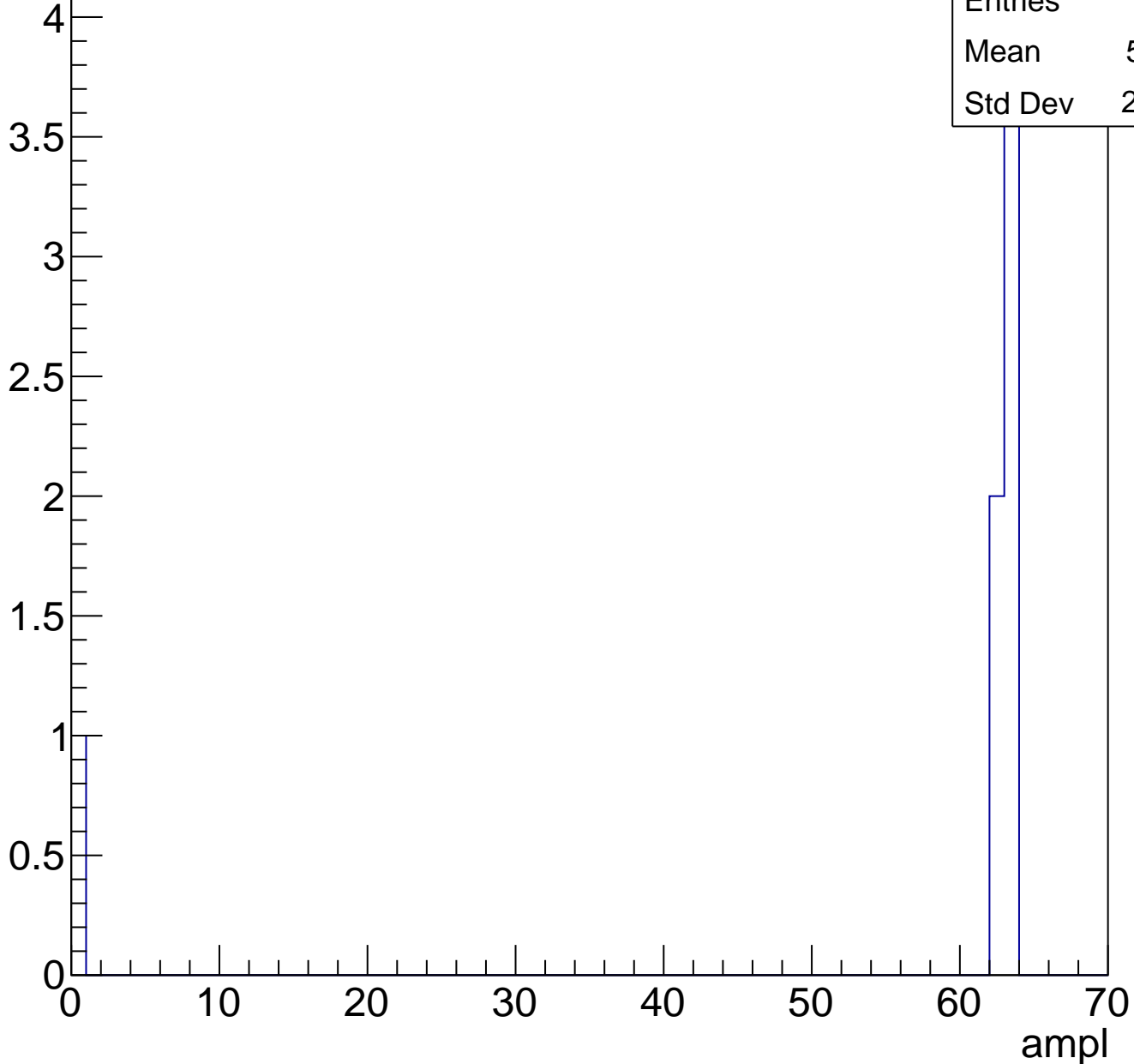
ampl

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.79 |
| Std Dev | 9.33 |

B1L001S, U26-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



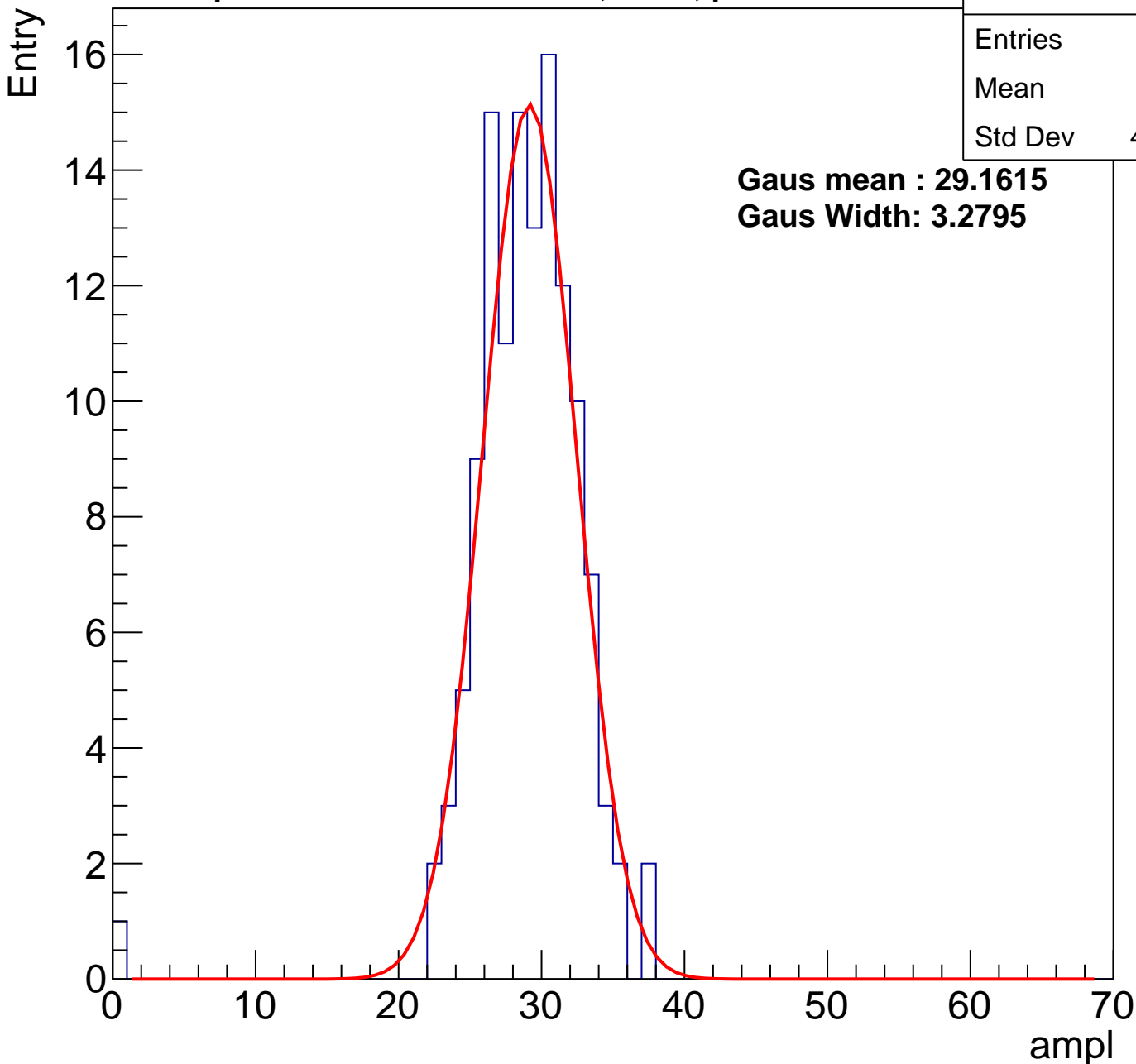
B1L001S, U26-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 126 |
| Mean | 28.5 |
| Std Dev | 4.003 |

Gaus mean : 29.1615

Gaus Width: 3.2795



B1L001S, U26-ch126, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 146 |
| Mean | 35.73 |
| Std Dev | 3.661 |

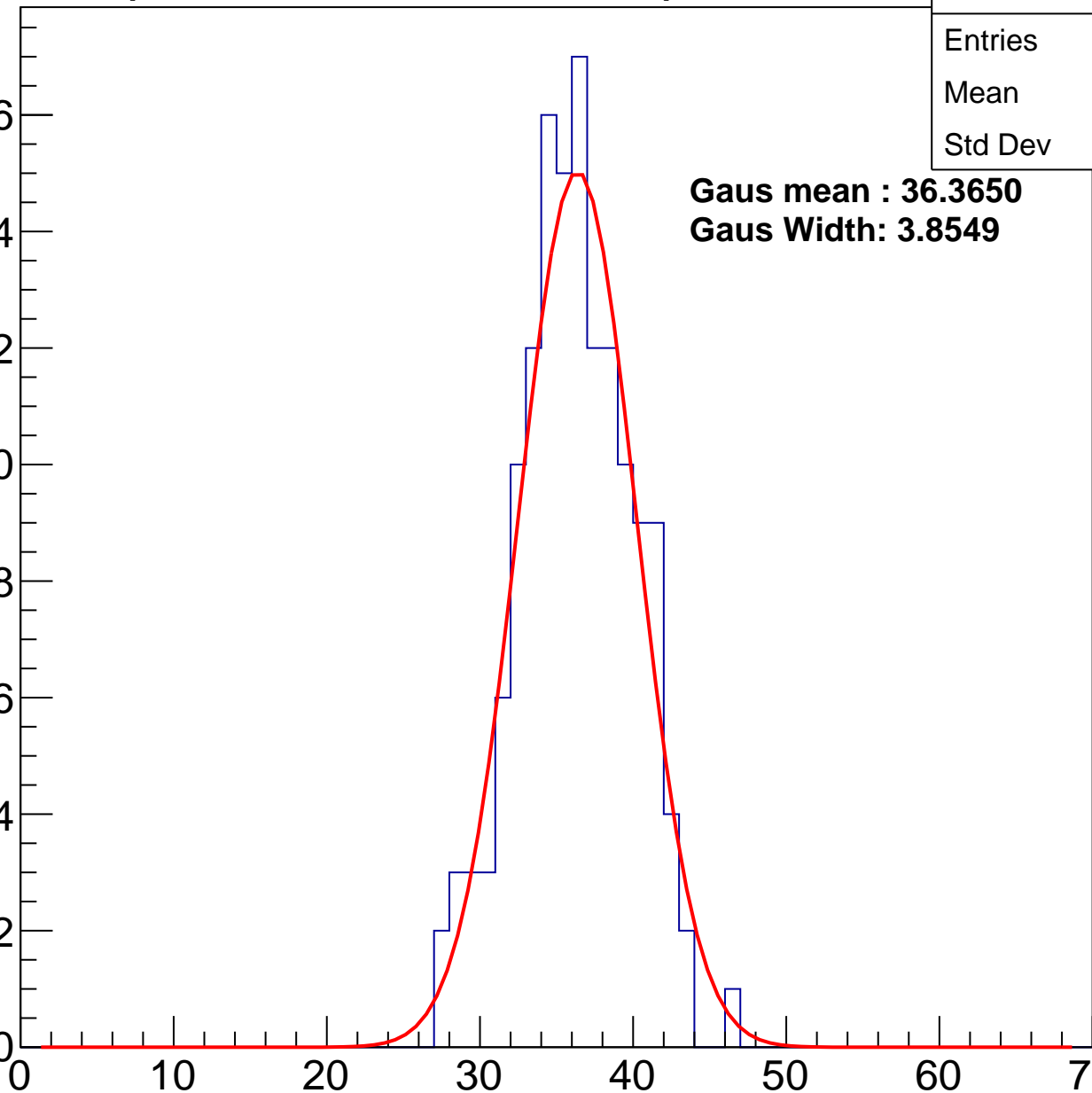
Gaus mean : 36.3650

Gaus Width: 3.8549

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U26-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

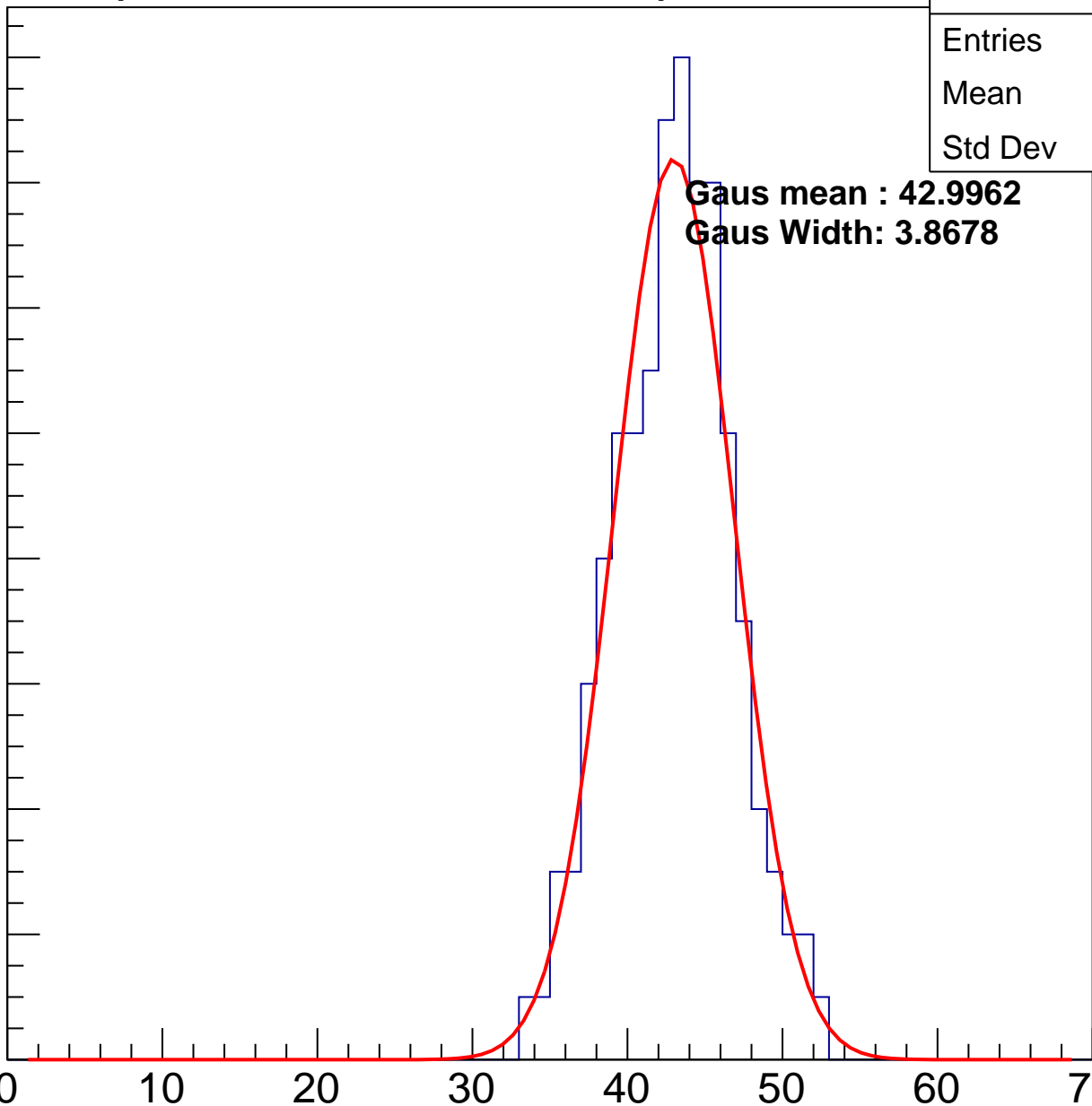
16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 141 |
| Mean | 42.5 |
| Std Dev | 3.764 |

Gaus mean : 42.9962

Gaus Width: 3.8678

ampl



B1L001S, U26-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

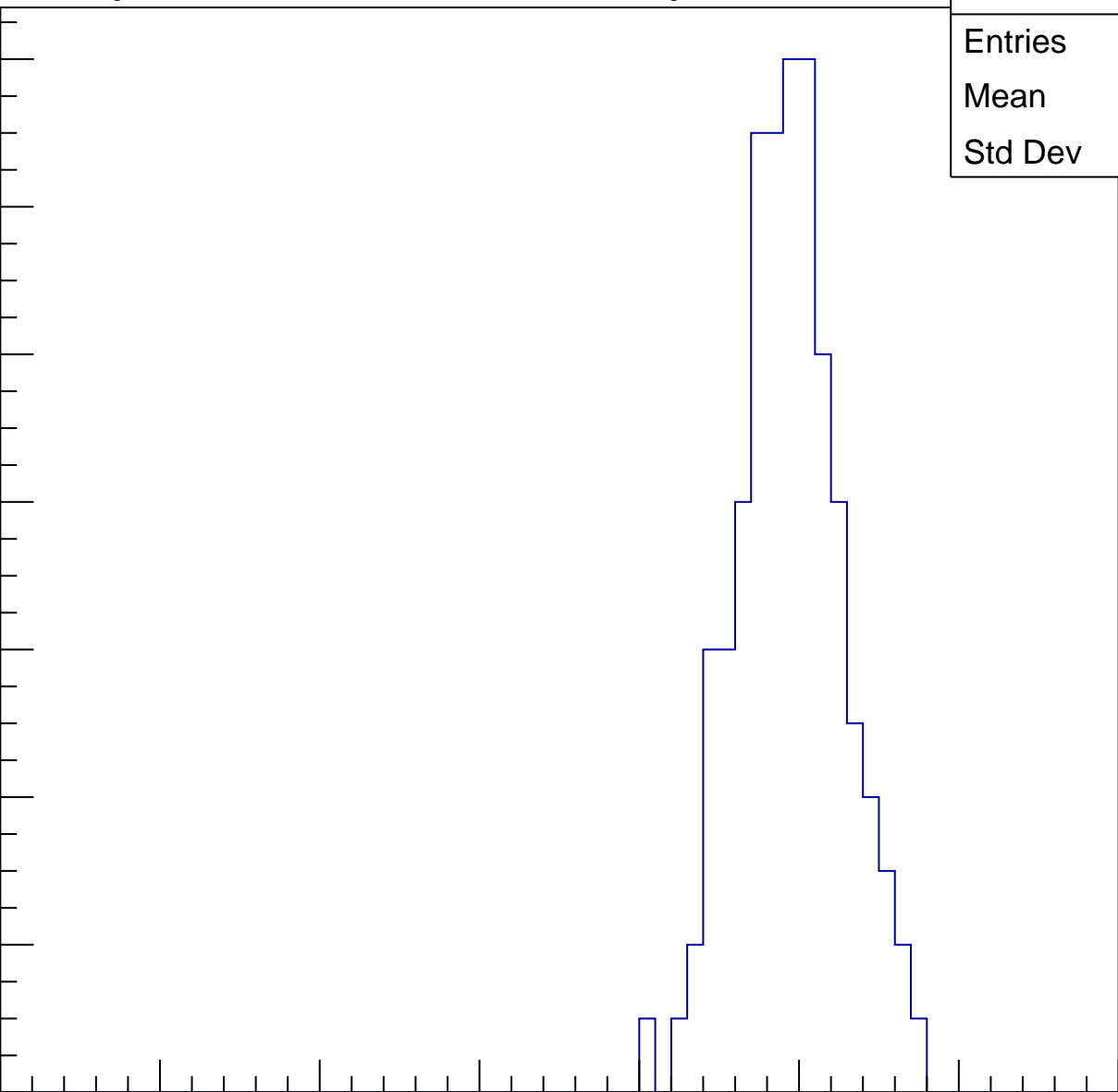
| | |
|---------|-------|
| Entries | 111 |
| Mean | 48.94 |
| Std Dev | 3.233 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

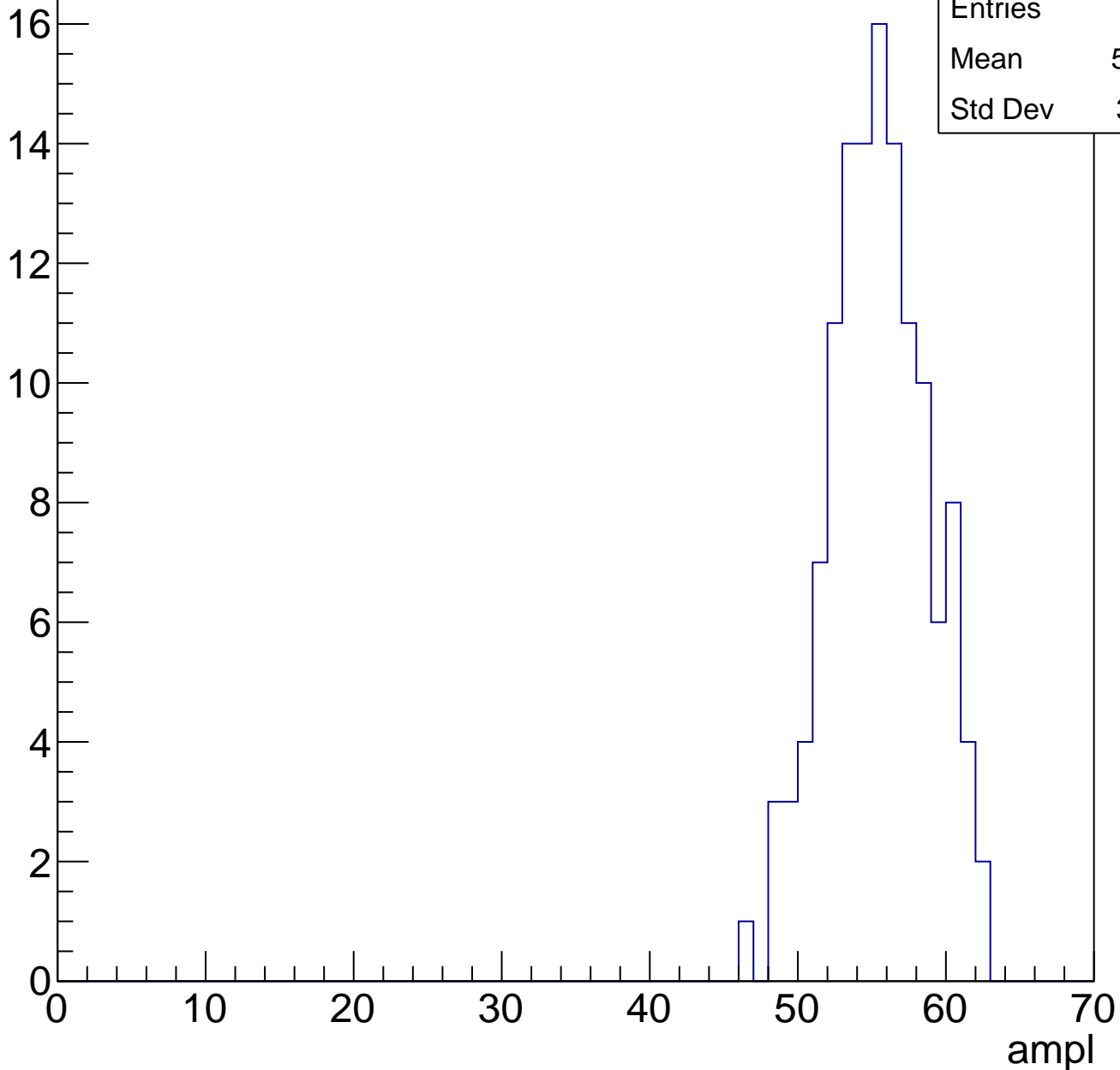
ampl



B1L001S, U26-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch126, adc5

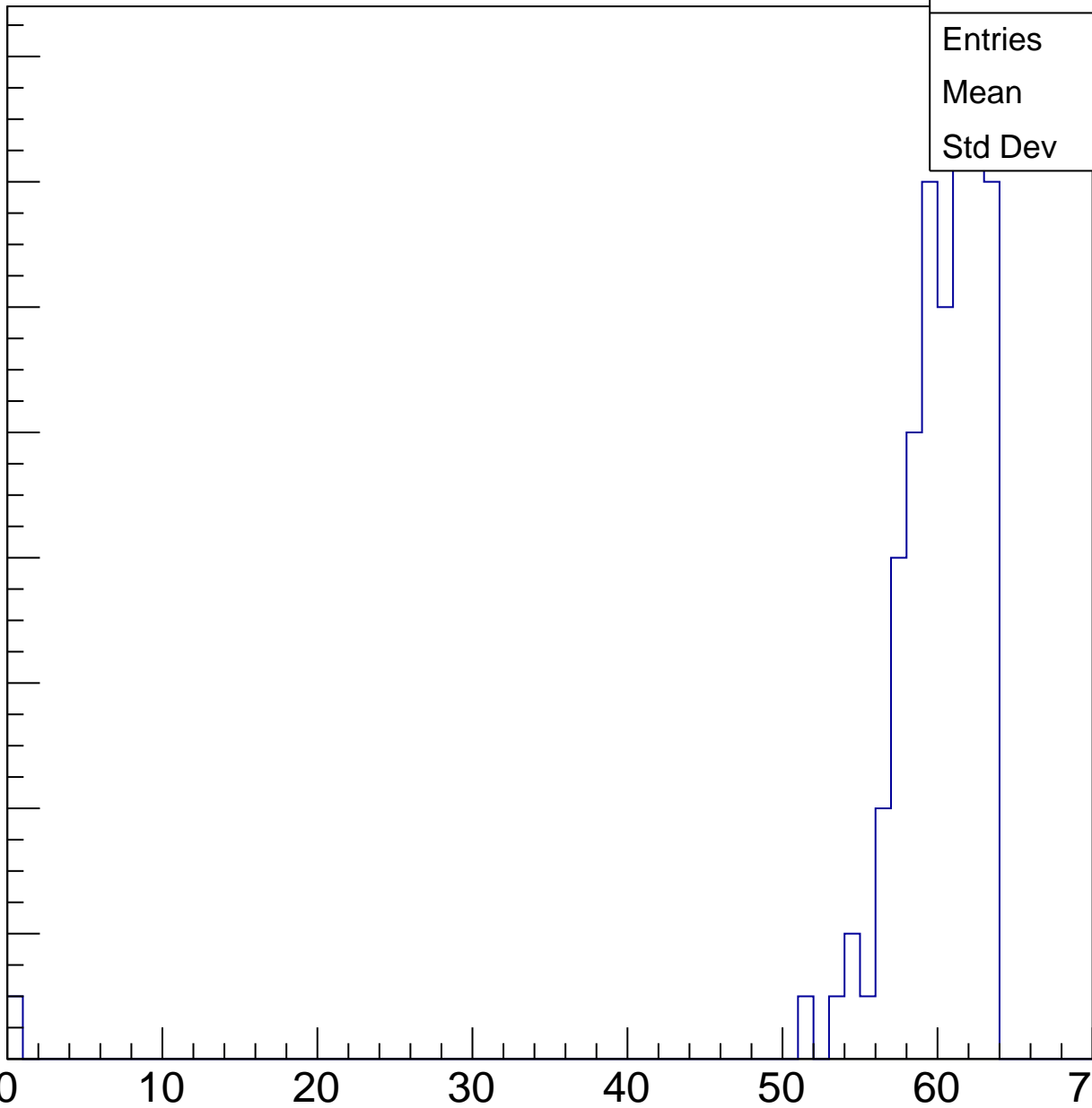
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 99 |
| Mean | 59.2 |
| Std Dev | 6.479 |

ampl

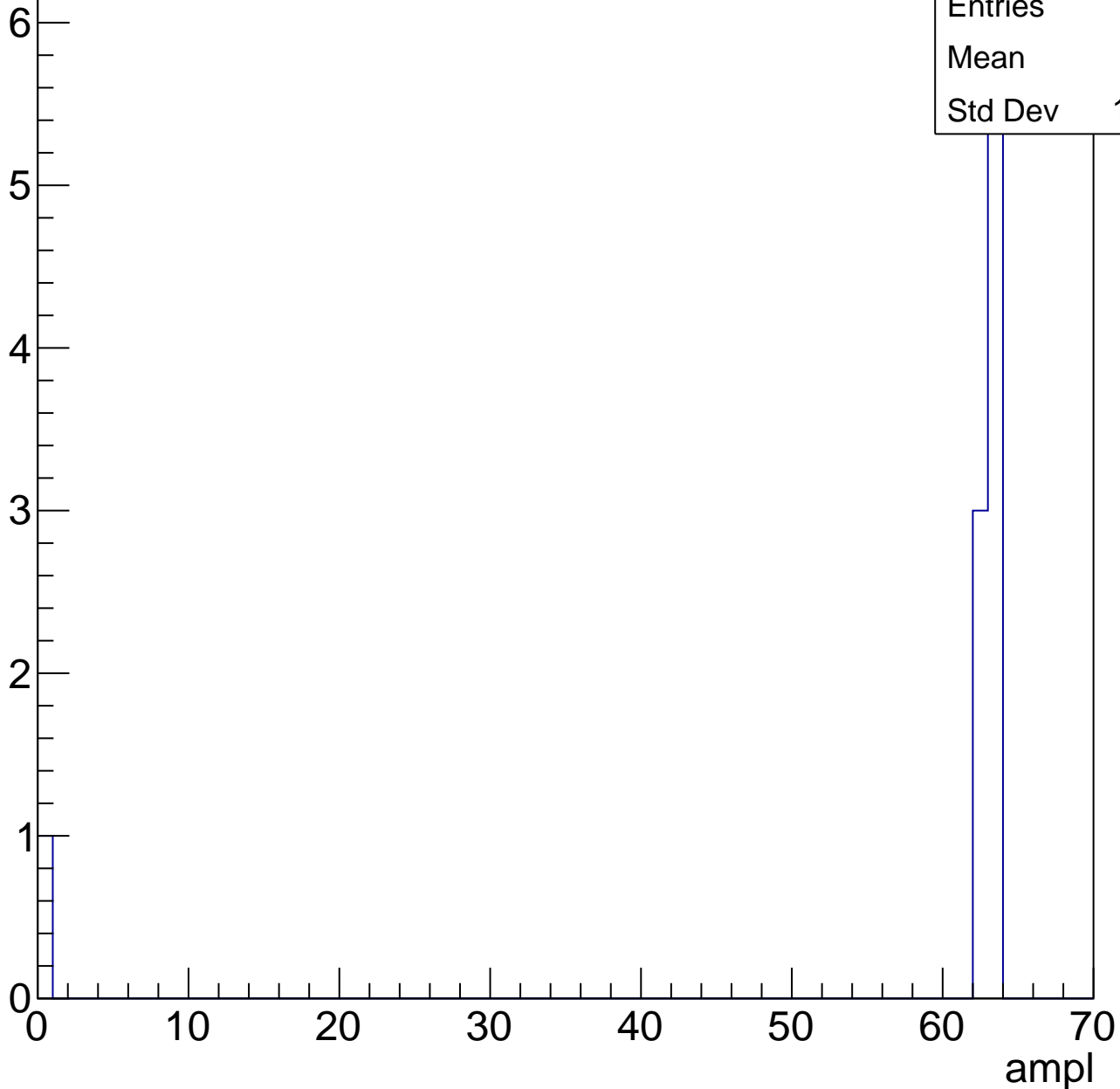


B1L001S, U26-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 56.4 |
| Std Dev | 18.81 |



B1L001S, U26-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 21 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch127, adc0

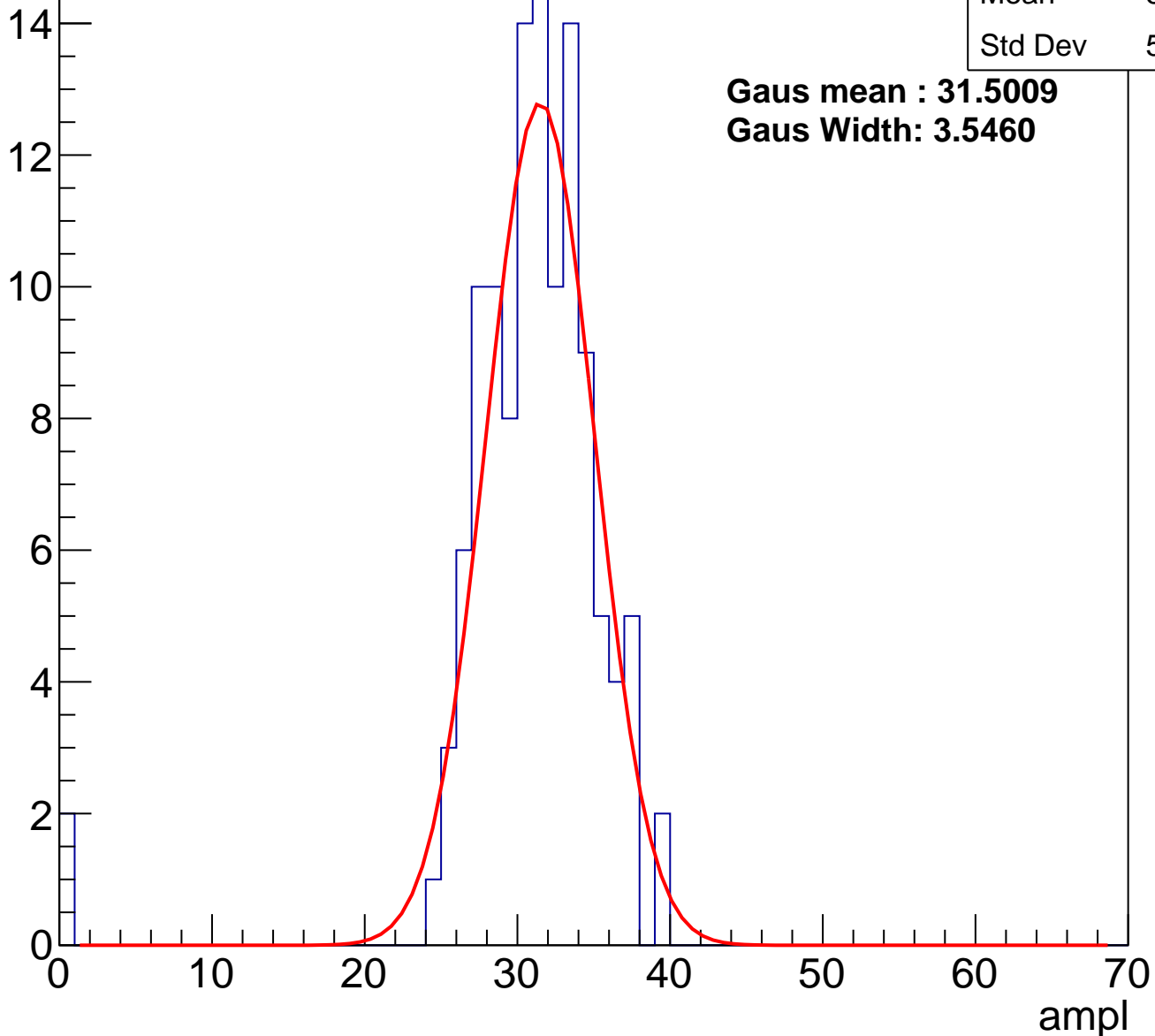
calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 118 |
| Mean | 30.44 |
| Std Dev | 5.145 |

Gaus mean : 31.5009

Gaus Width: 3.5460

Entry



B1L001S, U26-ch127, adc1

calib_packv5_042523_0143.root, FC#2, port C2

| | |
|---------|-------|
| Entries | 168 |
| Mean | 37.85 |
| Std Dev | 3.709 |

Gaus mean : 38.3556

Gaus Width: 3.9074

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

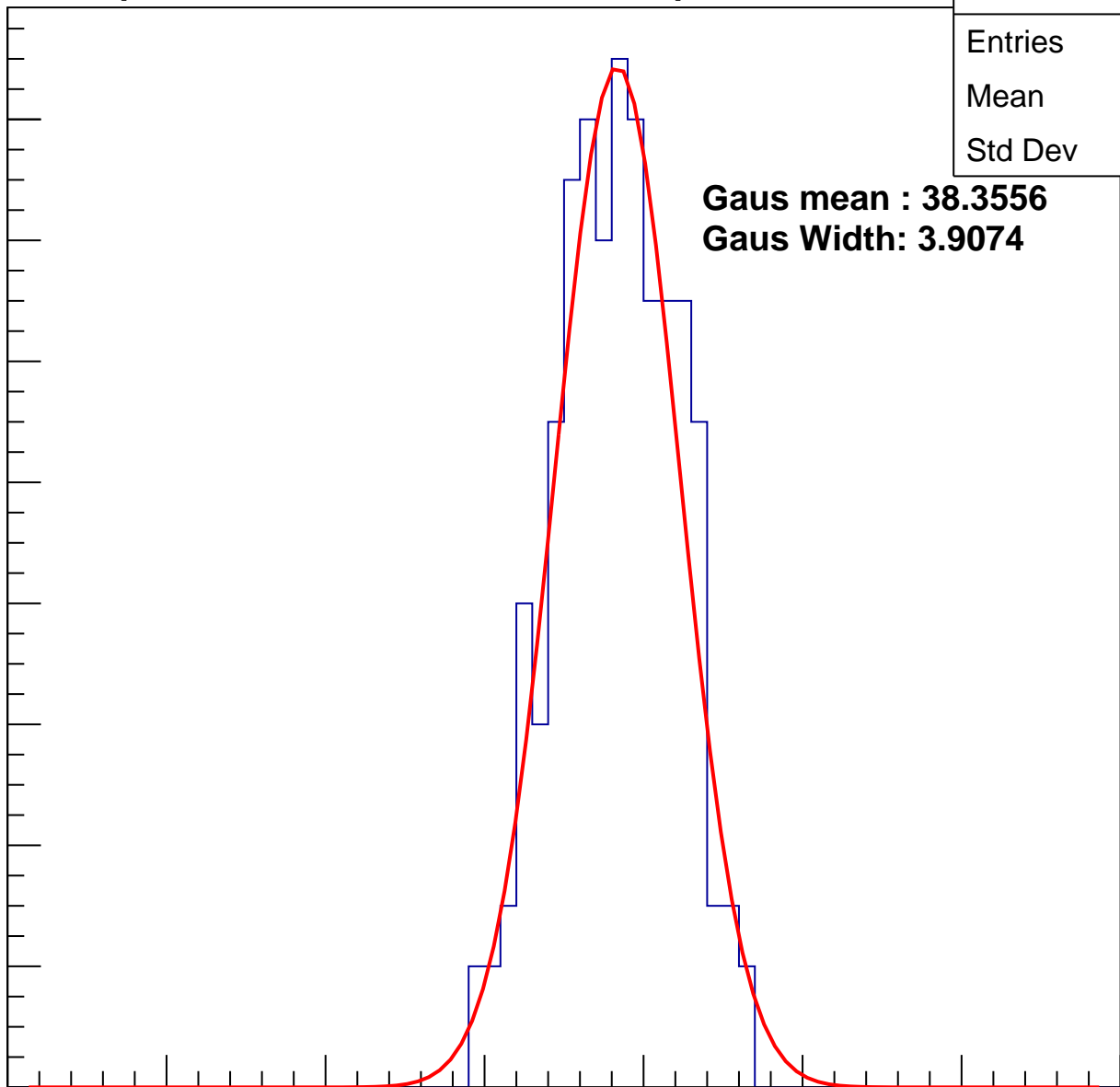
40

50

60

70

ampl



B1L001S, U26-ch127, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

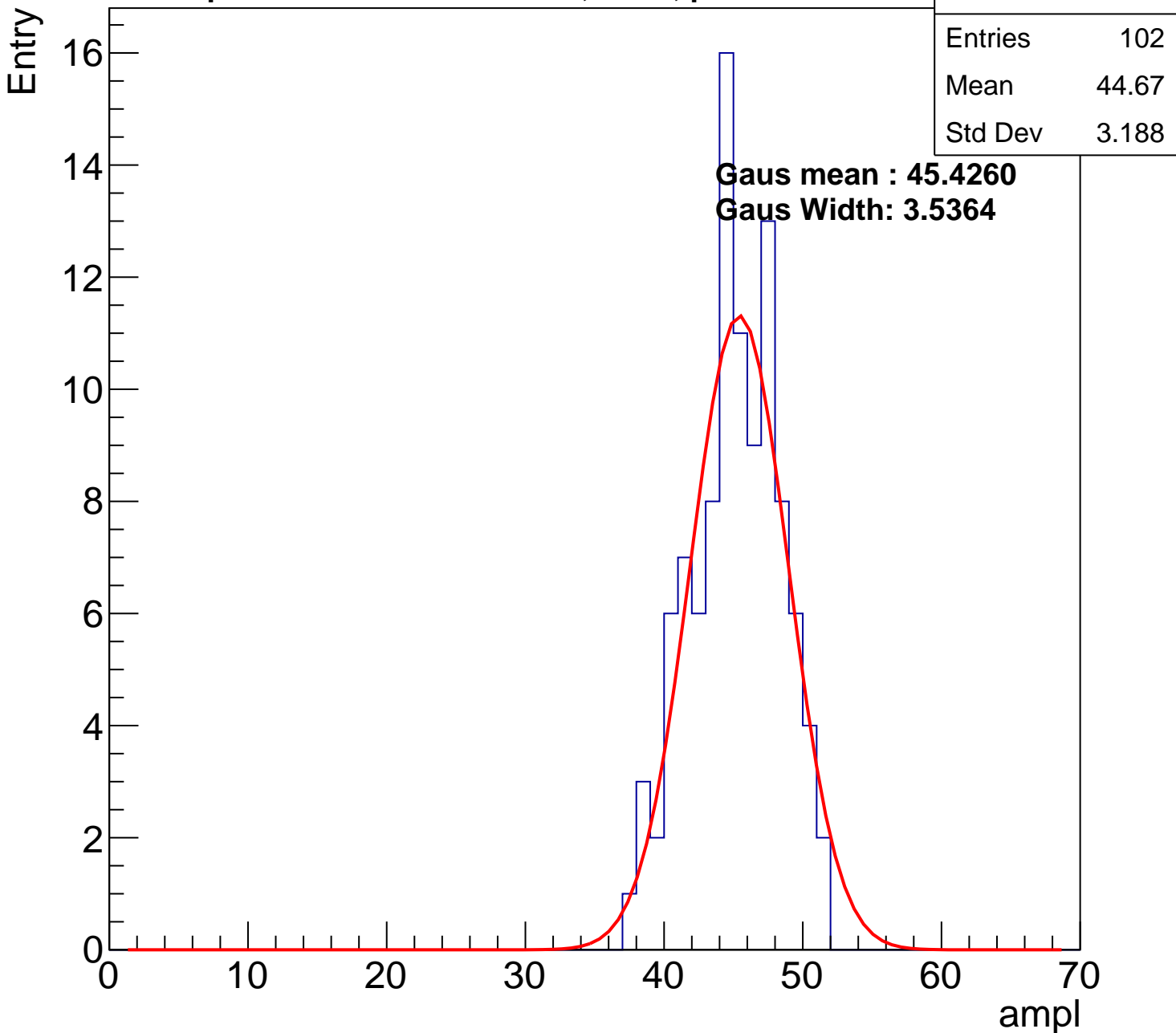
| | |
|---------|-------|
| Entries | 102 |
| Mean | 44.67 |
| Std Dev | 3.188 |

Gaus mean : 45.4260

Gaus Width: 3.5364

ampl

0 10 20 30 40 50 60 70

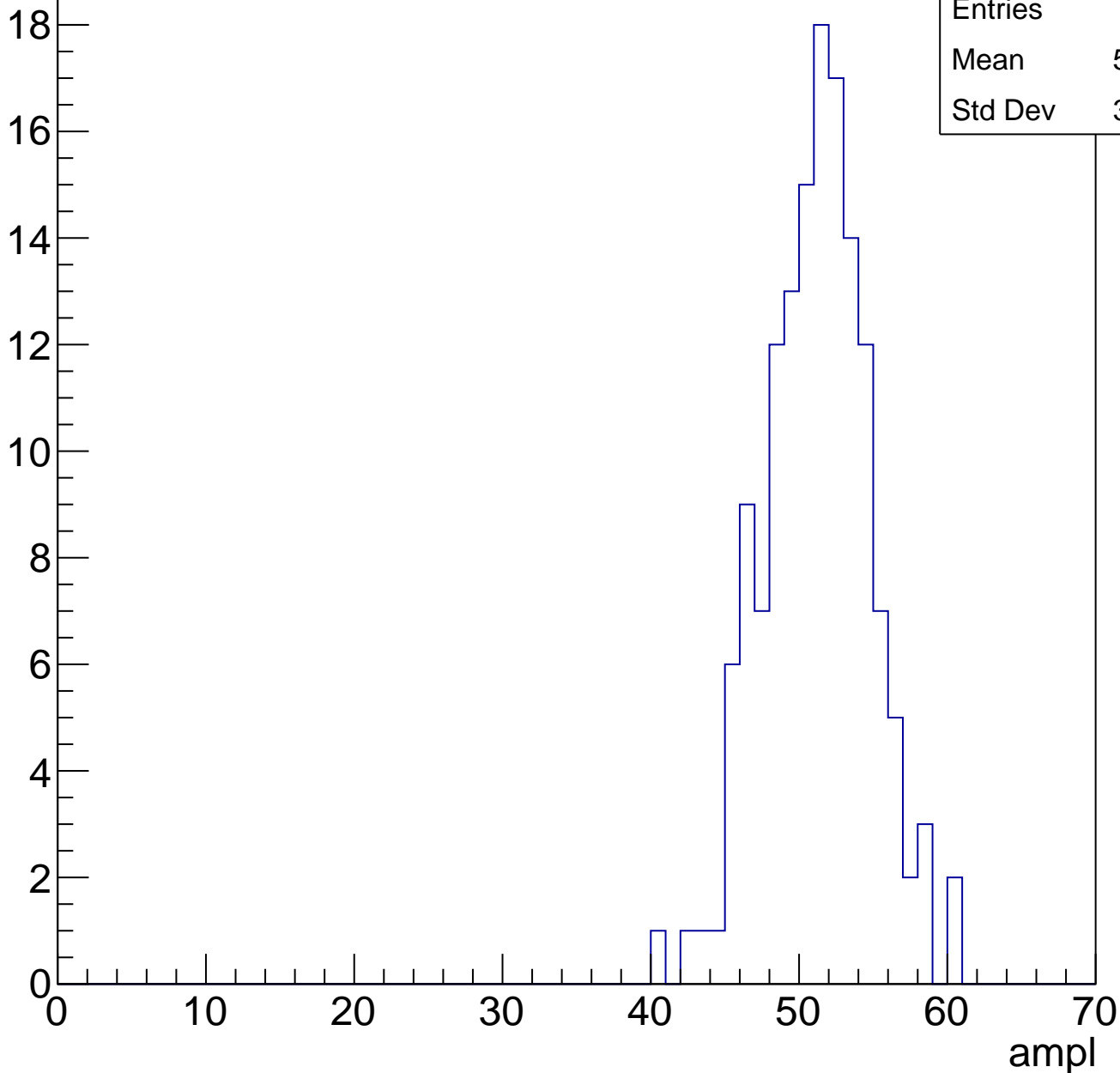


B1L001S, U26-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

| | |
|---------|-------|
| Entries | 146 |
| Mean | 50.75 |
| Std Dev | 3.543 |



B1L001S, U26-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

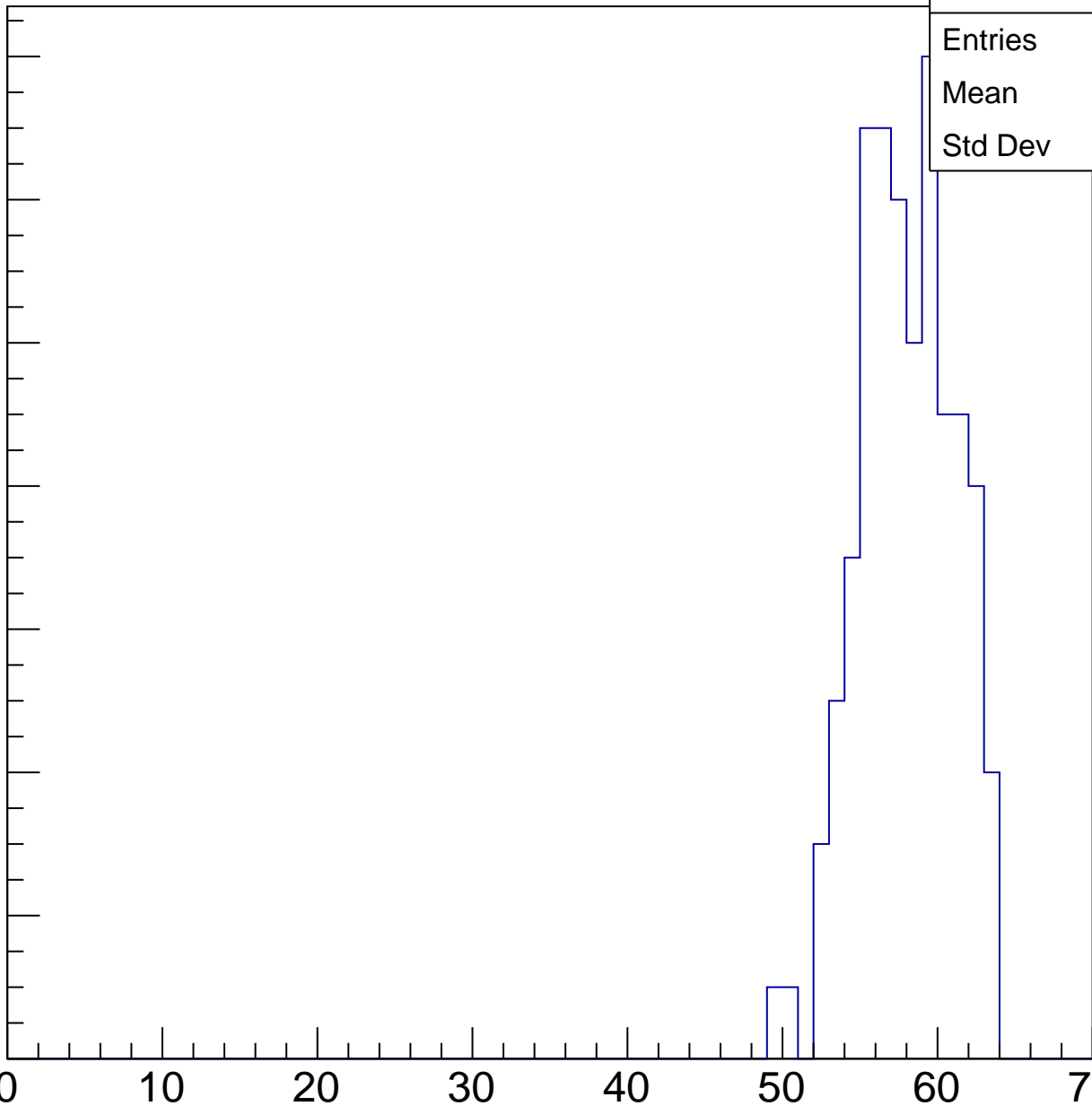
| | |
|---------|-------|
| Entries | 109 |
| Mean | 57.5 |
| Std Dev | 3.034 |

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U26-ch127, adc5

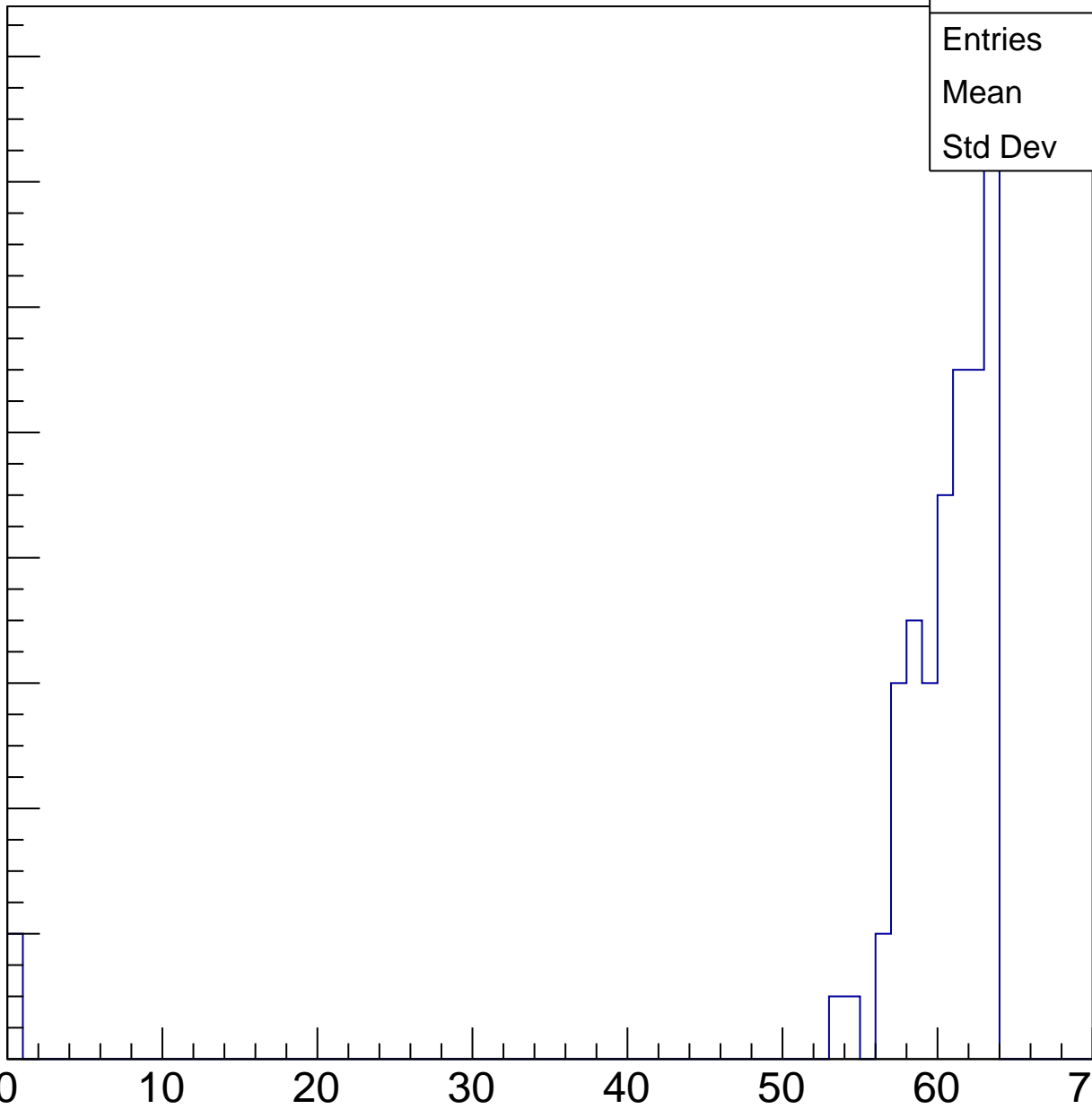
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 58.64 |
| Std Dev | 10.19 |

ampl



B1L001S, U26-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L001S, U26-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |