

B0L001S, U26-ch0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.2272

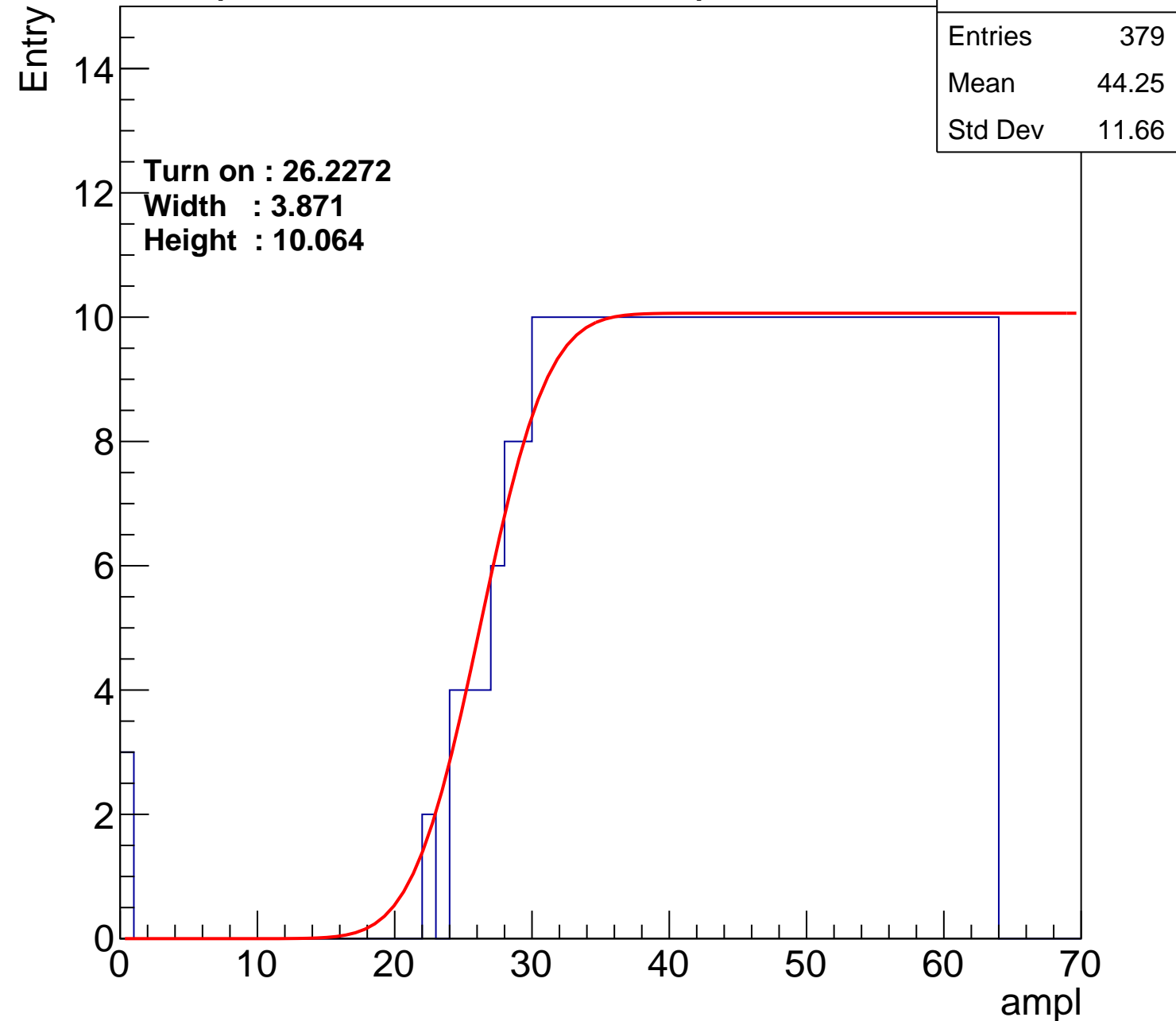
Width : 3.871

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	346
Mean	45.9
Std Dev	10.72

Turn on : 30.4309

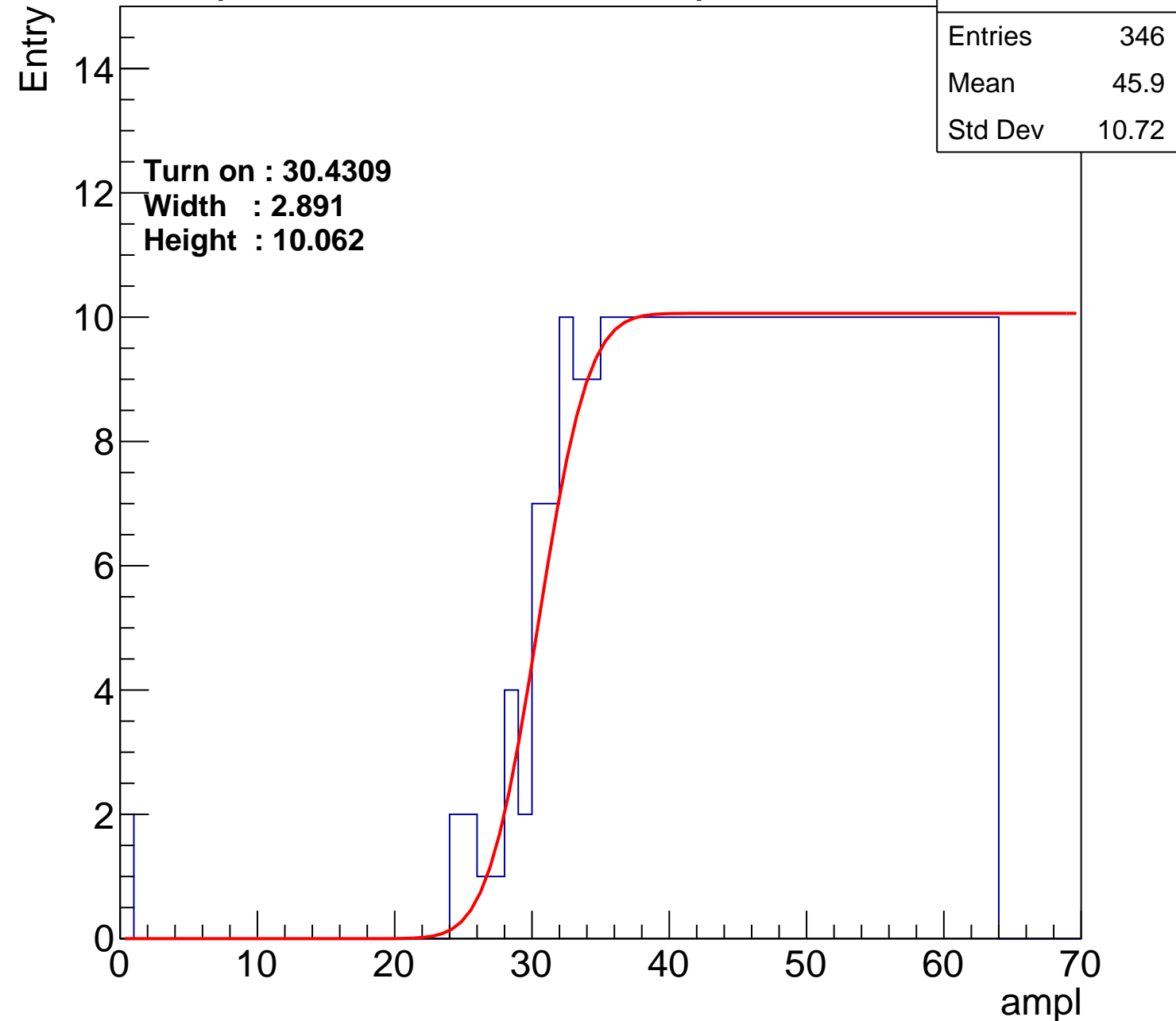
Width : 2.891

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch2

calib_packv5_042523_0143.root, FC#9, port A1

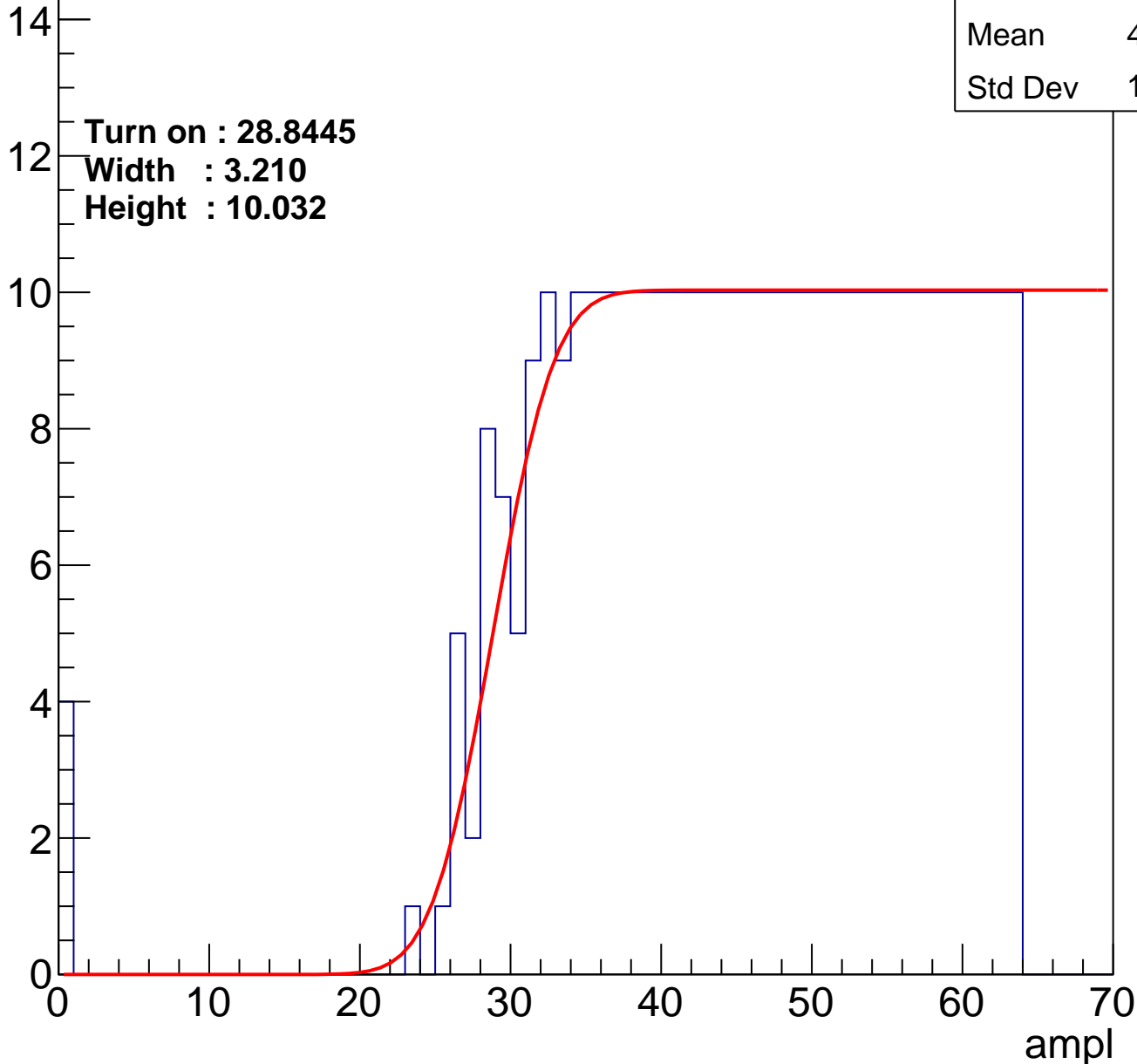
Entry

Entries	361
Mean	45.03
Std Dev	11.48

Turn on : 28.8445

Width : 3.210

Height : 10.032



B0L001S, U26-ch3

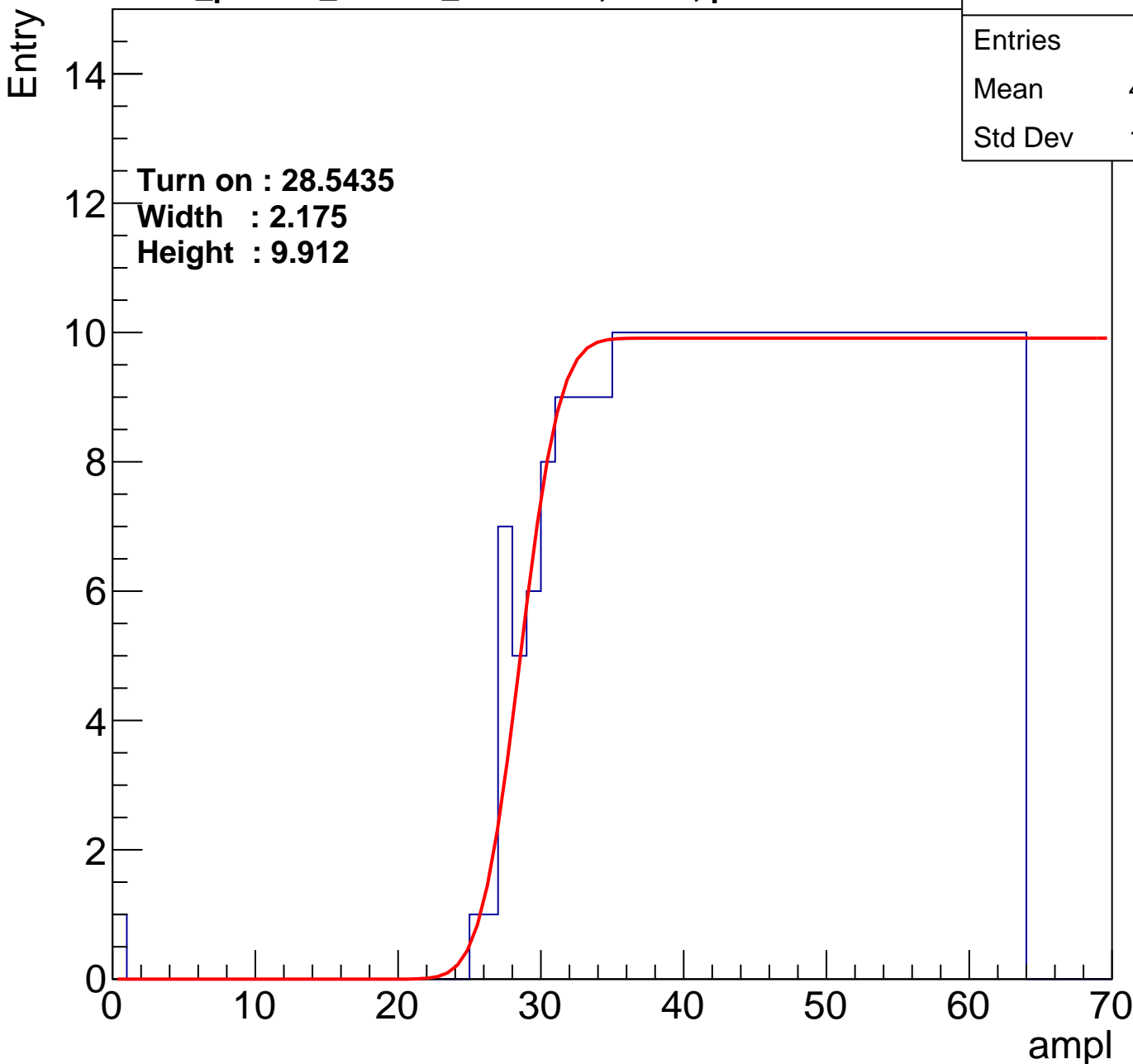
calib_packv5_042523_0143.root, FC#9, port A1

Entries	355
Mean	45.56
Std Dev	10.66

Turn on : 28.5435

Width : 2.175

Height : 9.912



B0L001S, U26-ch4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.3549

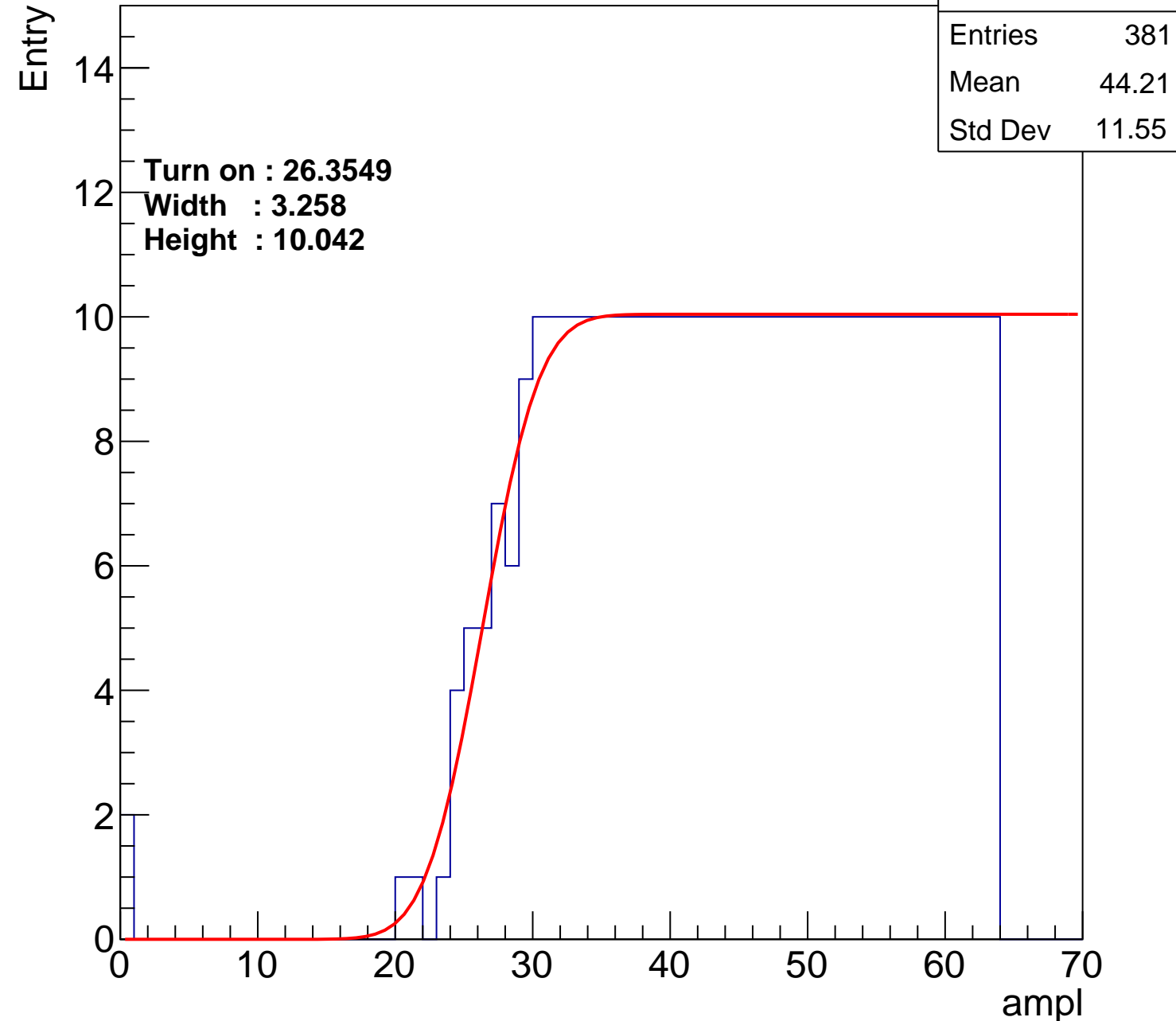
Width : 3.258

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.32
Std Dev	11.14

Turn on : 28.6404

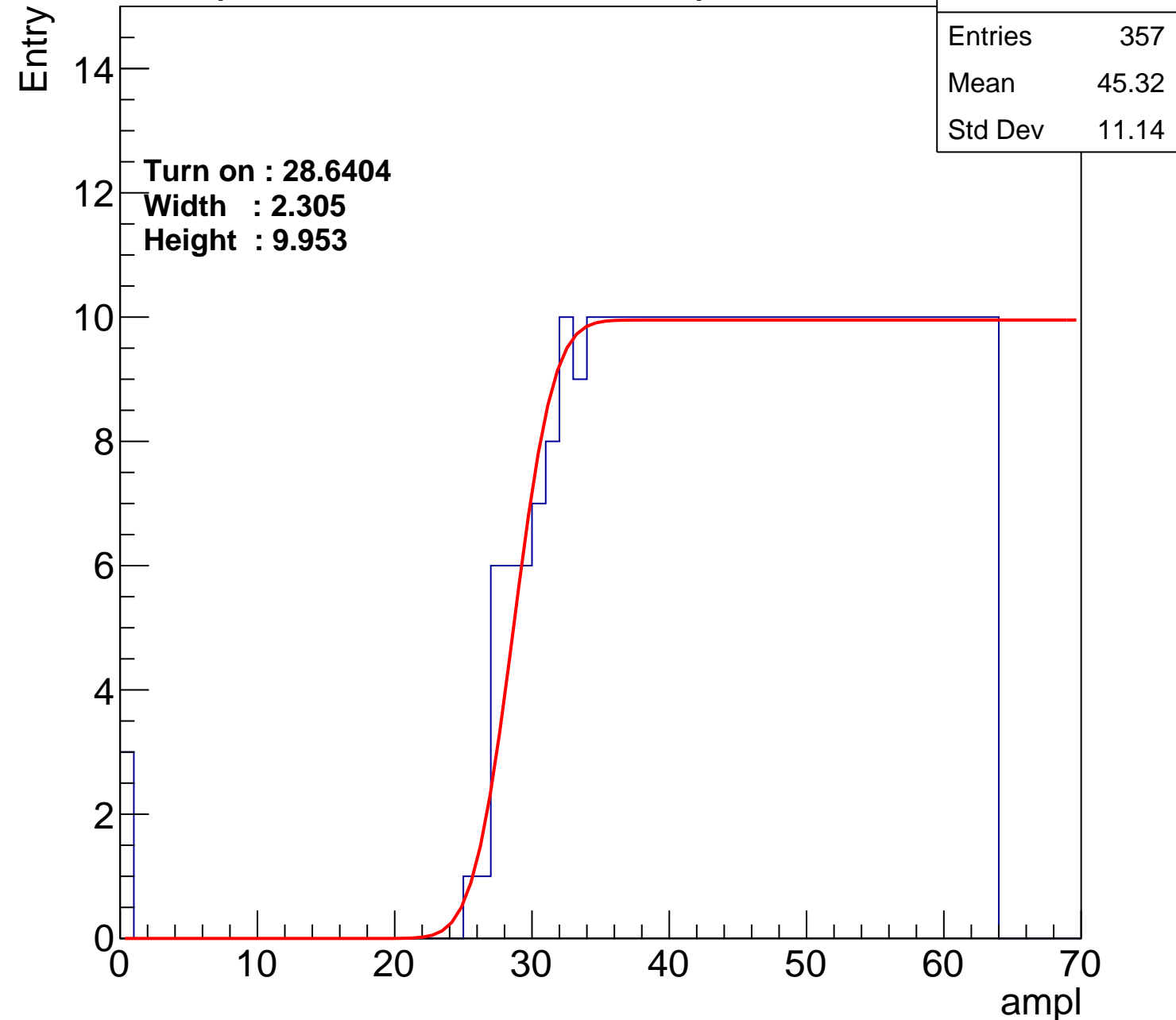
Width : 2.305

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch6

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.8
Std Dev	11.9

Turn on : 28.6007

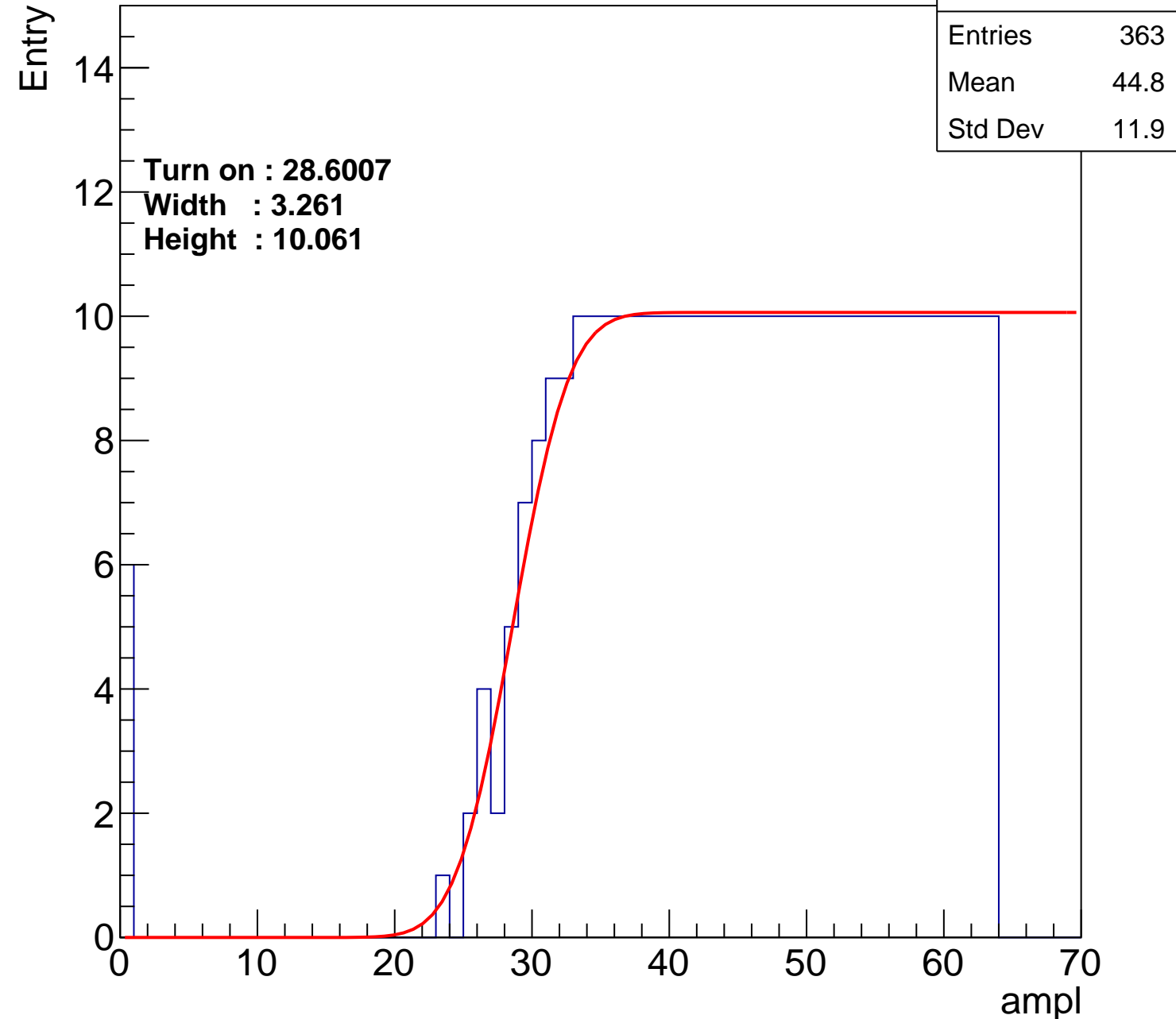
Width : 3.261

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch7

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.11
Std Dev	11.07

Turn on : 27.9763

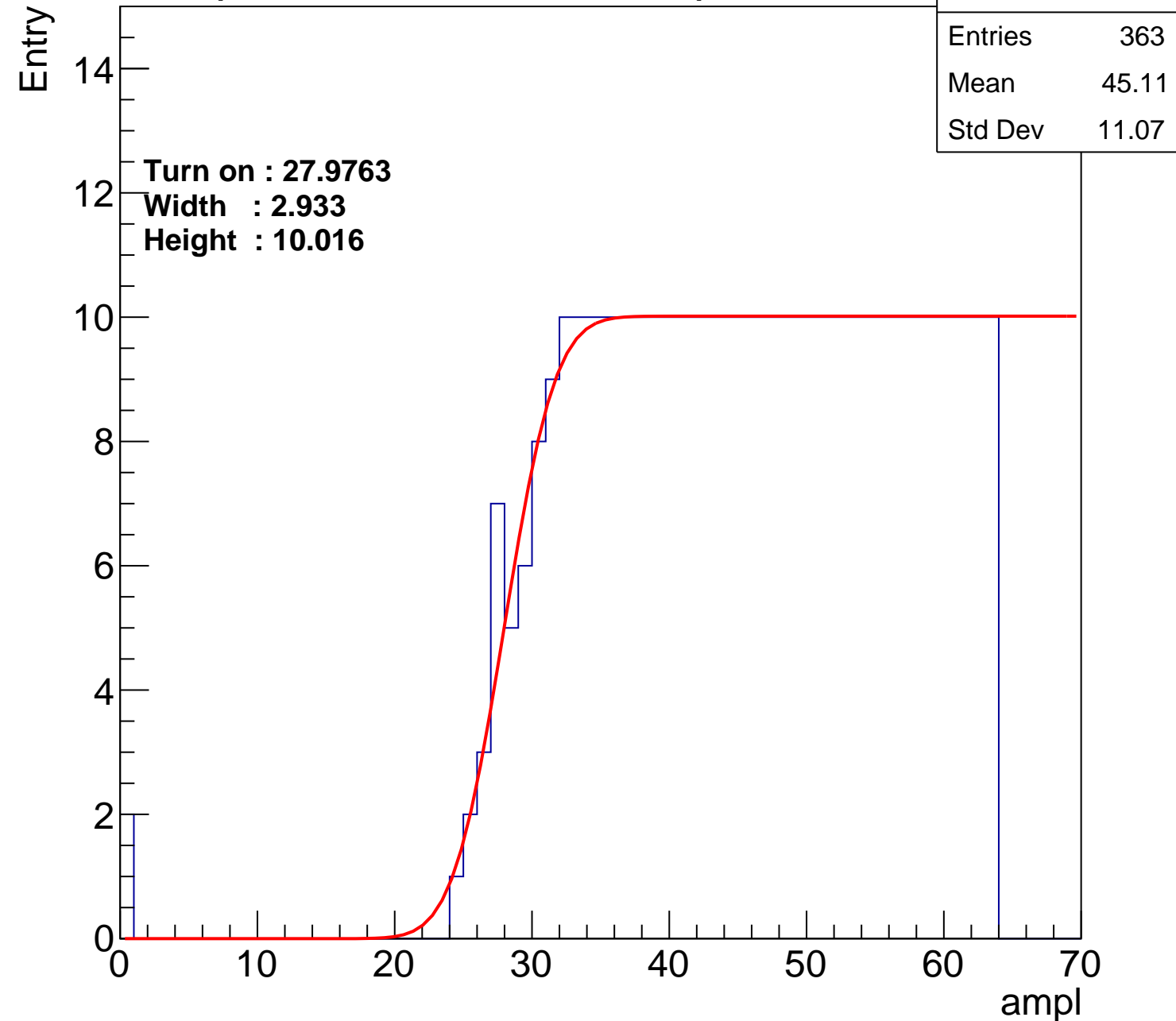
Width : 2.933

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch8

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.24
Std Dev	11.06

Turn on : 28.2338

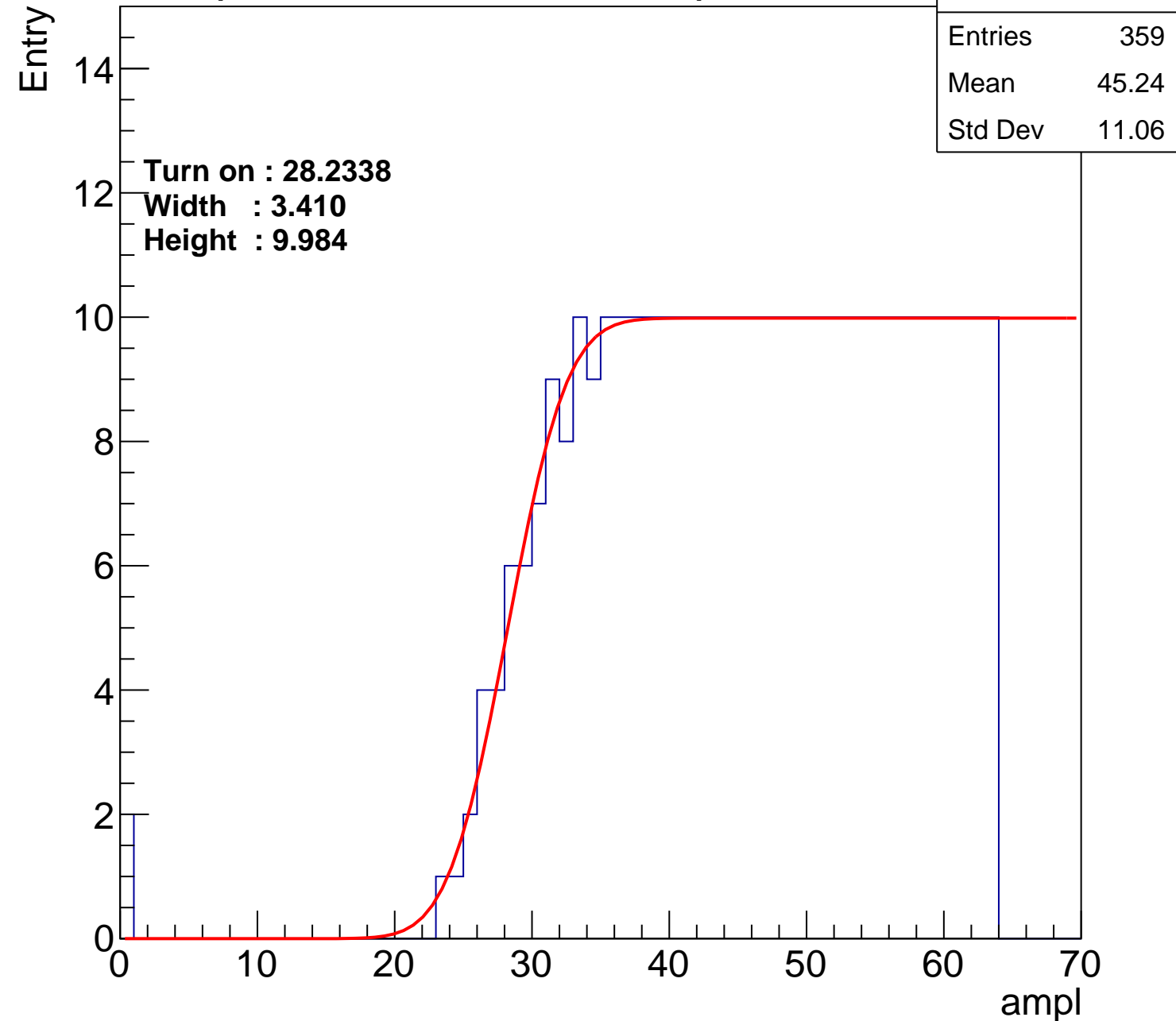
Width : 3.410

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch9

calib_packv5_042523_0143.root, FC#9, port A1

Entries	342
Mean	46.11
Std Dev	10.6

Turn on : 29.4402

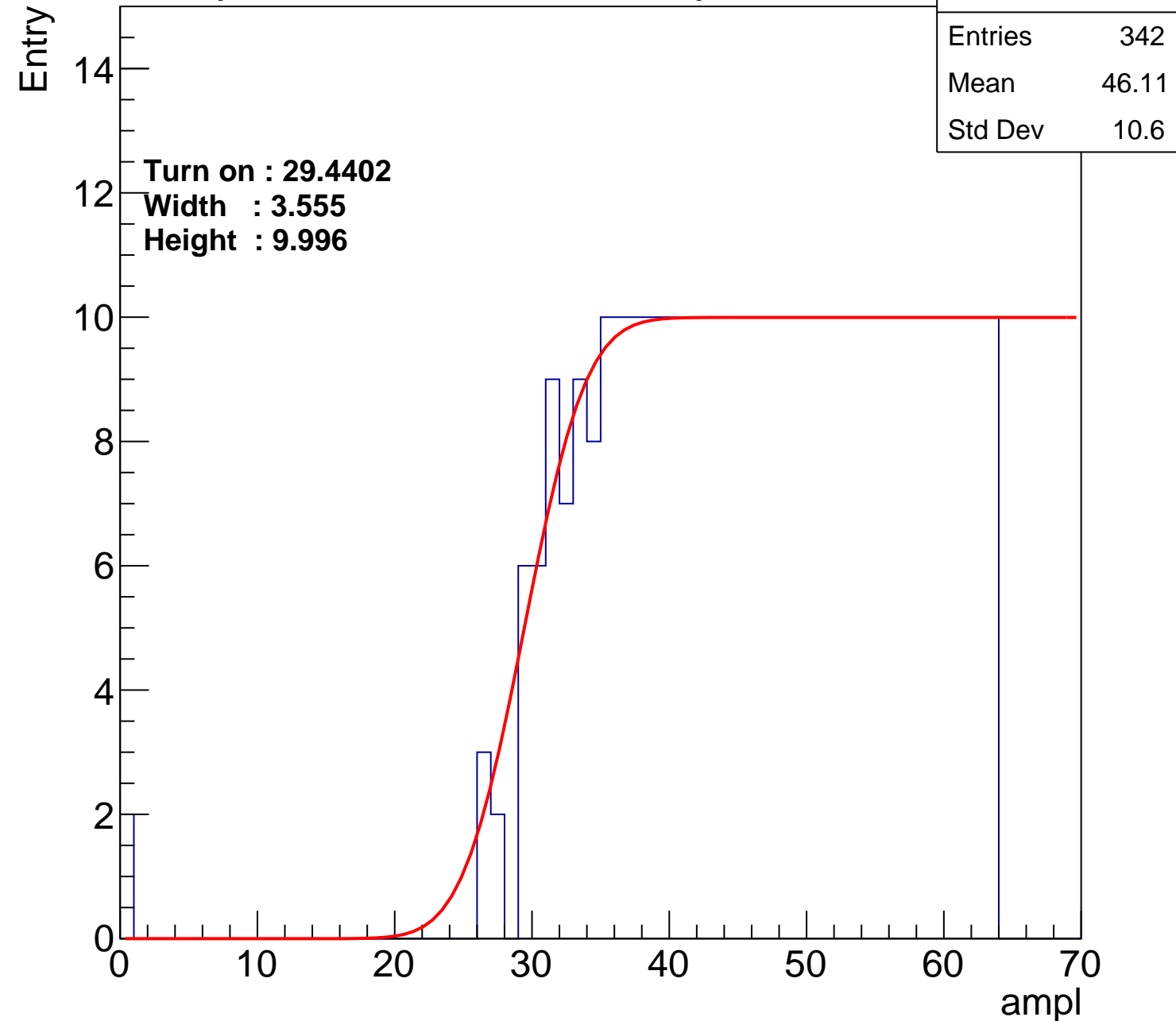
Width : 3.555

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch10

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.13
Std Dev	10.93

Turn on : 27.8741

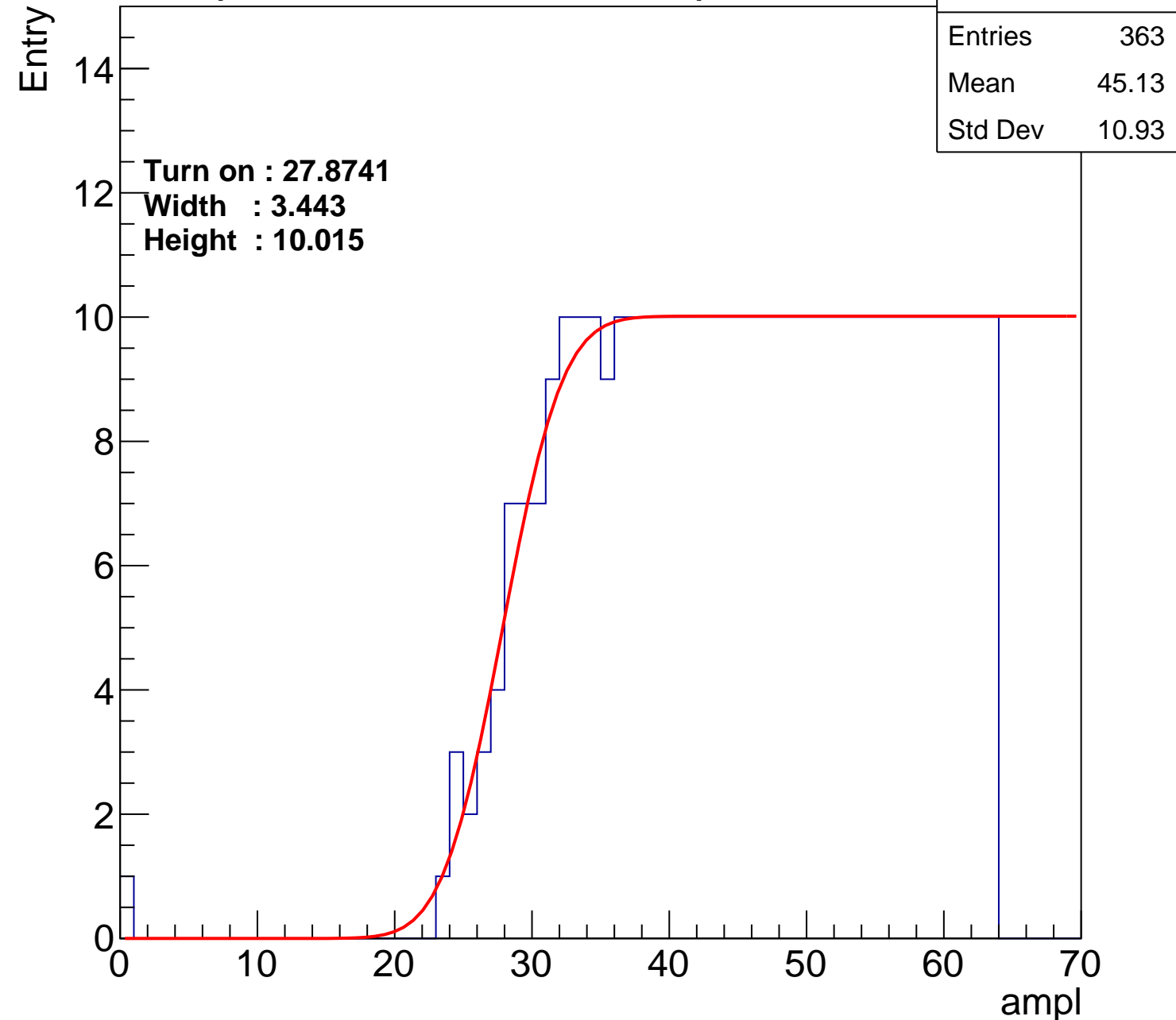
Width : 3.443

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch11

calib_packv5_042523_0143.root, FC#9, port A1

Entries	351
Mean	45.73
Std Dev	10.61

Turn on : 28.9861

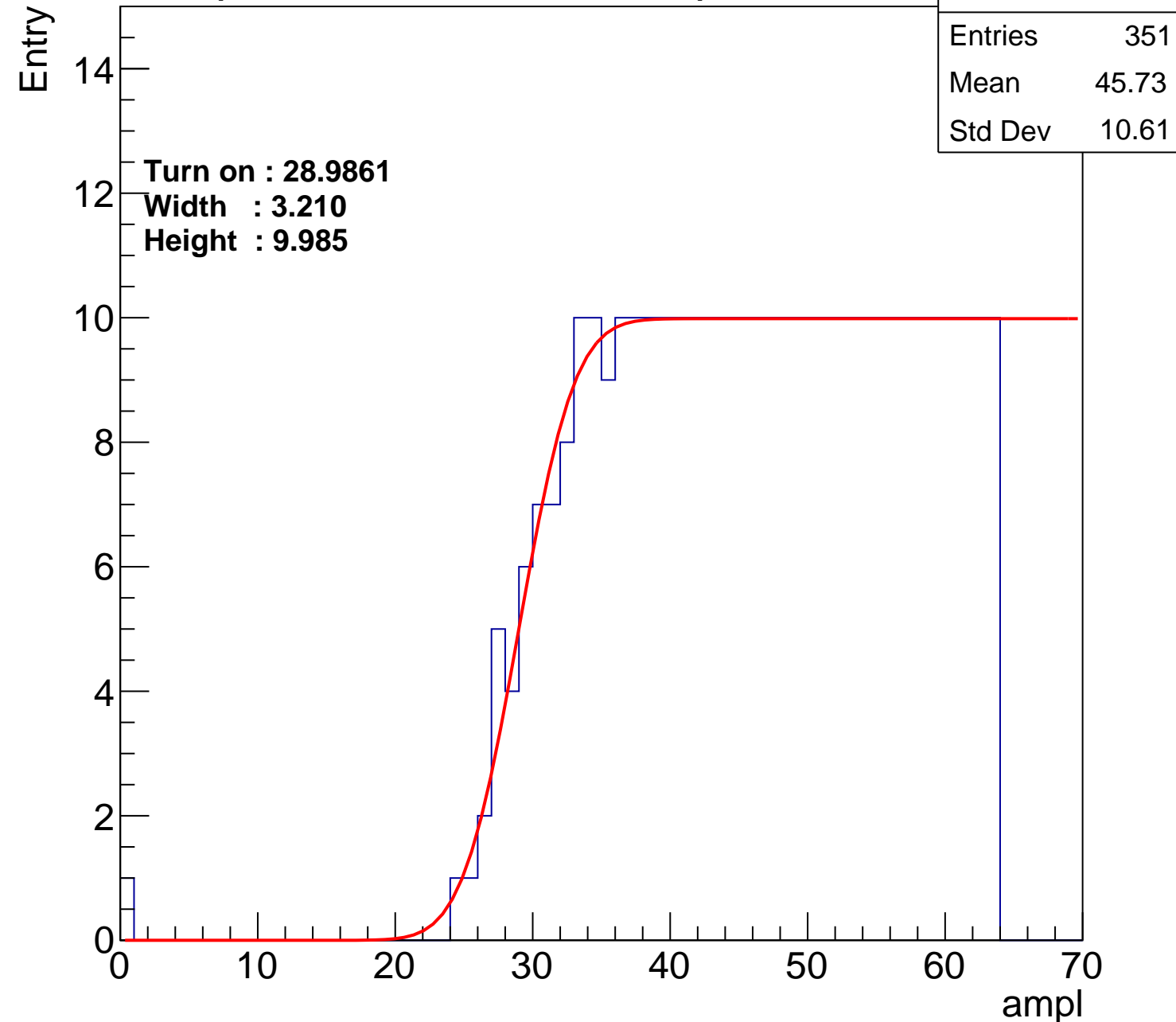
Width : 3.210

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch12

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.15
Std Dev	11.4

Turn on : 28.6421

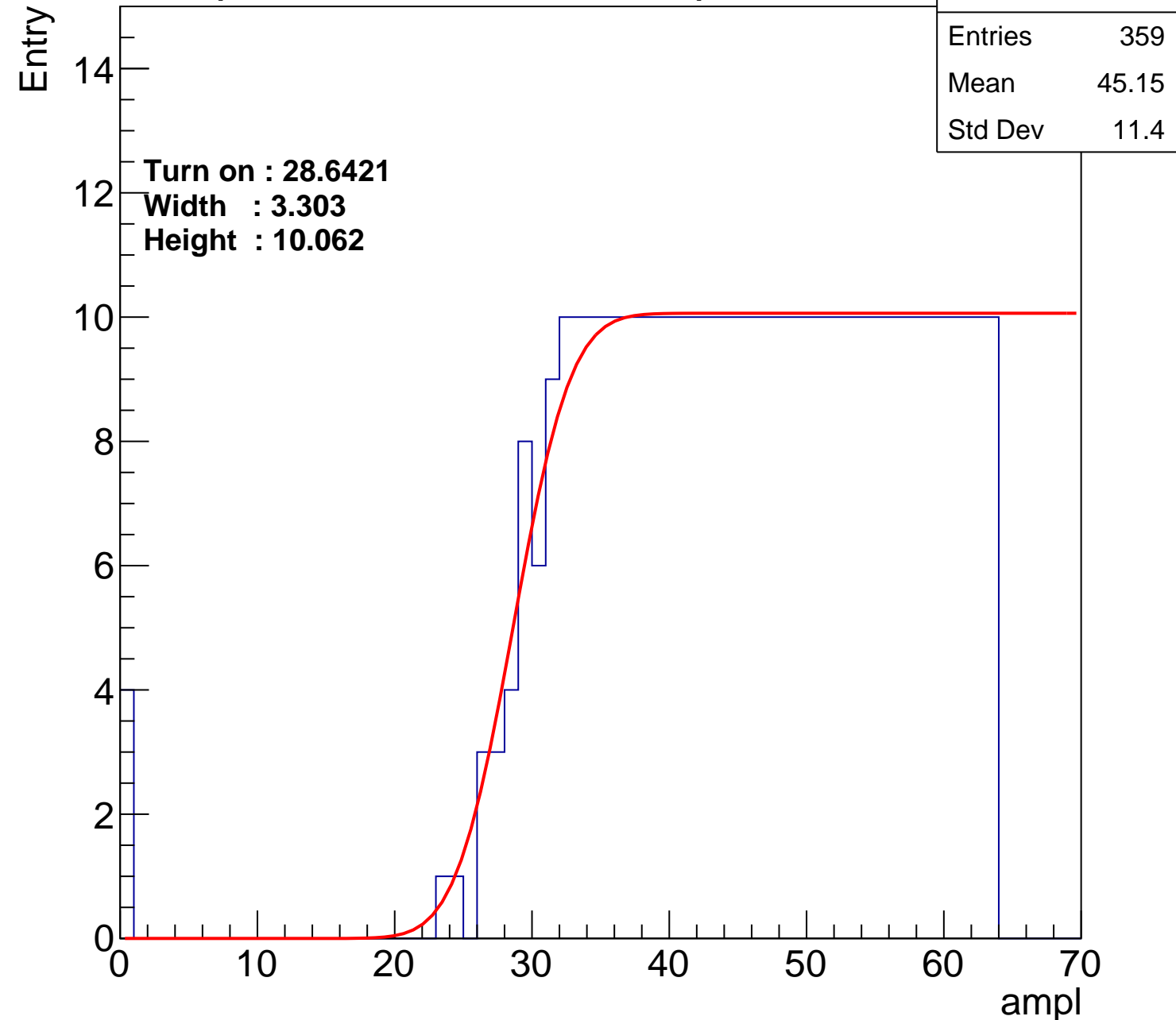
Width : 3.303

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch13

calib_packv5_042523_0143.root, FC#9, port A1

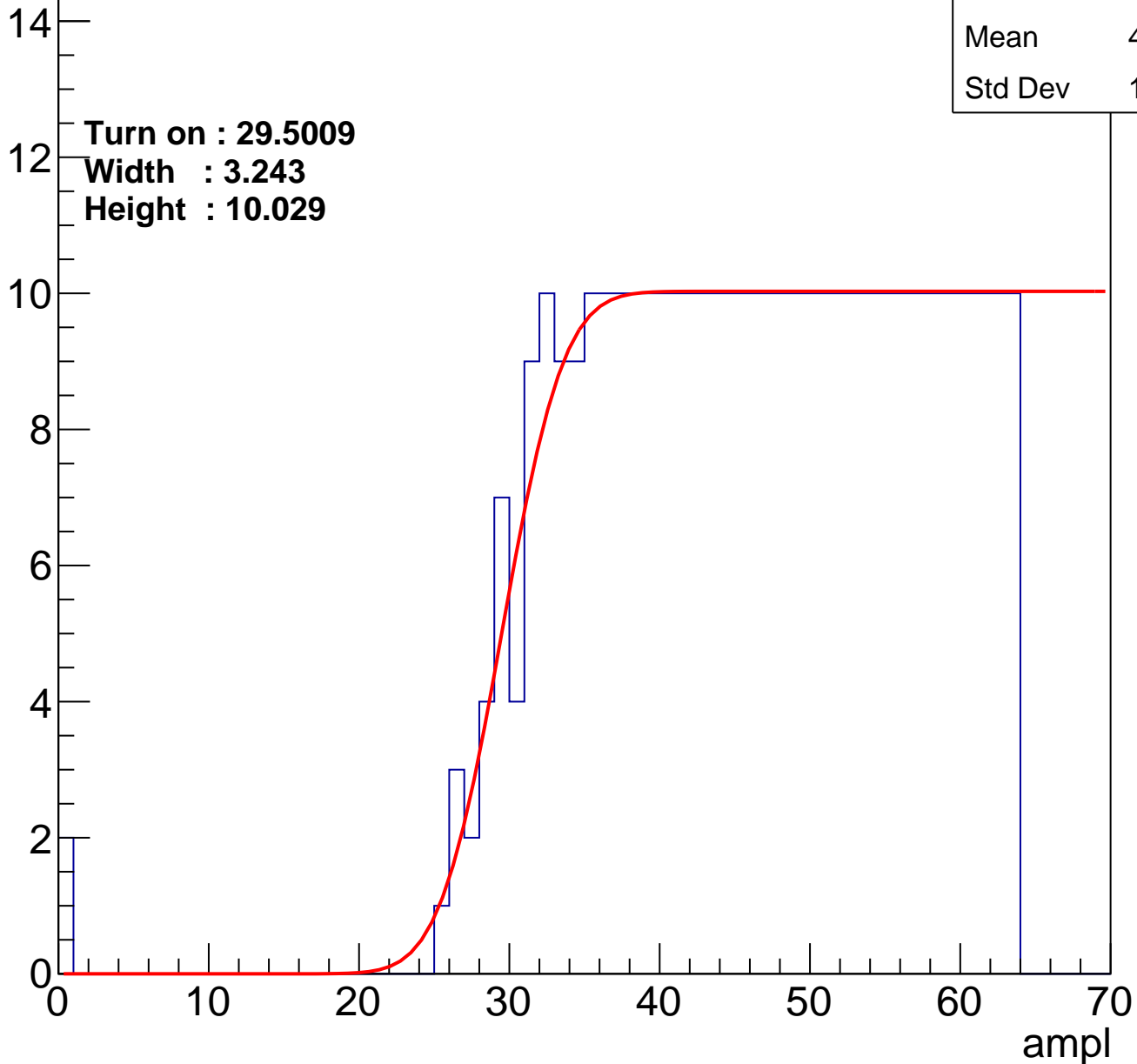
Entries	350
Mean	45.73
Std Dev	10.77

Turn on : 29.5009

Width : 3.243

Height : 10.029

Entry



B0L001S, U26-ch14

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.96
Std Dev	11.49

Turn on : 28.5714

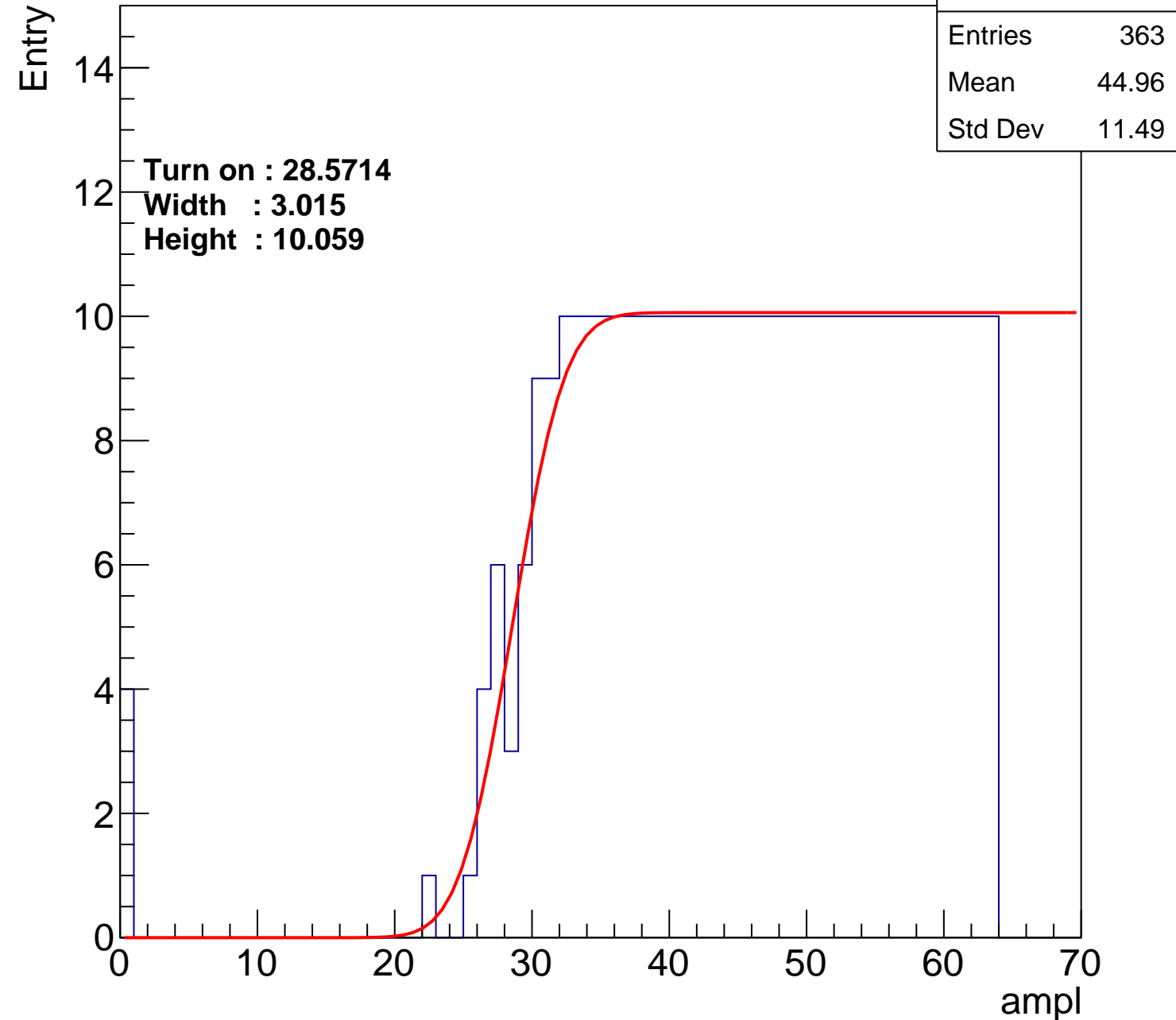
Width : 3.015

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch15

calib_packv5_042523_0143.root, FC#9, port A1

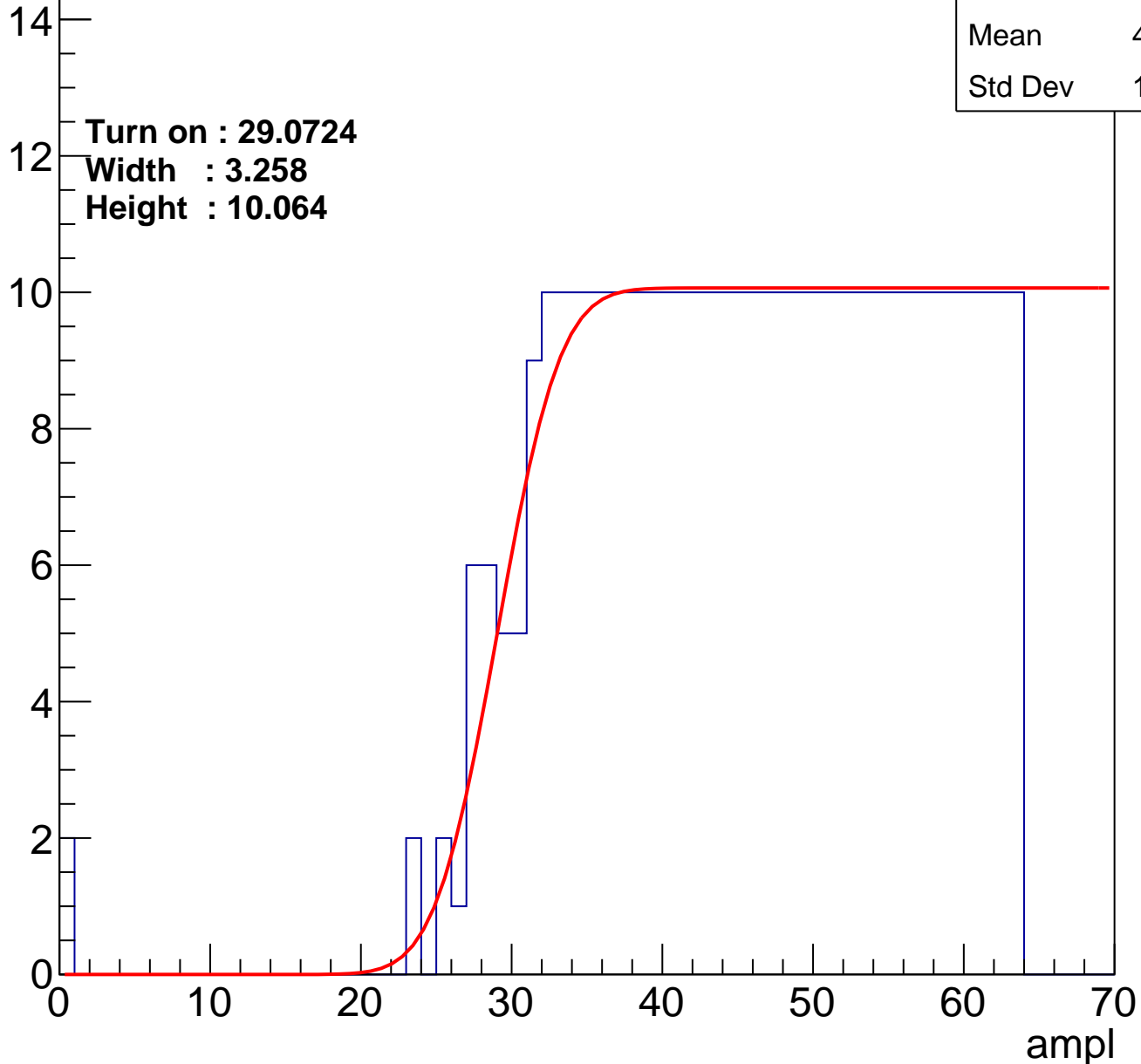
Entries	358
Mean	45.32
Std Dev	10.99

Turn on : 29.0724

Width : 3.258

Height : 10.064

Entry



B0L001S, U26-ch16

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.2
Std Dev	10.83

Turn on : 27.9606

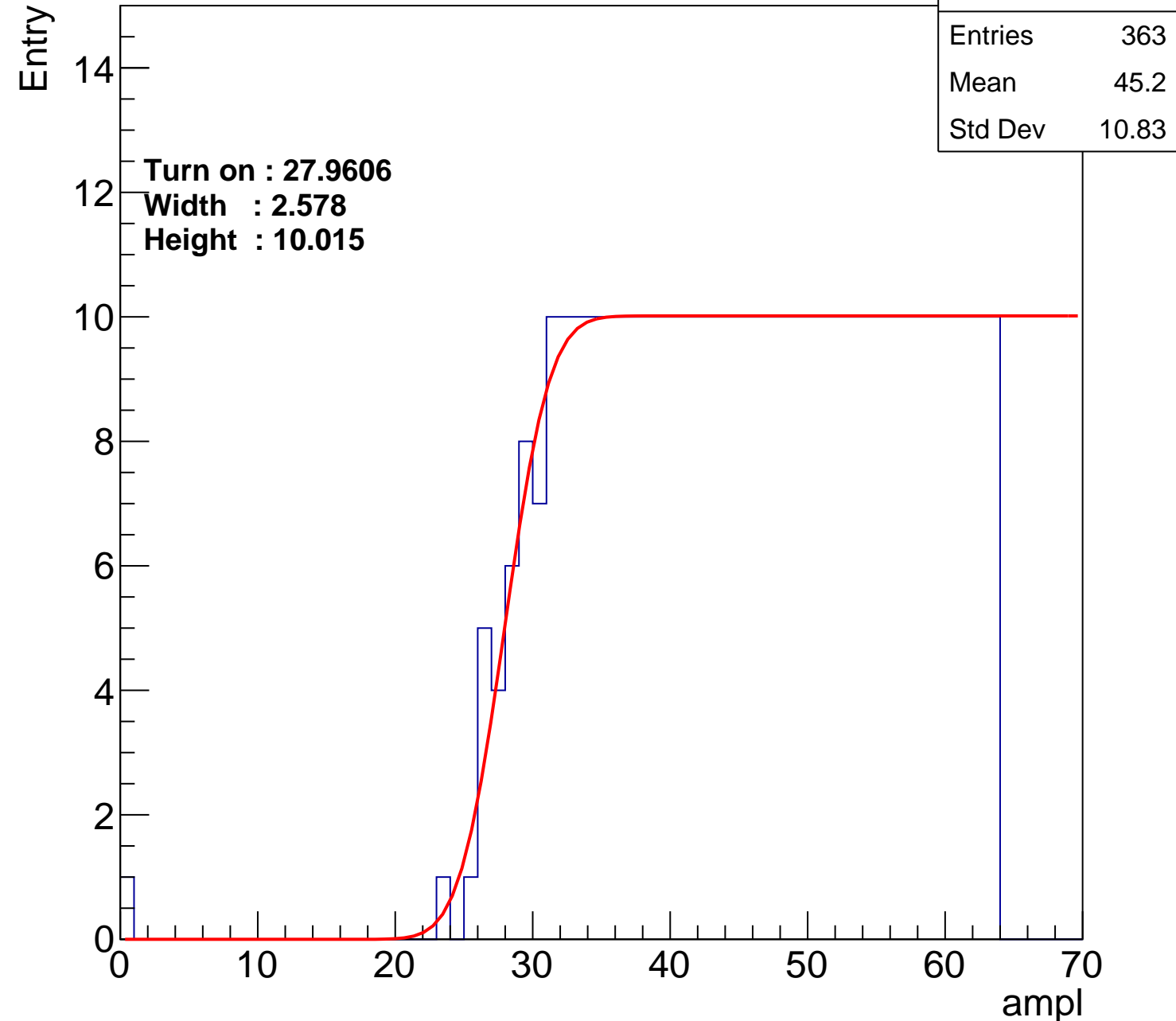
Width : 2.578

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch17

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.83
Std Dev	10.67

Turn on : 29.3512

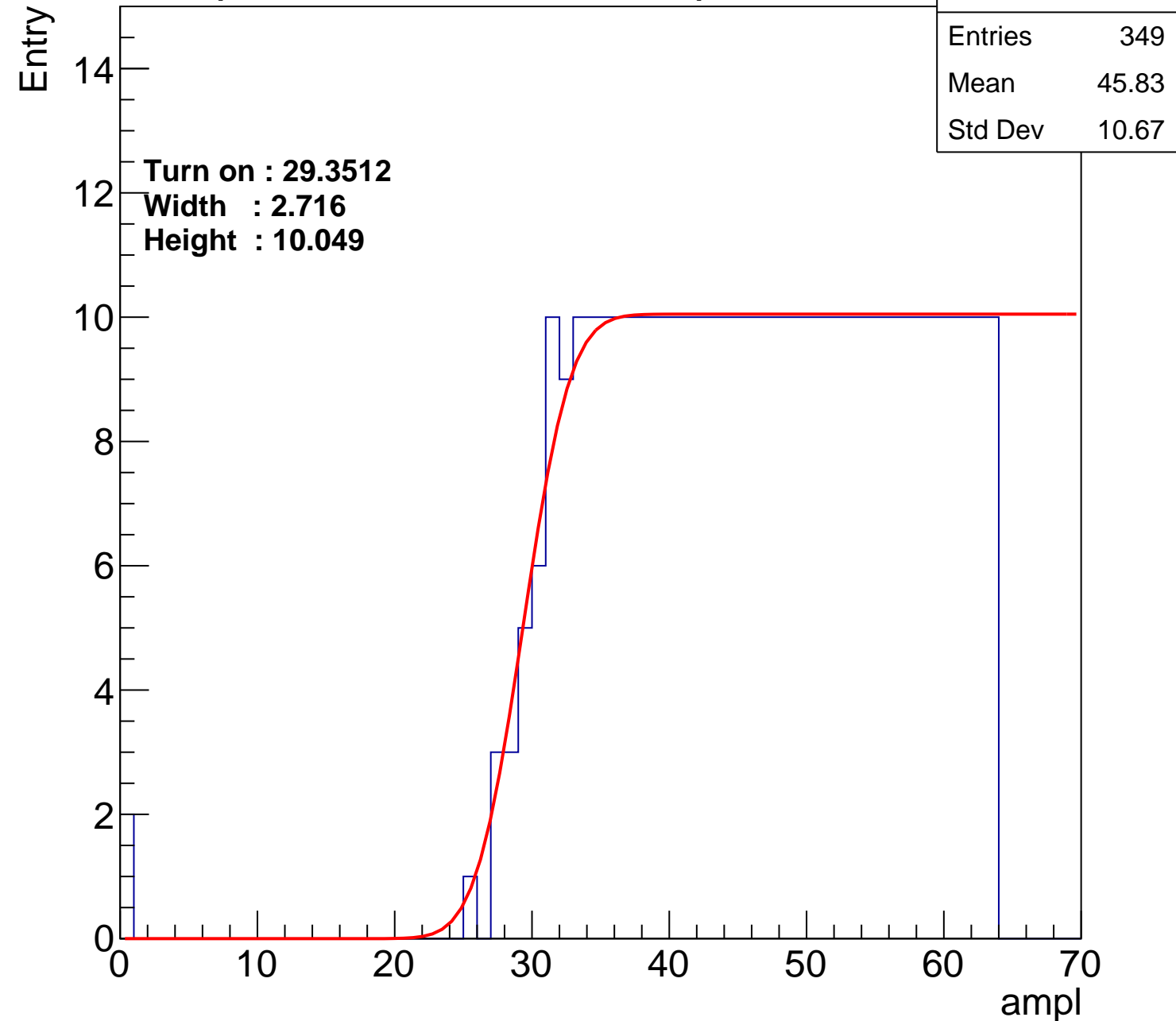
Width : 2.716

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch18

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	45.87
Std Dev	10.93

Turn on : 30.1270

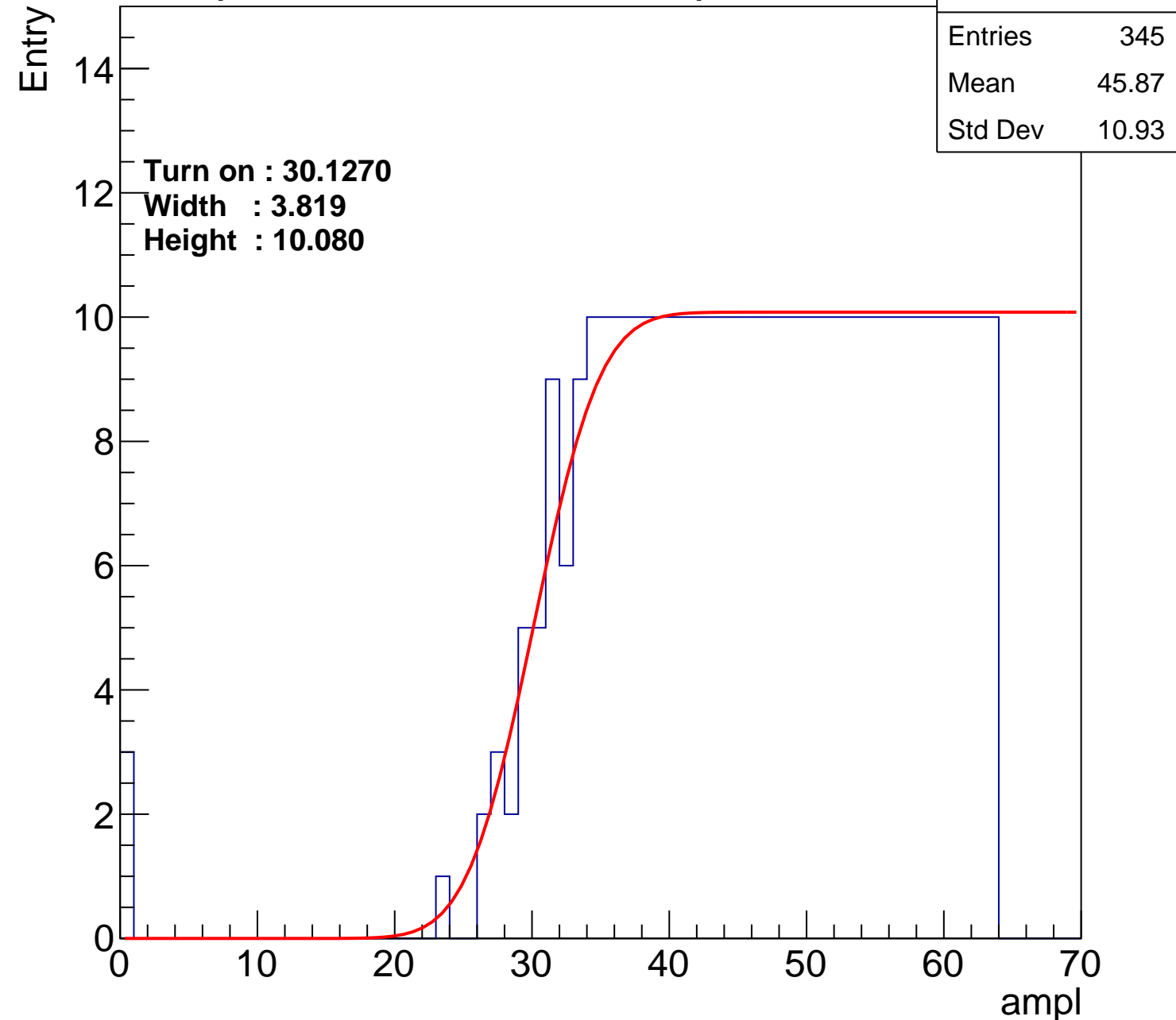
Width : 3.819

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch19

calib_packv5_042523_0143.root, FC#9, port A1

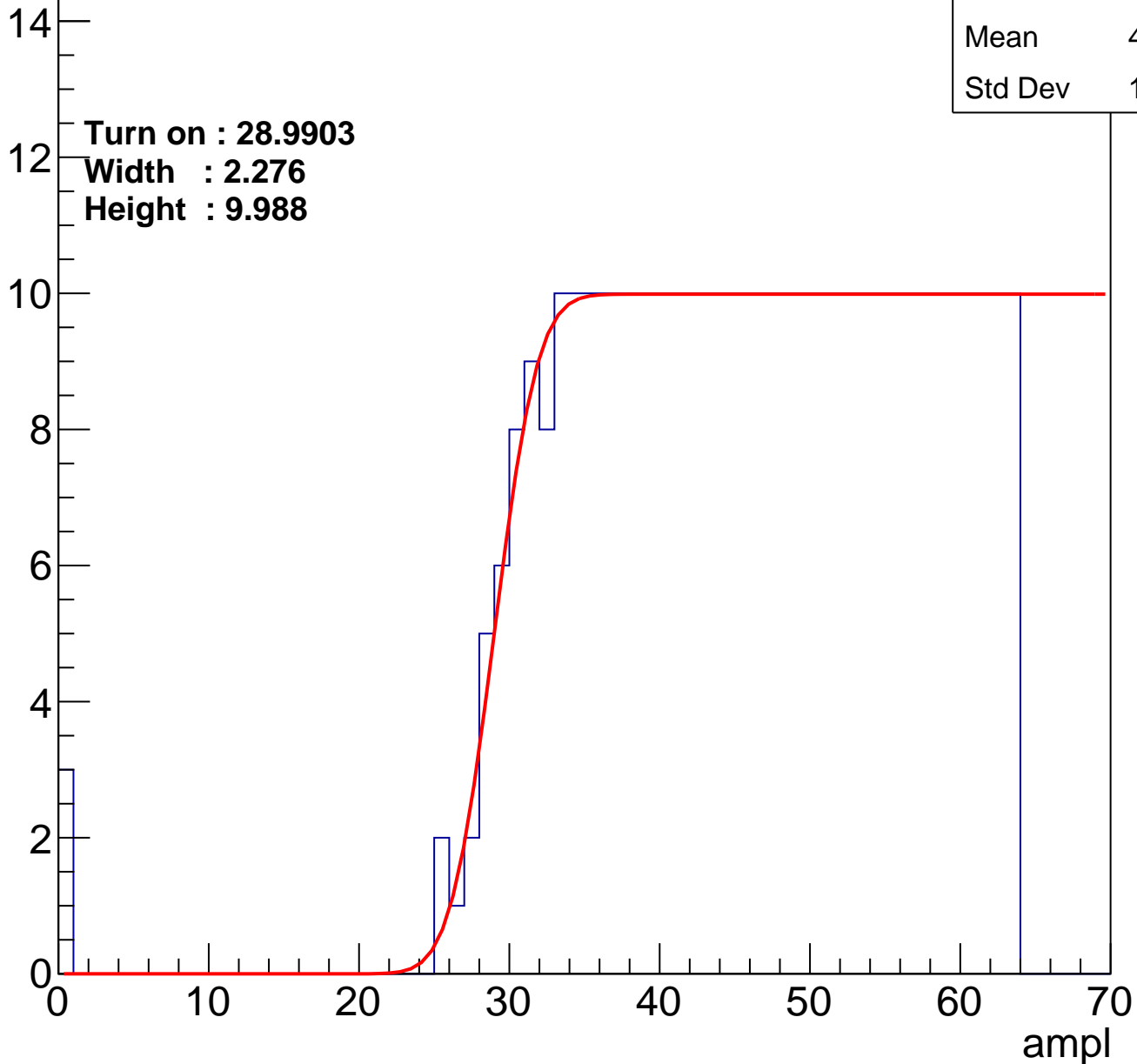
Entries	354
Mean	45.48
Std Dev	11.06

Turn on : 28.9903

Width : 2.276

Height : 9.988

Entry



B0L001S, U26-ch20

calib_packv5_042523_0143.root, FC#9, port A1

Entries	384
Mean	43.95
Std Dev	11.93

Turn on : 25.7501

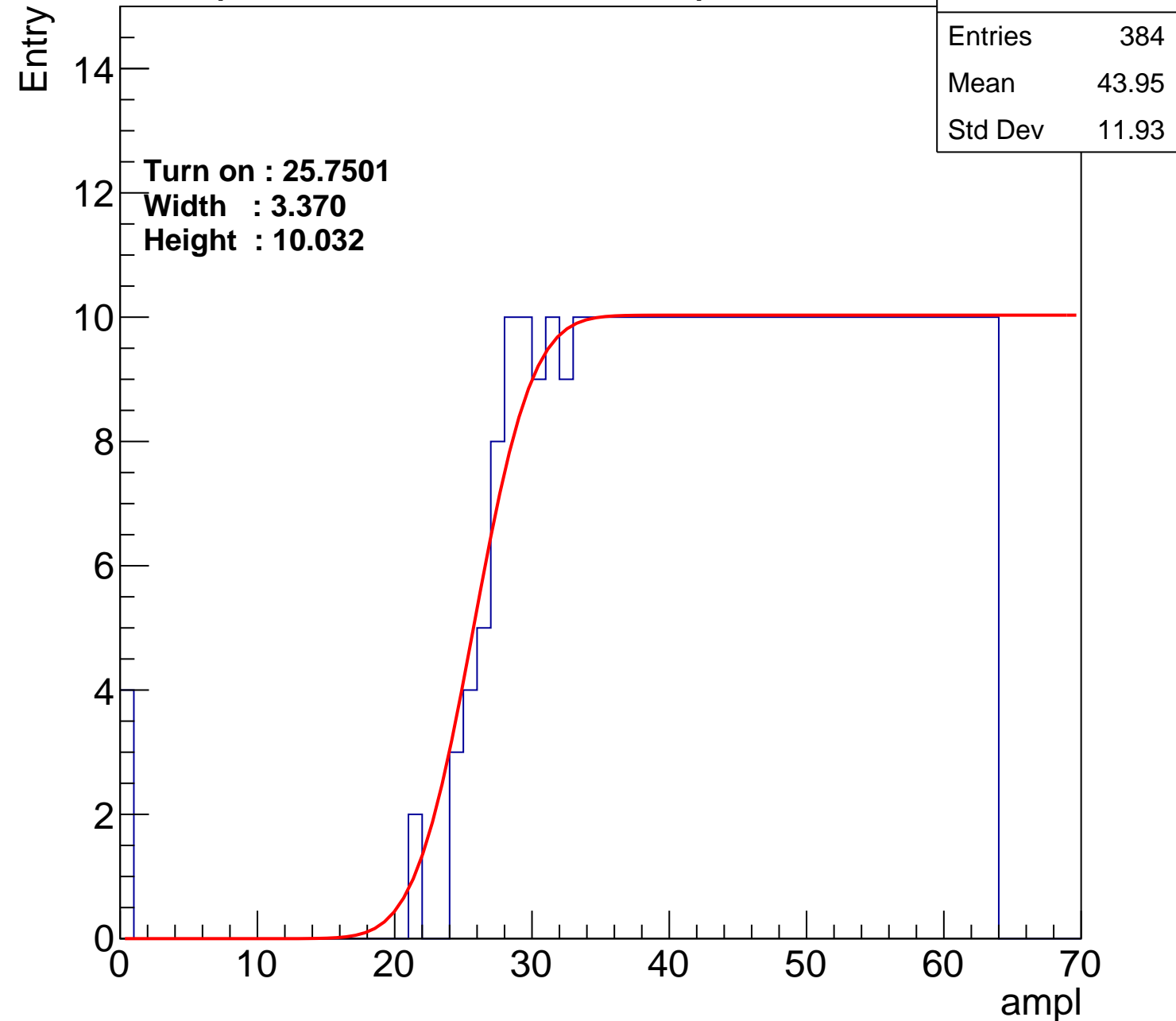
Width : 3.370

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch21

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.24
Std Dev	11.41

Turn on : 28.7678

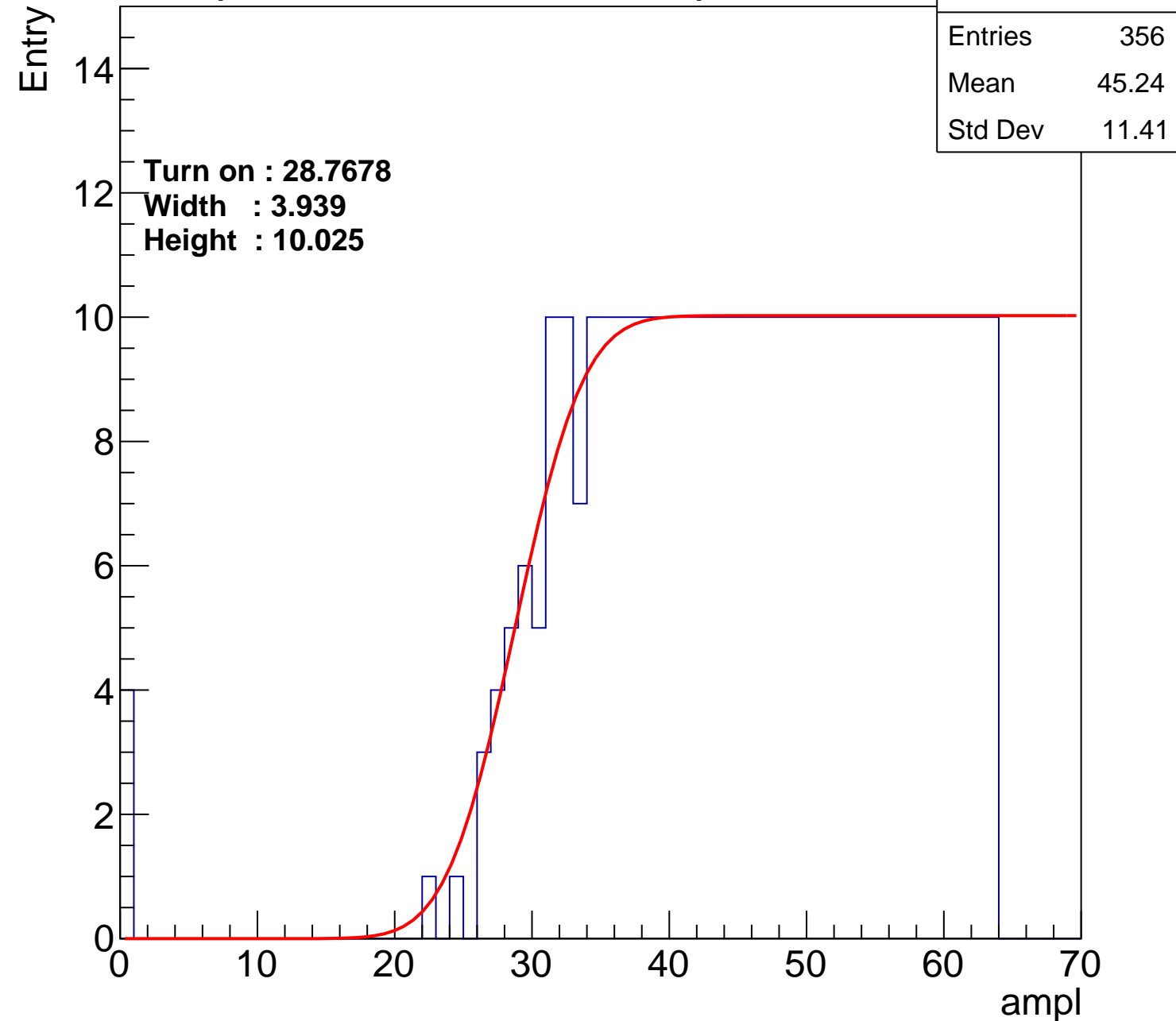
Width : 3.939

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch22

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.69
Std Dev	11.31

Turn on : 29.9839

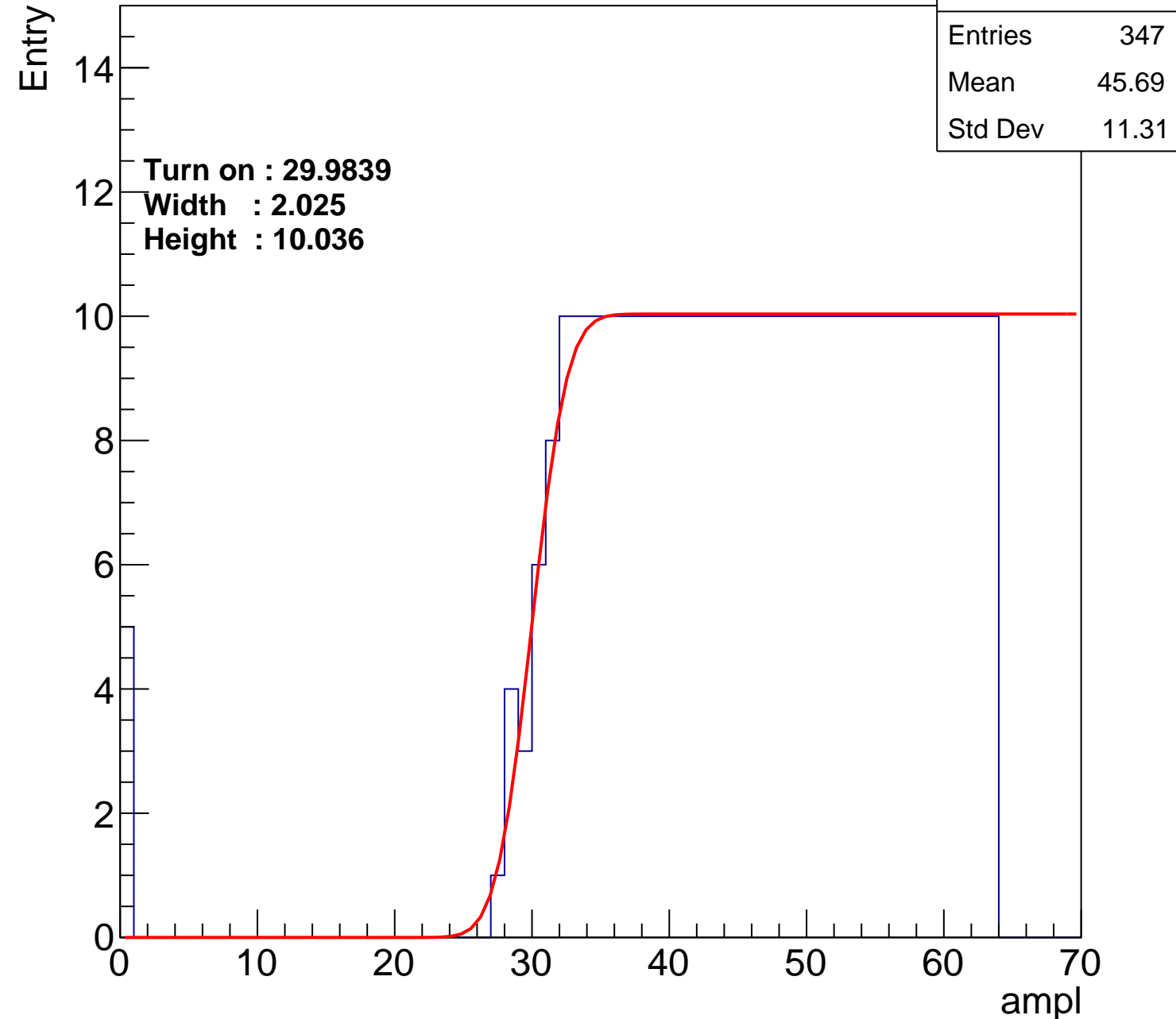
Width : 2.025

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch23

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.3
Std Dev	10.97

Turn on : 28.1858

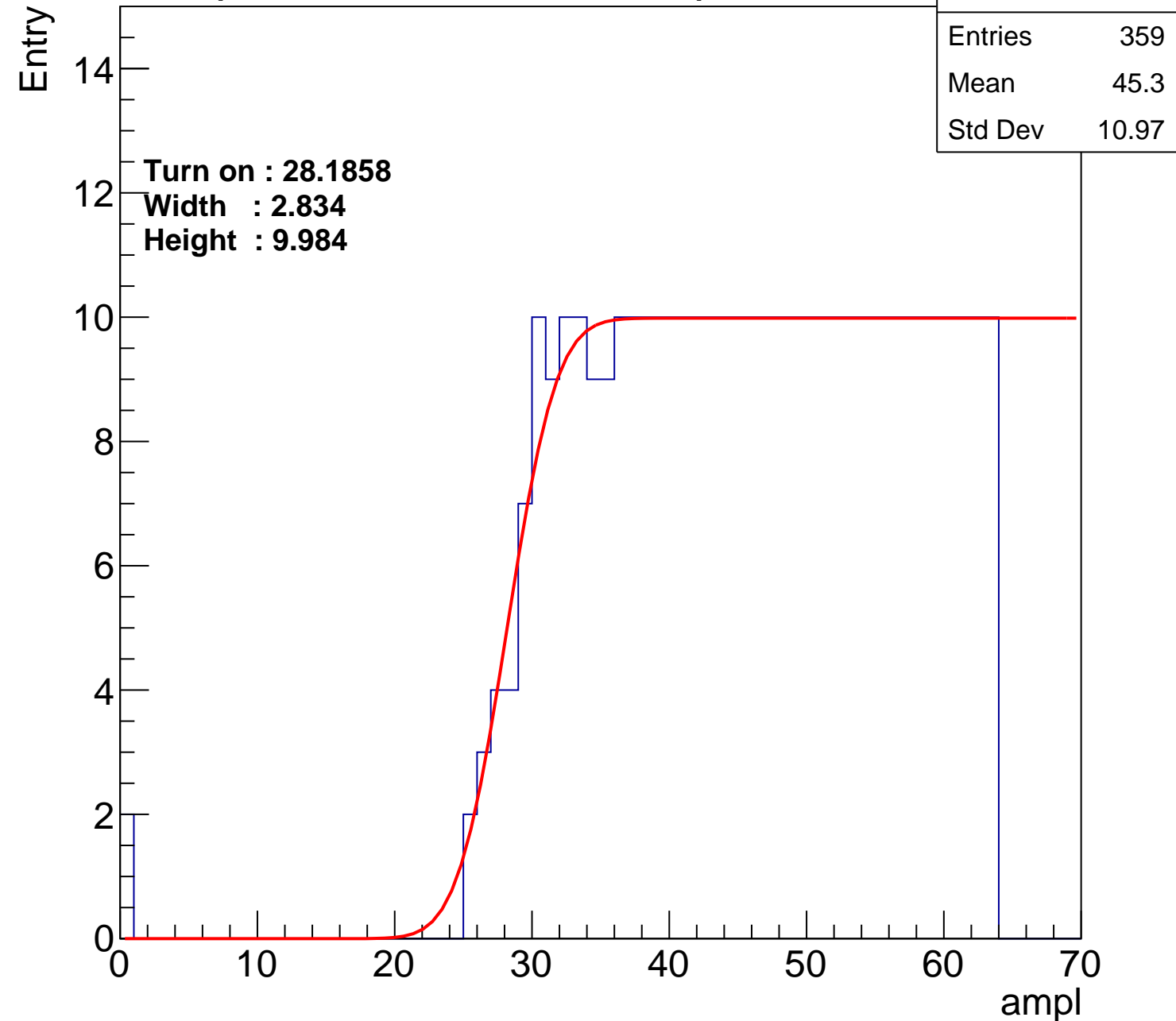
Width : 2.834

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch24

calib_packv5_042523_0143.root, FC#9, port A1

Entries	351
Mean	45.63
Std Dev	10.98

Turn on : 29.3104

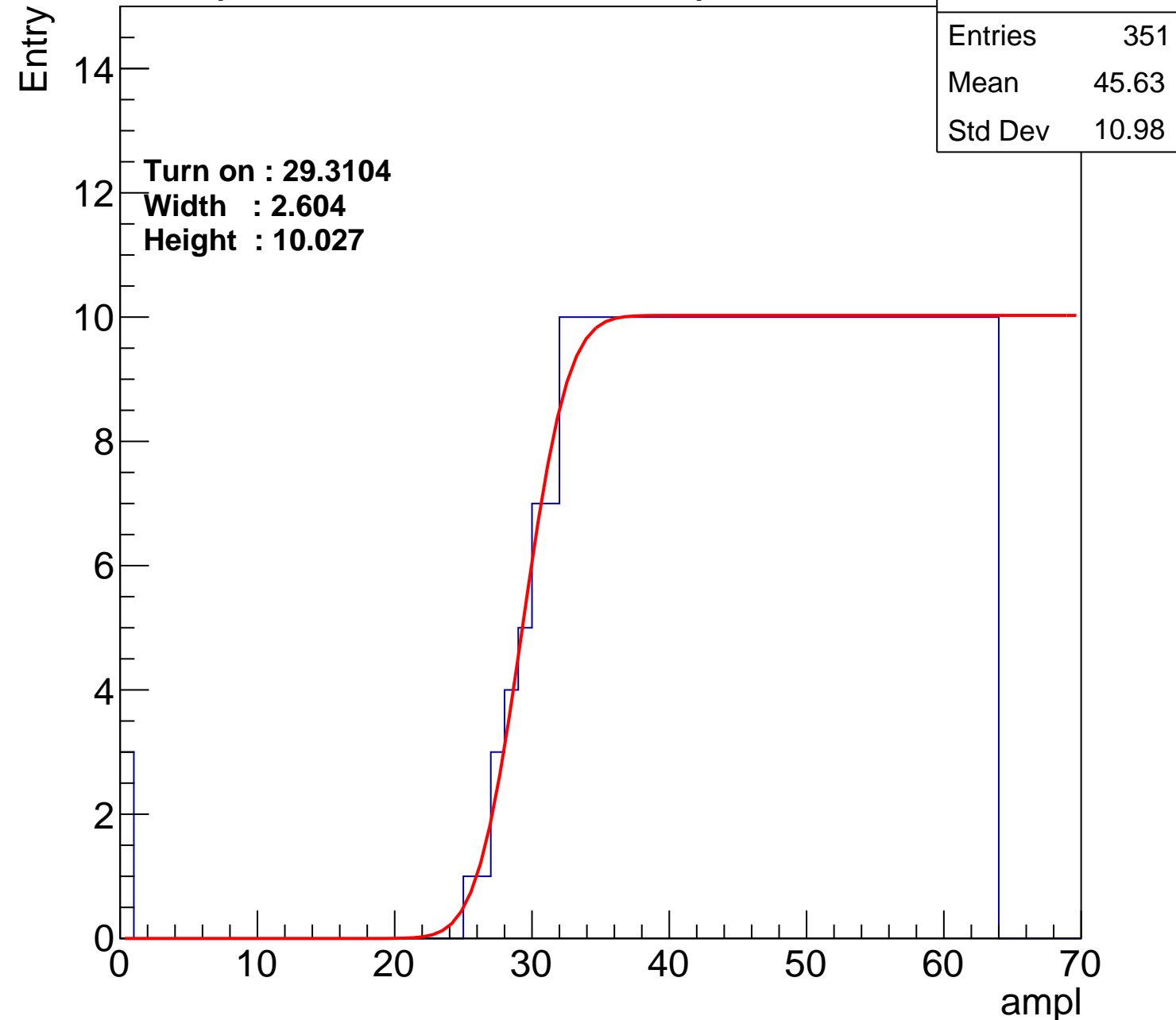
Width : 2.604

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch25

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.22
Std Dev	11.05

Turn on : 28.6618

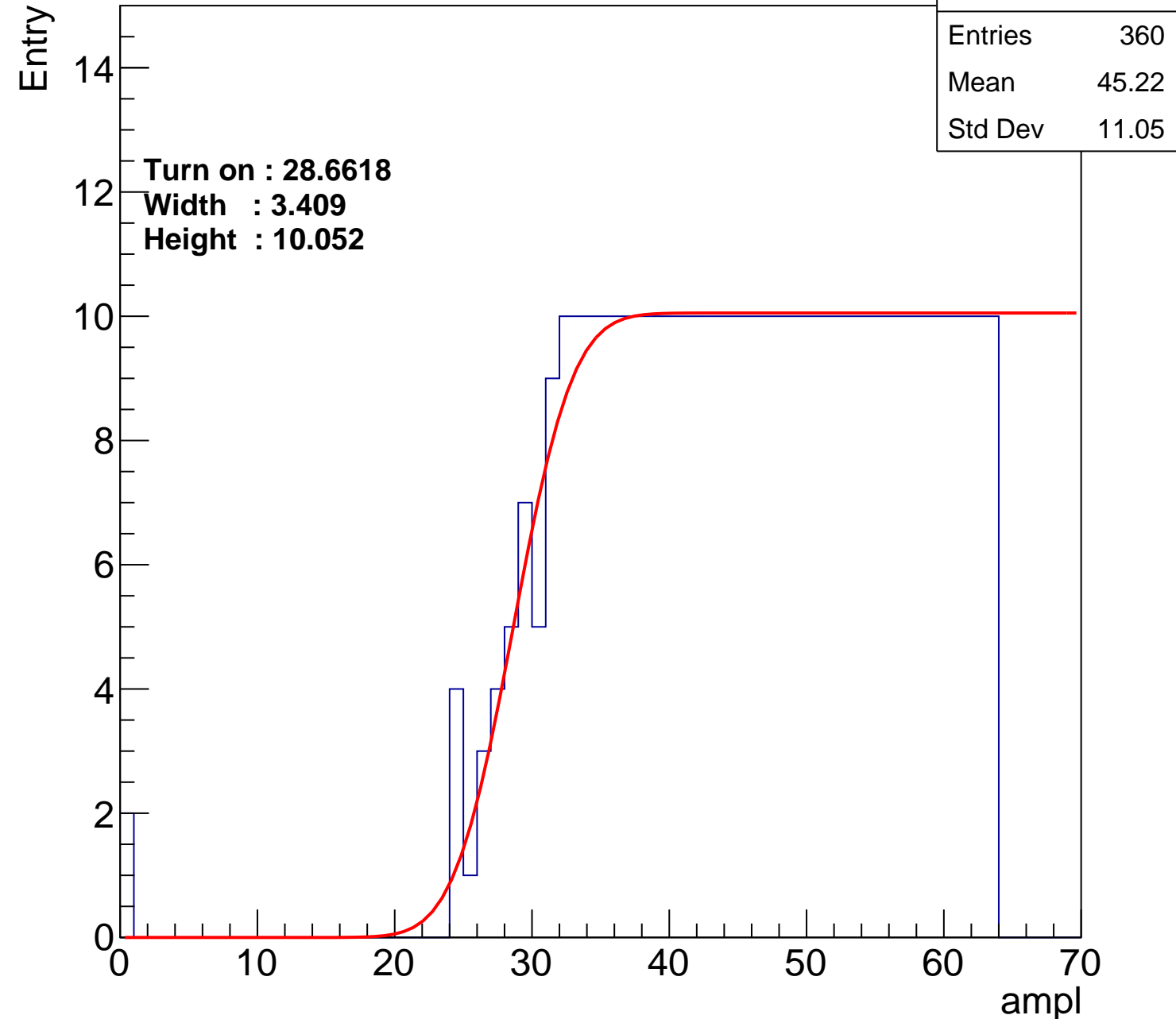
Width : 3.409

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch26

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.11
Std Dev	11.07

Turn on : 28.2856

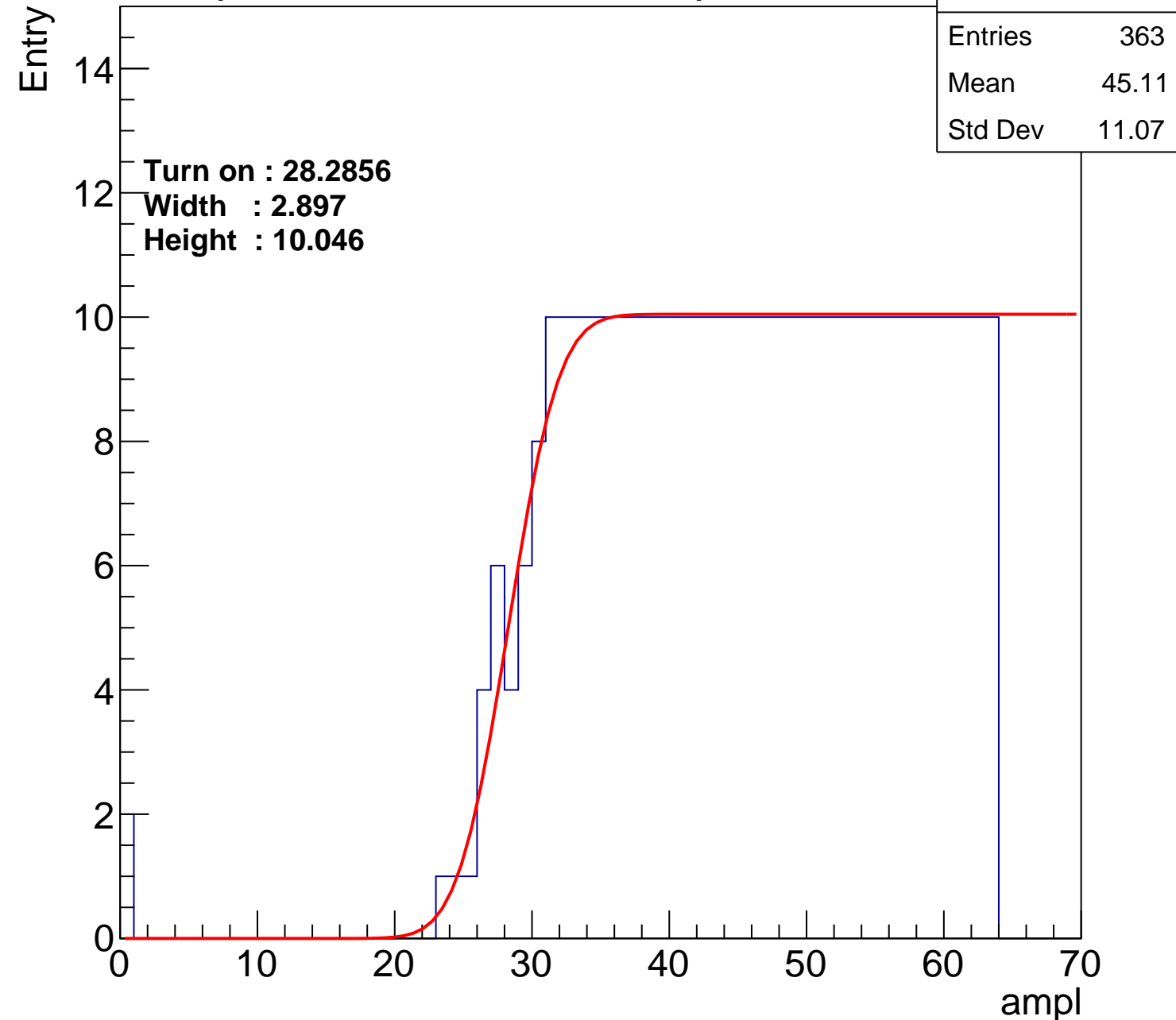
Width : 2.897

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch27

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.31
Std Dev	10.87

Turn on : 28.6260

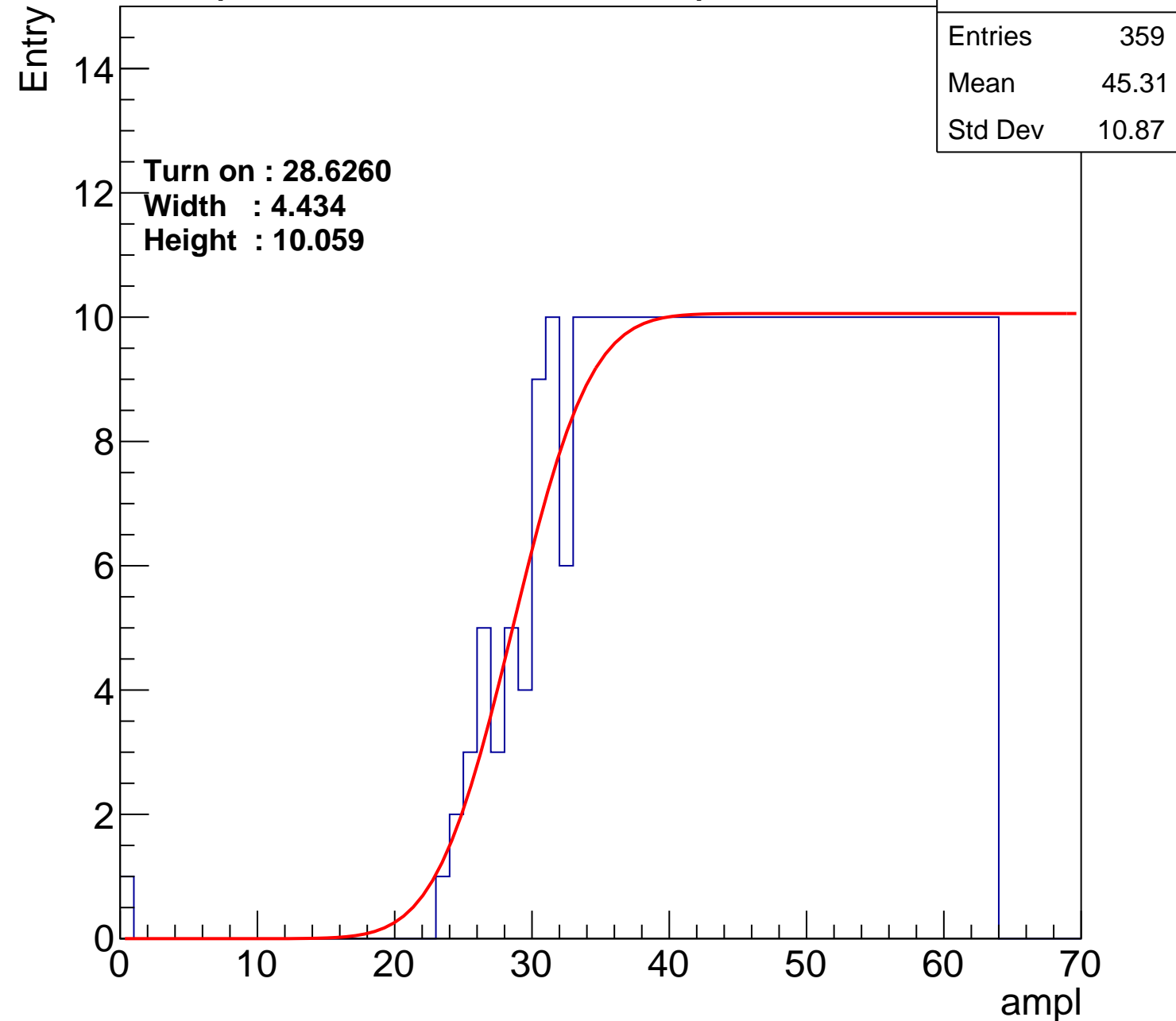
Width : 4.434

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch28

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.31
Std Dev	10.97

Turn on : 28.4748

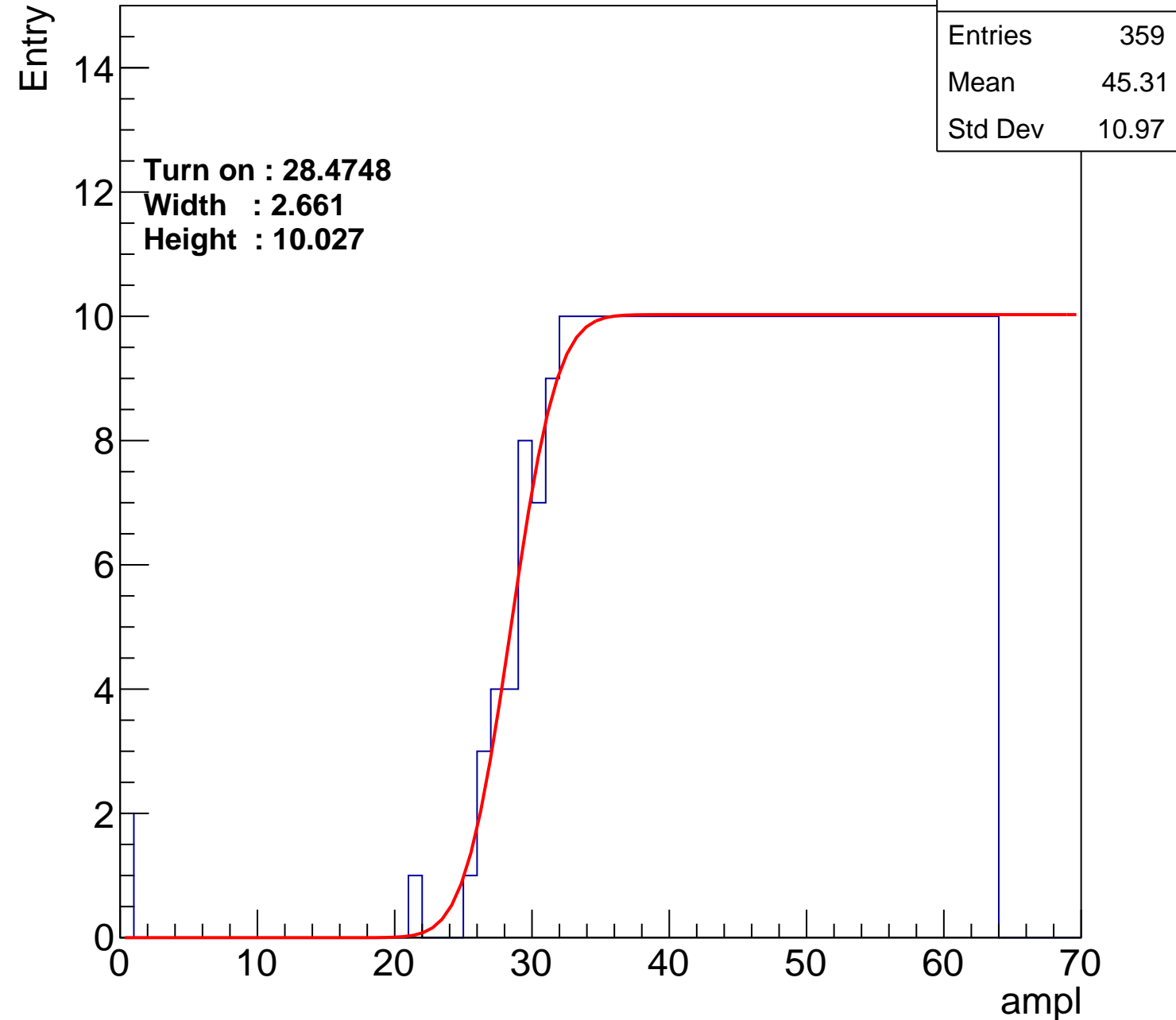
Width : 2.661

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch29

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.5568

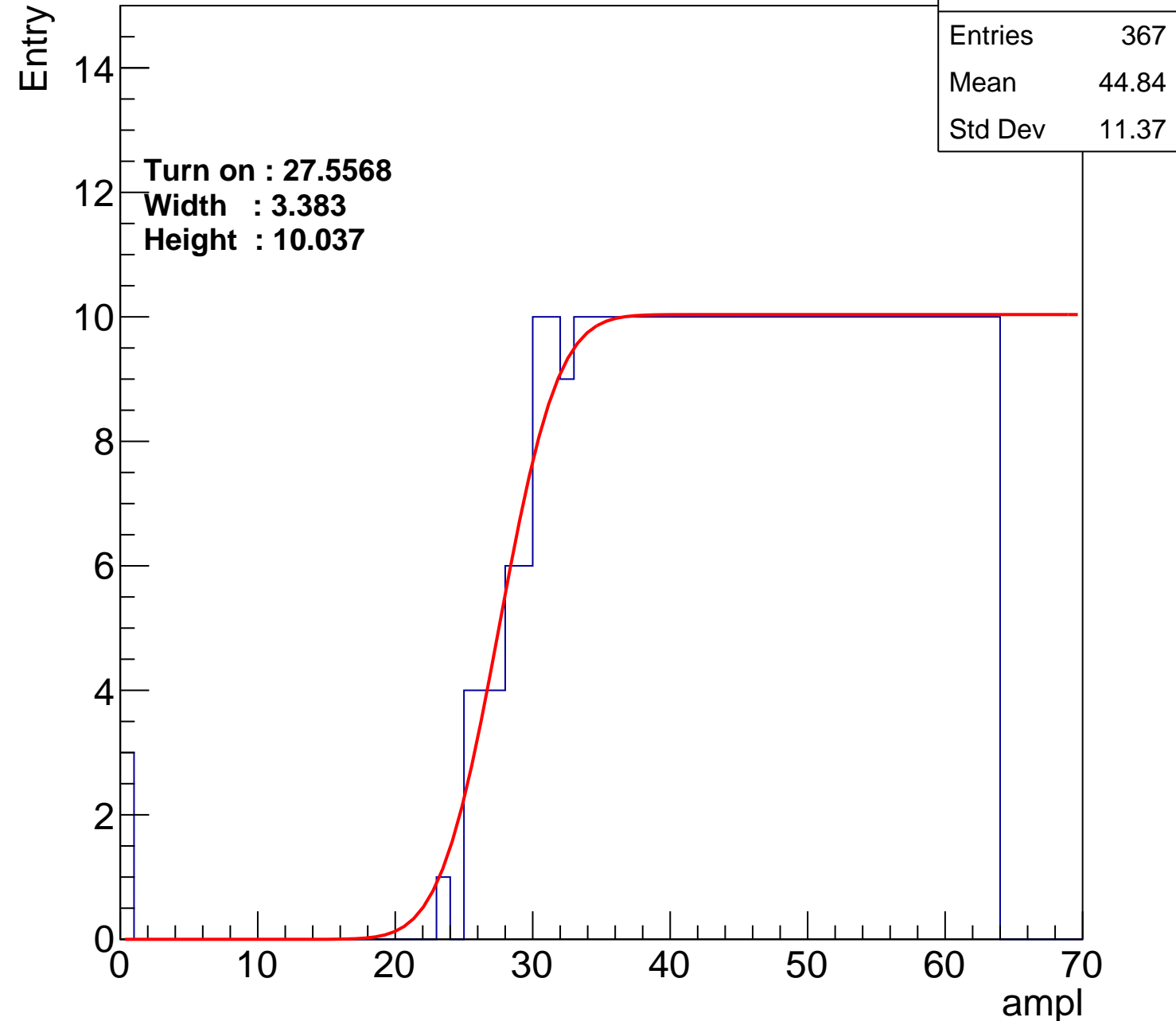
Width : 3.383

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch30

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.52
Std Dev	11.68

Turn on : 26.9705

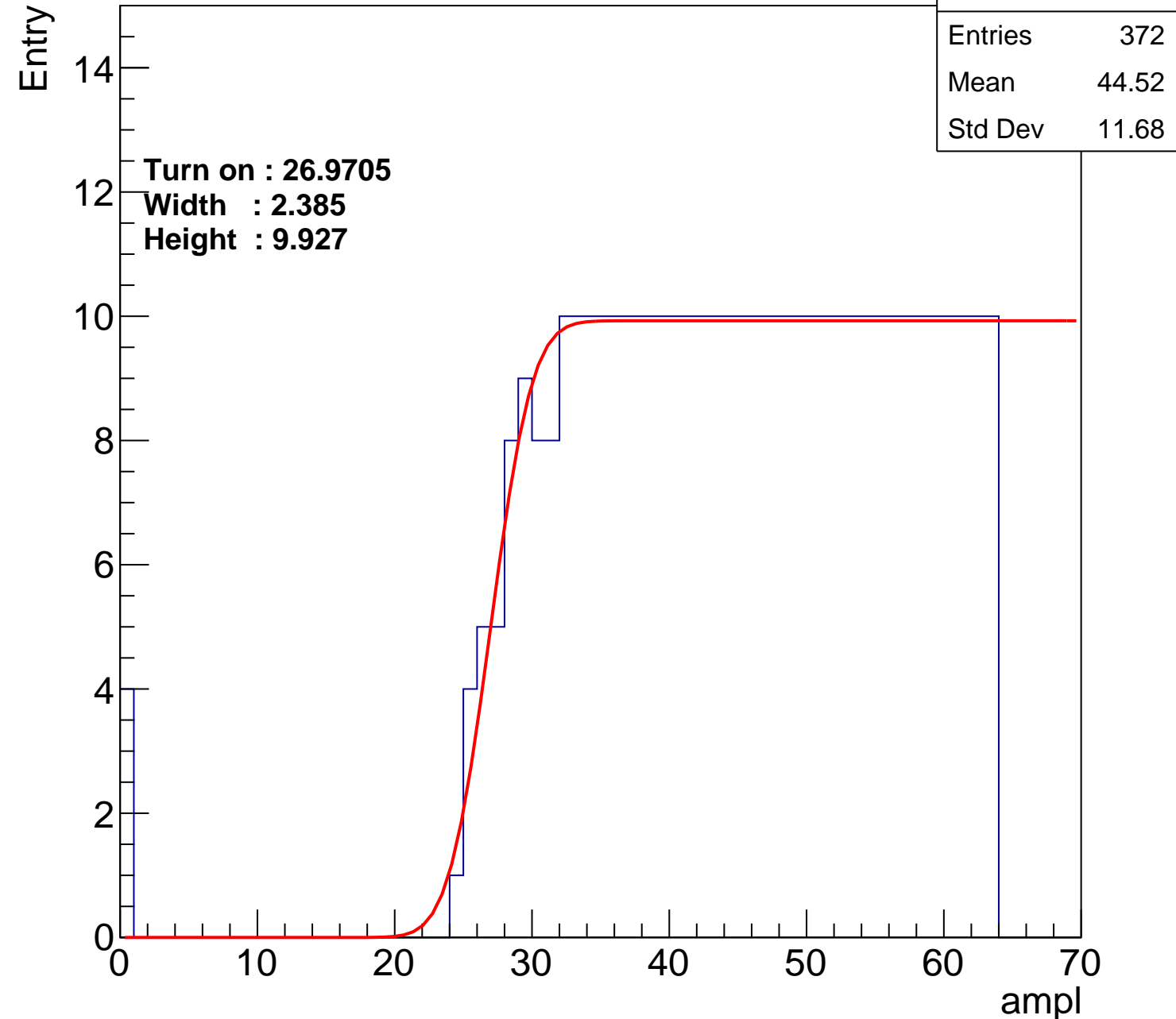
Width : 2.385

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl

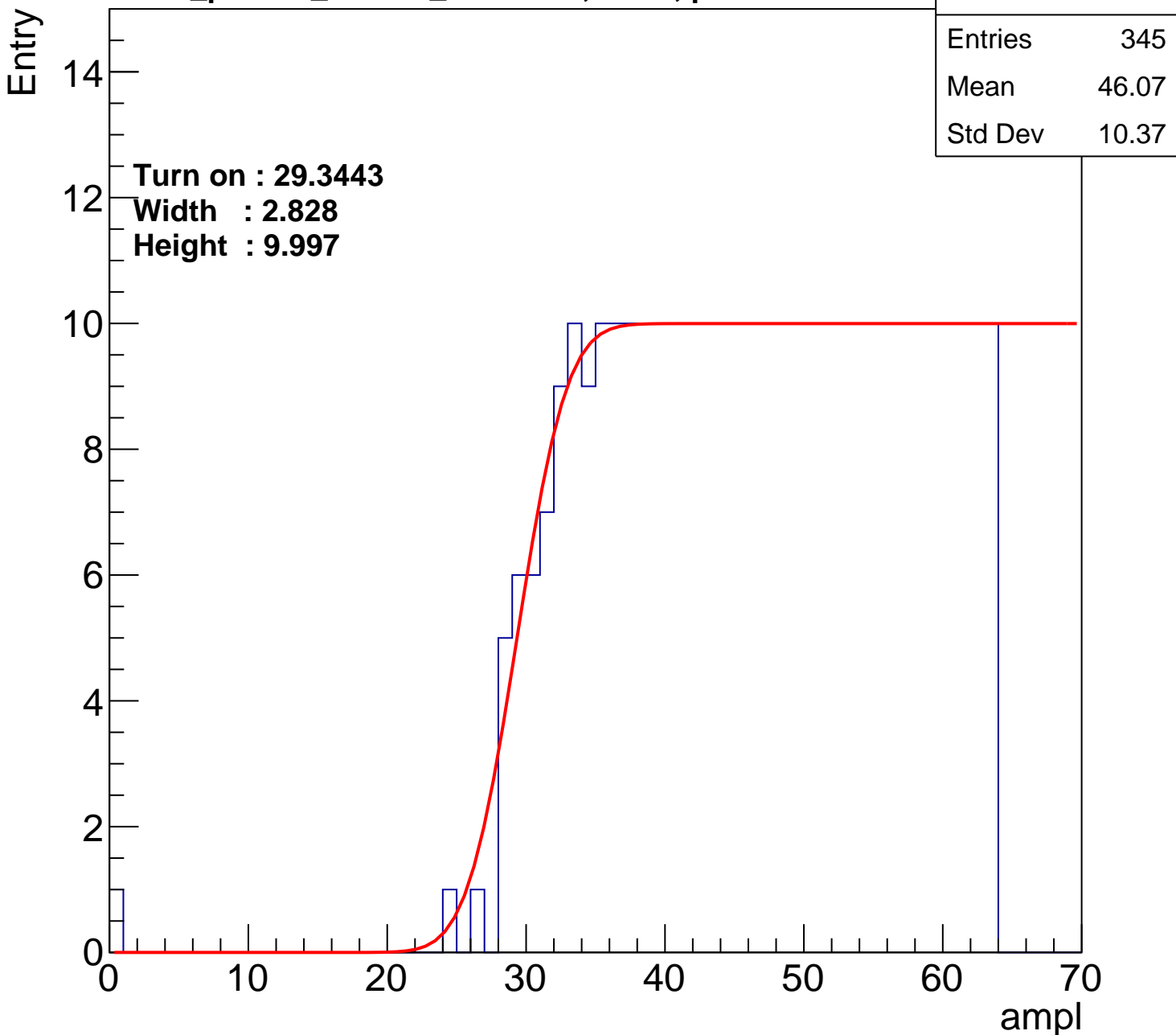


calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	46.07
Std Dev	10.37

Height : 9.997



B0L001S, U26-ch32

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 28.2519

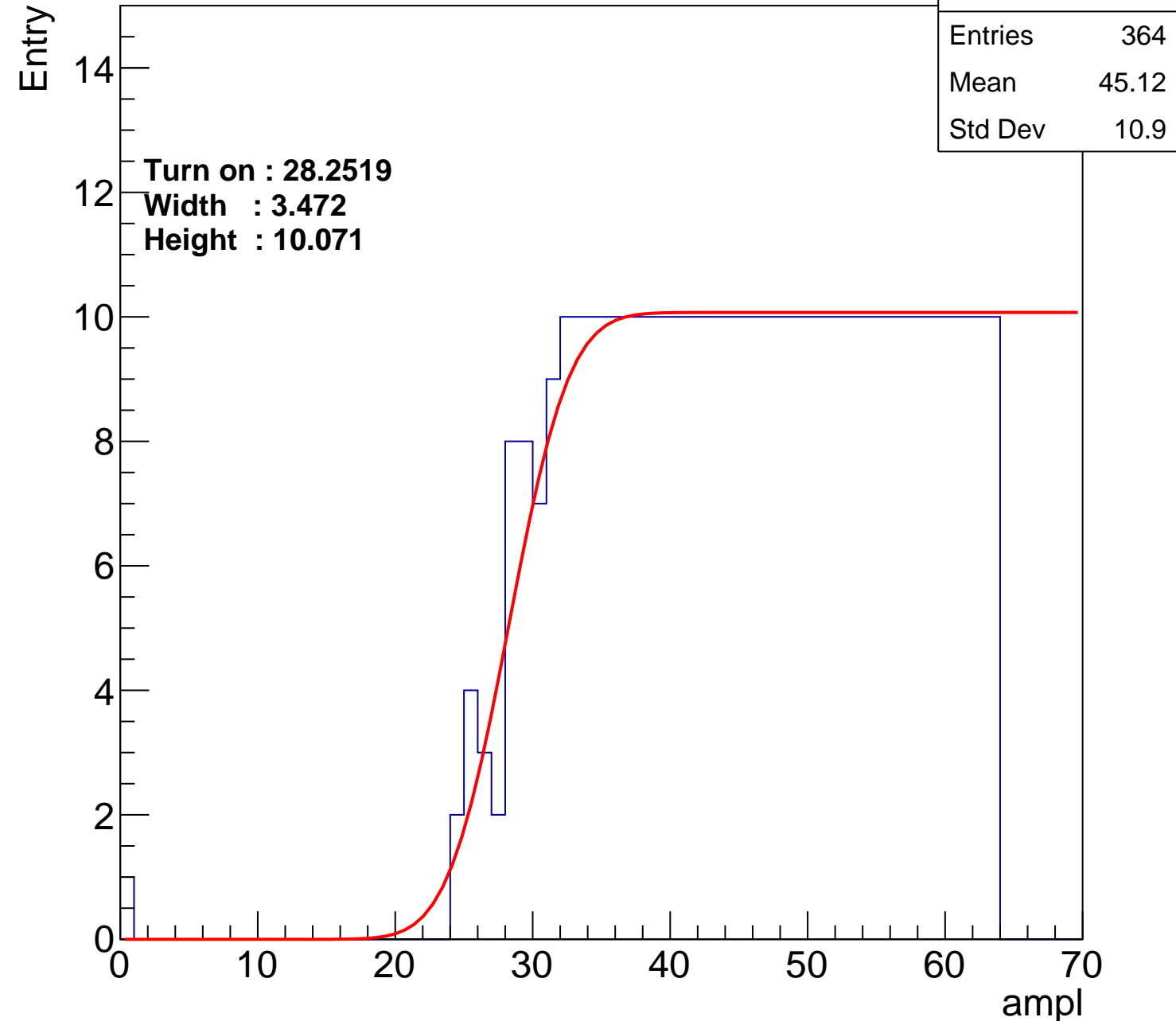
Width : 3.472

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch33

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	45.99
Std Dev	10.62

Turn on : 29.6463

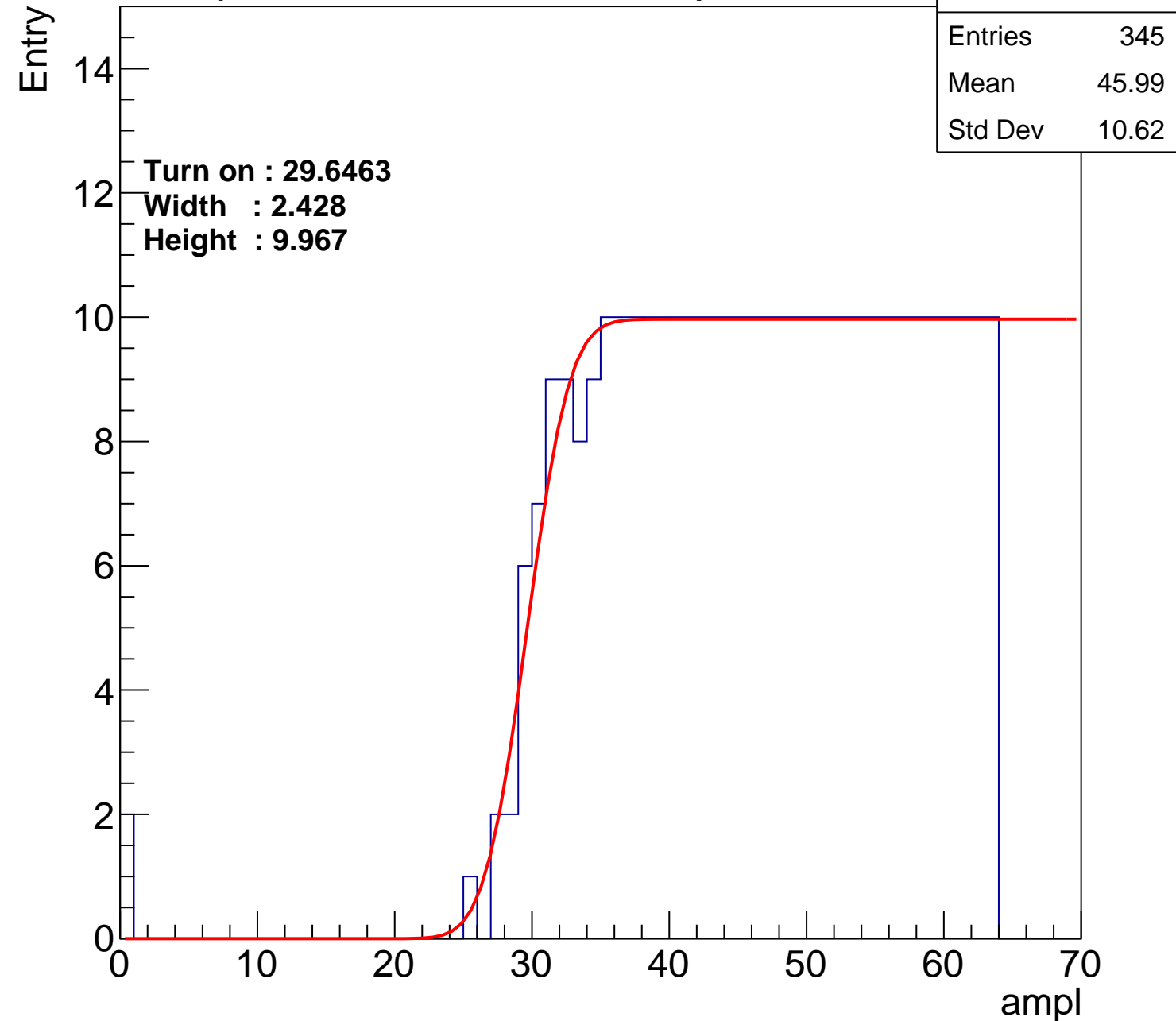
Width : 2.428

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch34

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.72
Std Dev	10.85

Turn on : 30.1893

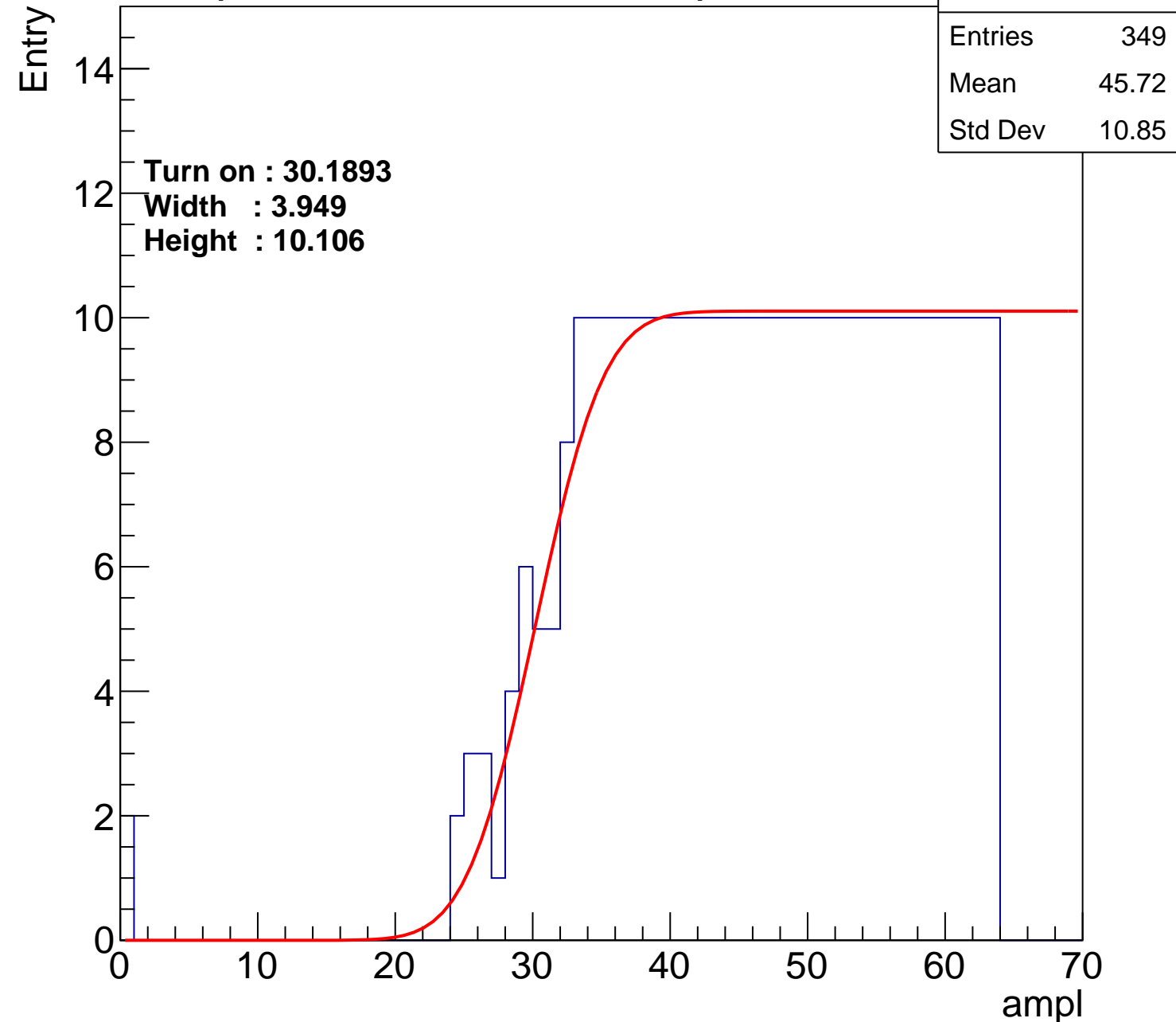
Width : 3.949

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch35

calib_packv5_042523_0143.root, FC#9, port A1

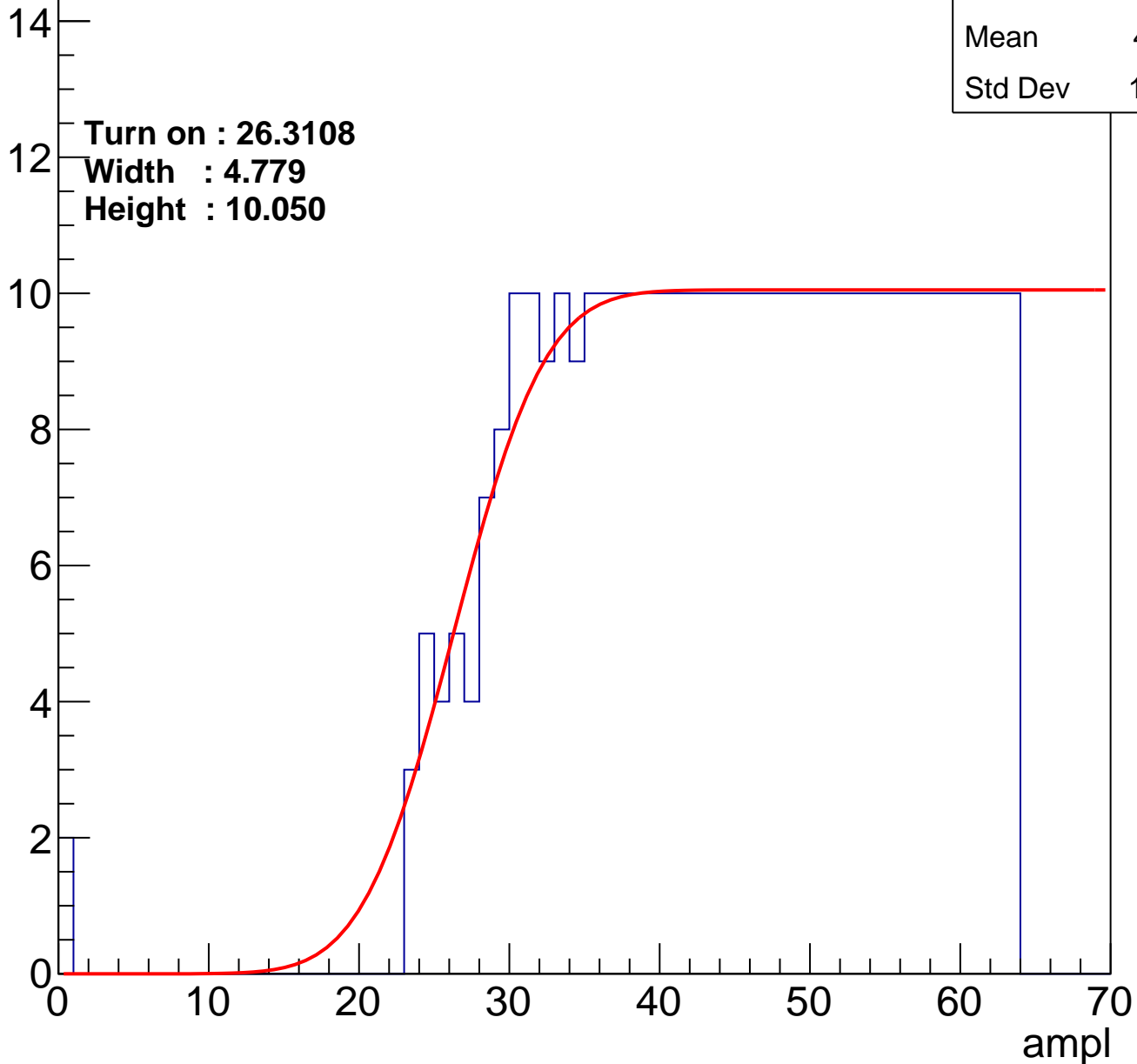
Entries	376
Mean	44.41
Std Dev	11.48

Turn on : 26.3108

Width : 4.779

Height : 10.050

Entry



B0L001S, U26-ch36

calib_packv5_042523_0143.root, FC#9, port A1

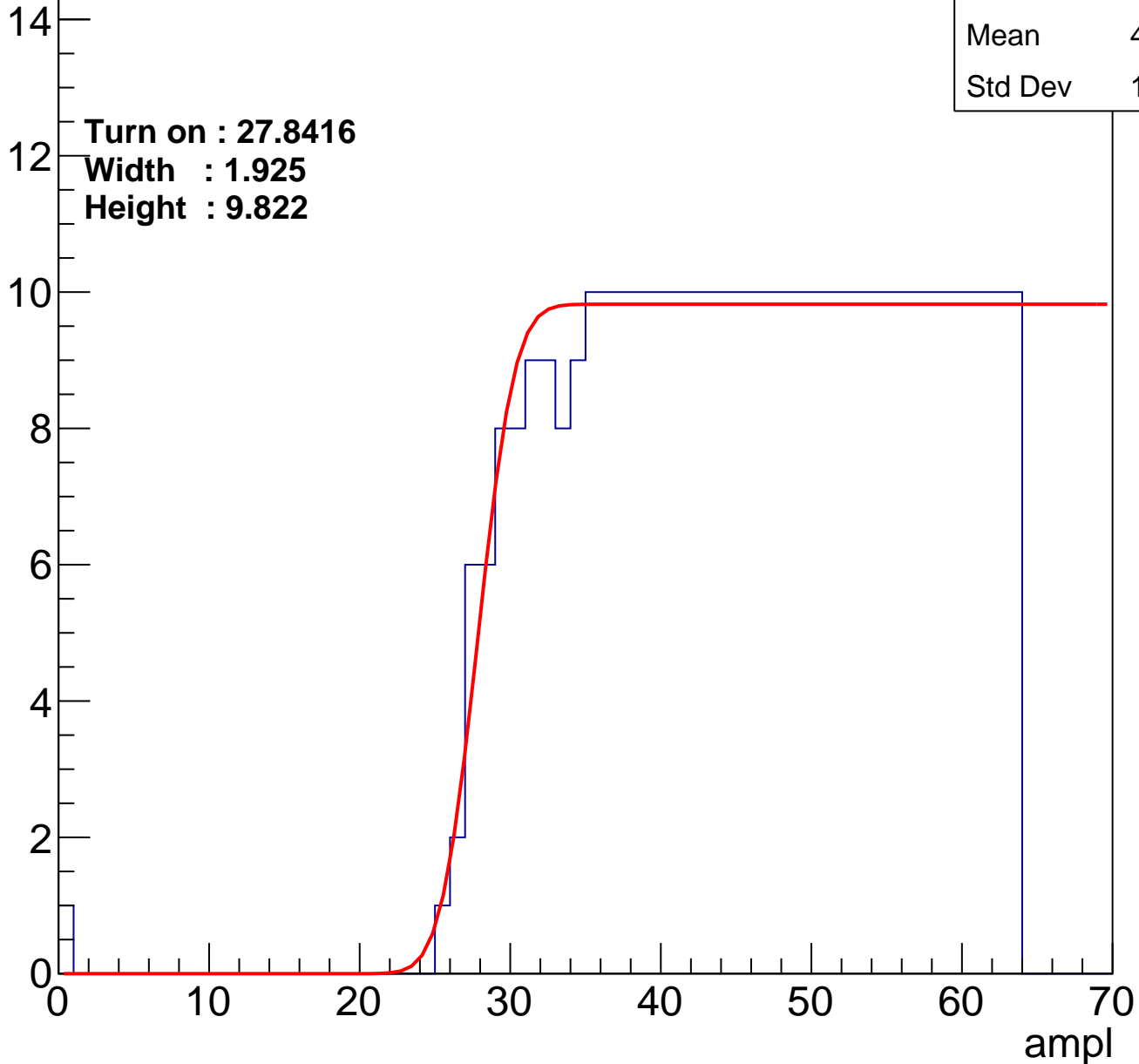
Entries	357
Mean	45.45
Std Dev	10.73

Turn on : 27.8416

Width : 1.925

Height : 9.822

Entry



B0L001S, U26-ch37

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.33
Std Dev	10.82

Turn on : 28.2616

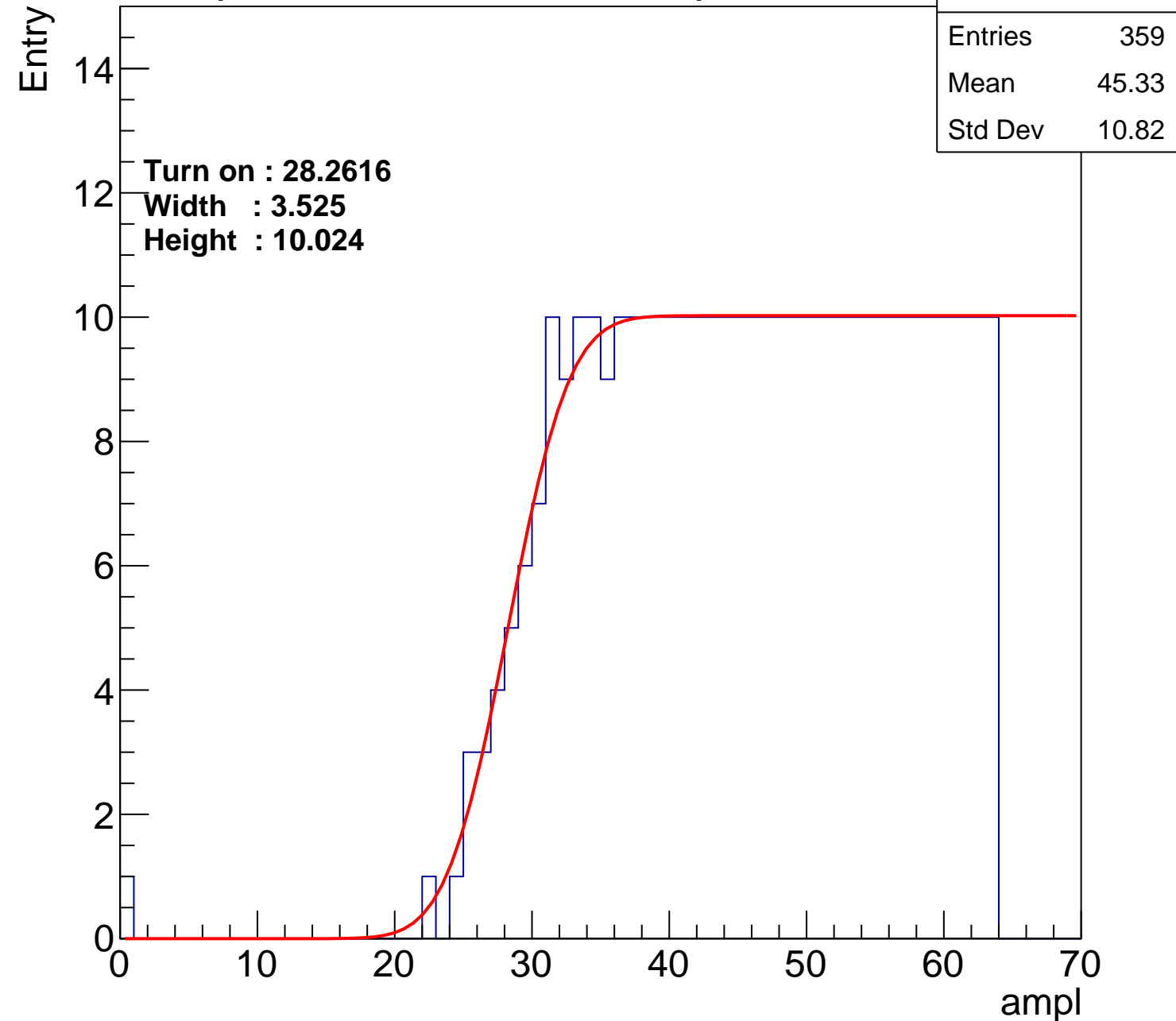
Width : 3.525

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch38

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.93
Std Dev	11.29

Turn on : 28.3045

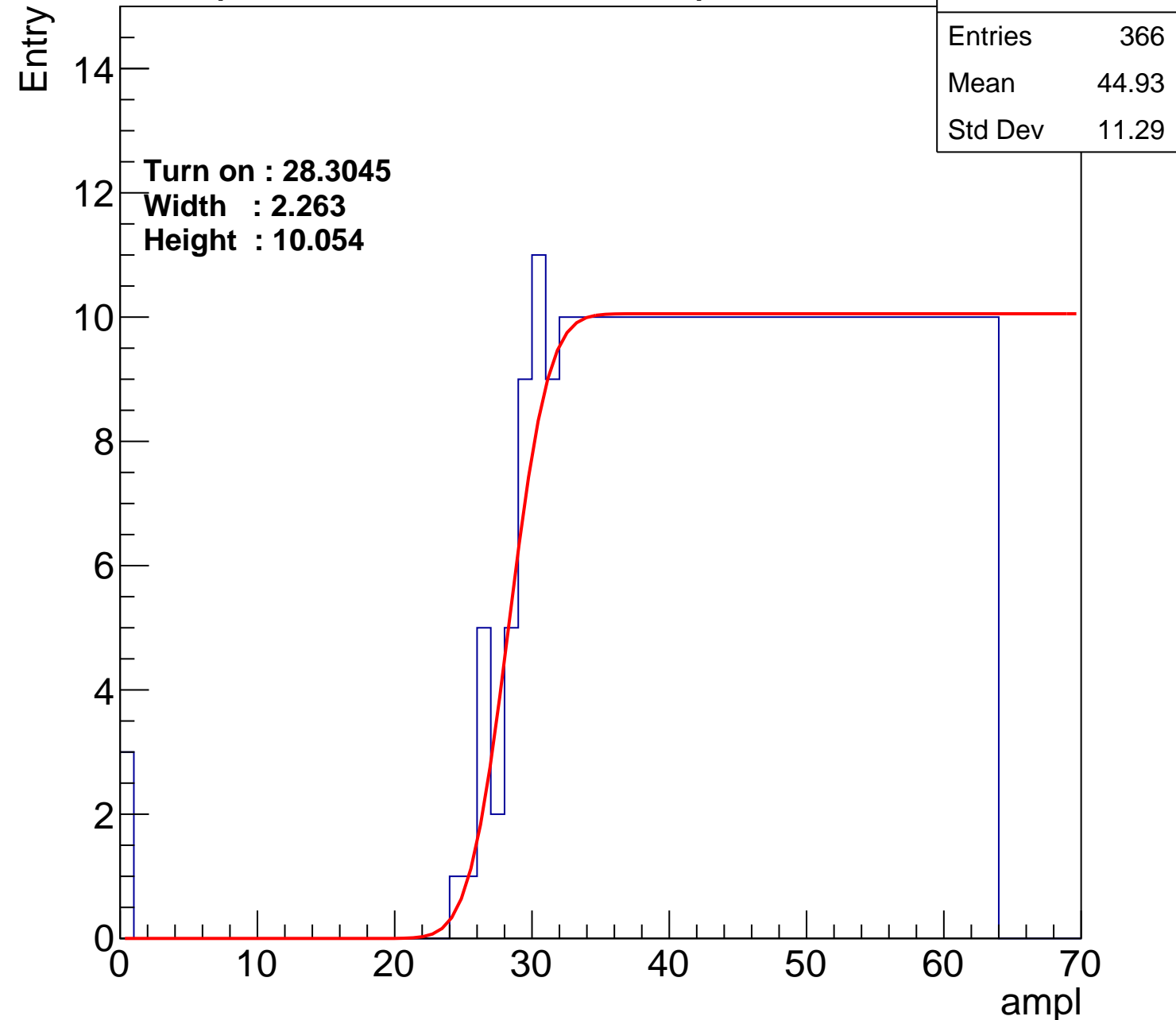
Width : 2.263

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch39

calib_packv5_042523_0143.root, FC#9, port A1

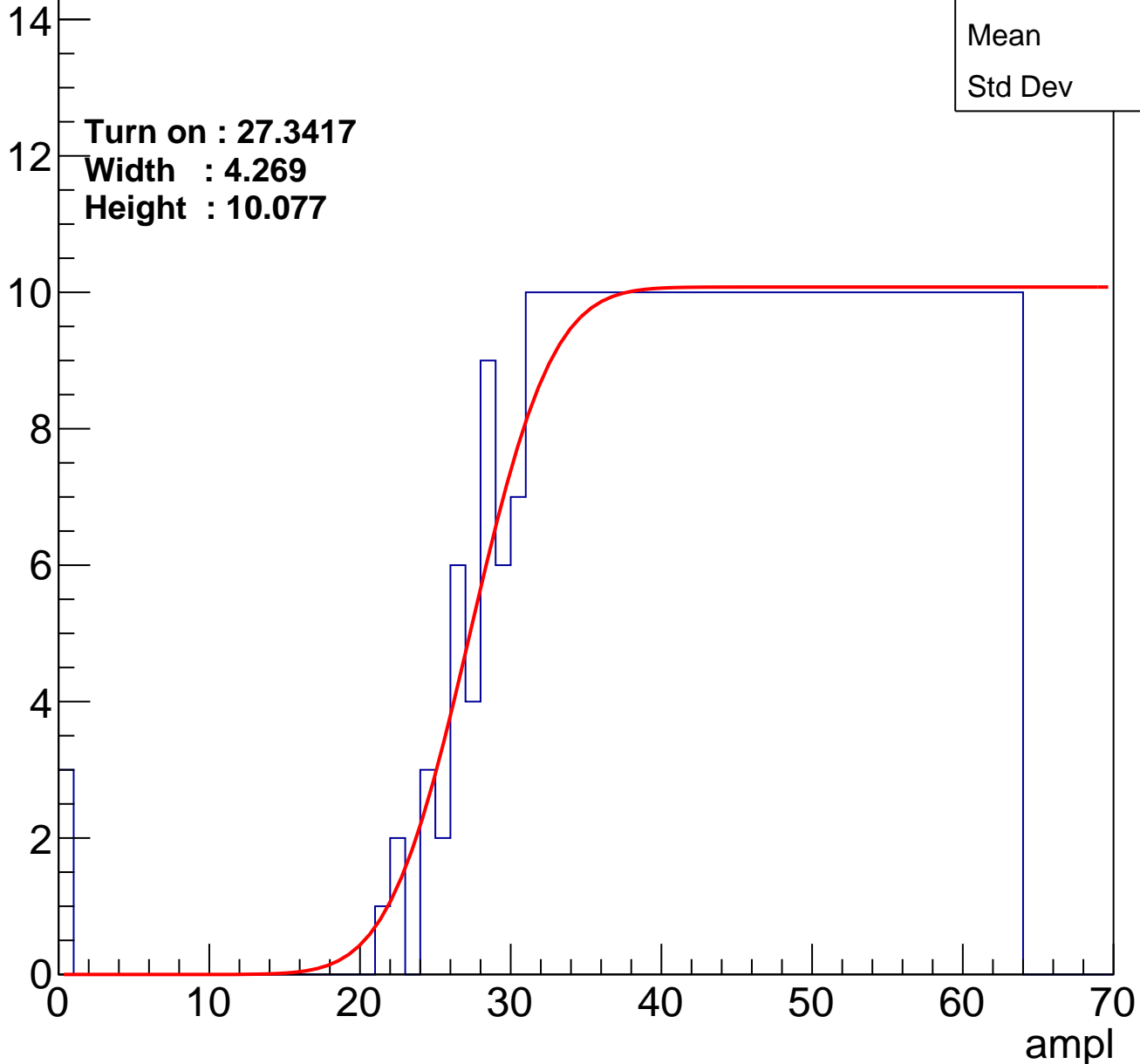
Entries	373
Mean	44.5
Std Dev	11.6

Turn on : 27.3417

Width : 4.269

Height : 10.077

Entry



B0L001S, U26-ch40

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.97
Std Dev	11.33

Turn on : 28.0390

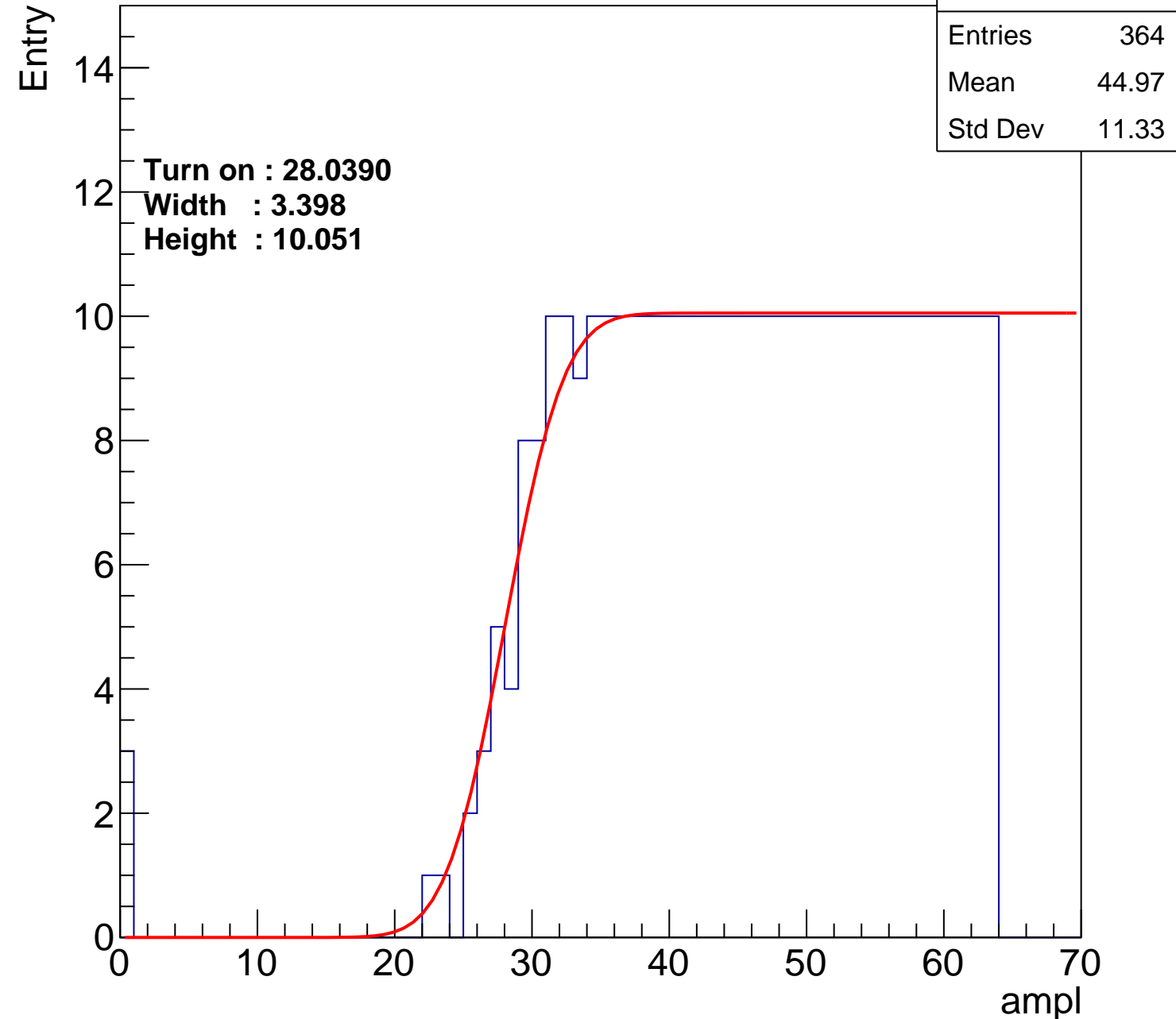
Width : 3.398

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch41

calib_packv5_042523_0143.root, FC#9, port A1

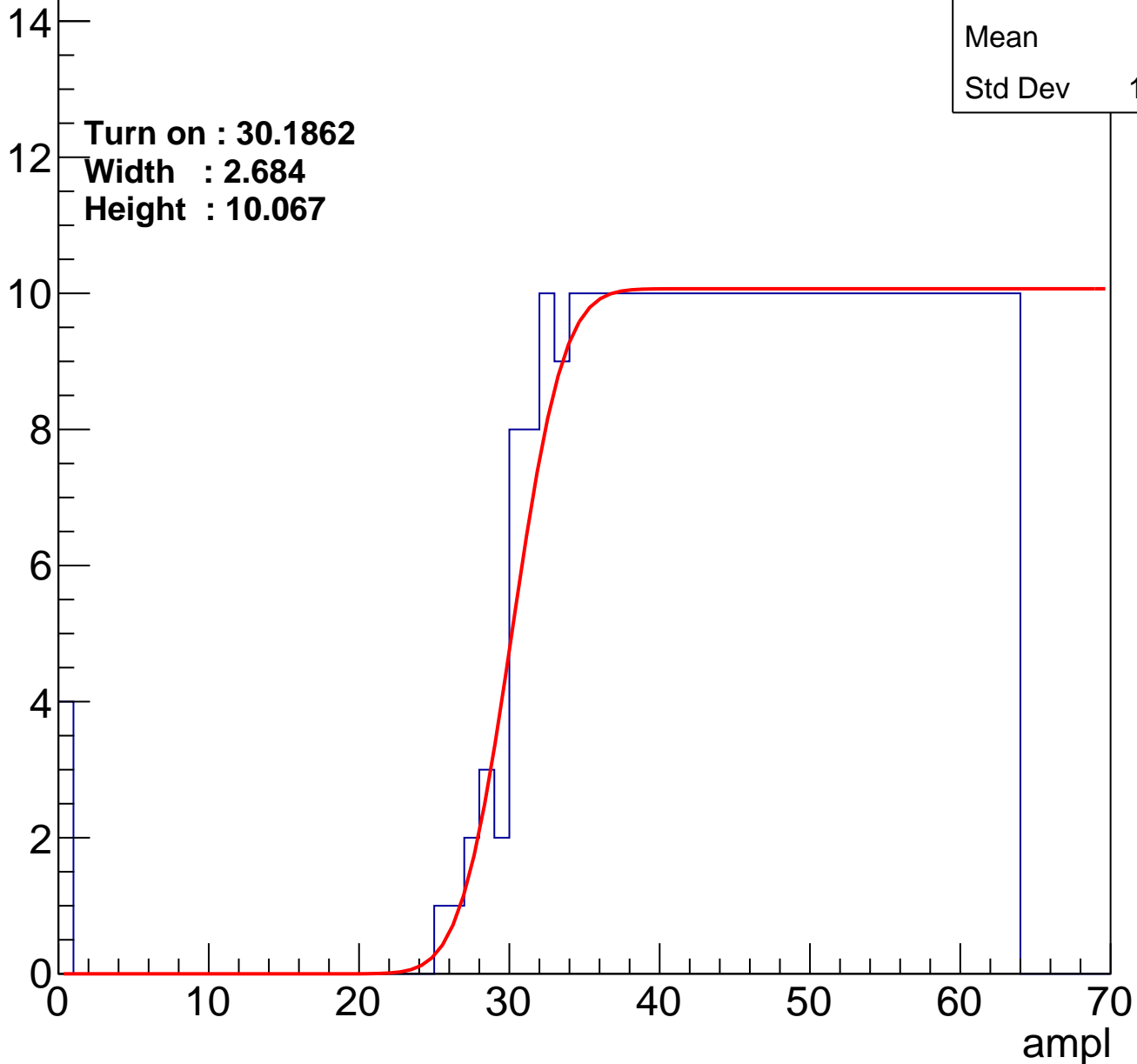
Entries	348
Mean	45.7
Std Dev	11.14

Turn on : 30.1862

Width : 2.684

Height : 10.067

Entry



B0L001S, U26-ch42

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.5
Std Dev	11.9

Turn on : 27.6373

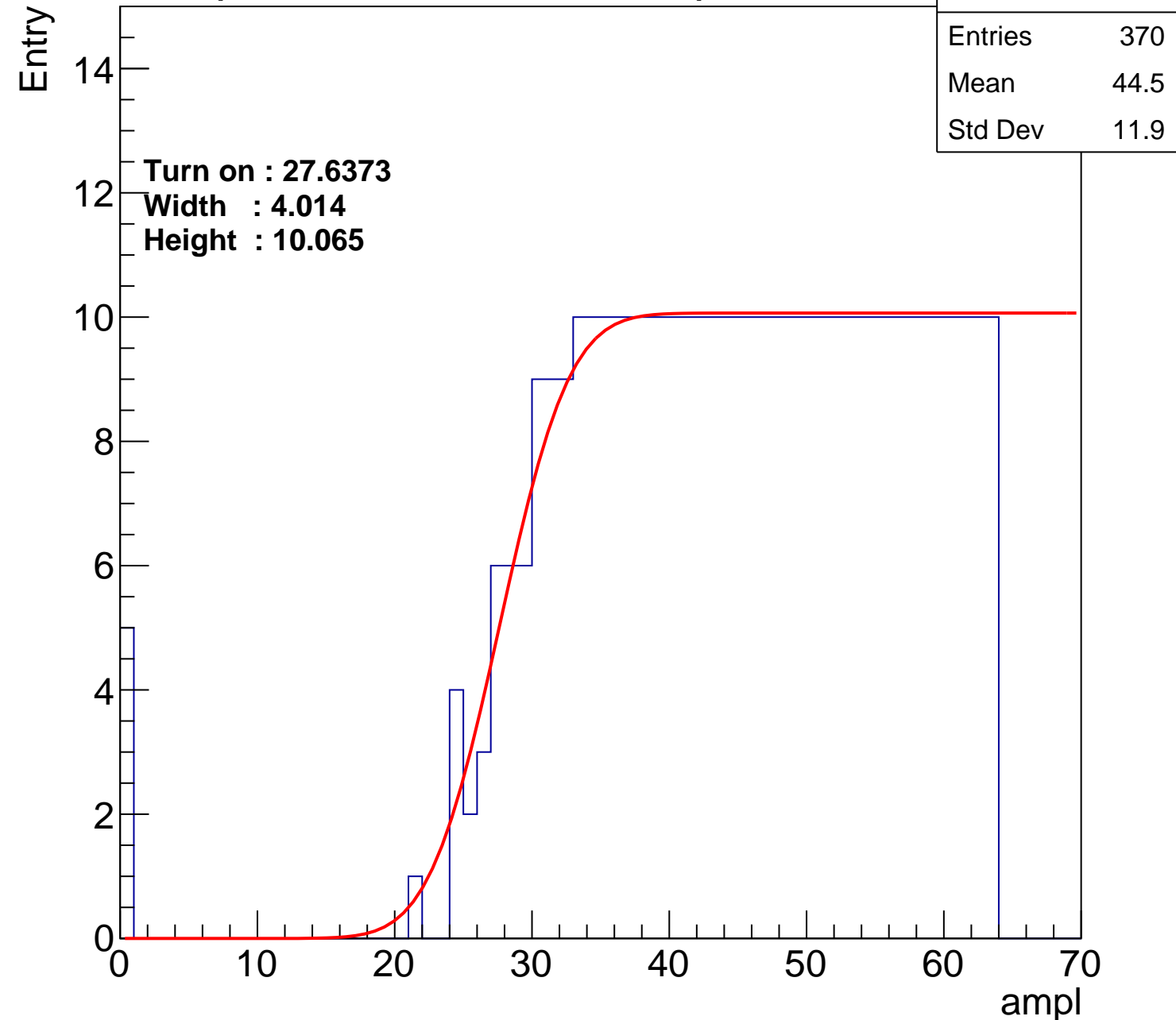
Width : 4.014

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch43

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.37
Std Dev	10.97

Turn on : 28.8581

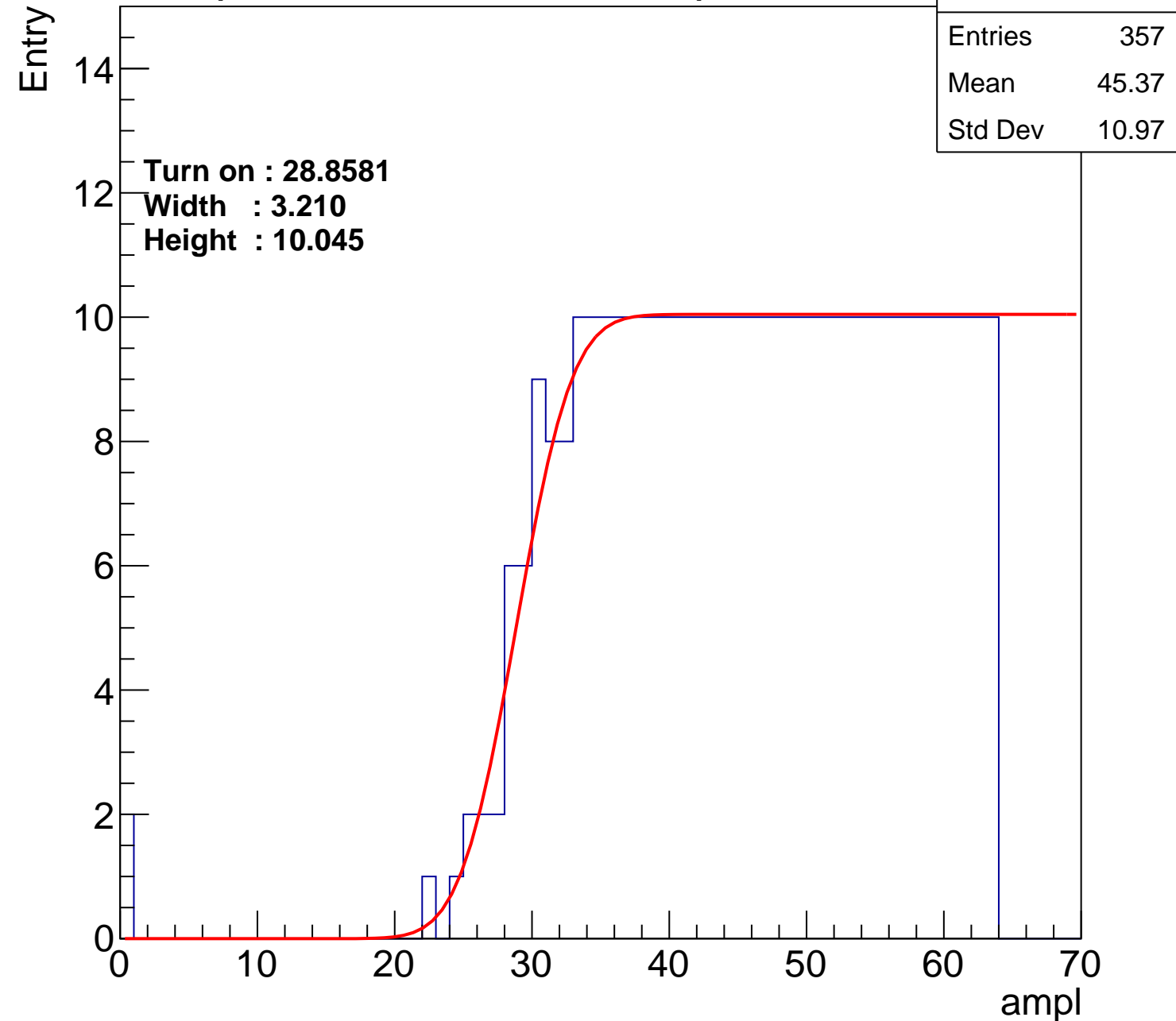
Width : 3.210

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#9, port A1

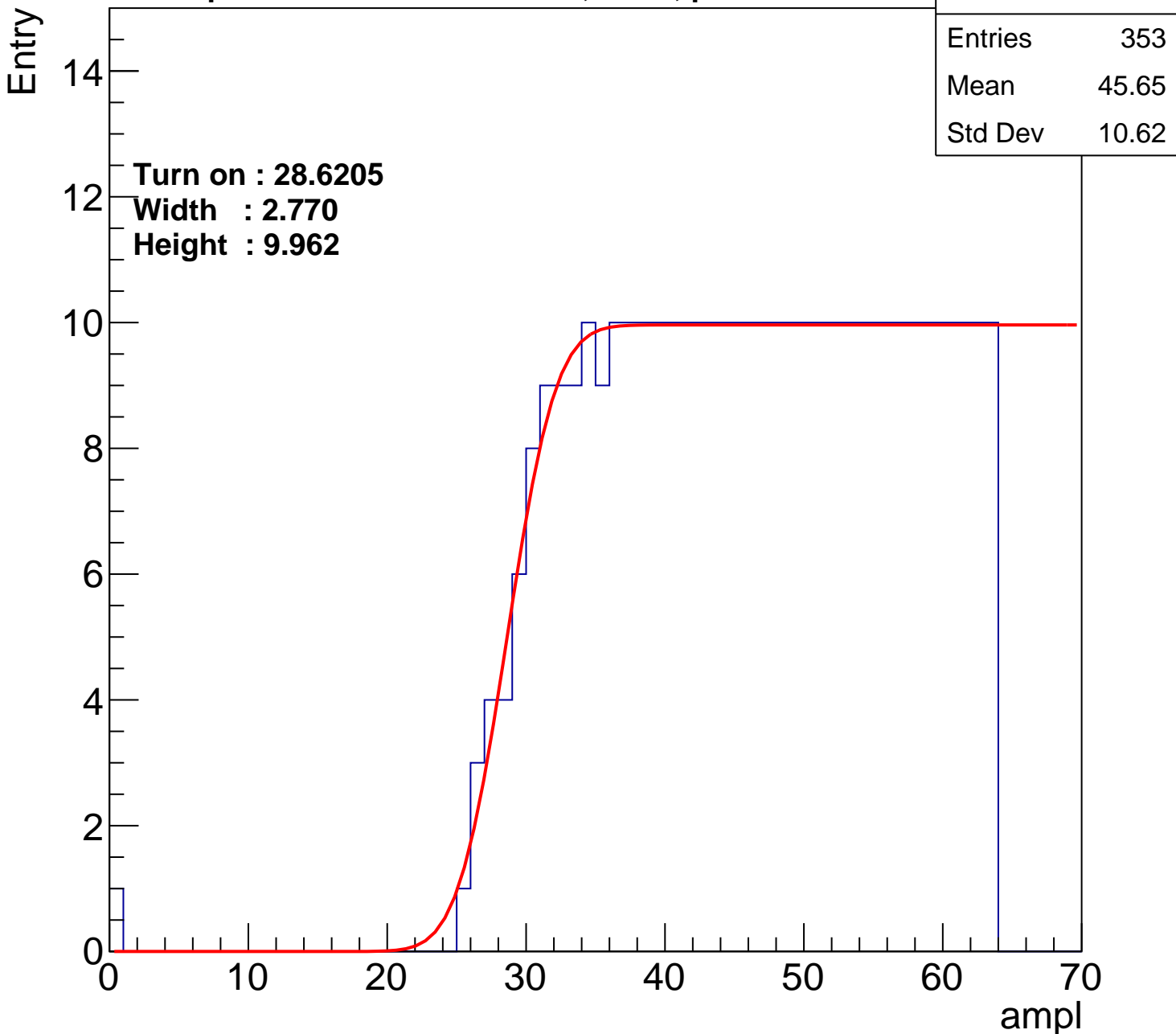
Entries	353
Mean	45.65
Std Dev	10.62

Mean	45.65
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Std Dev	10.62
---------	-------

Width : 2.770

Height : 9.962



B0L001S, U26-ch45

calib_packv5_042523_0143.root, FC#9, port A1

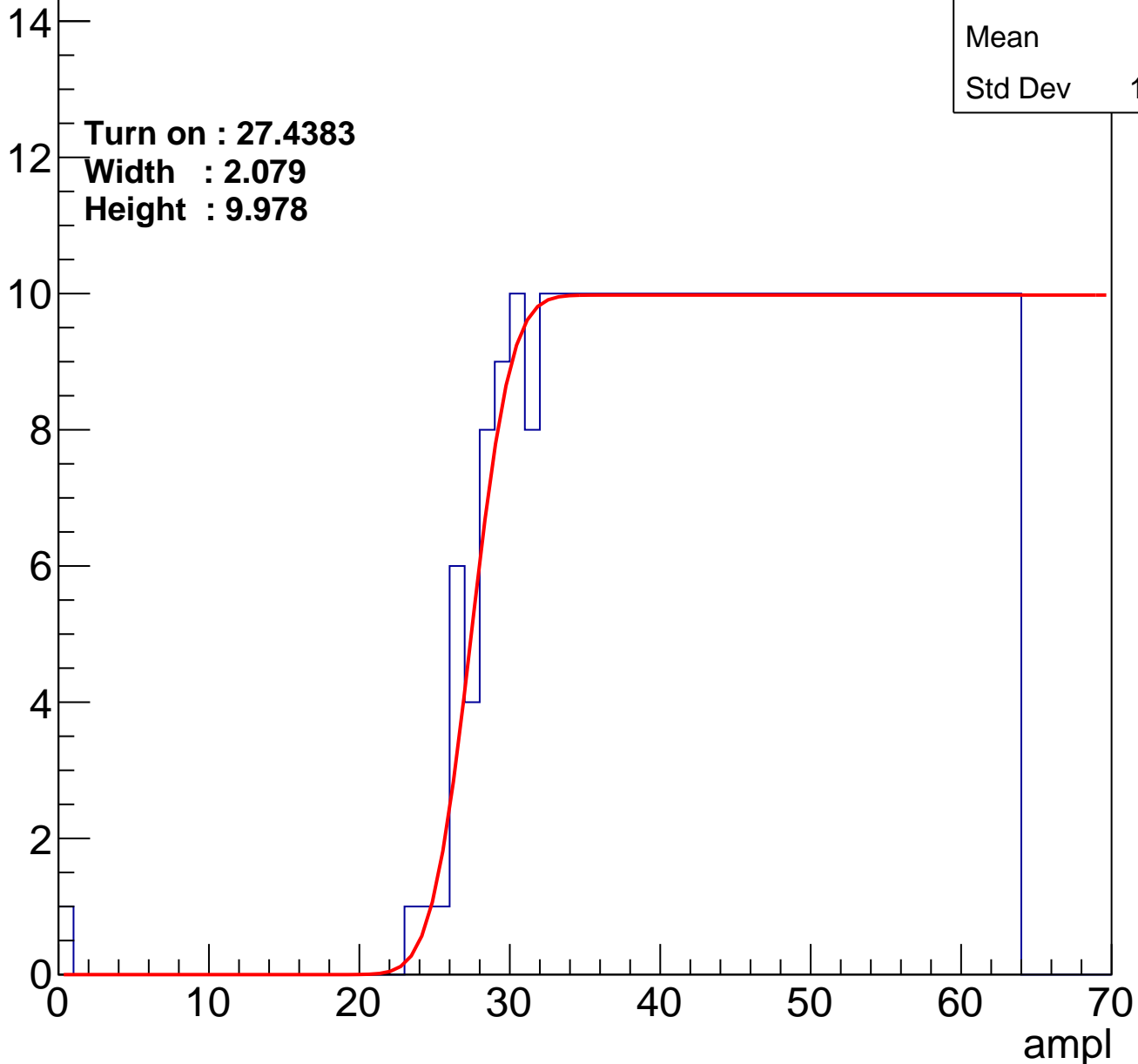
Entries	369
Mean	44.9
Std Dev	10.98

Turn on : 27.4383

Width : 2.079

Height : 9.978

Entry



B0L001S, U26-ch46

calib_packv5_042523_0143.root, FC#9, port A1

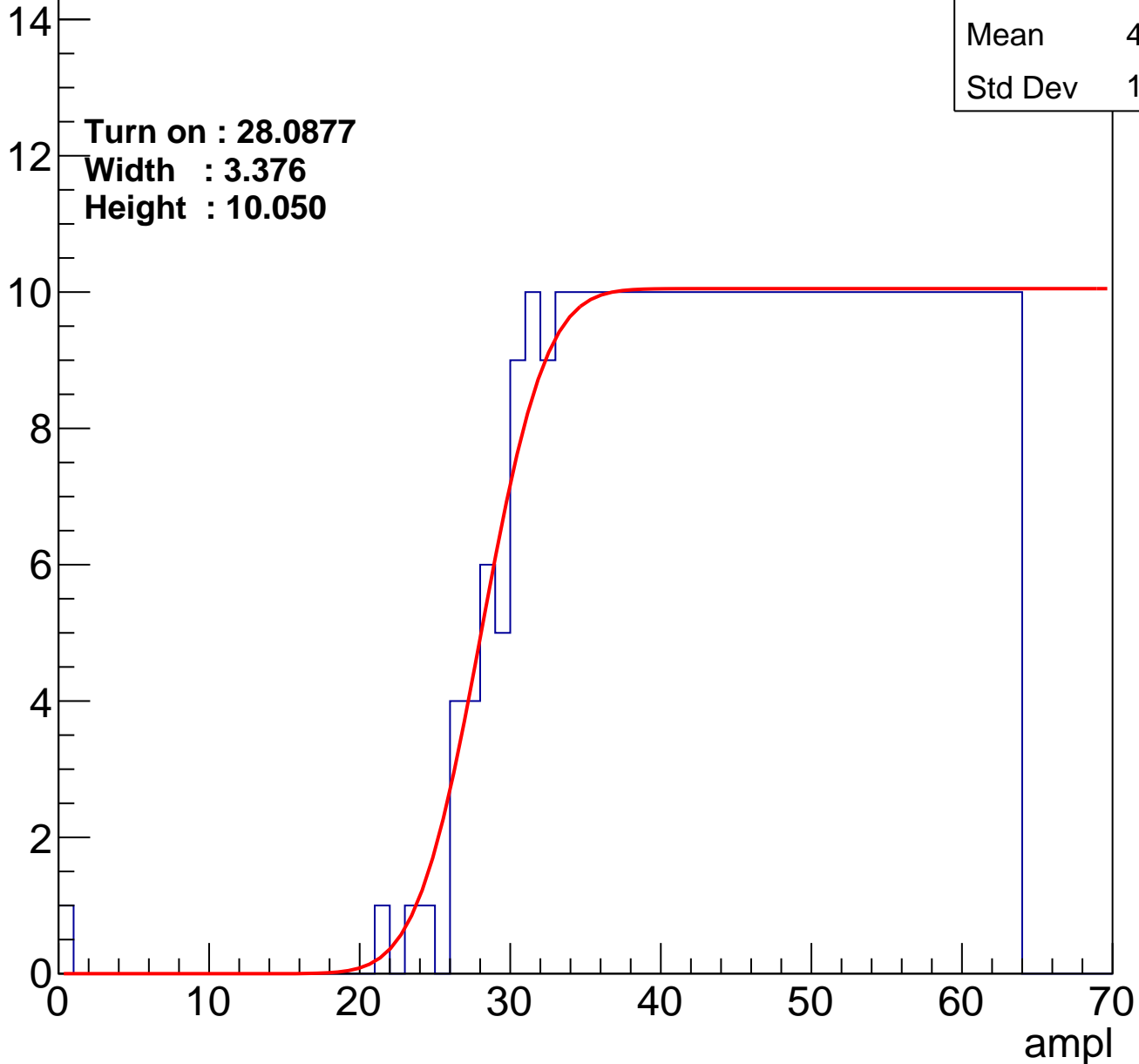
Entries	361
Mean	45.27
Std Dev	10.83

Turn on : 28.0877

Width : 3.376

Height : 10.050

Entry



B0L001S, U26-ch47

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.57
Std Dev	10.6

Turn on : 28.6622

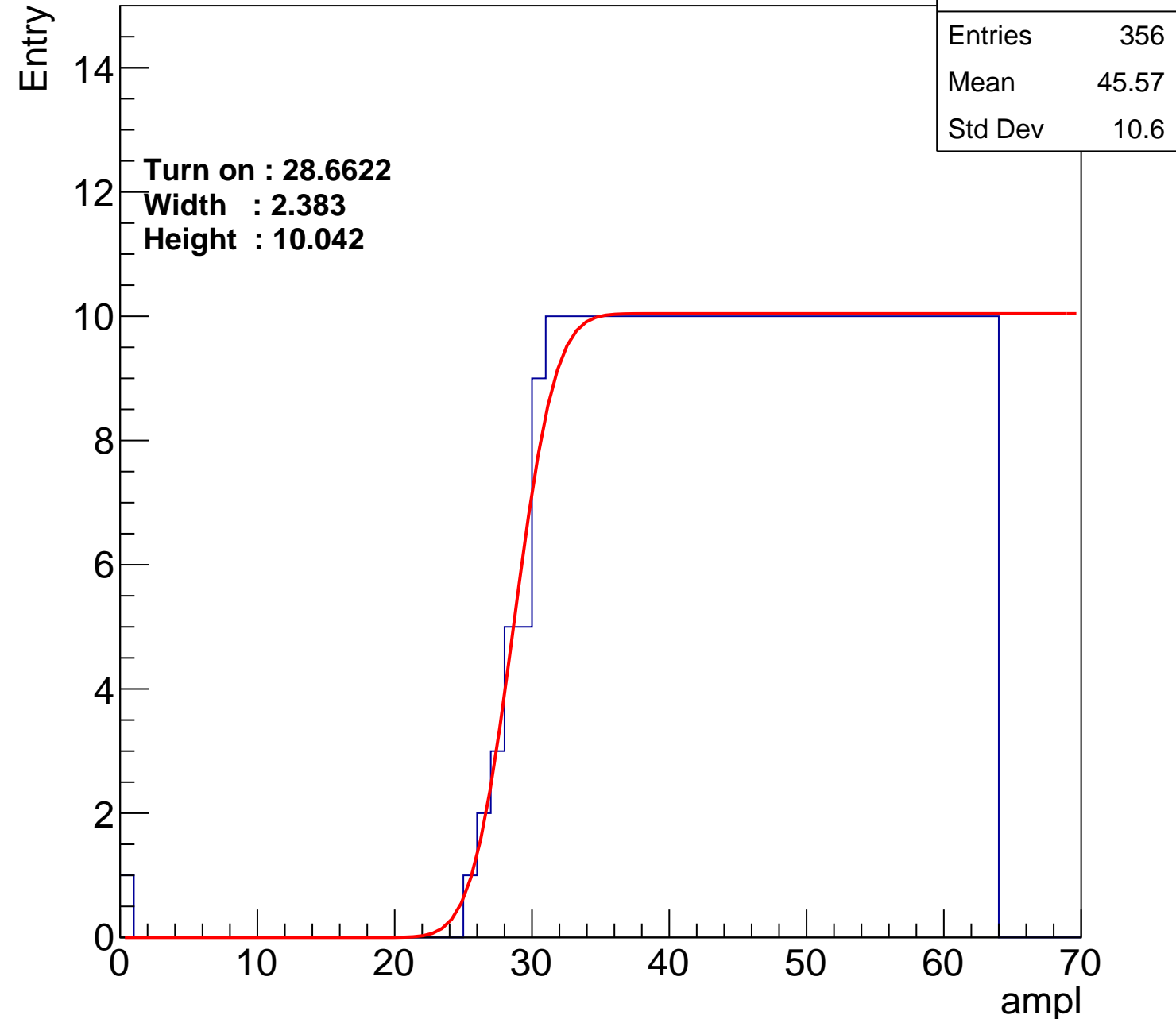
Width : 2.383

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch48

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.86
Std Dev	10.51

Turn on : 29.4010

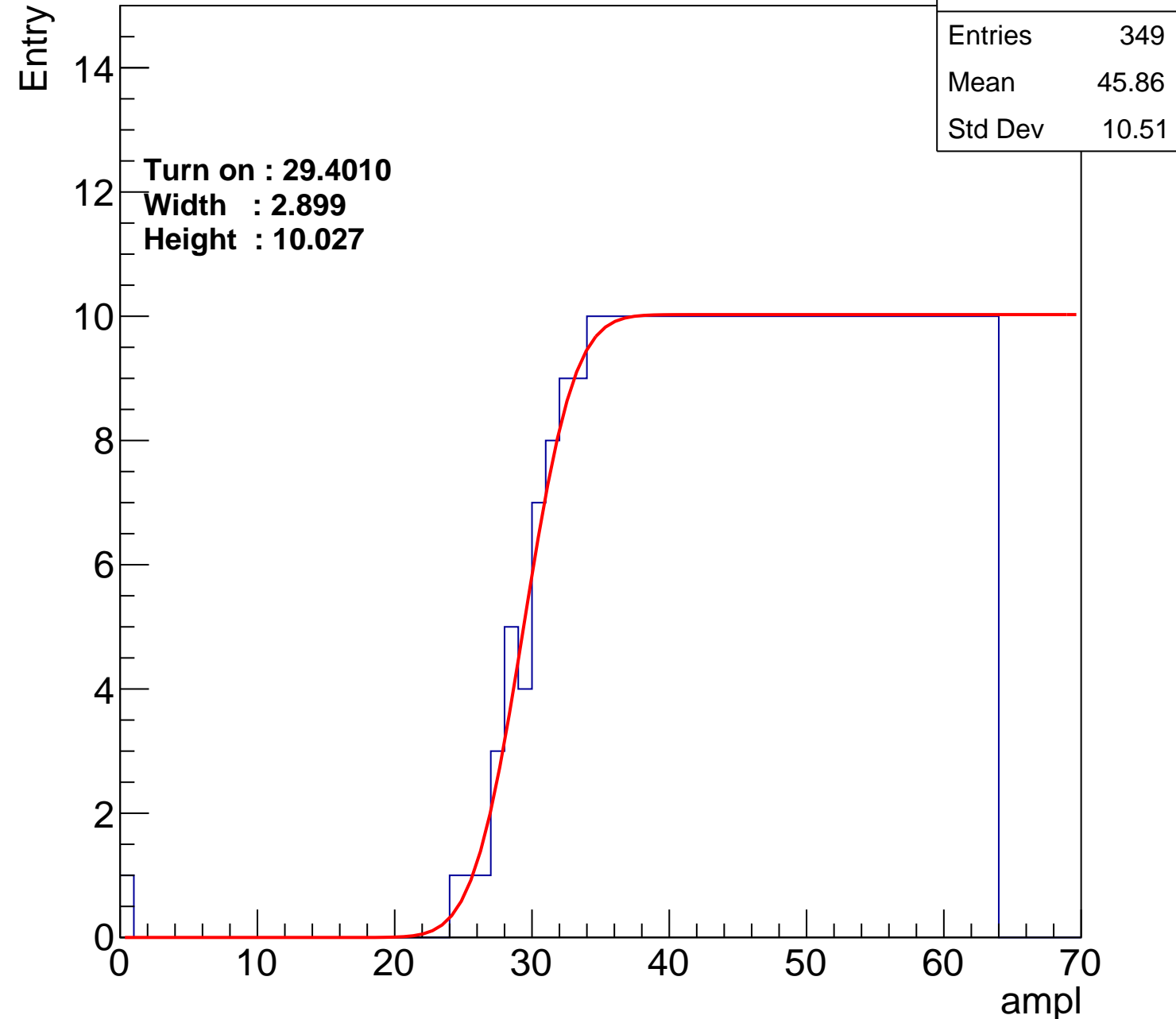
Width : 2.899

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch49

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.08
Std Dev	11.17

Turn on : 28.6709

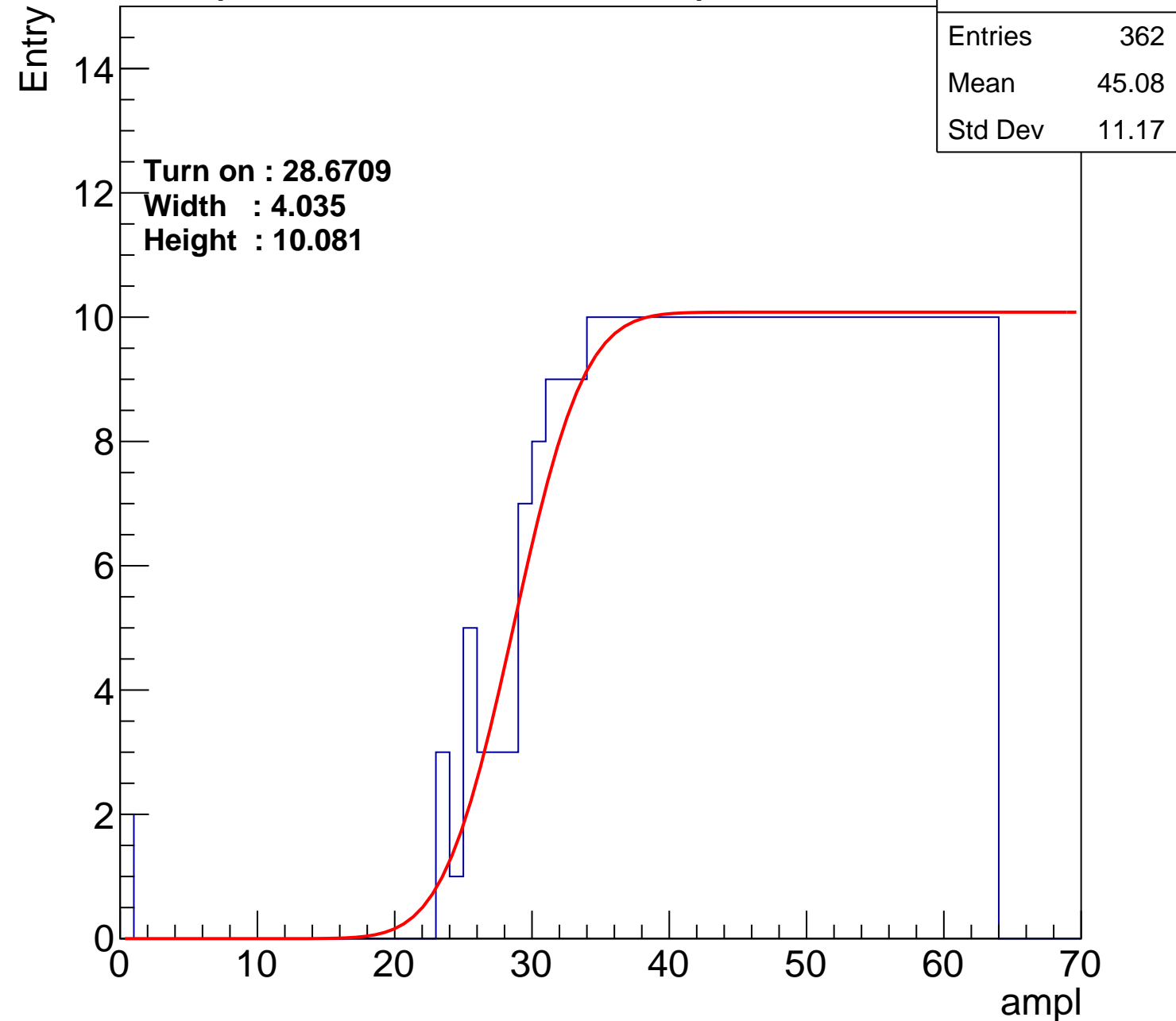
Width : 4.035

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch50

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.48
Std Dev	10.69

Turn on : 28.2931

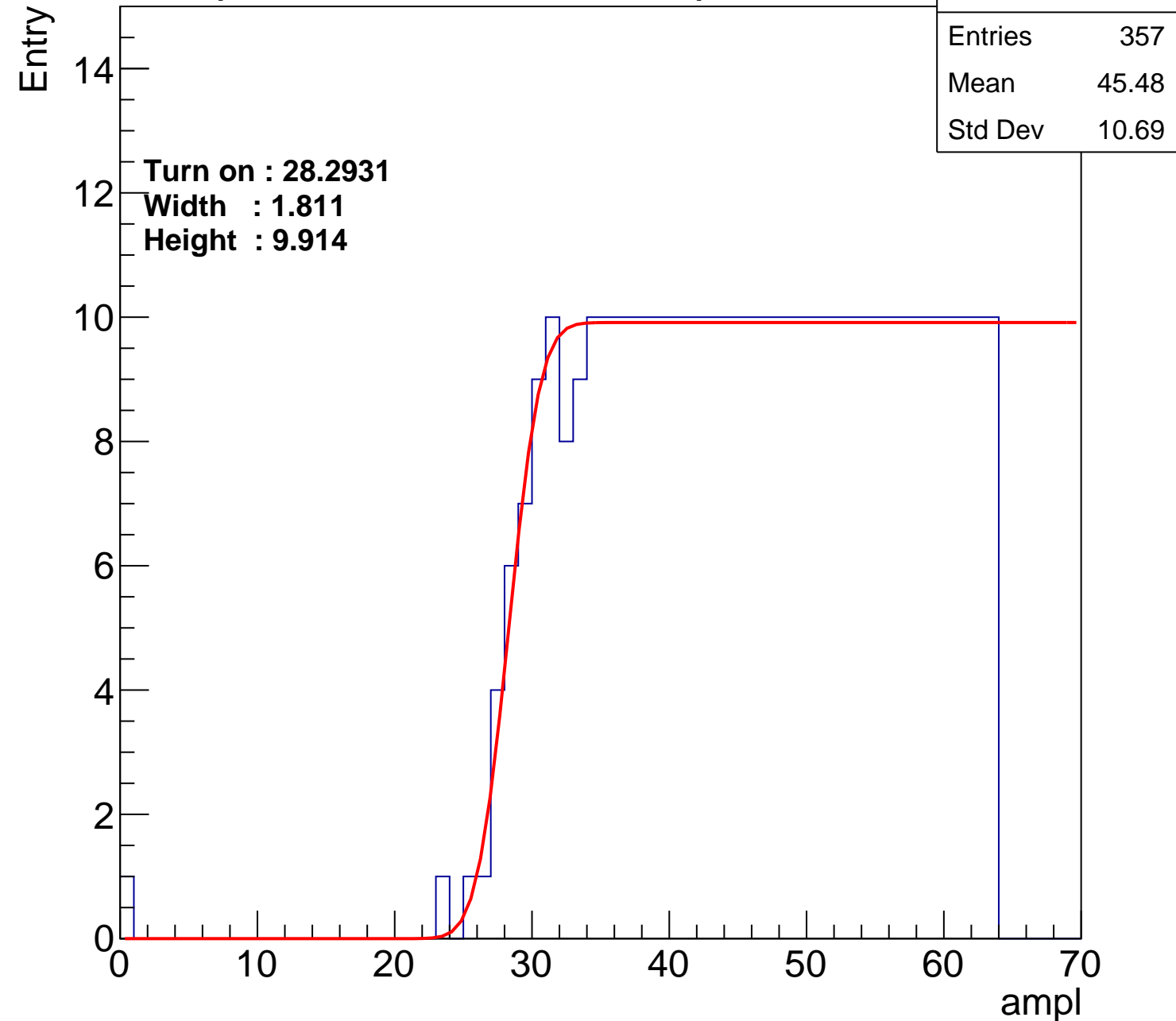
Width : 1.811

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch51

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.75
Std Dev	10.79

Turn on : 29.4075

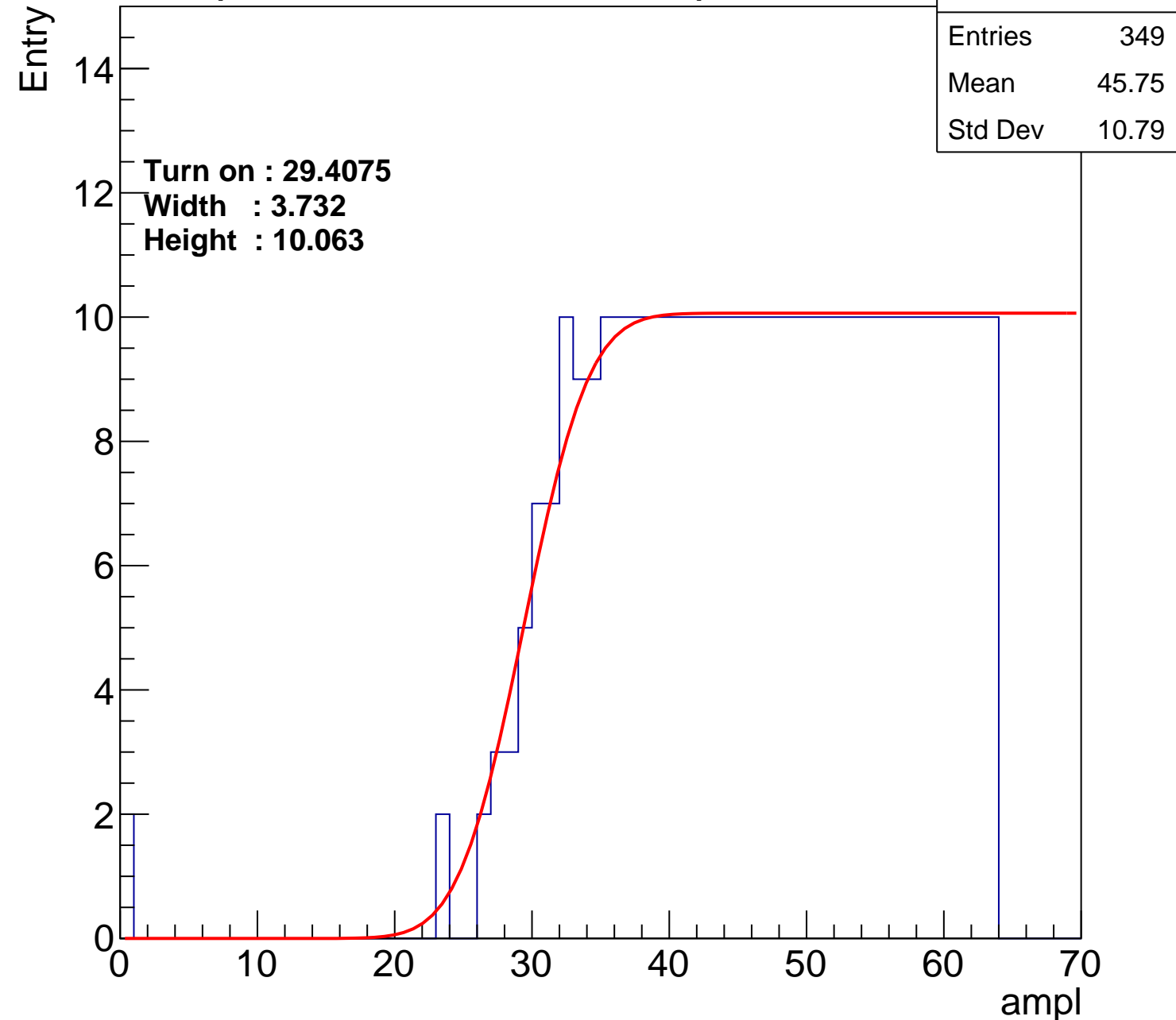
Width : 3.732

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch52

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.2
Std Dev	11.18

Turn on : 28.6778

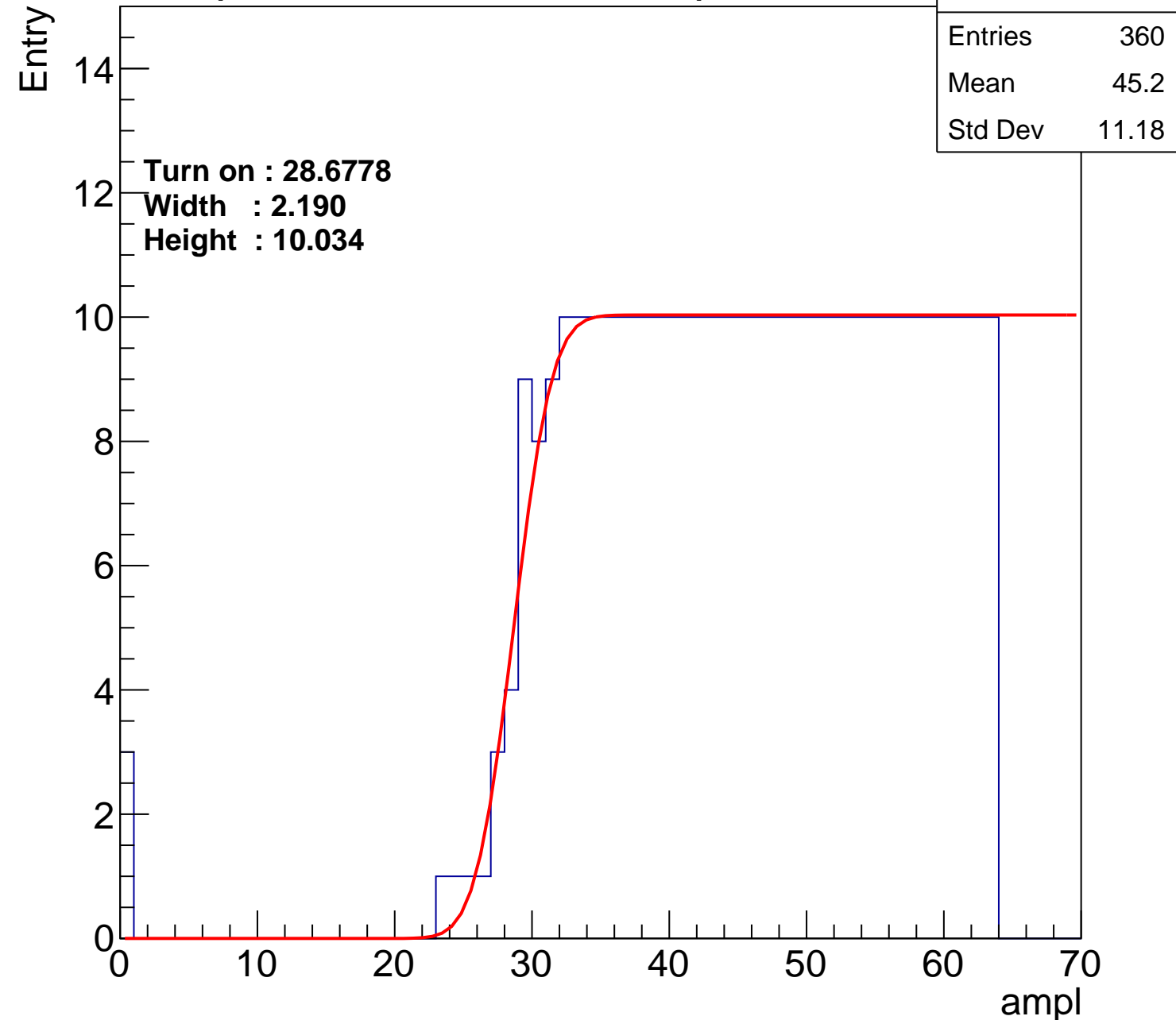
Width : 2.190

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch53

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.24
Std Dev	11.81

Turn on : 26.9119

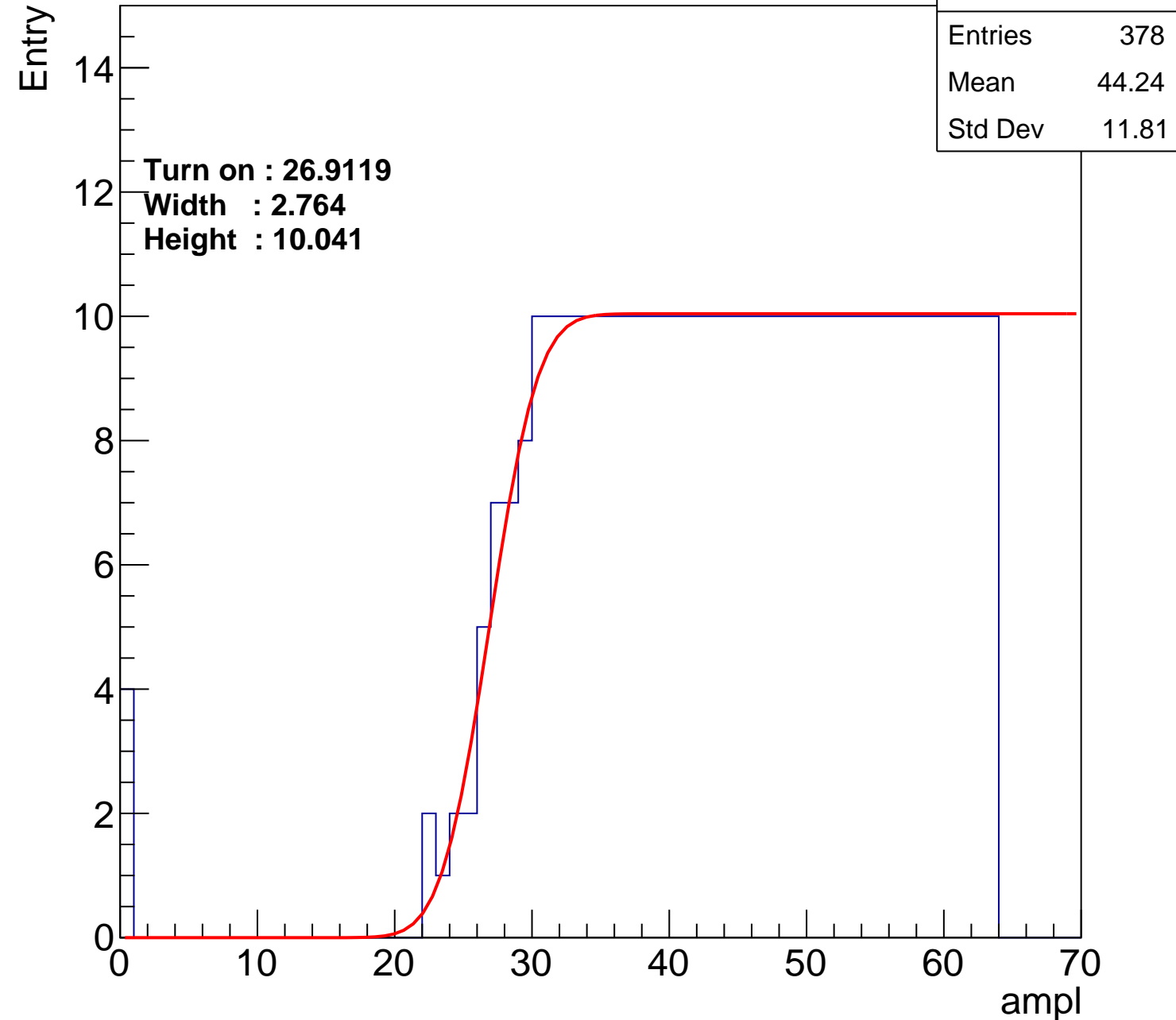
Width : 2.764

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch54

calib_packv5_042523_0143.root, FC#9, port A1

Entries	350
Mean	45.69
Std Dev	10.83

Turn on : 29.0080

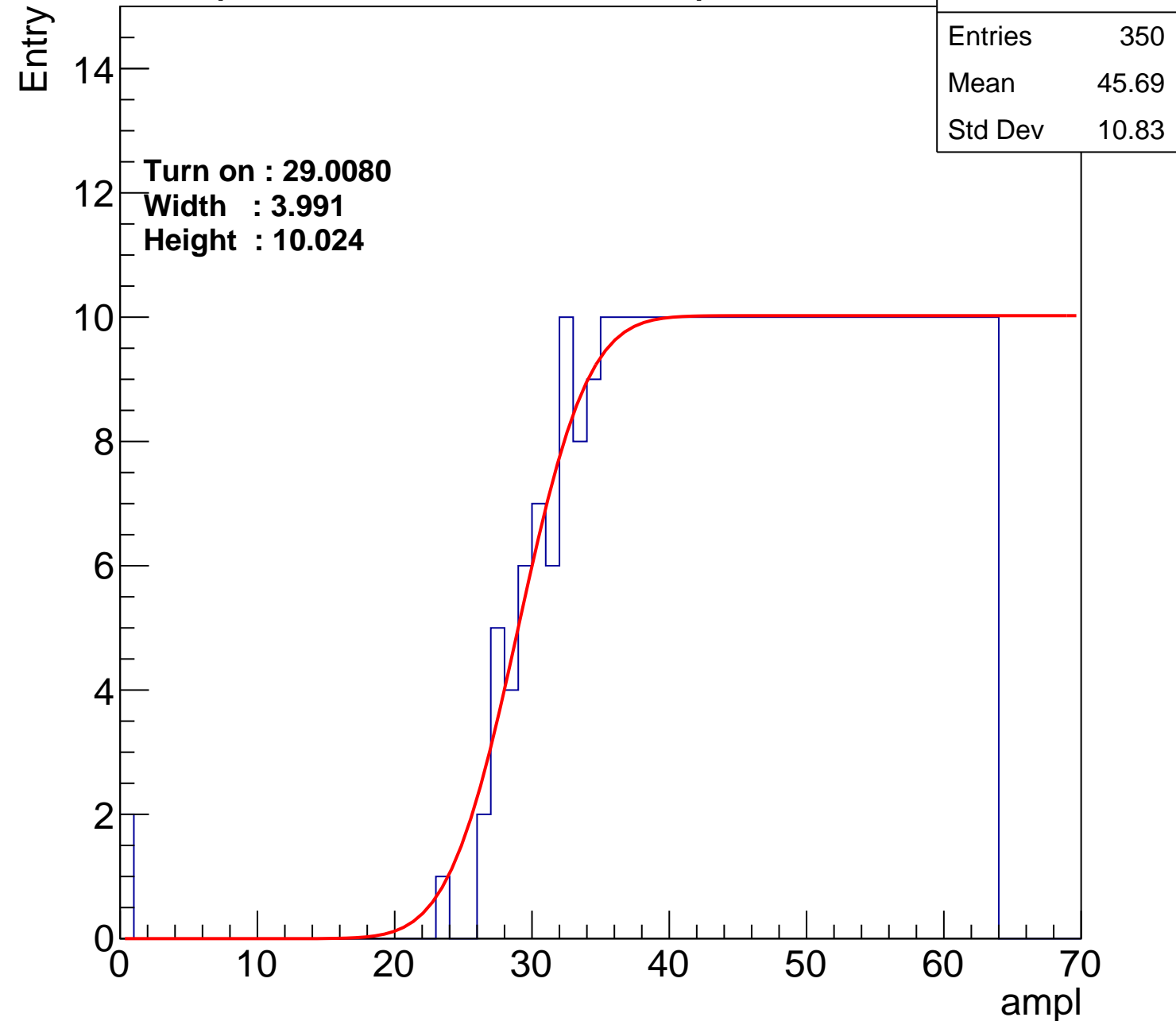
Width : 3.991

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch55

calib_packv5_042523_0143.root, FC#9, port A1

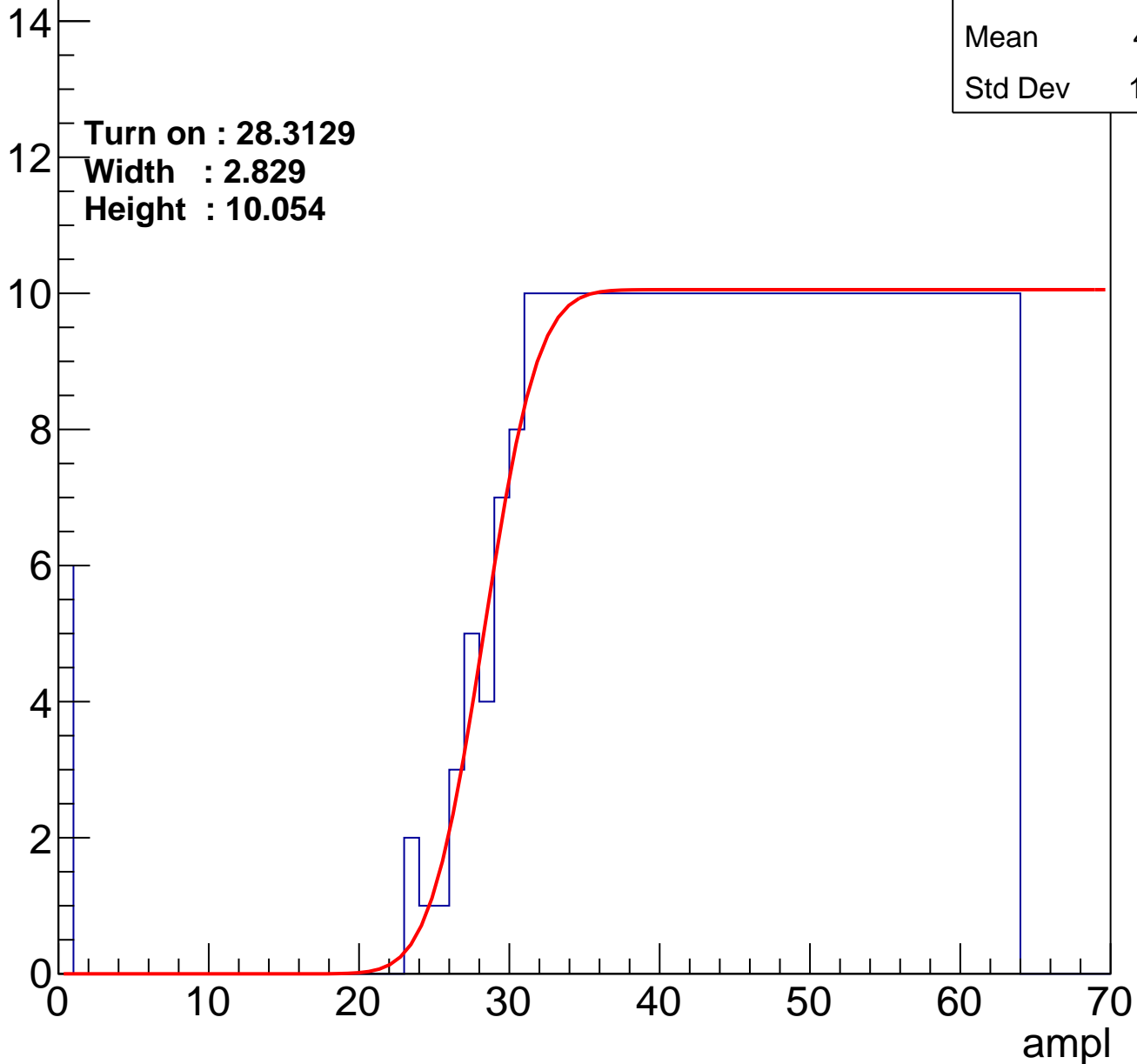
Entries	367
Mean	44.61
Std Dev	11.97

Turn on : 28.3129

Width : 2.829

Height : 10.054

Entry



B0L001S, U26-ch56

calib_packv5_042523_0143.root, FC#9, port A1

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.8273

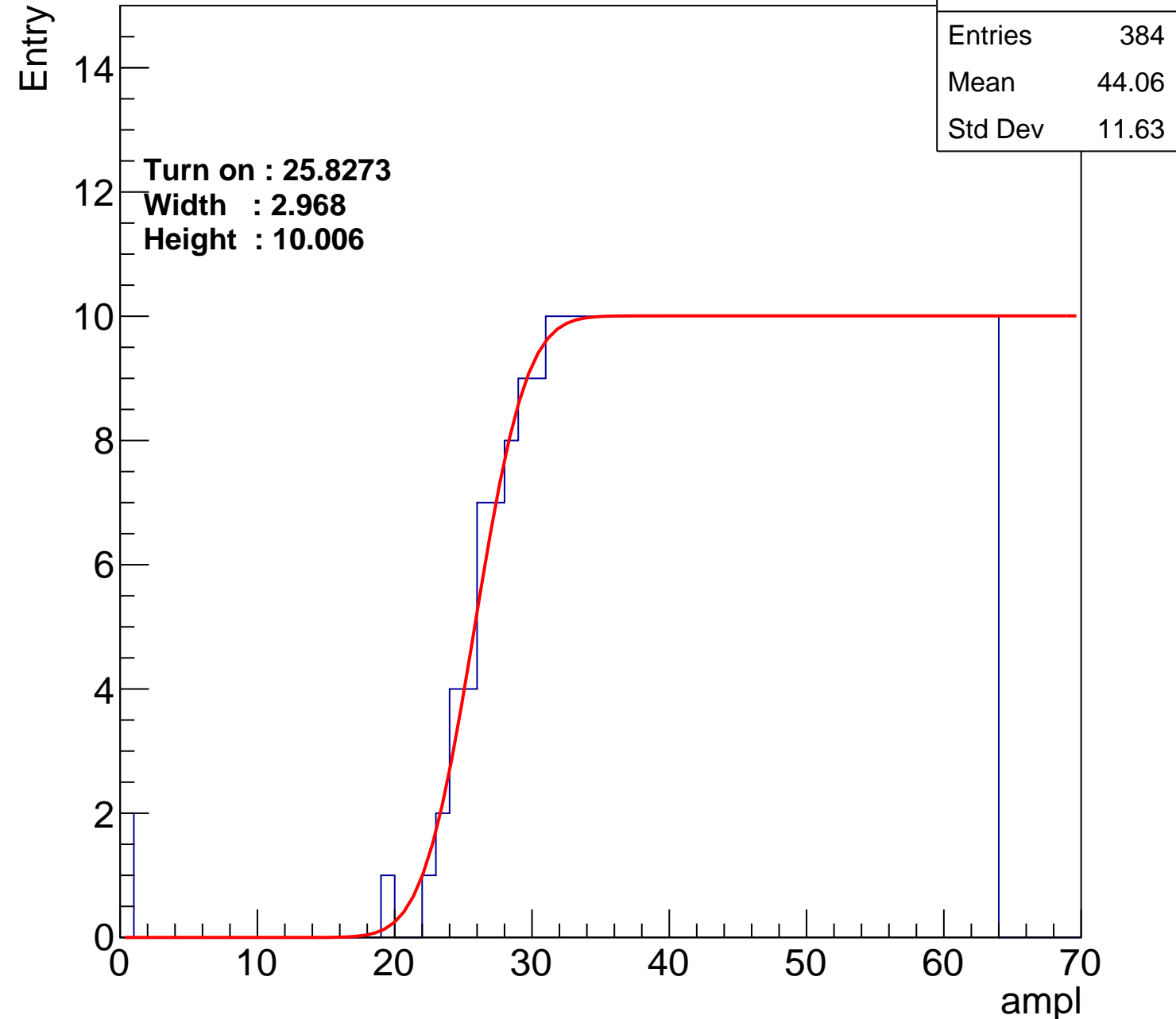
Width : 2.968

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch57

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.09
Std Dev	11.44

Turn on : 28.4985

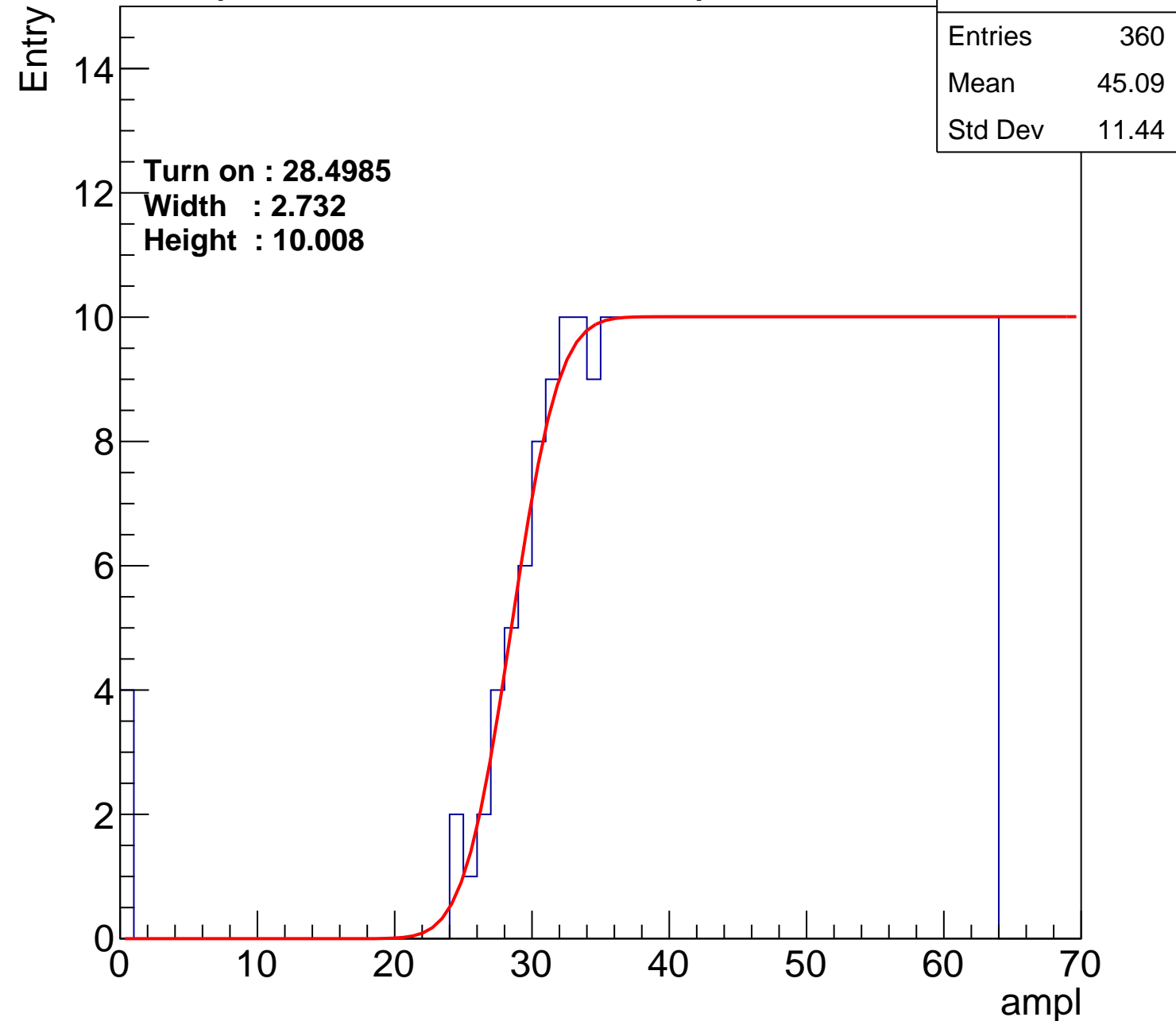
Width : 2.732

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch58

calib_packv5_042523_0143.root, FC#9, port A1

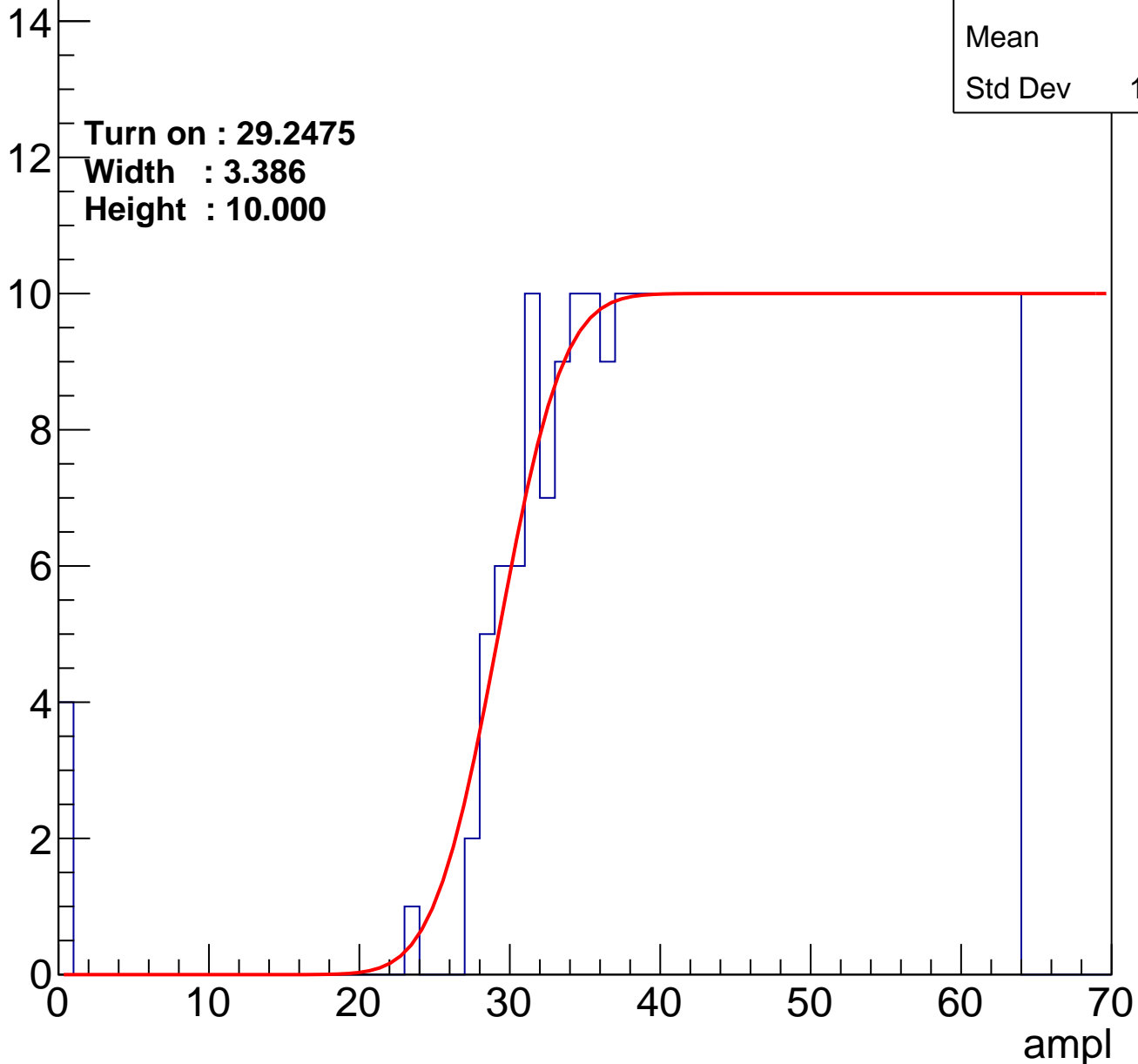
Entries	349
Mean	45.6
Std Dev	11.22

Turn on : 29.2475

Width : 3.386

Height : 10.000

Entry



B0L001S, U26-ch59

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	45.03
Std Dev	11.72

Turn on : 29.0812

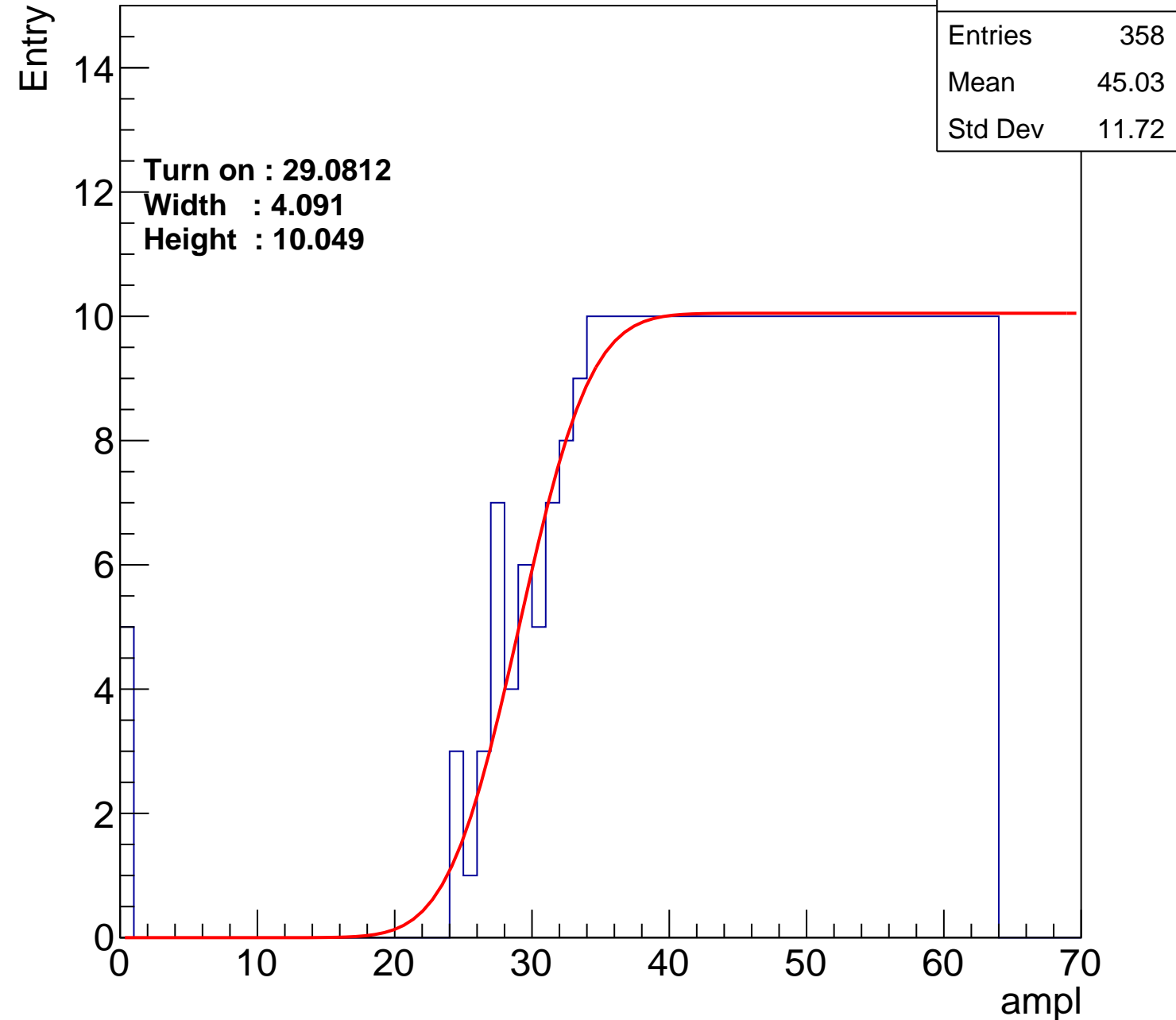
Width : 4.091

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch60

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.1367

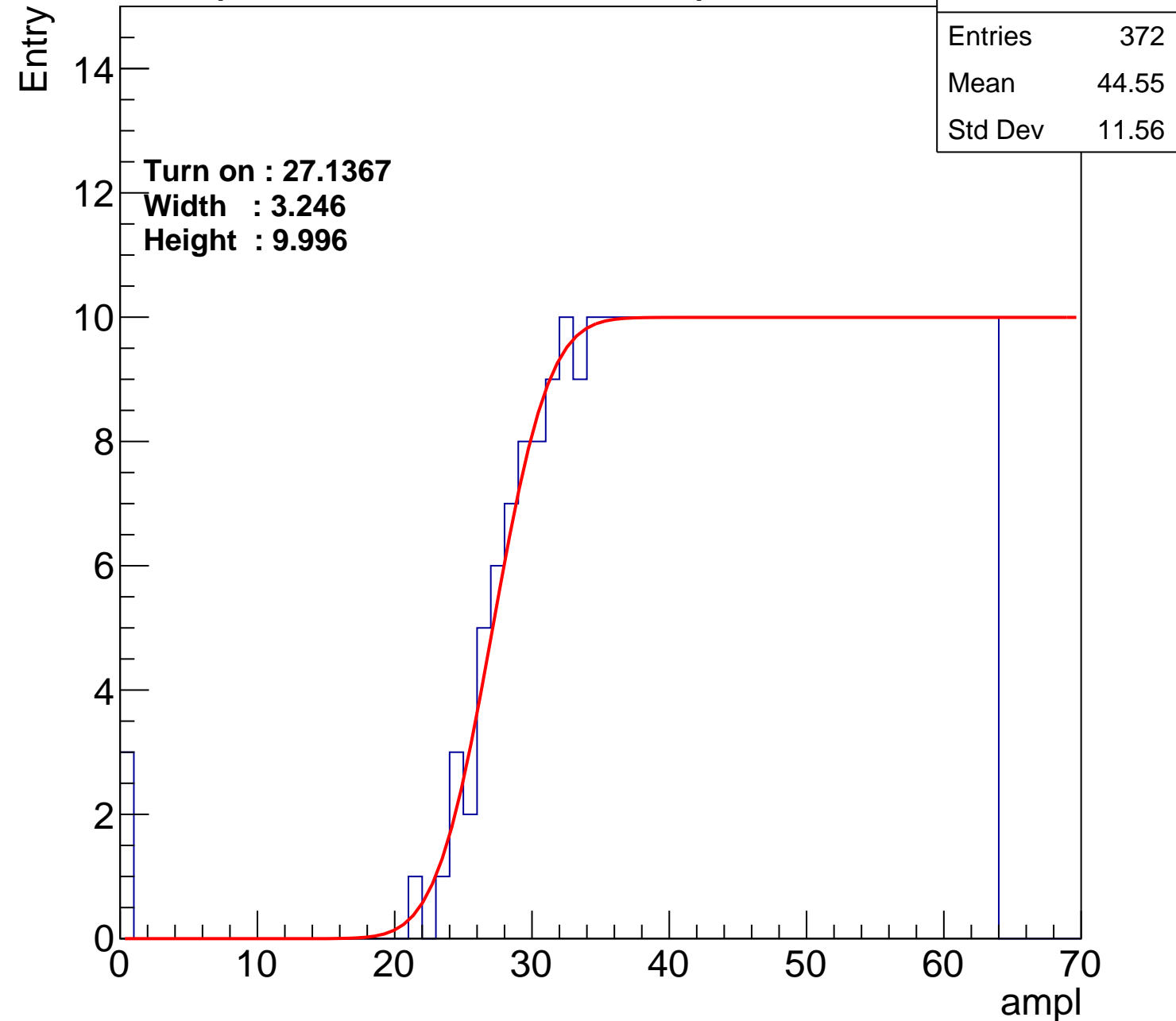
Width : 3.246

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch61

calib_packv5_042523_0143.root, FC#9, port A1

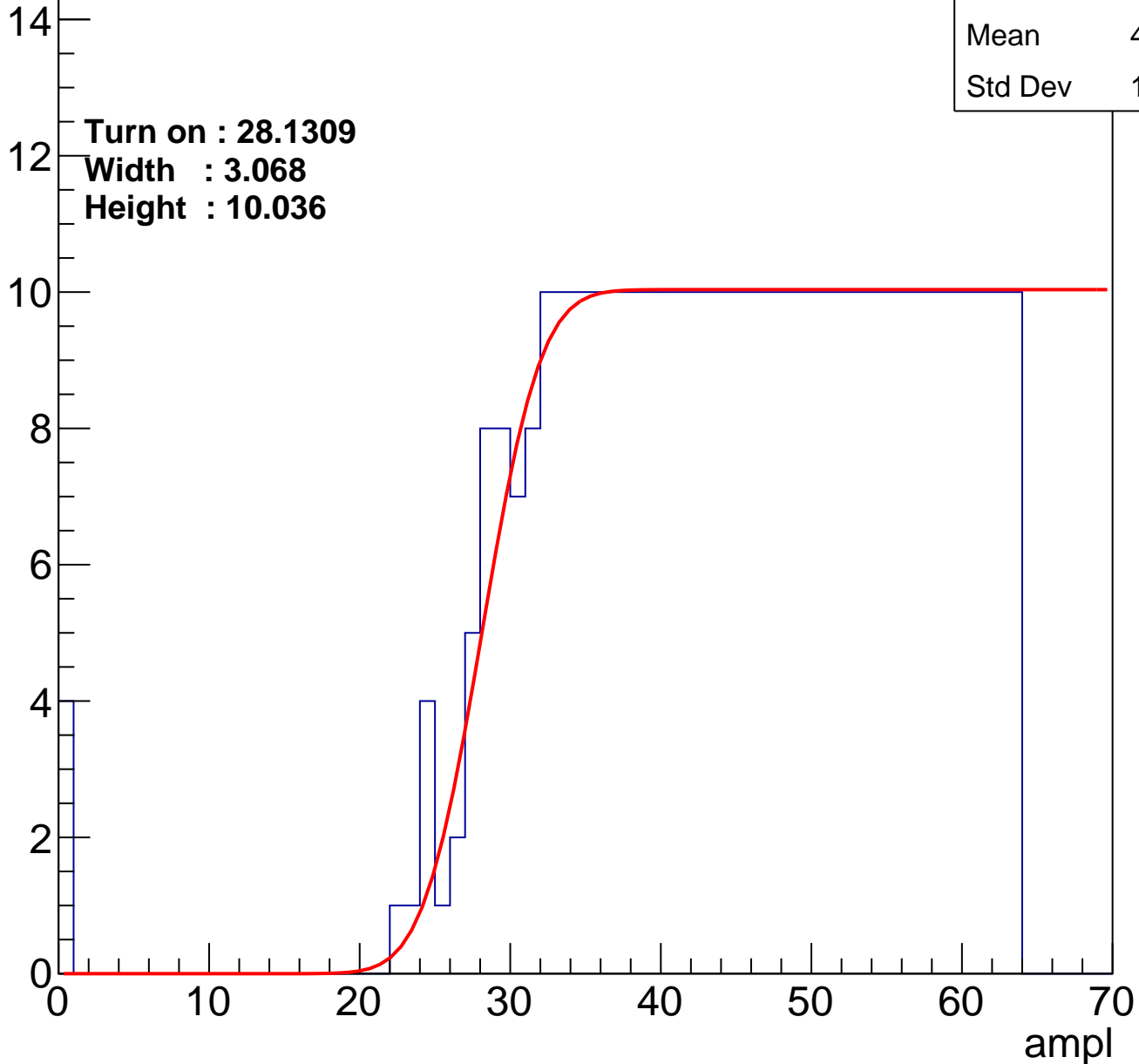
Entries	369
Mean	44.63
Std Dev	11.68

Turn on : 28.1309

Width : 3.068

Height : 10.036

Entry



B0L001S, U26-ch62

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 27.9782

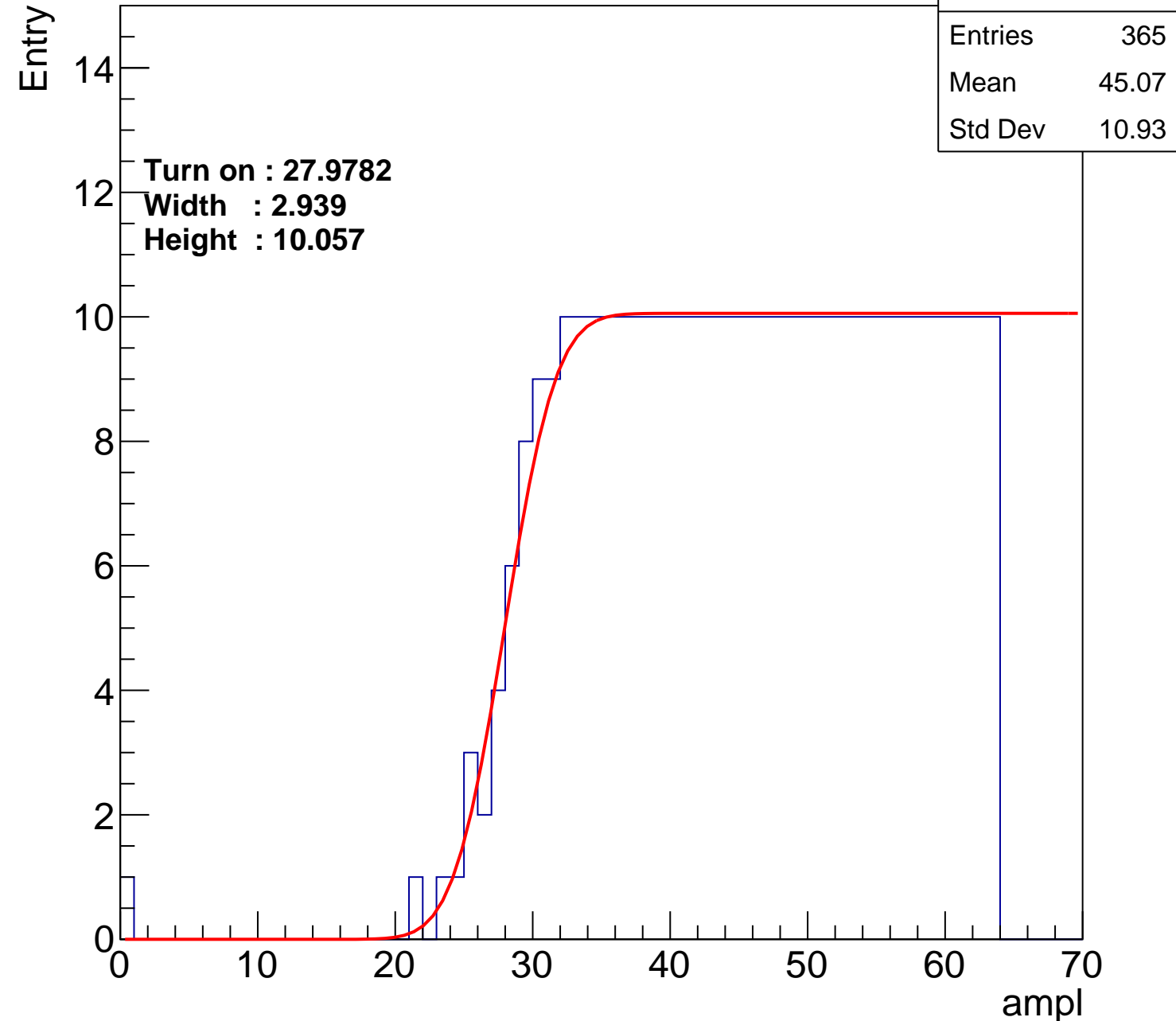
Width : 2.939

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch63

calib_packv5_042523_0143.root, FC#9, port A1

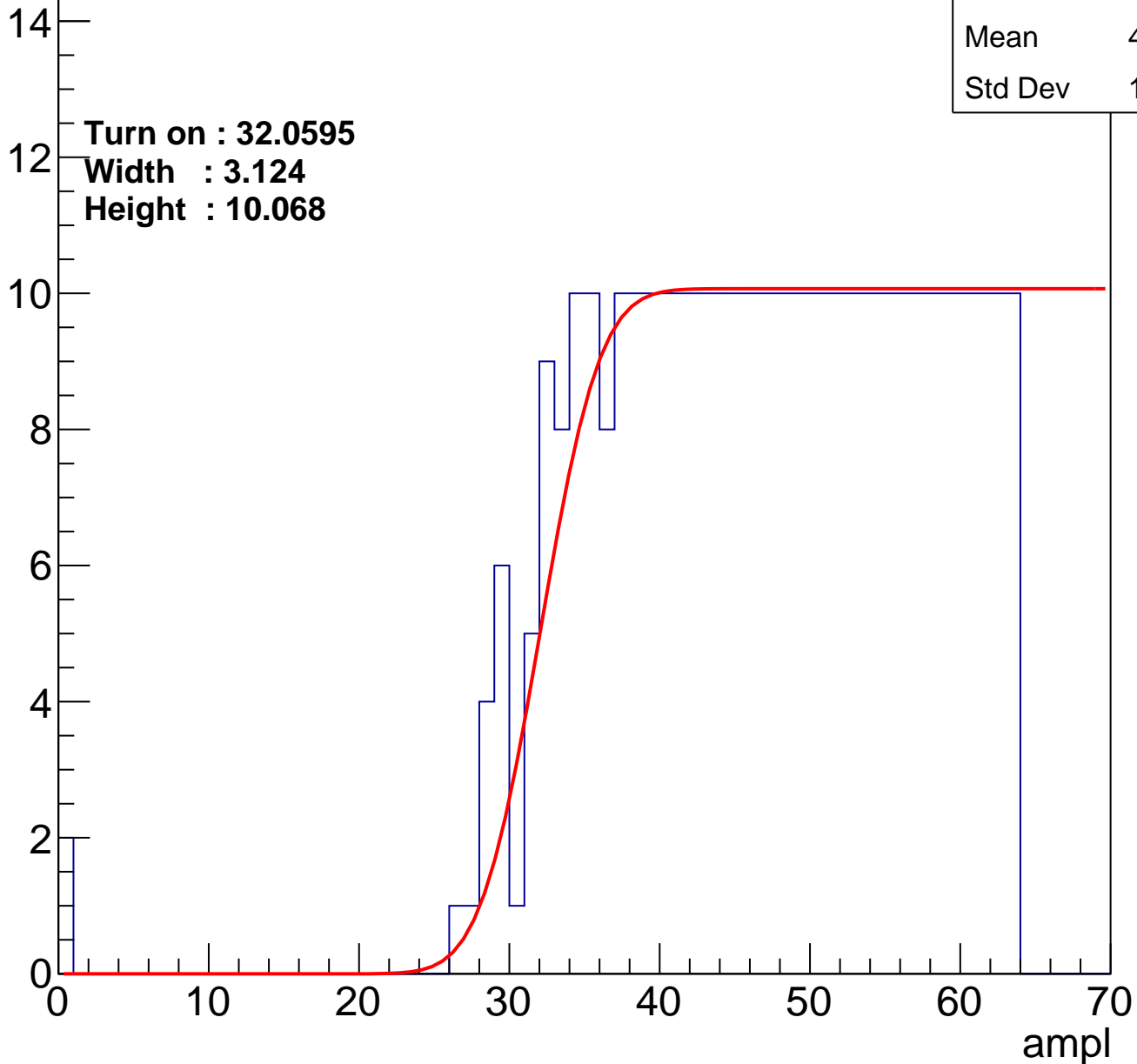
Entries	335
Mean	46.43
Std Dev	10.45

Turn on : 32.0595

Width : 3.124

Height : 10.068

Entry



B0L001S, U26-ch64

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.57
Std Dev	12.04

Turn on : 28.2288

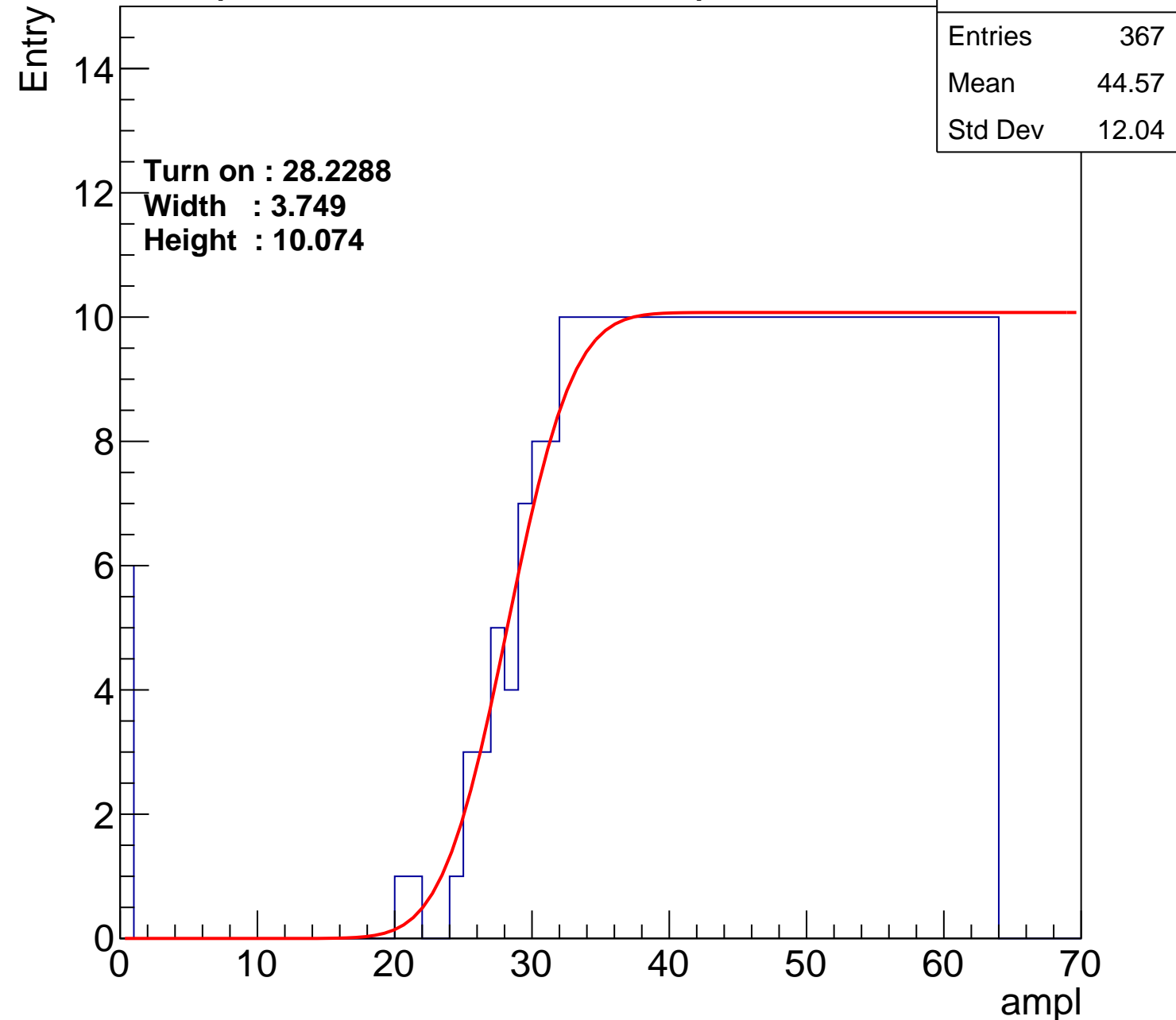
Width : 3.749

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch65

calib_packv5_042523_0143.root, FC#9, port A1

Entries	348
Mean	45.92
Std Dev	10.47

Turn on : 29.5916

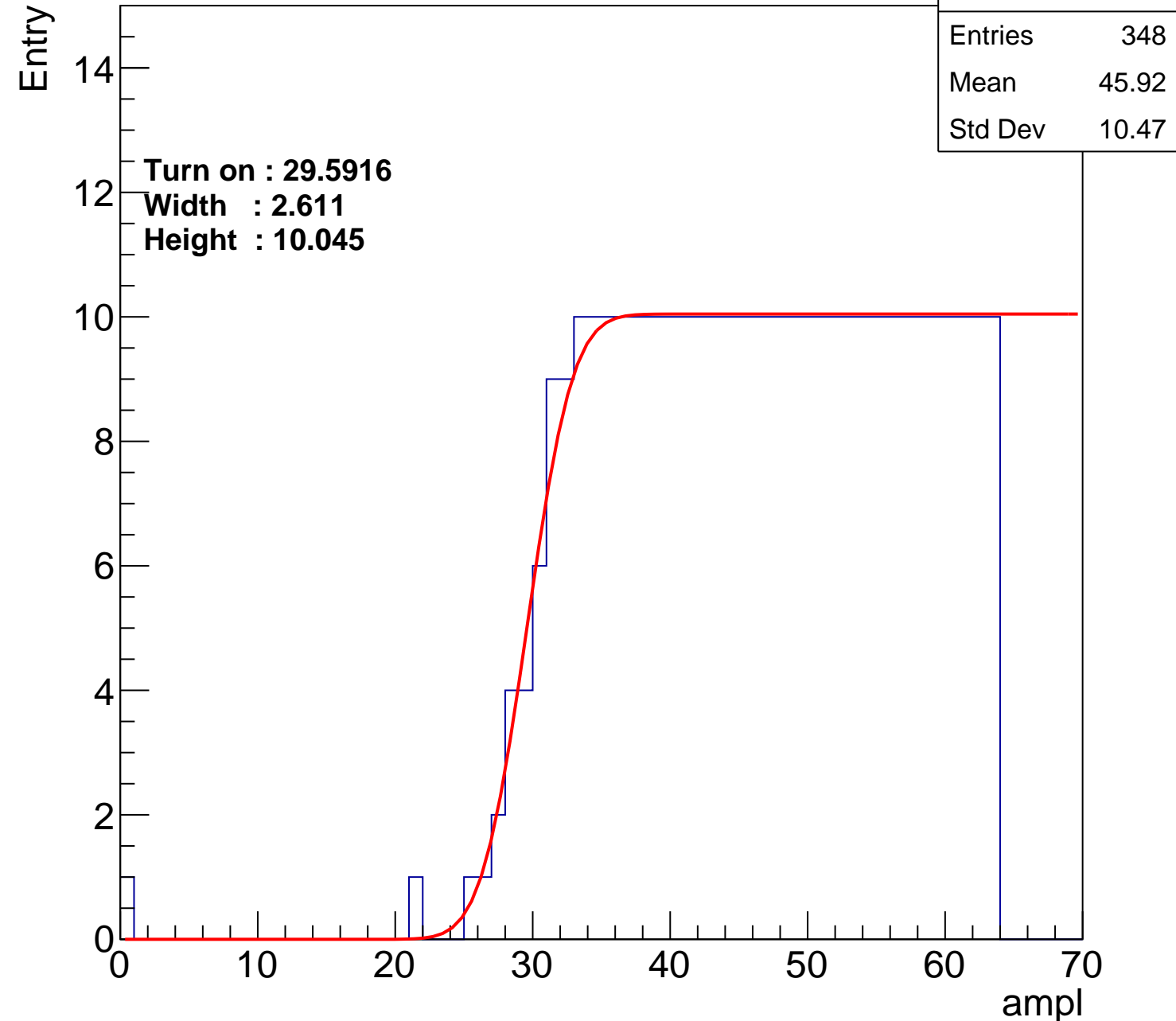
Width : 2.611

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch66

calib_packv5_042523_0143.root, FC#9, port A1

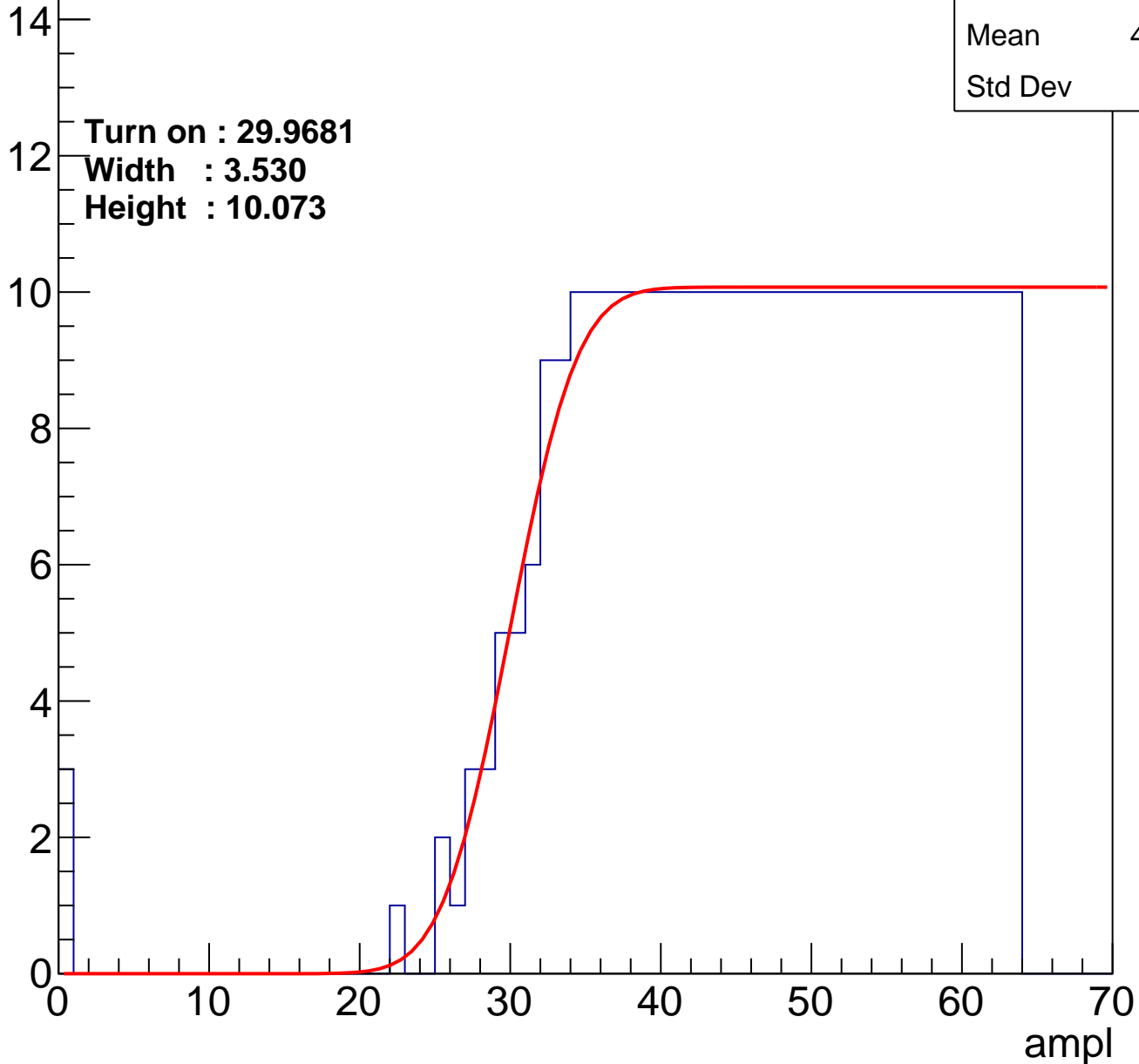
Entries	347
Mean	45.76
Std Dev	11

Turn on : 29.9681

Width : 3.530

Height : 10.073

Entry



B0L001S, U26-ch67

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.62
Std Dev	10.83

Turn on : 28.8999

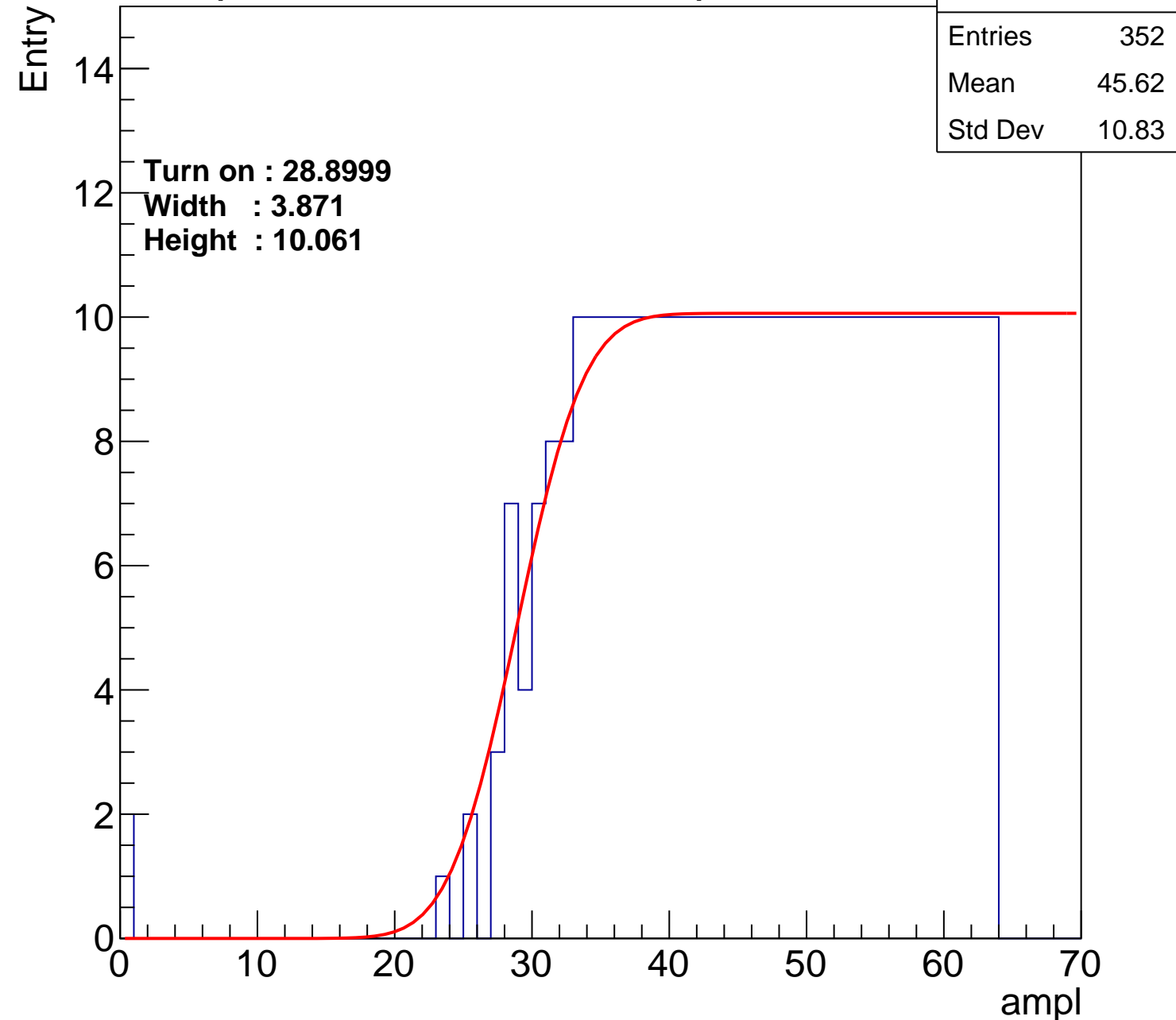
Width : 3.871

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch68

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.72
Std Dev	11.38

Turn on : 27.6595

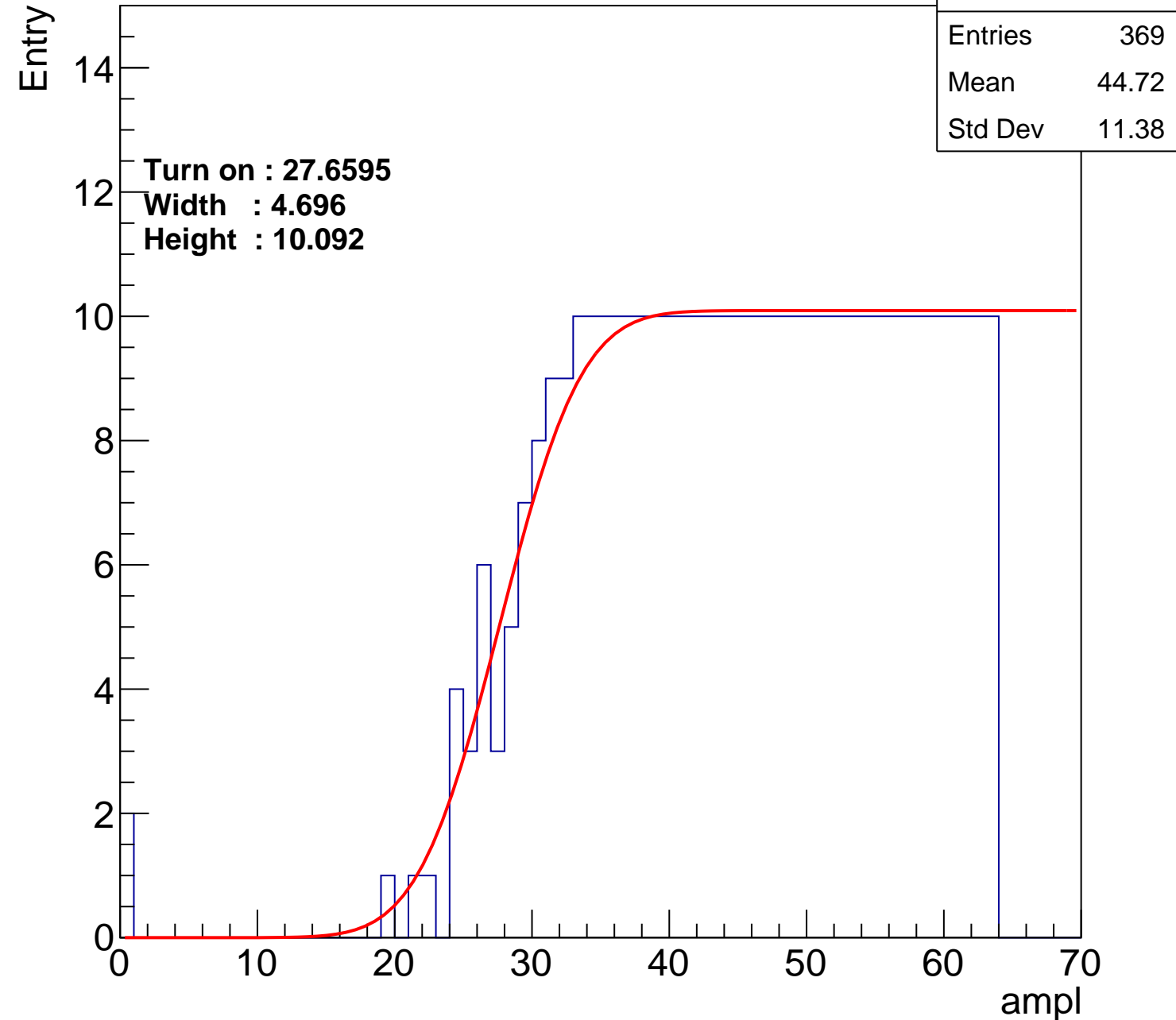
Width : 4.696

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch69

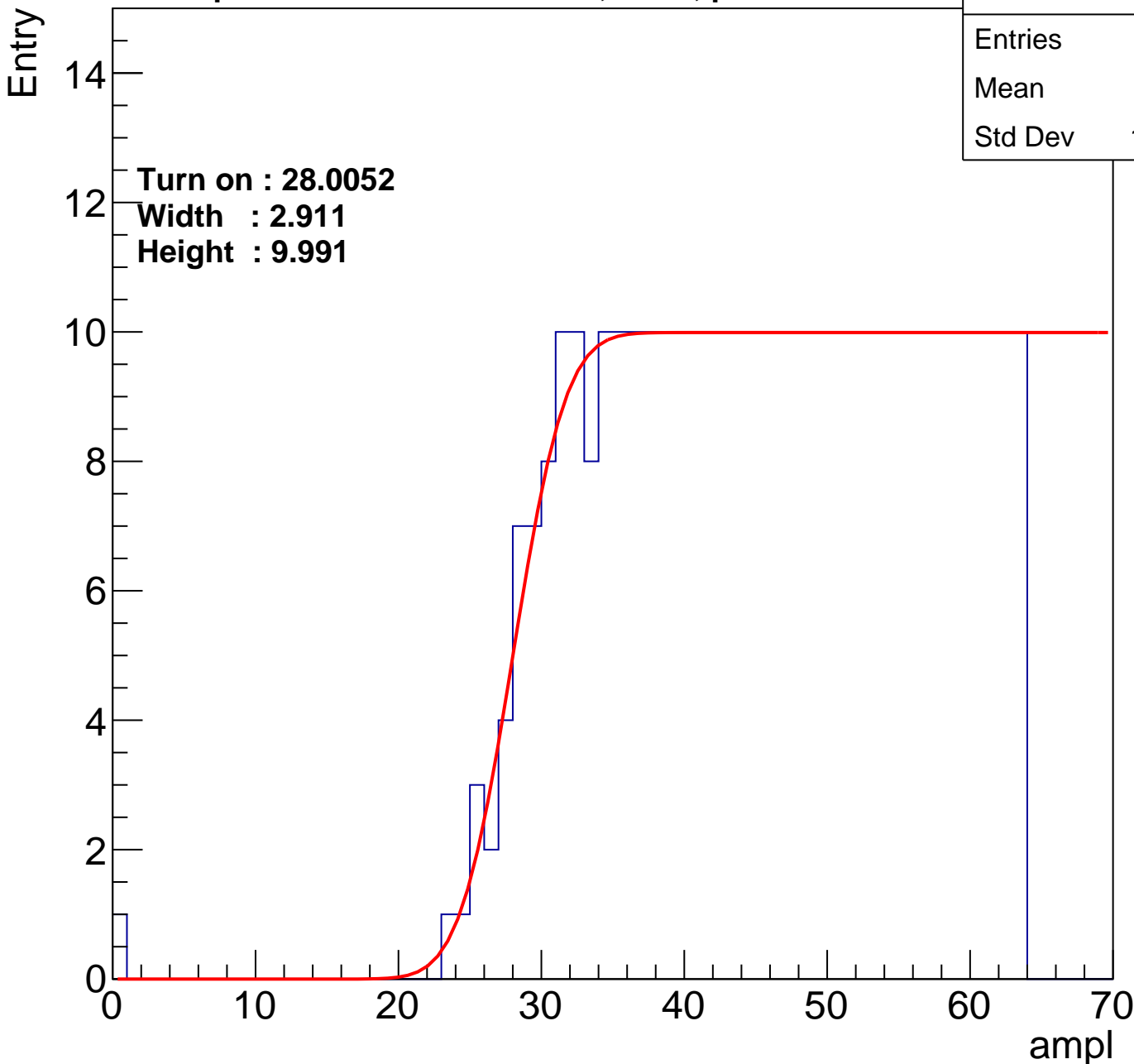
calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.21
Std Dev	10.86

Turn on : 28.0052

Width : 2.911

Height : 9.991



B0L001S, U26-ch70

calib_packv5_042523_0143.root, FC#9, port A1

Entries	380
Mean	44.22
Std Dev	11.67

Turn on : 26.8797

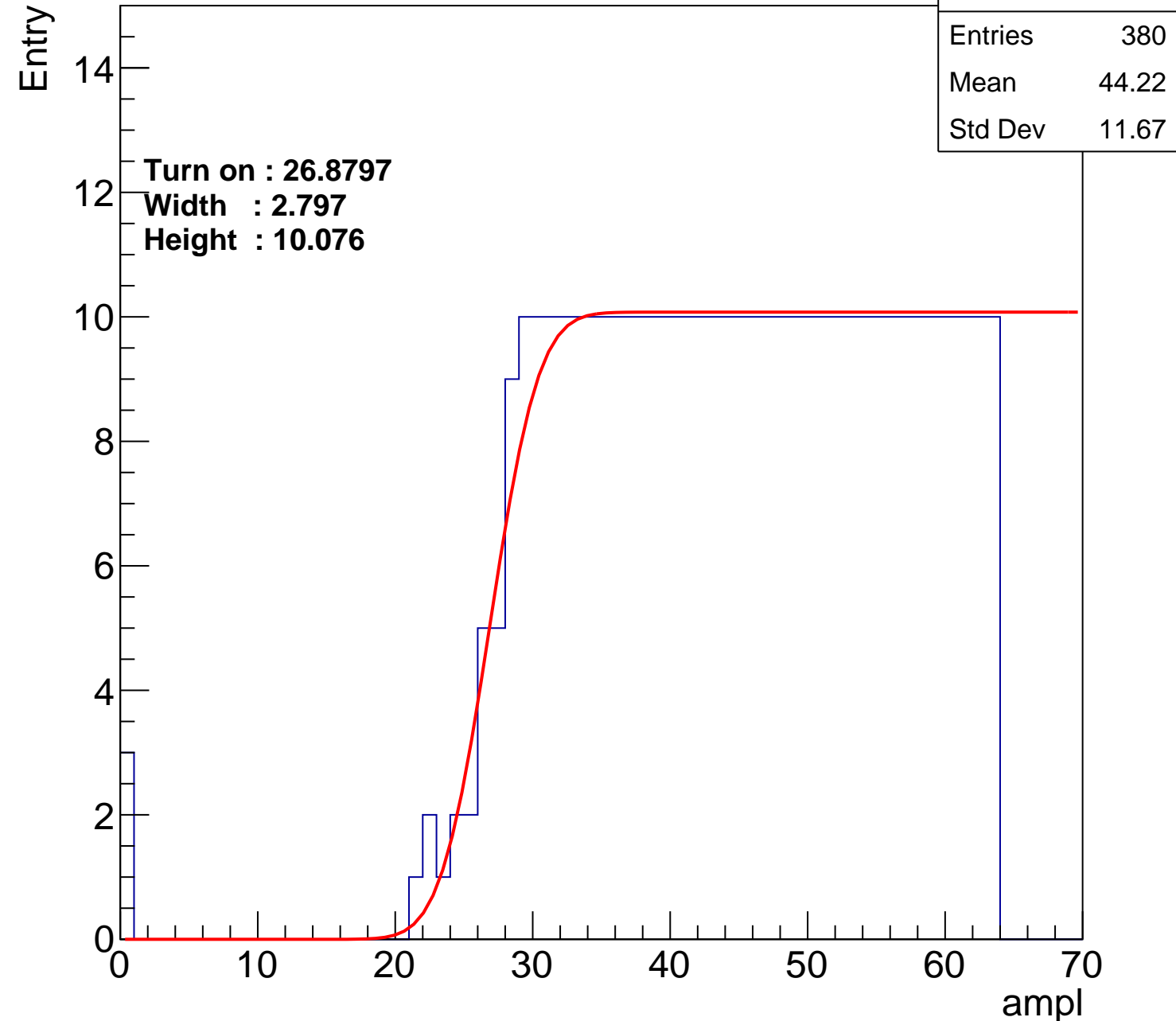
Width : 2.797

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch71

calib_packv5_042523_0143.root, FC#9, port A1

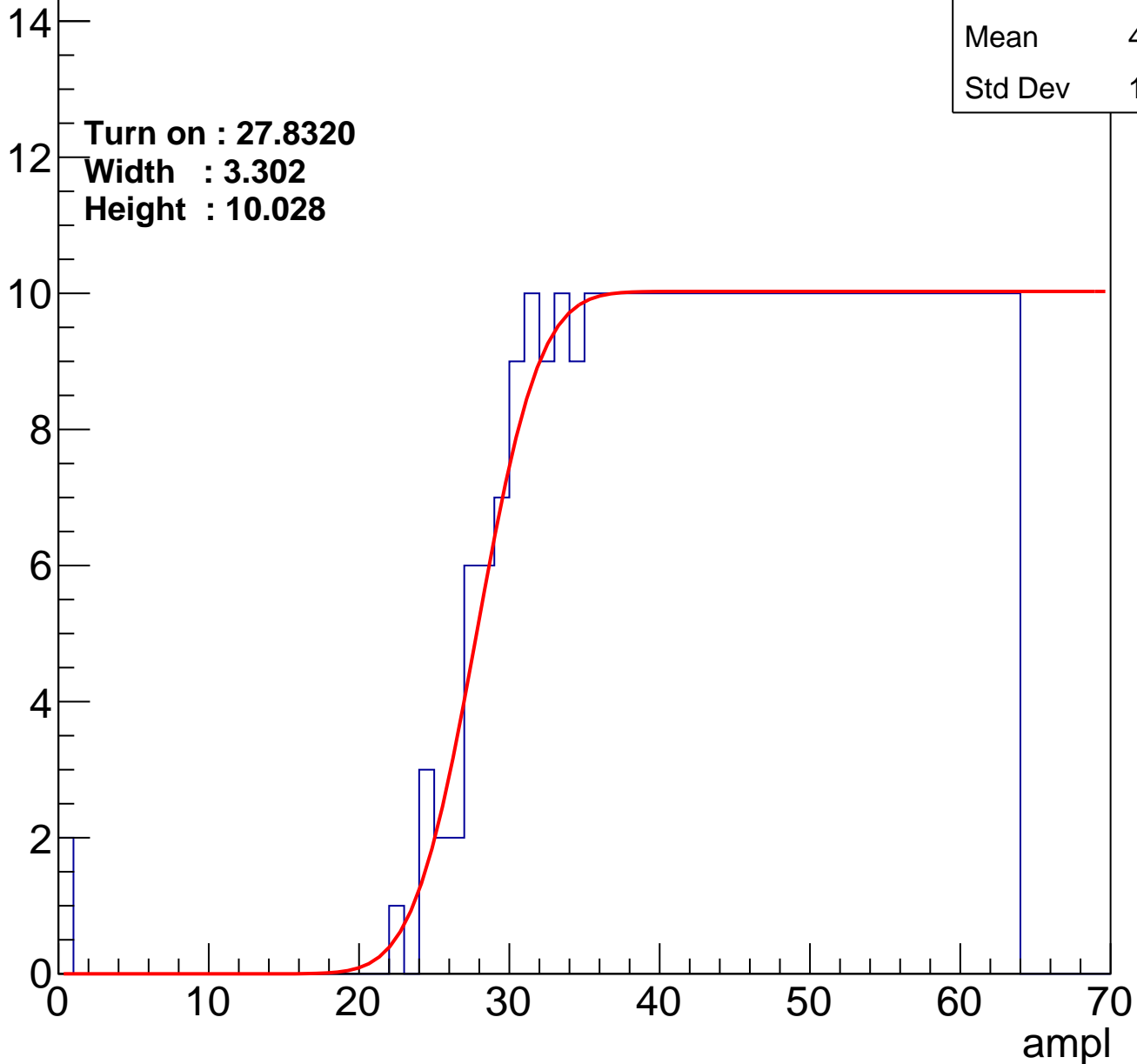
Entry

Entries	366
Mean	44.93
Std Dev	11.19

Turn on : 27.8320

Width : 3.302

Height : 10.028



B0L001S, U26-ch72

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.27
Std Dev	11.37

Turn on : 29.0466

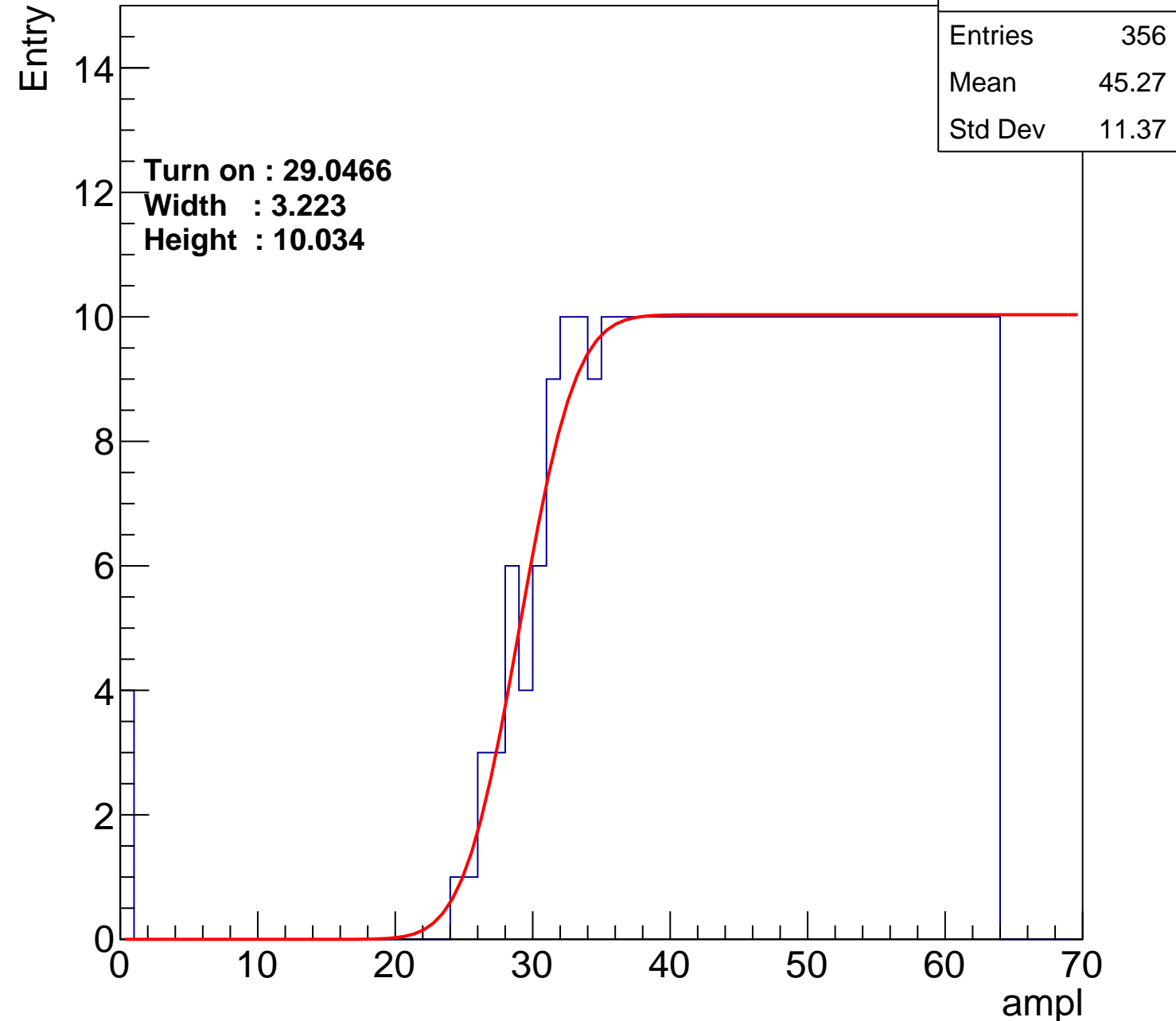
Width : 3.223

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch73

calib_packv5_042523_0143.root, FC#9, port A1

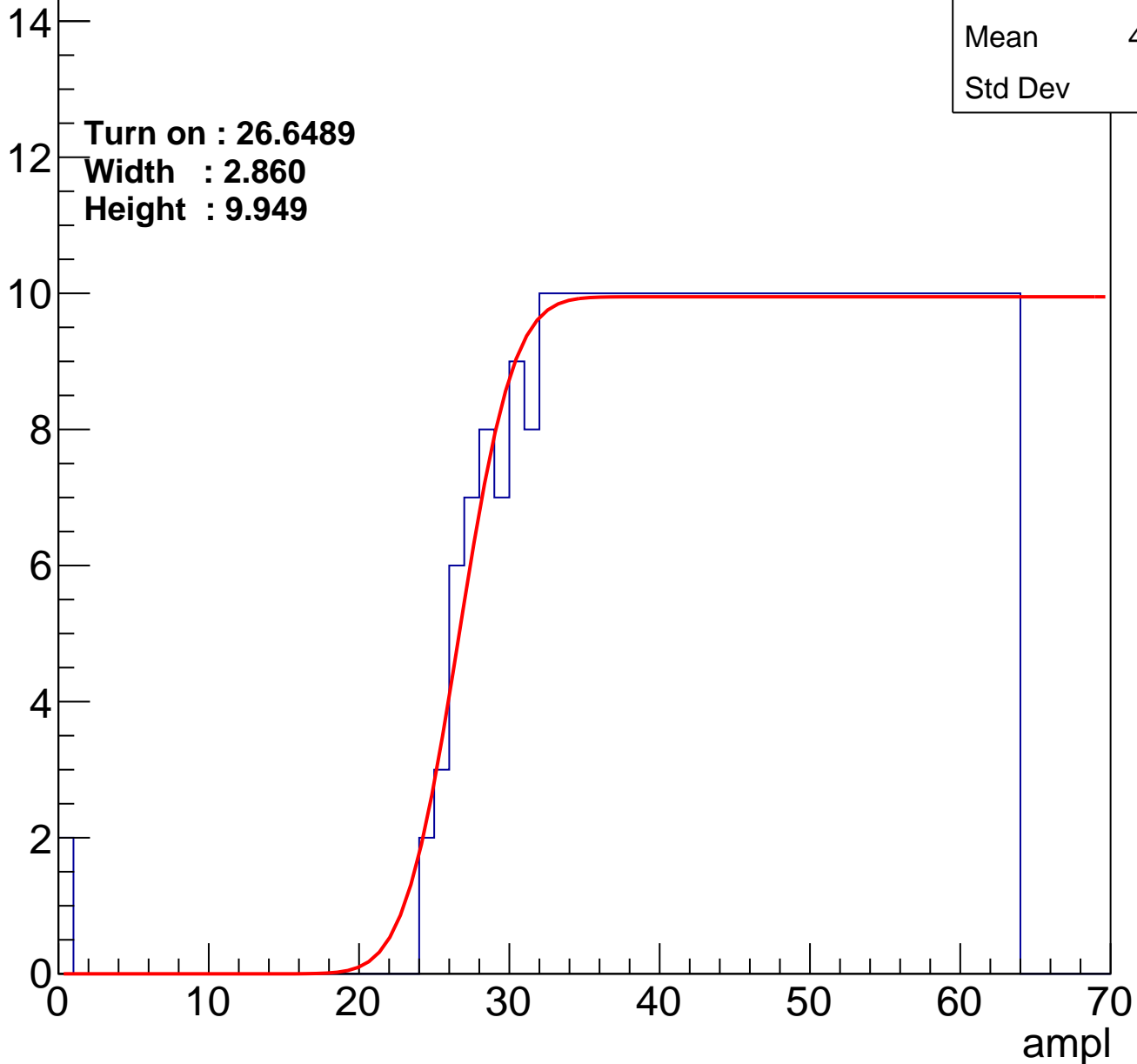
Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 26.6489

Width : 2.860

Height : 9.949

Entry



B0L001S, U26-ch74

calib_packv5_042523_0143.root, FC#9, port A1

Entries	353
Mean	45.59
Std Dev	10.82

Turn on : 28.9345

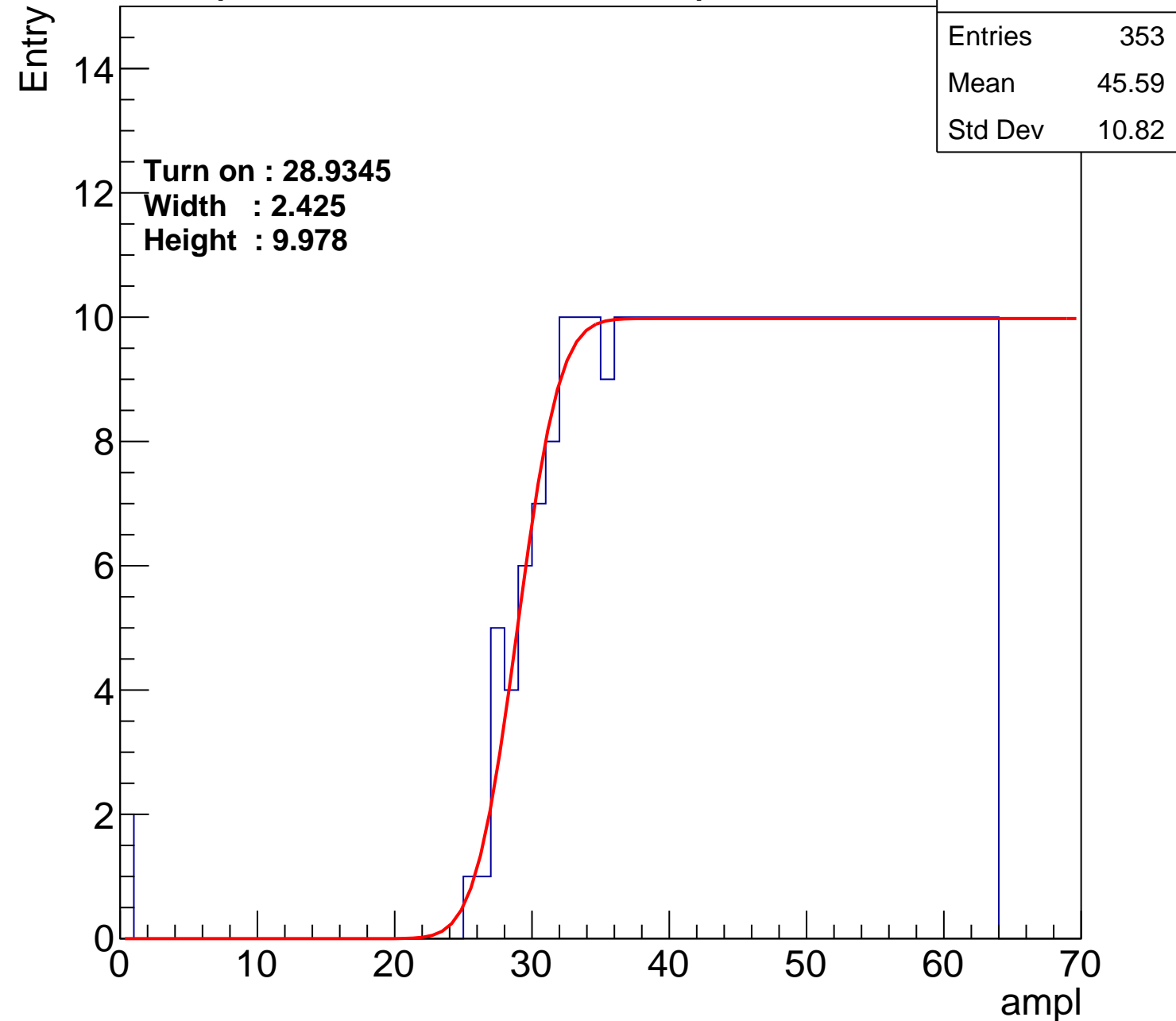
Width : 2.425

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch75

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.96
Std Dev	11.48

Turn on : 28.3172

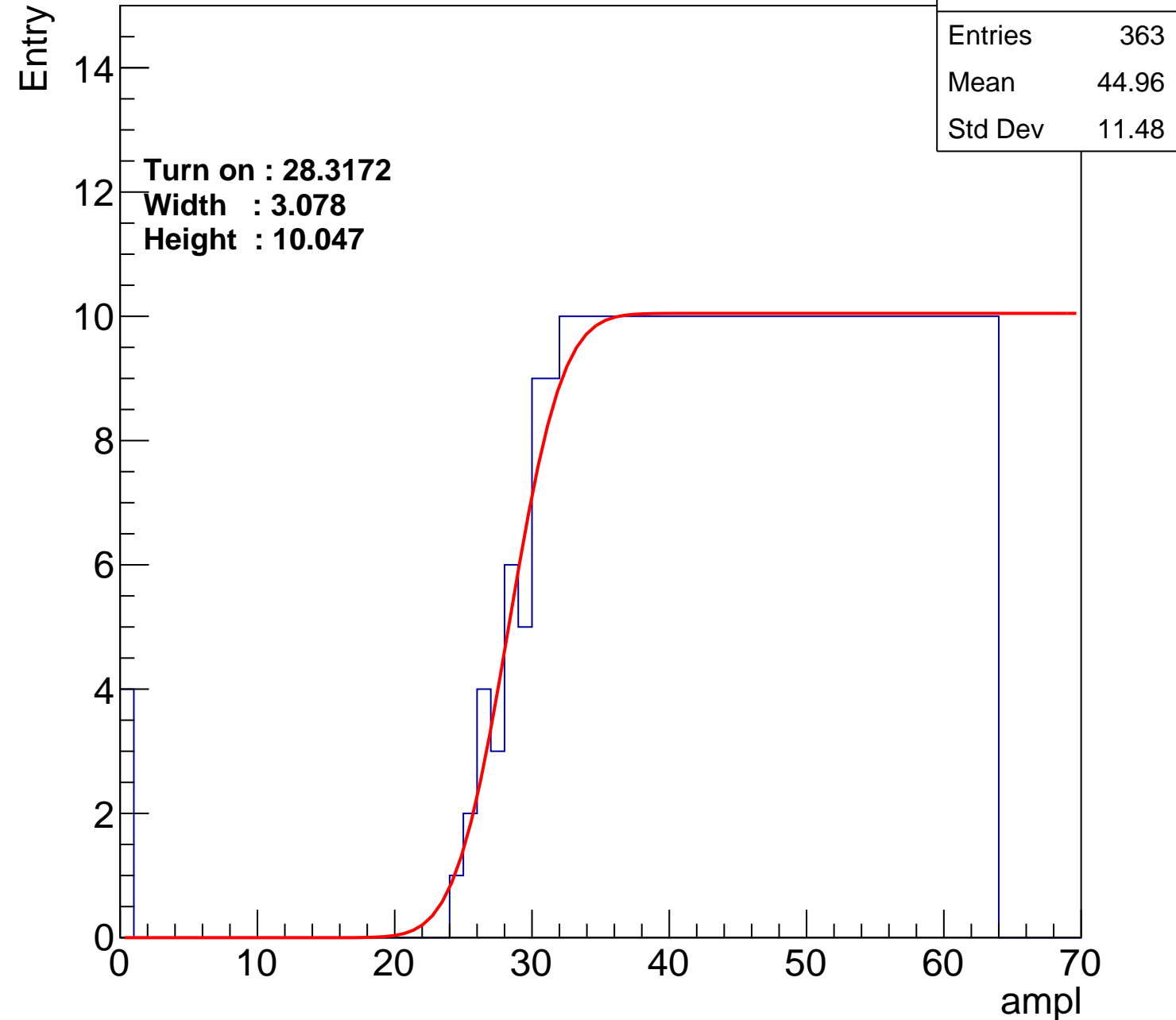
Width : 3.078

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch76

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.81
Std Dev	11.55

Turn on : 27.8287

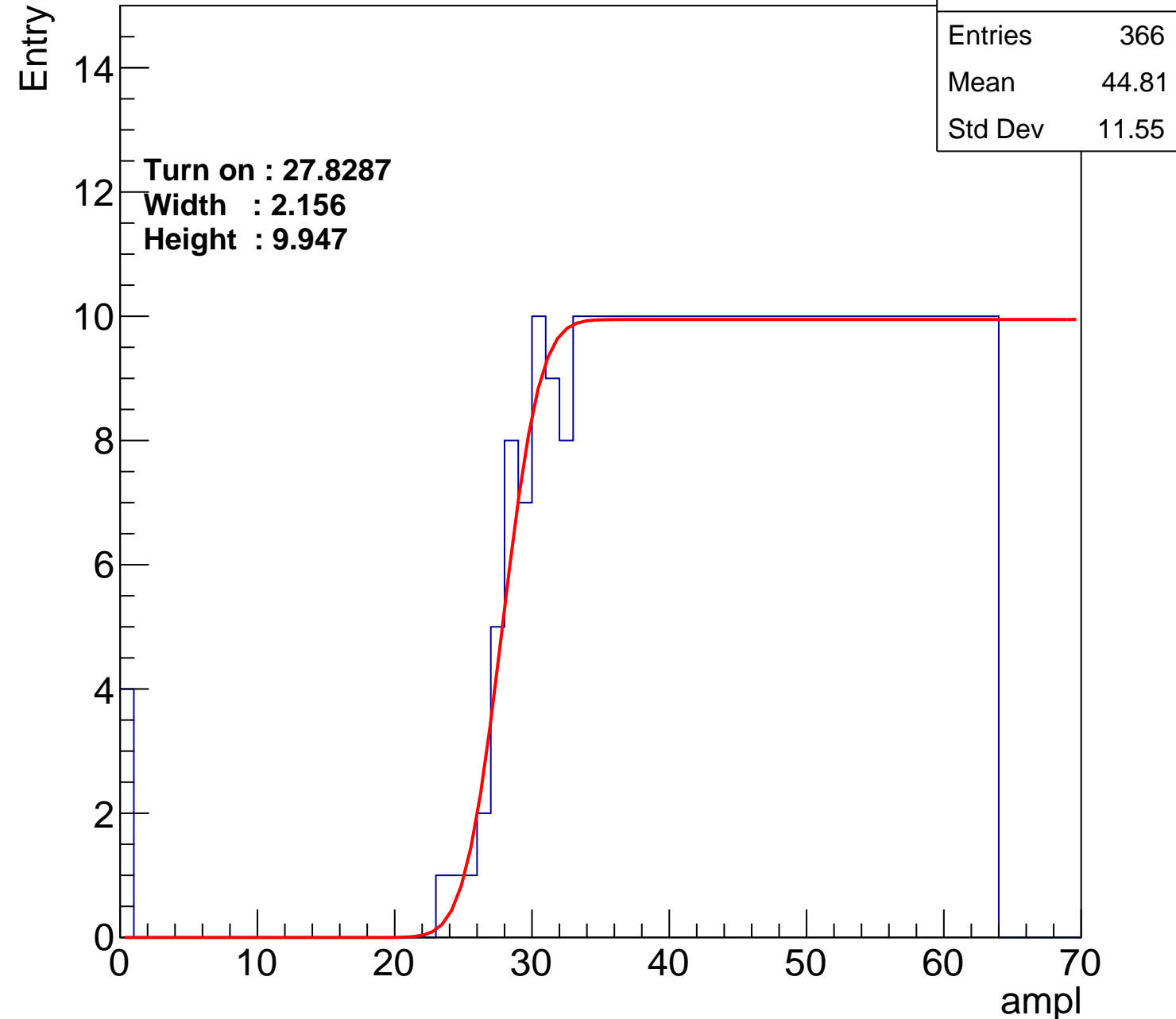
Width : 2.156

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch77

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.43
Std Dev	11.33

Turn on : 29.3729

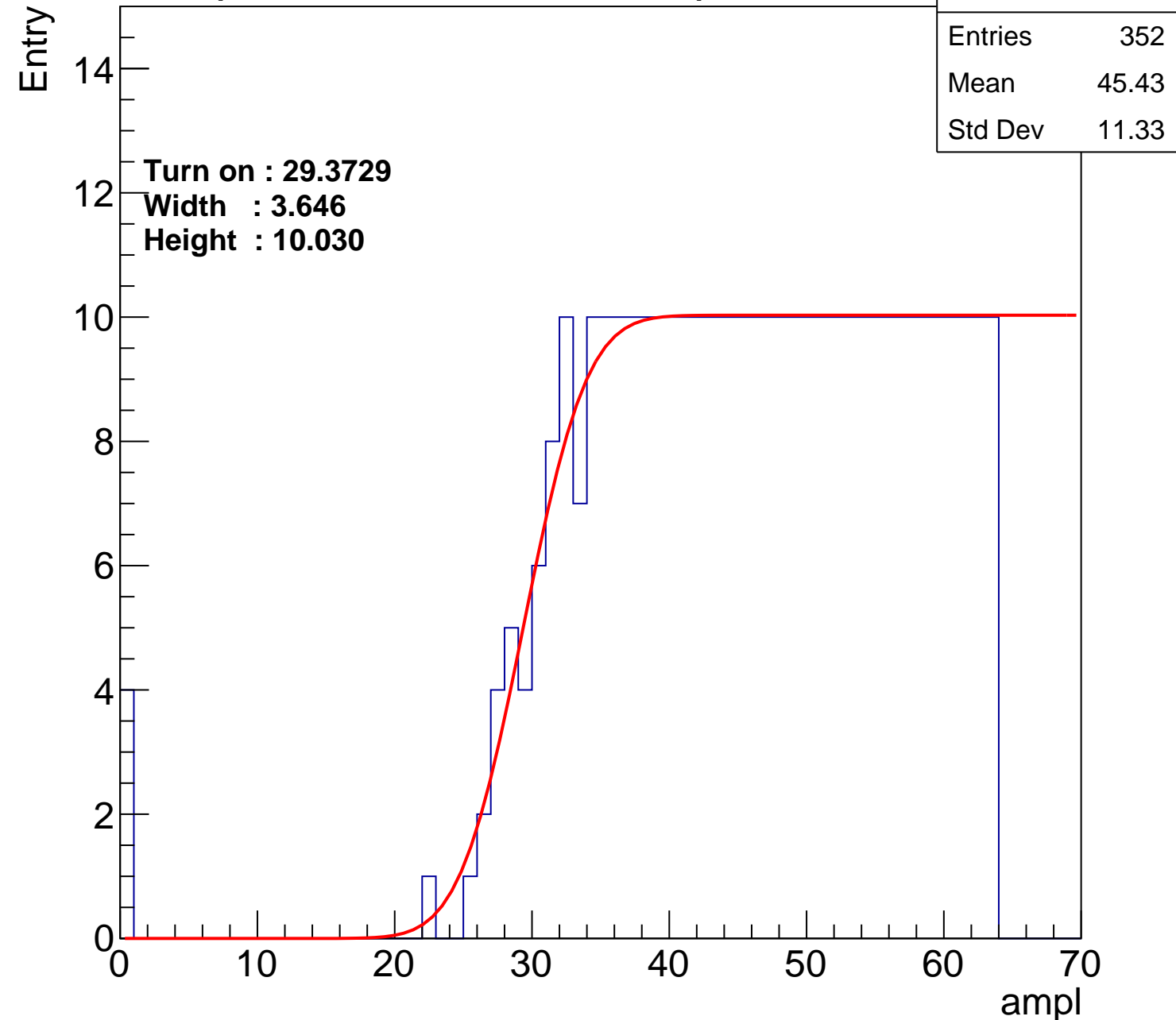
Width : 3.646

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch78

calib_packv5_042523_0143.root, FC#9, port A1

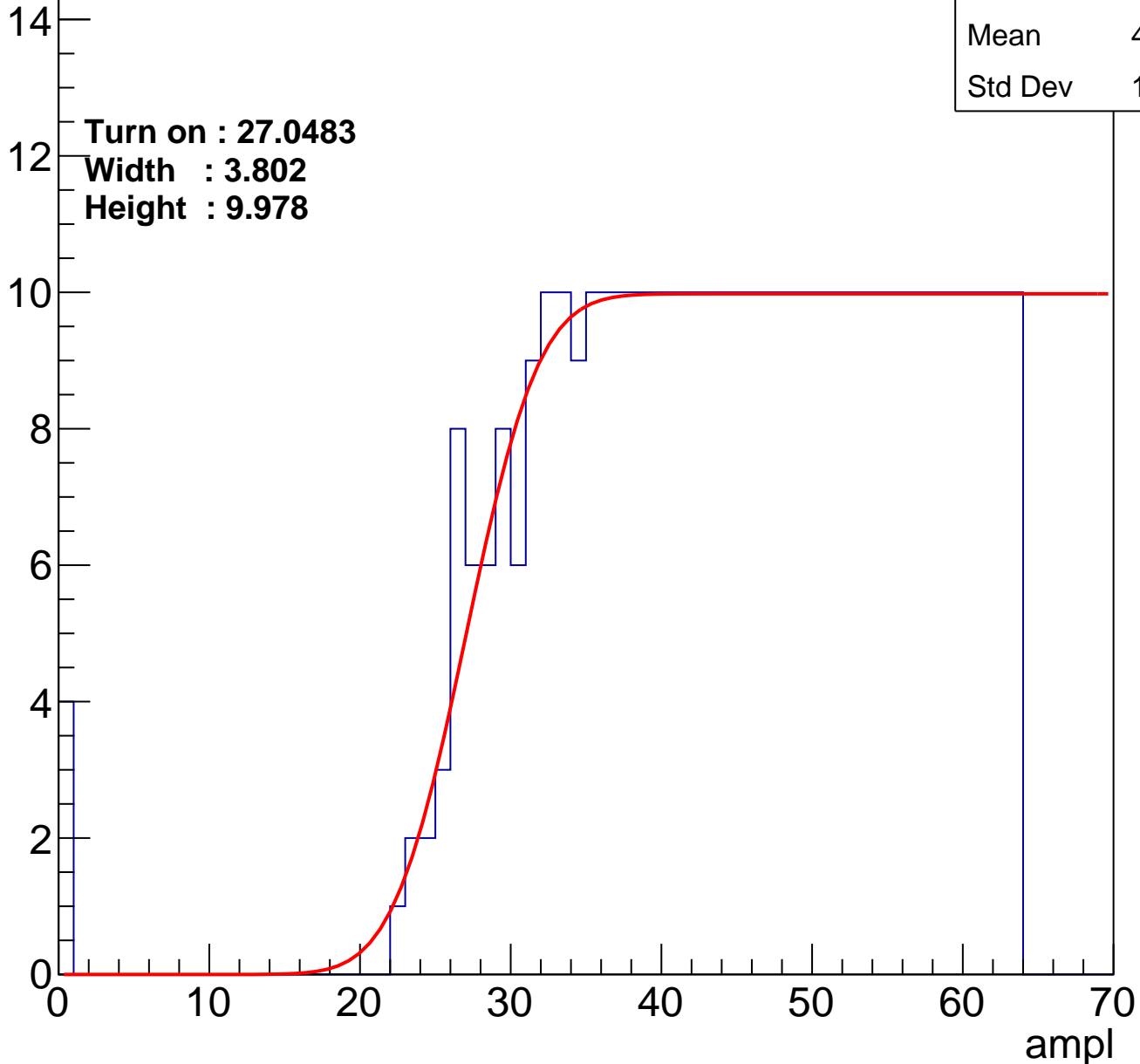
Entries	374
Mean	44.35
Std Dev	11.84

Turn on : 27.0483

Width : 3.802

Height : 9.978

Entry



B0L001S, U26-ch79

calib_packv5_042523_0143.root, FC#9, port A1

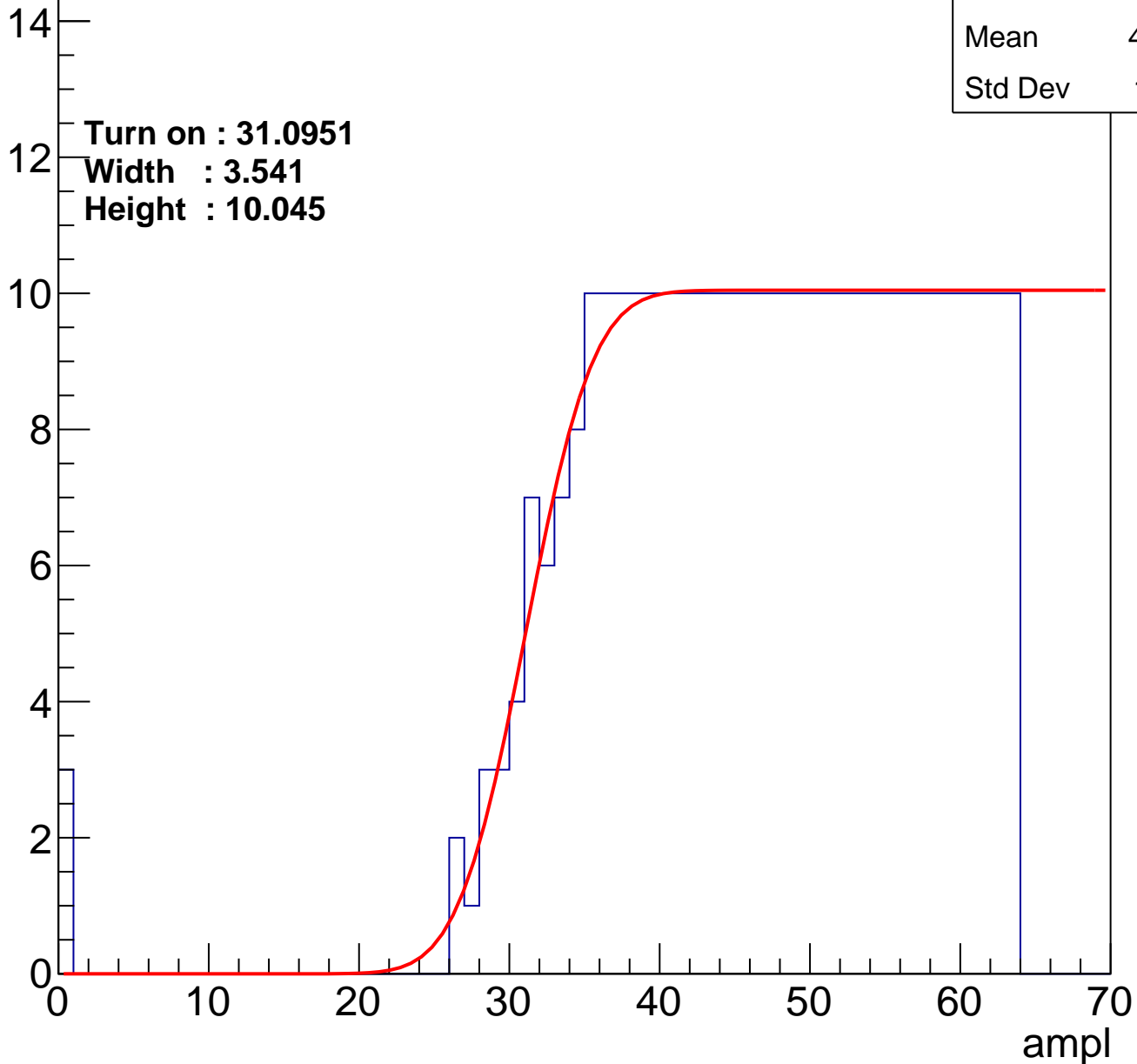
Entries	334
Mean	46.38
Std Dev	10.71

Turn on : 31.0951

Width : 3.541

Height : 10.045

Entry



B0L001S, U26-ch80

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 28.0777

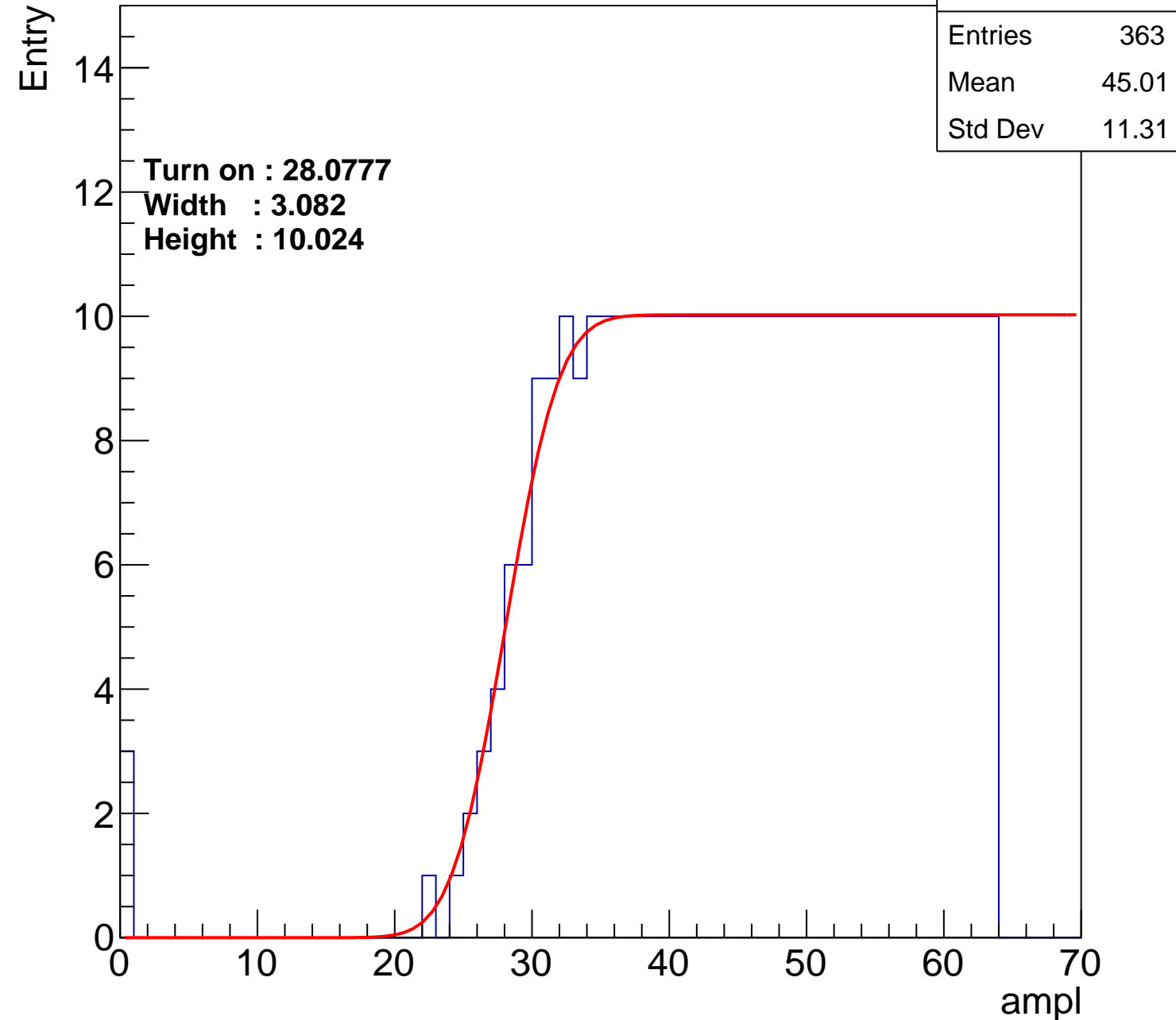
Width : 3.082

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch81

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.33
Std Dev	11.95

Turn on : 27.1115

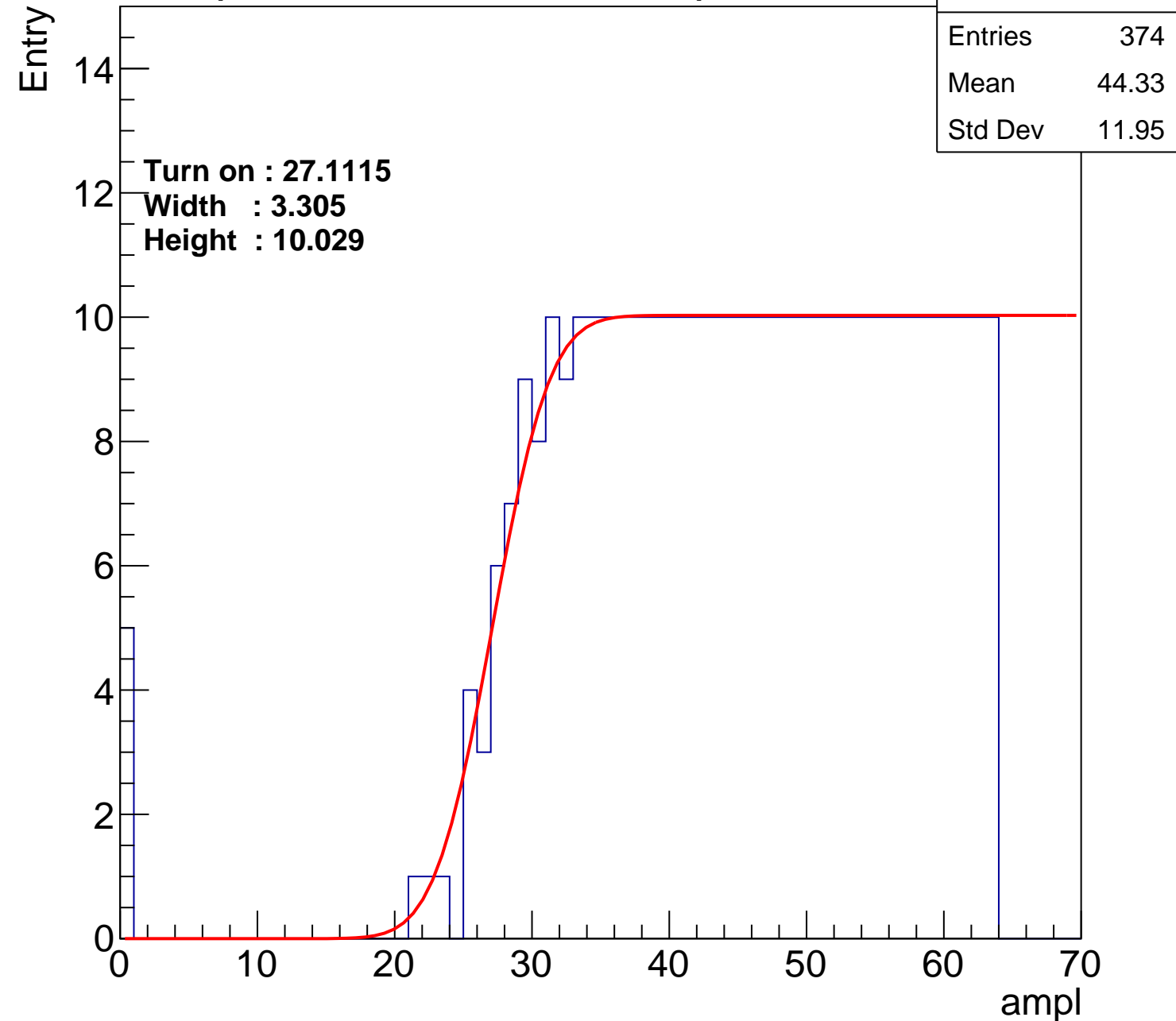
Width : 3.305

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch82

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.32
Std Dev	11.14

Turn on : 28.7911

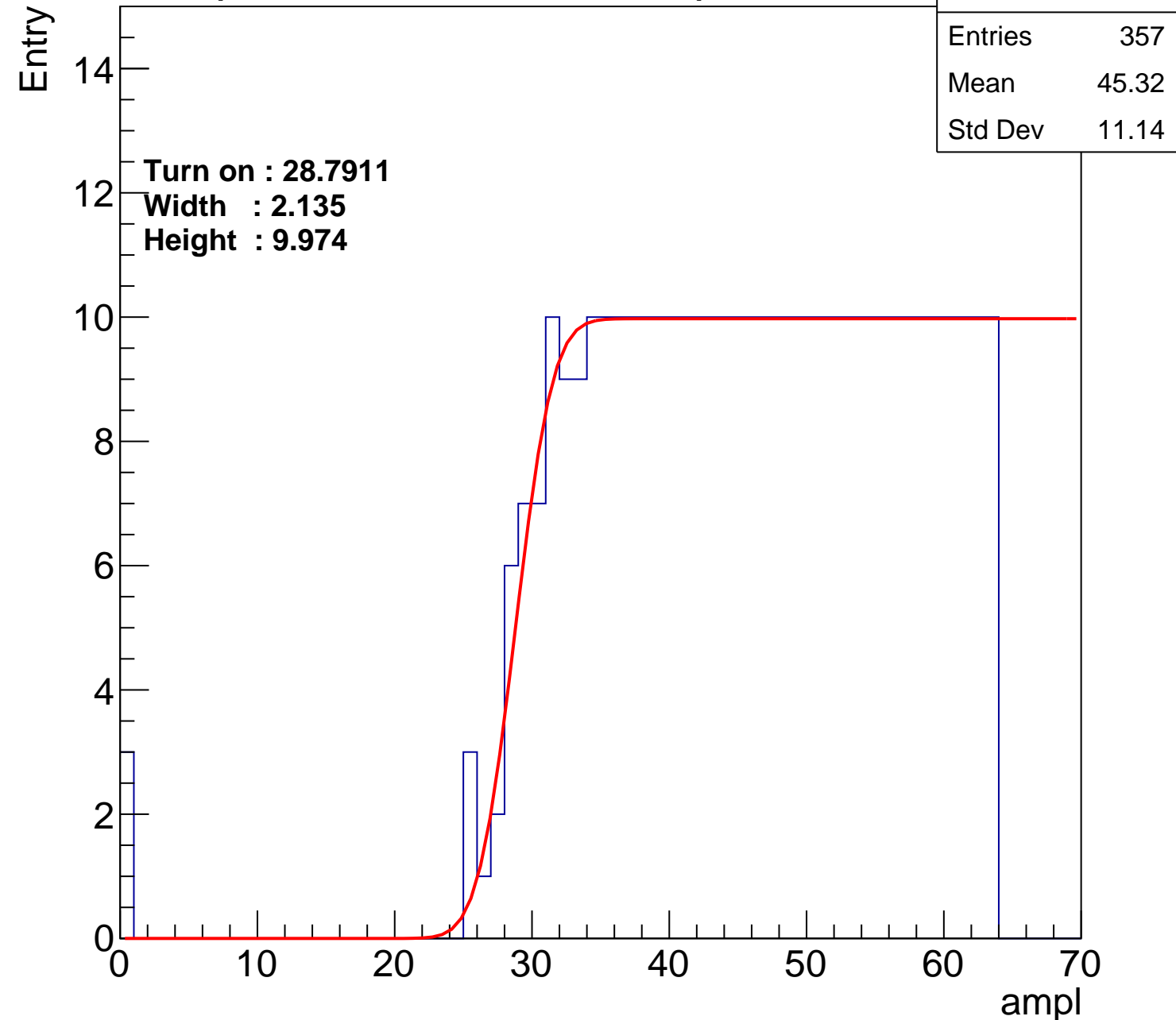
Width : 2.135

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch83

calib_packv5_042523_0143.root, FC#9, port A1

Entries	338
Mean	46.3
Std Dev	10.53

Turn on : 30.9611

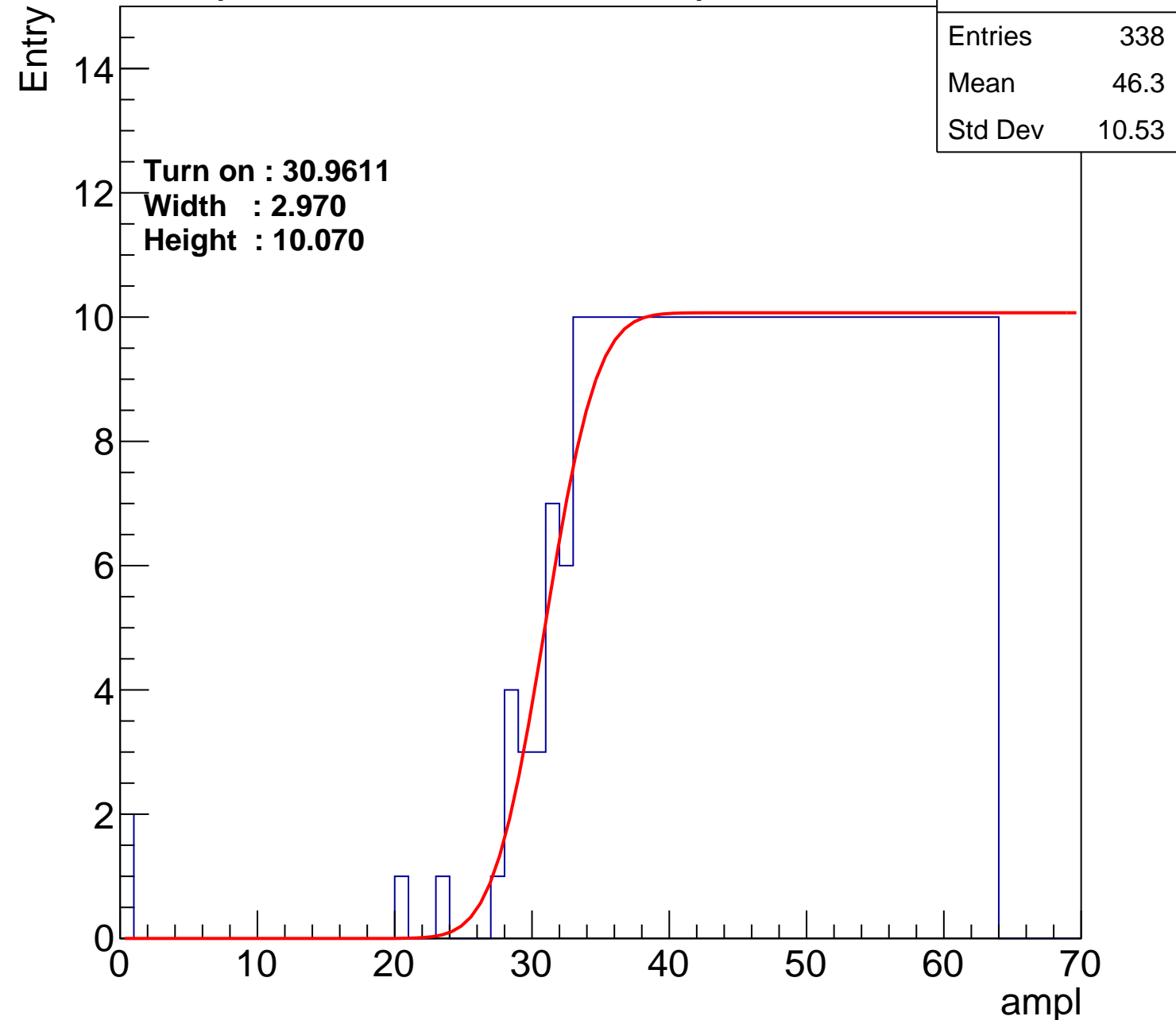
Width : 2.970

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch84

calib_packv5_042523_0143.root, FC#9, port A1

Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.2376

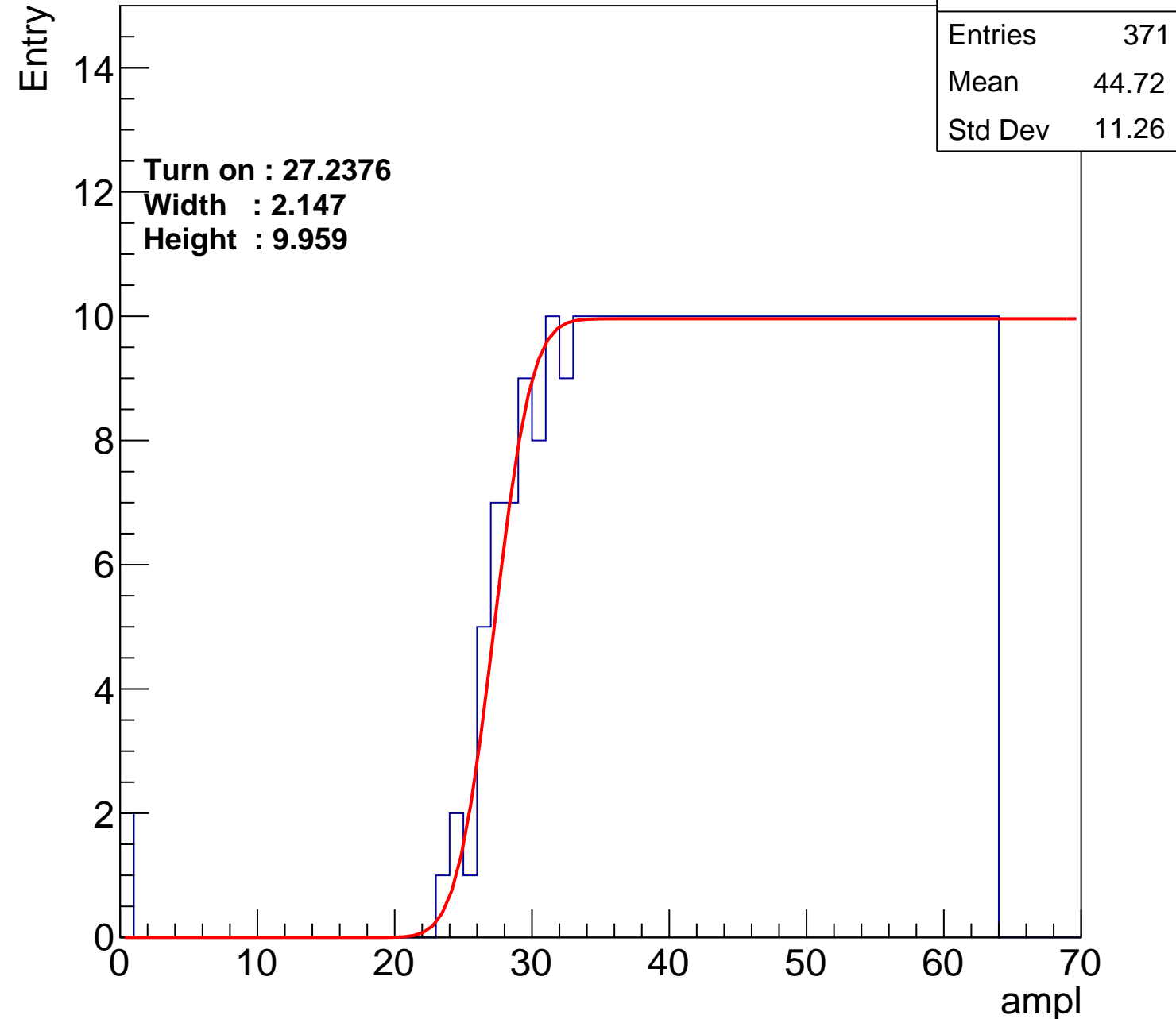
Width : 2.147

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch85

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.53
Std Dev	12.17

Turn on : 27.7516

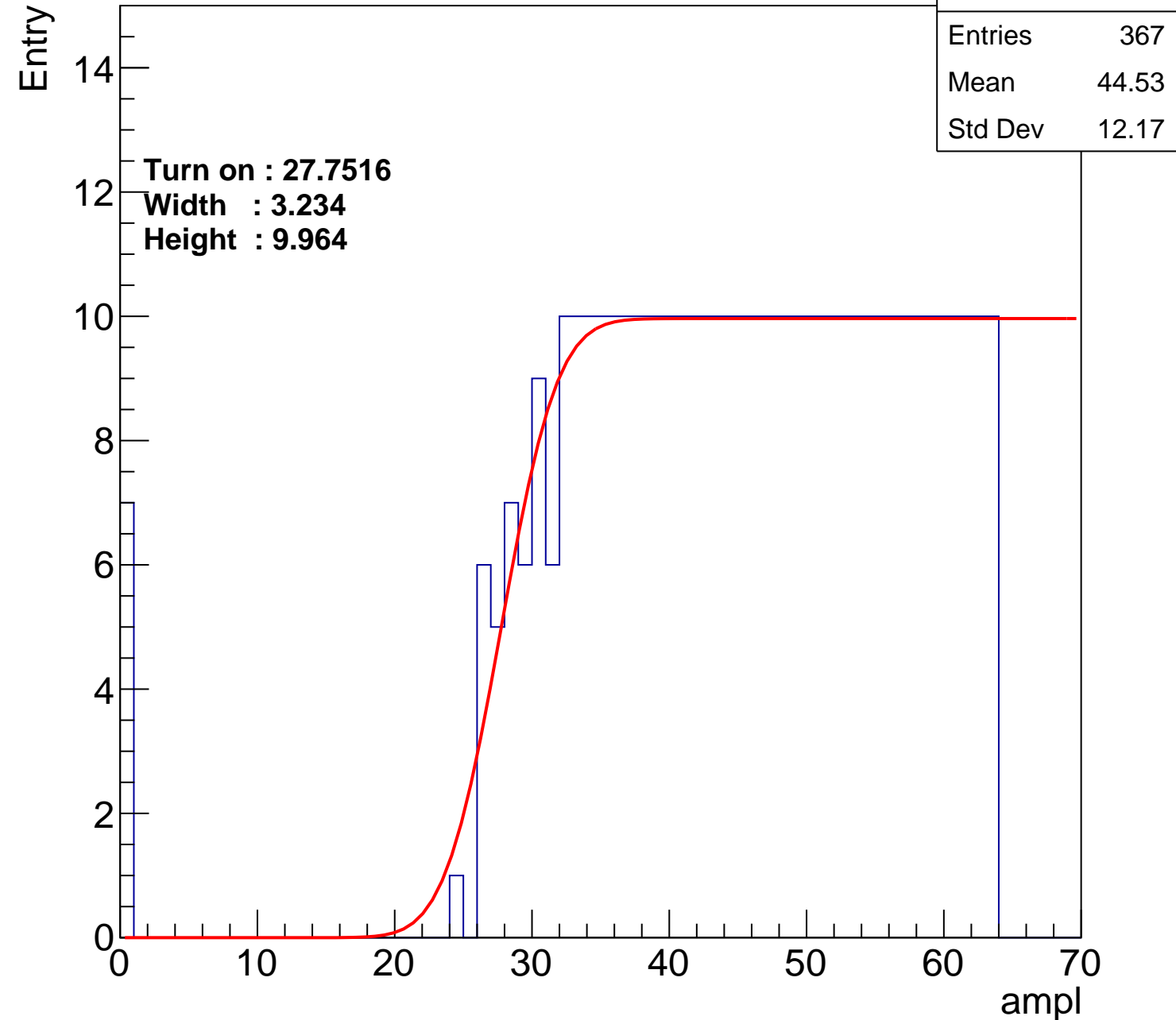
Width : 3.234

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch86

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 26.9317

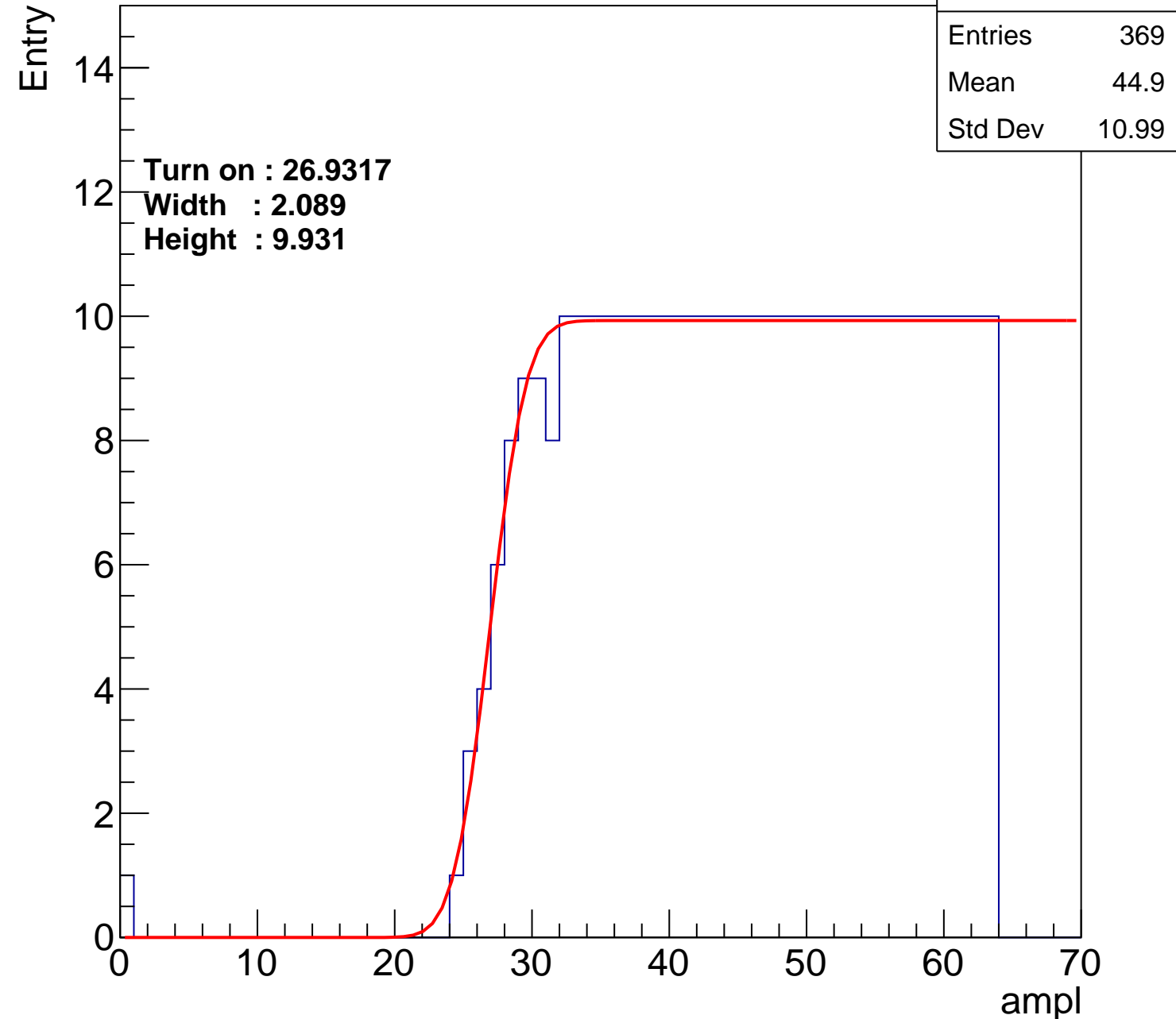
Width : 2.089

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch87

calib_packv5_042523_0143.root, FC#9, port A1

Entries	350
Mean	45.53
Std Dev	11.3

Turn on : 29.6343

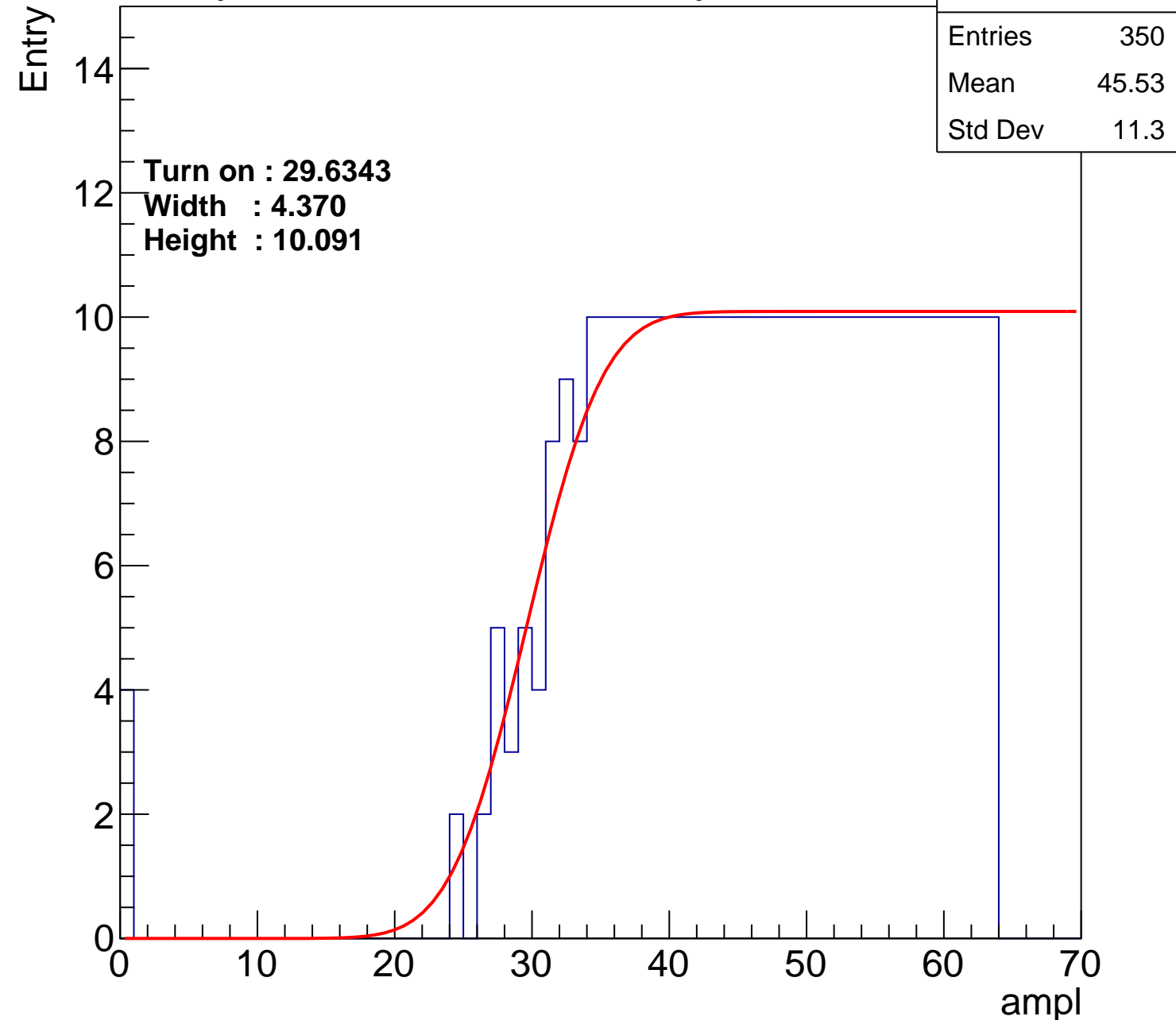
Width : 4.370

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch88

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.51
Std Dev	11.88

Turn on : 27.5105

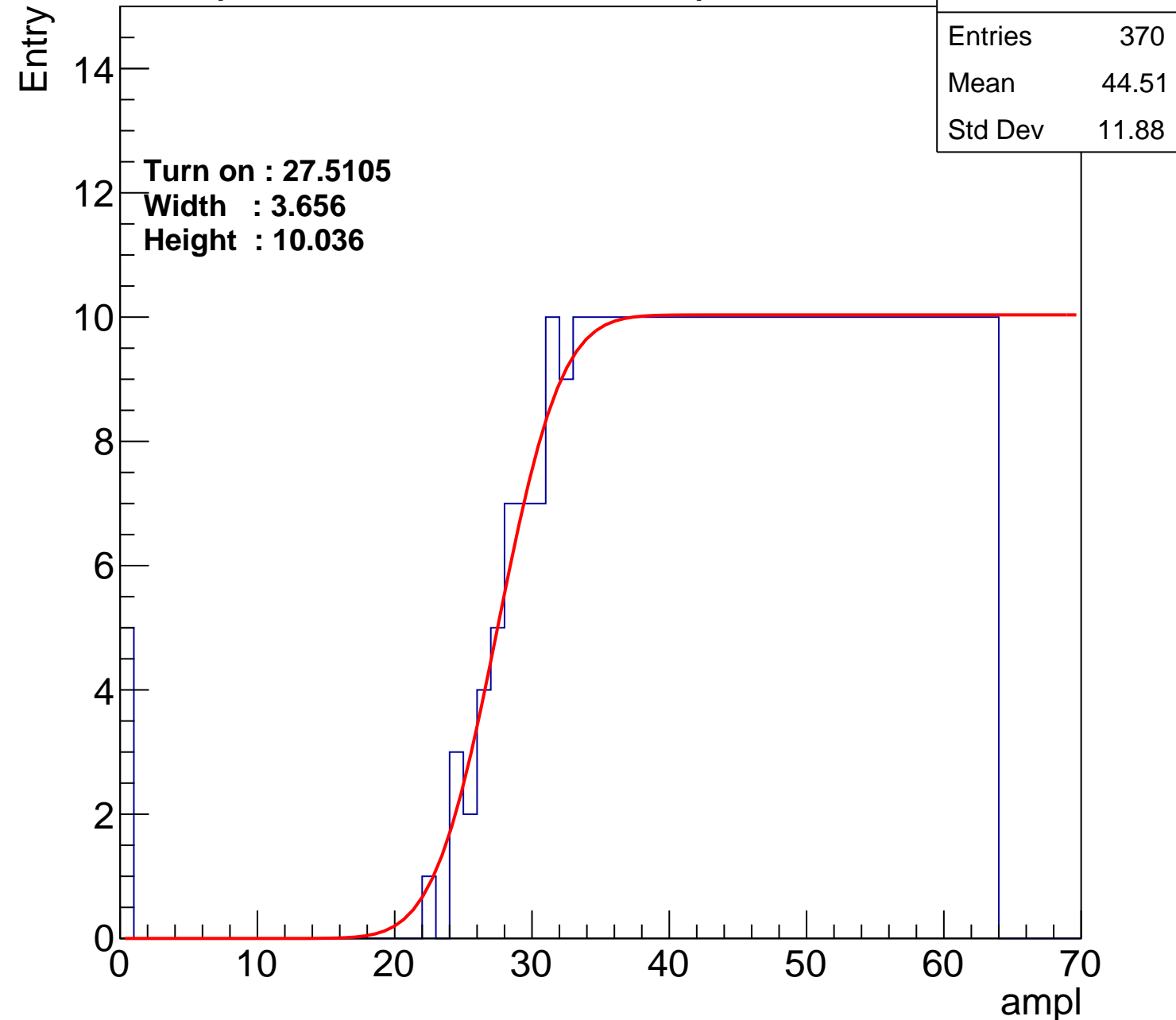
Width : 3.656

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch89

calib_packv5_042523_0143.root, FC#9, port A1

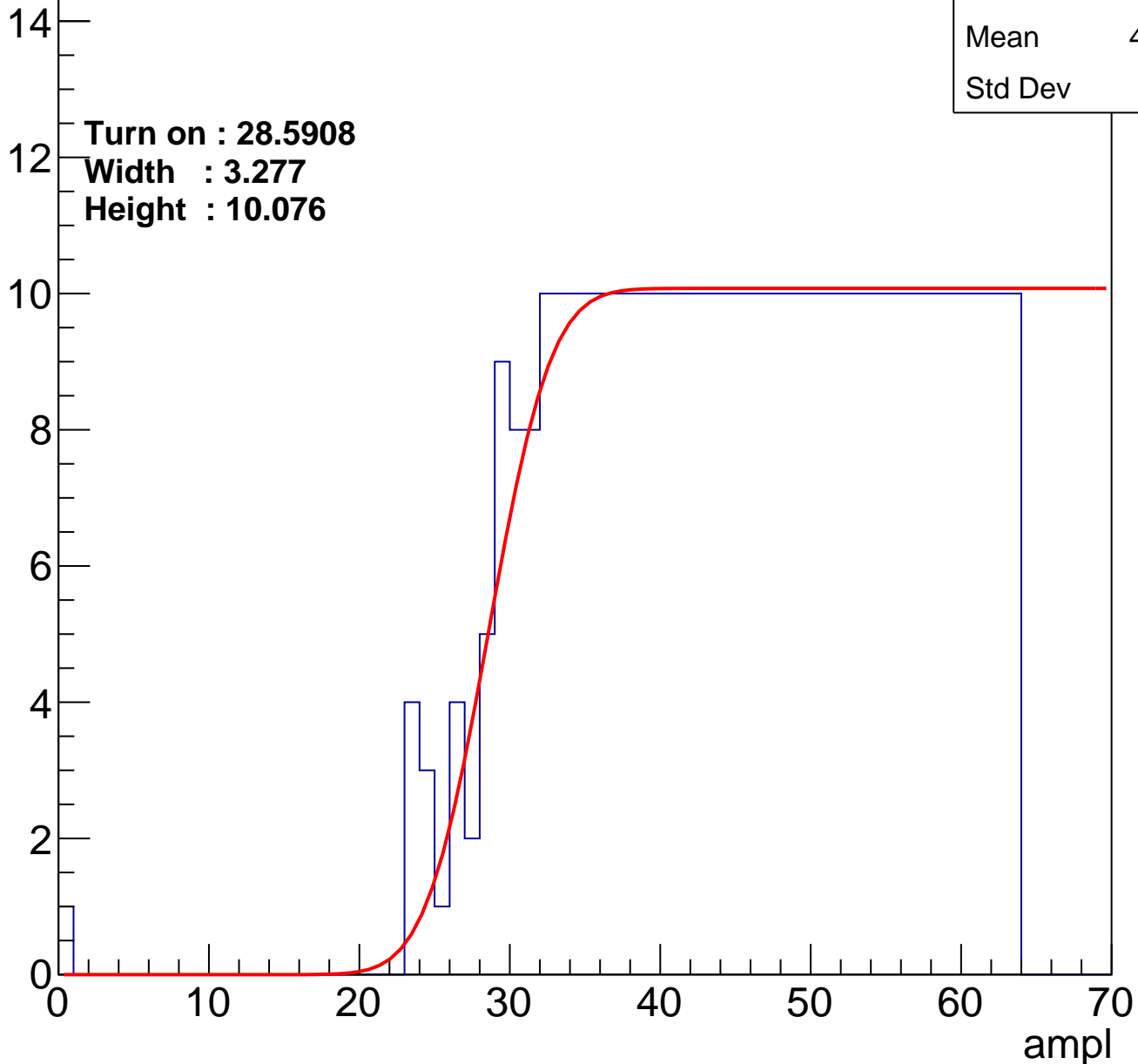
Entries	365
Mean	45.03
Std Dev	11

Turn on : 28.5908

Width : 3.277

Height : 10.076

Entry



B0L001S, U26-ch90

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.75
Std Dev	11.92

Turn on : 29.1381

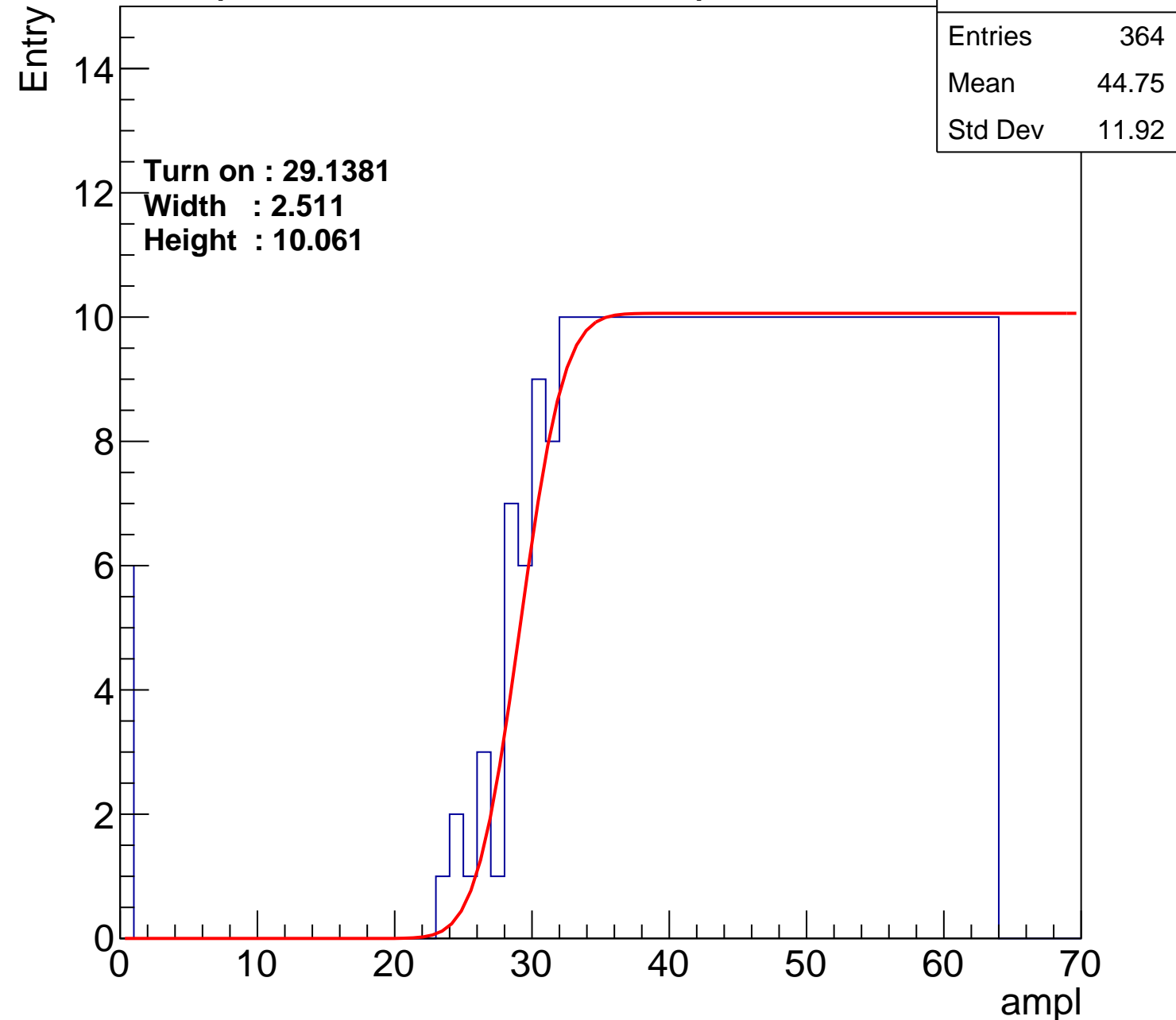
Width : 2.511

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch91

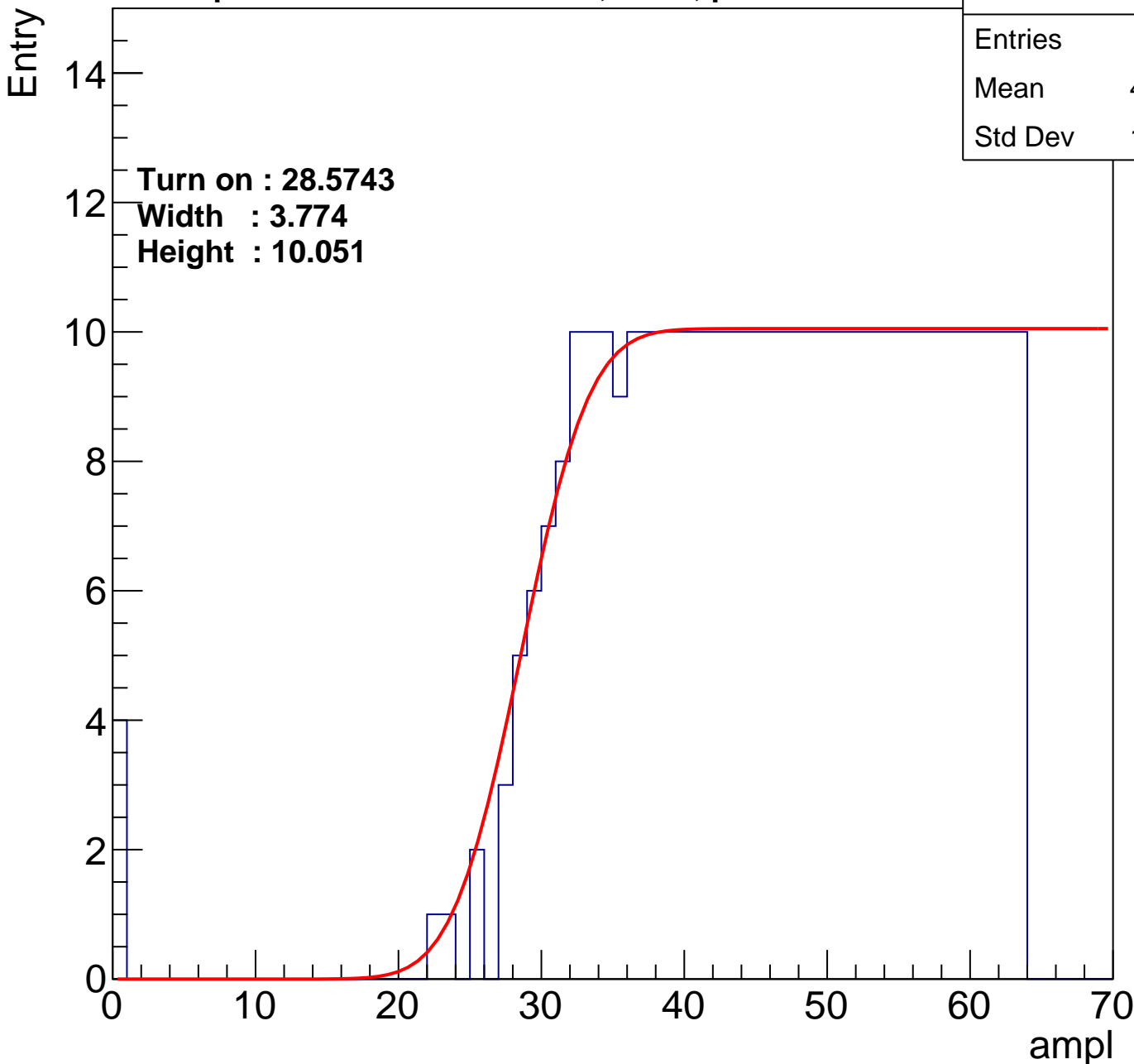
calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.26
Std Dev	11.39

Turn on : 28.5743

Width : 3.774

Height : 10.051



B0L001S, U26-ch92

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.34
Std Dev	10.76

Turn on : 28.3653

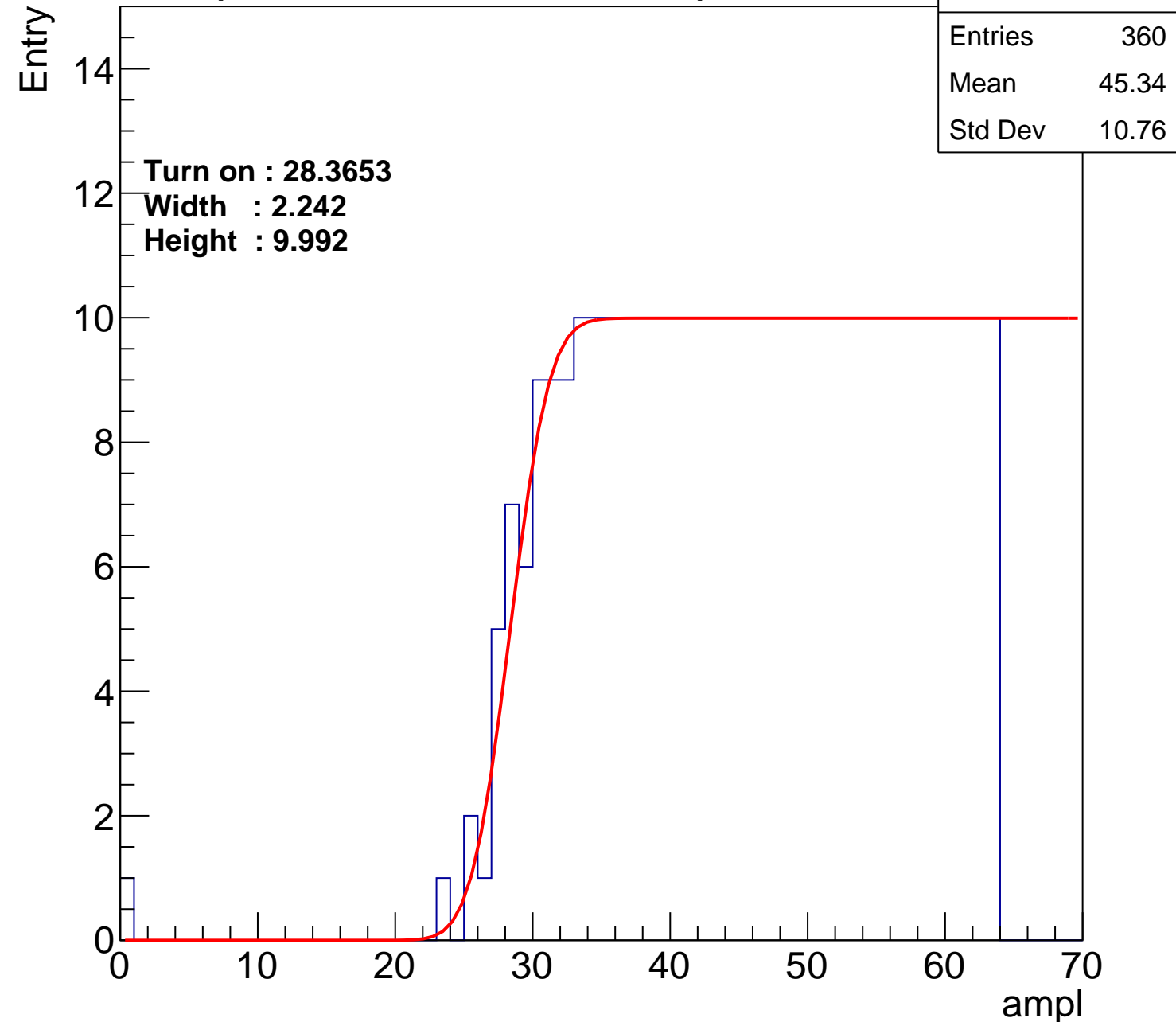
Width : 2.242

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch93

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.83
Std Dev	11.52

Turn on : 27.8429

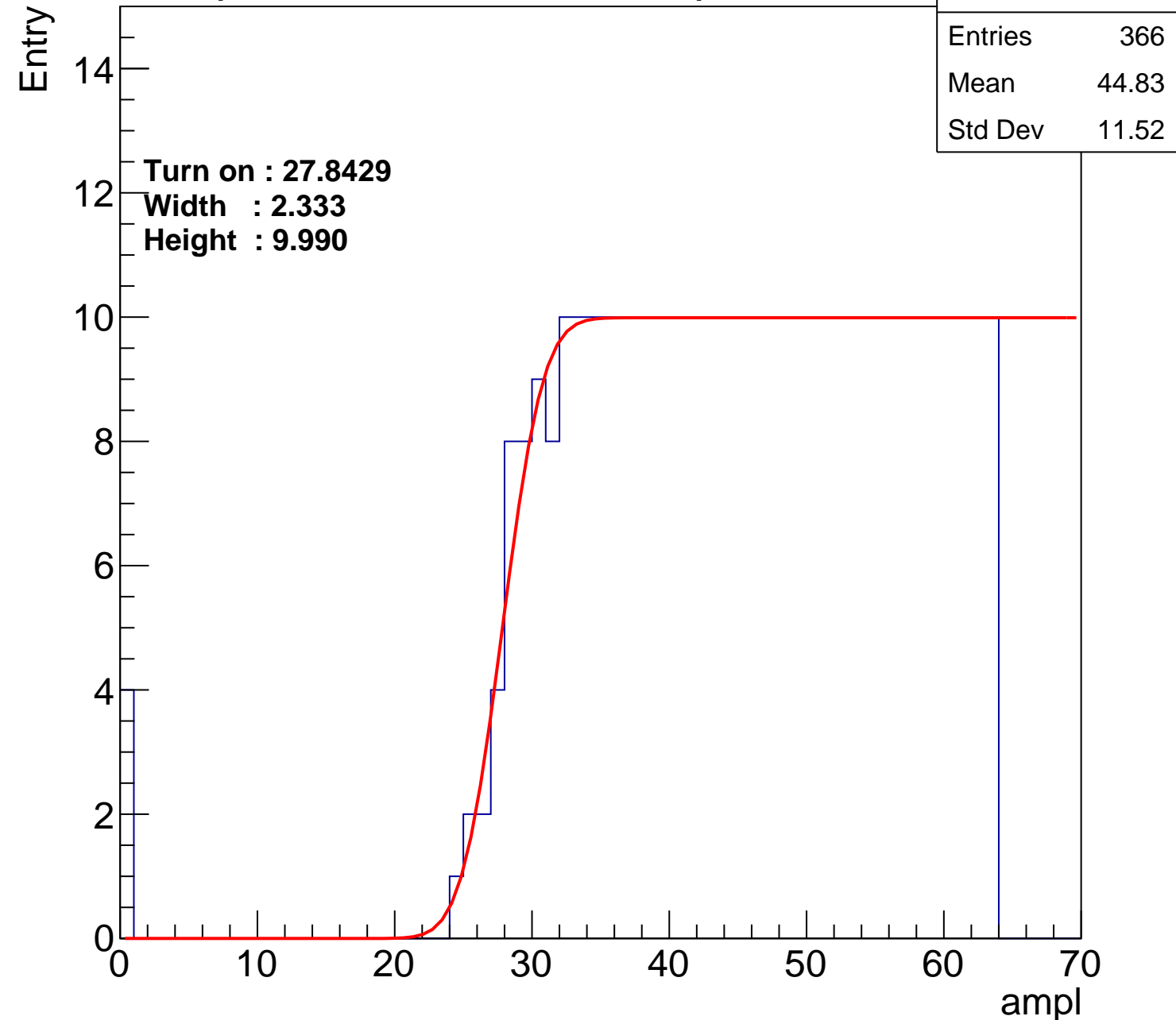
Width : 2.333

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch94

calib_packv5_042523_0143.root, FC#9, port A1

Entries	348
Mean	45.79
Std Dev	10.78

Turn on : 29.9637

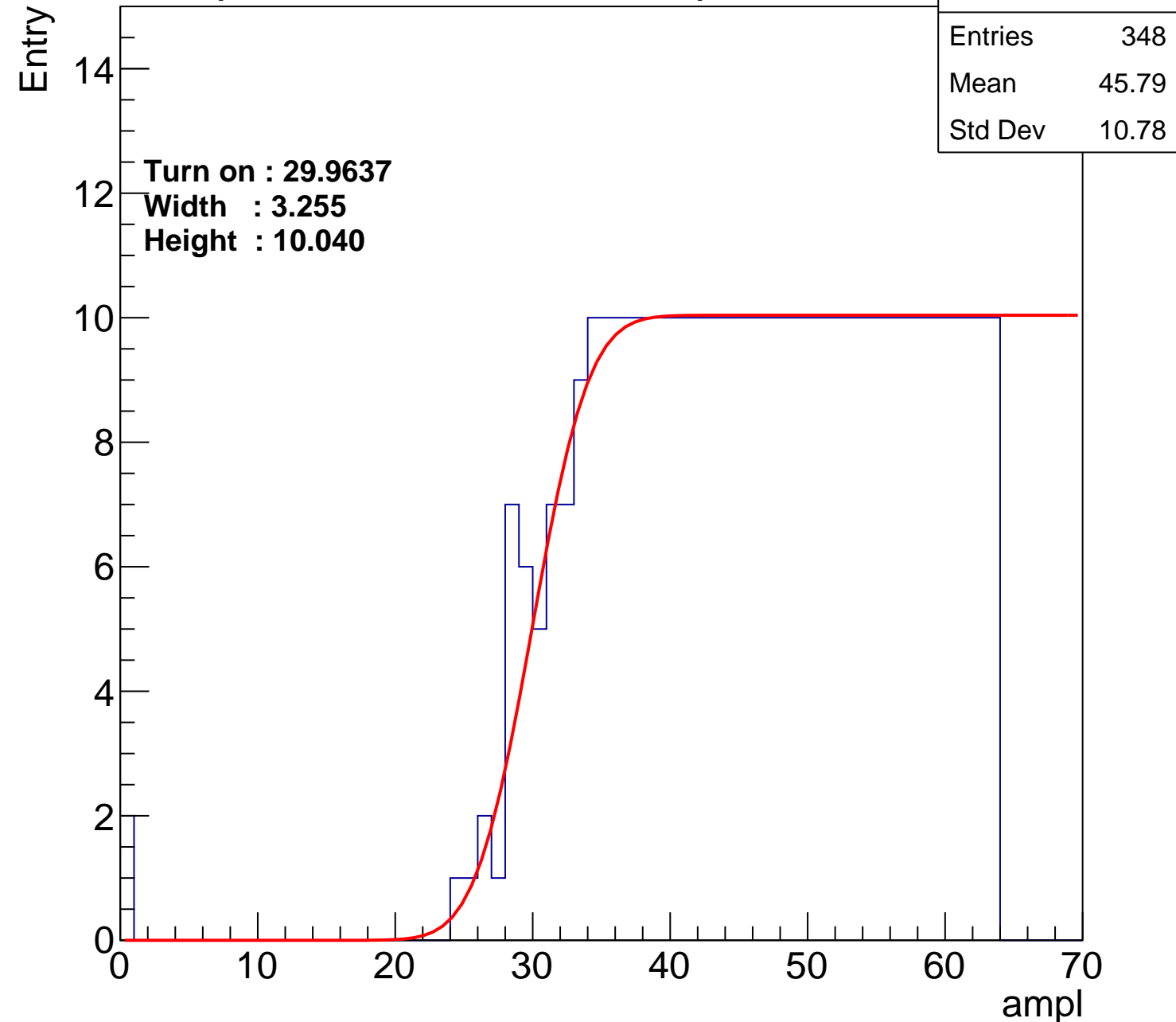
Width : 3.255

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch95

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 28.0974

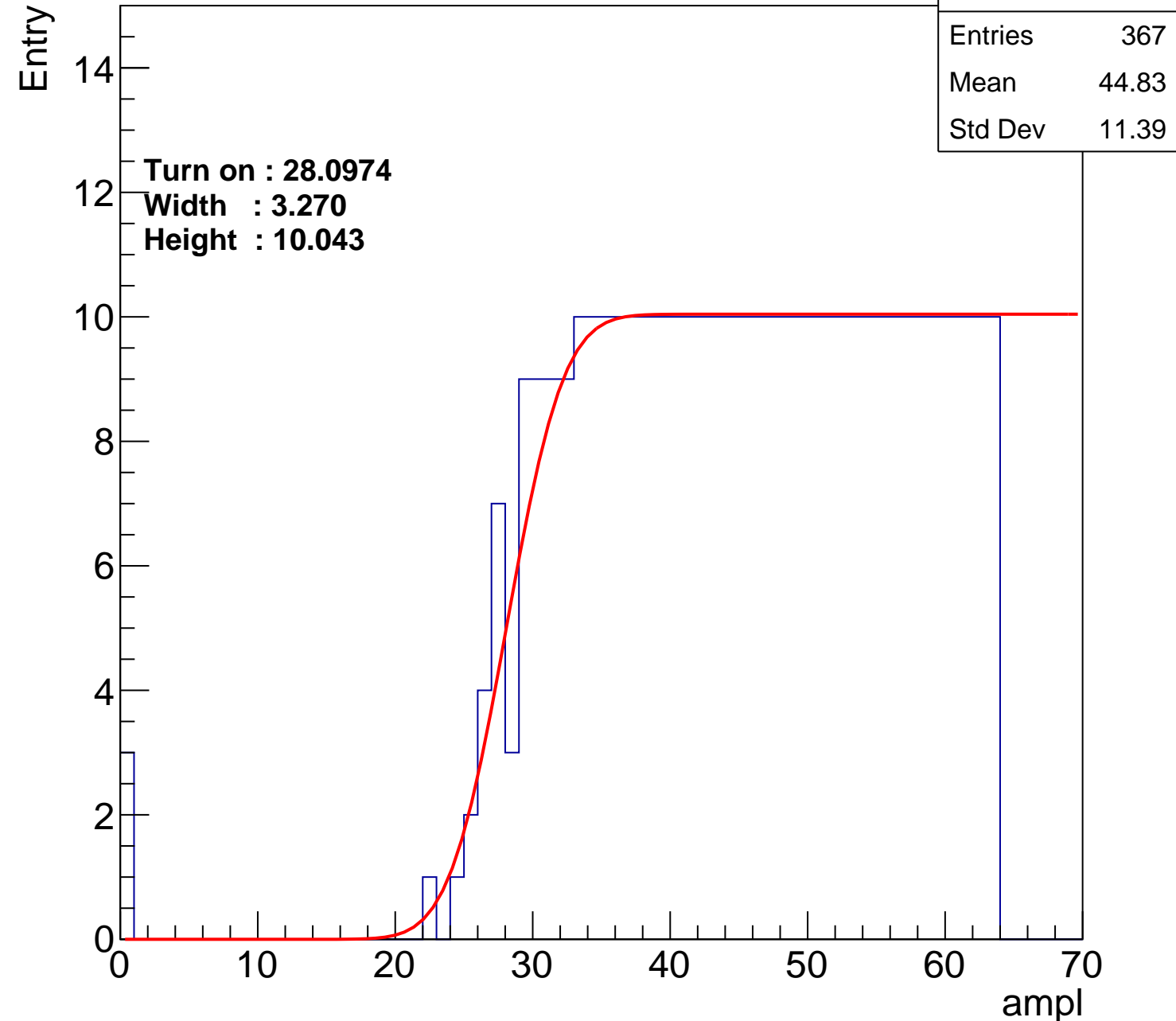
Width : 3.270

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch96

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.88
Std Dev	11.41

Turn on : 28.0752

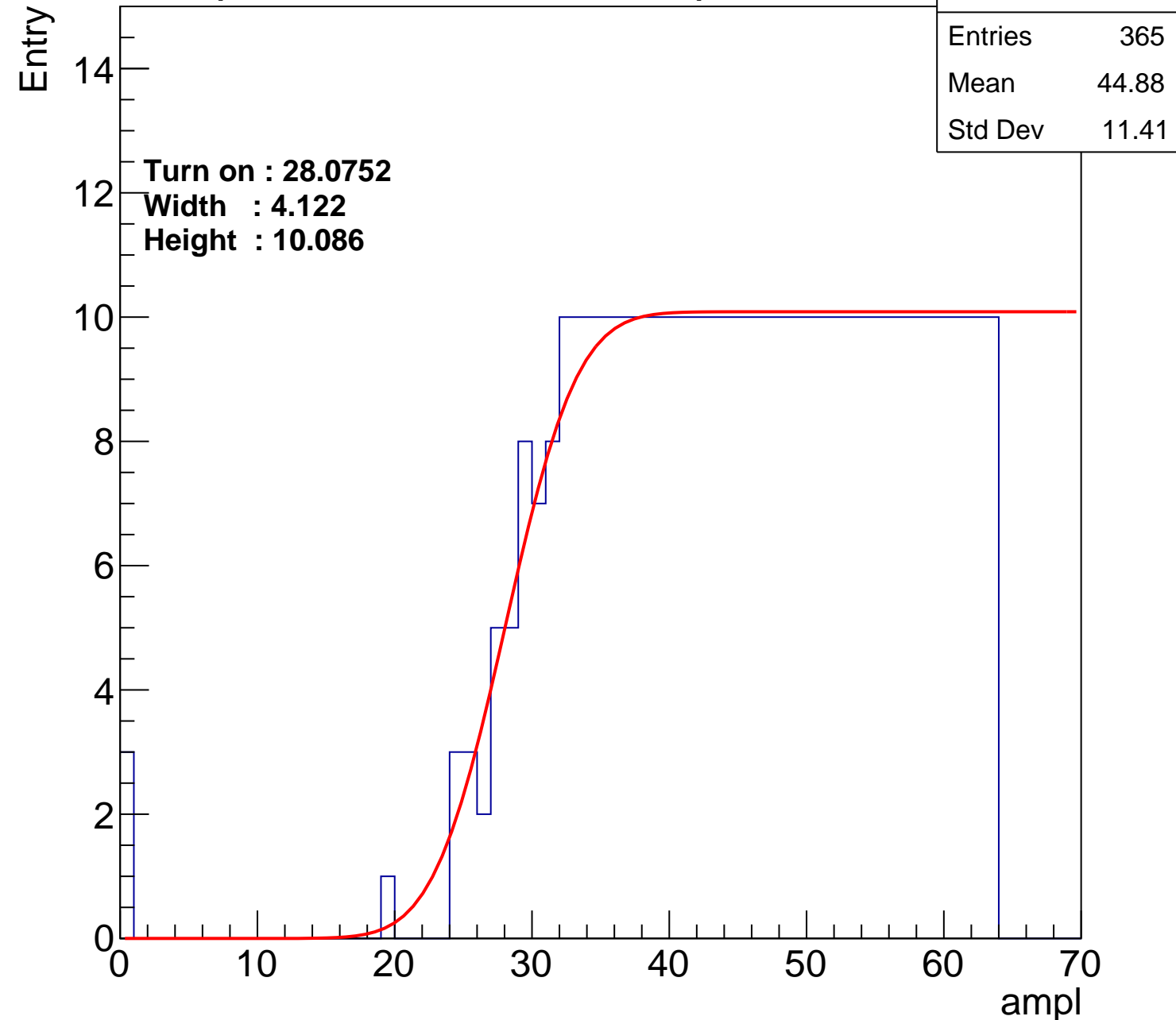
Width : 4.122

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch97

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.97
Std Dev	11.32

Turn on : 28.0950

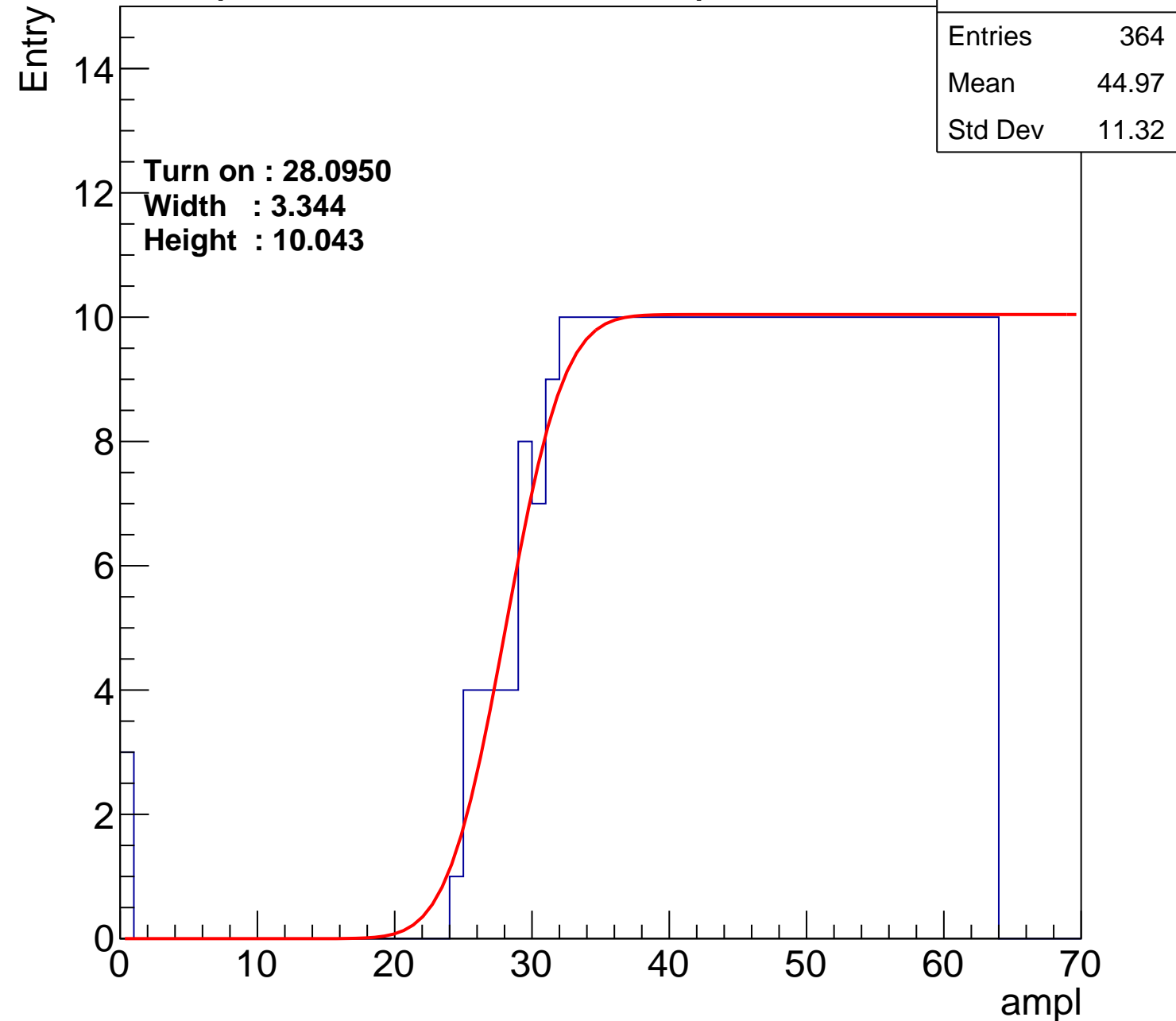
Width : 3.344

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch98

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.63
Std Dev	11.66

Turn on : 27.6247

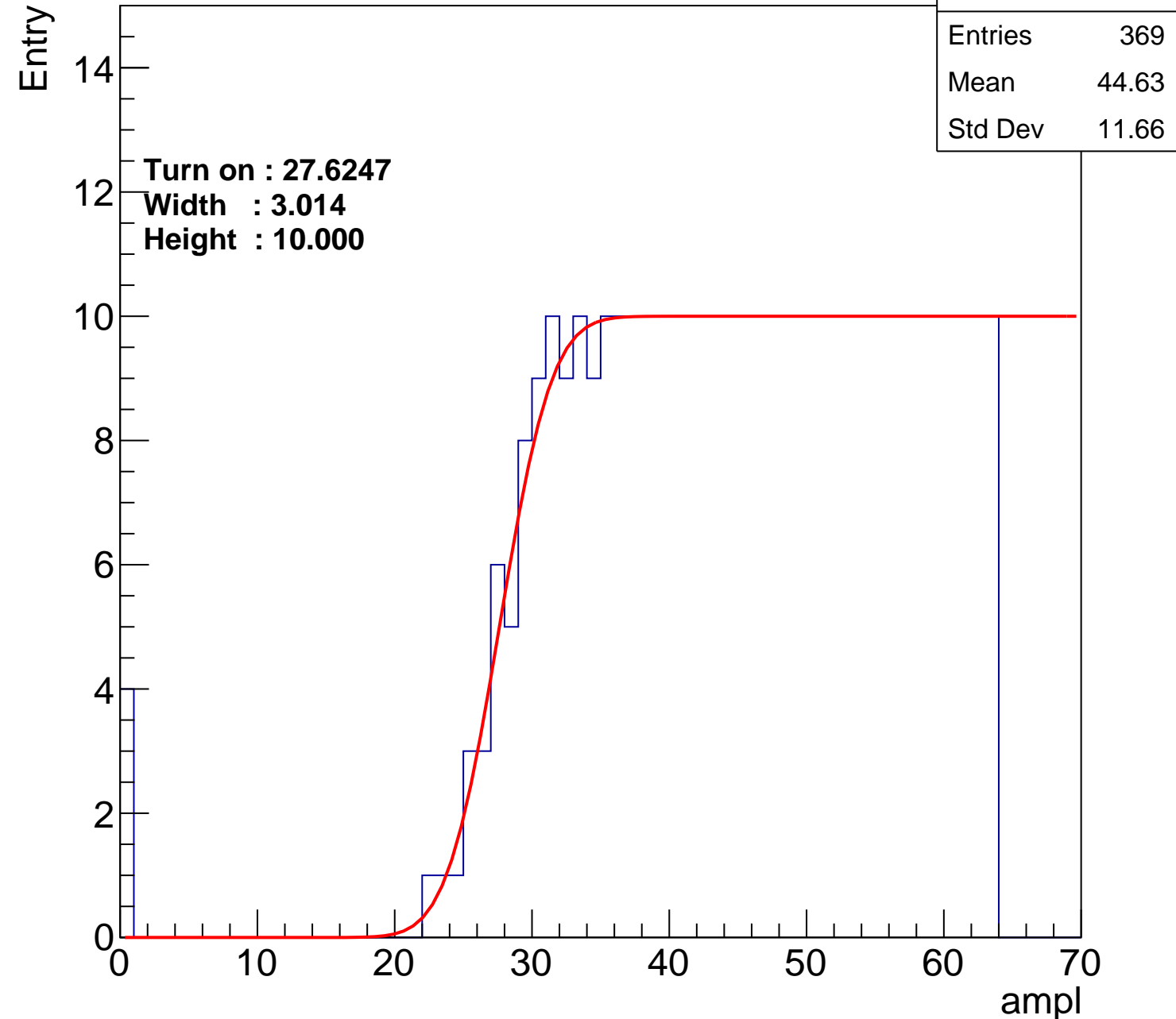
Width : 3.014

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch99

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.62
Std Dev	11.2

Turn on : 29.7175

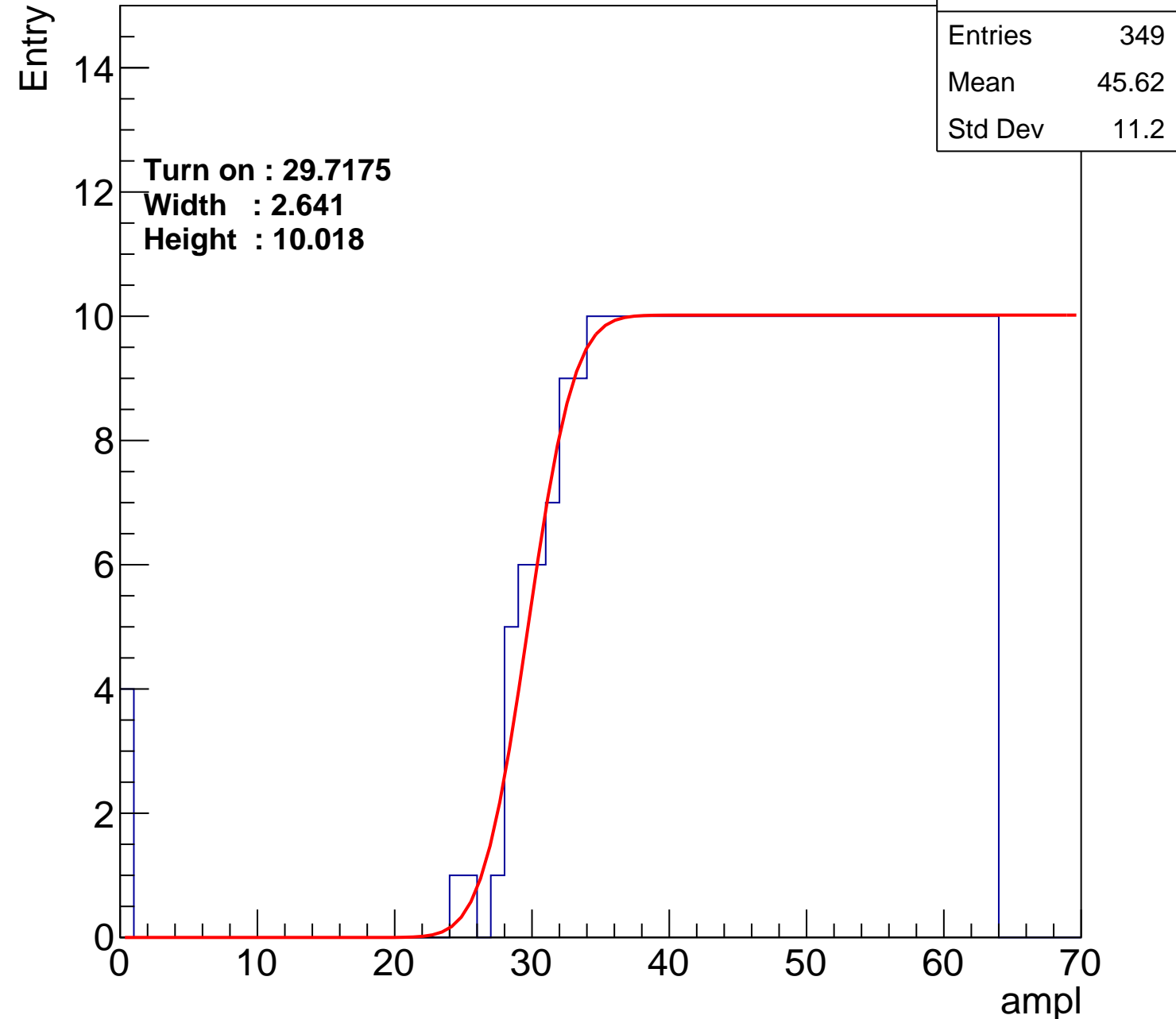
Width : 2.641

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch100

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.38
Std Dev	11.93

Turn on : 26.9620

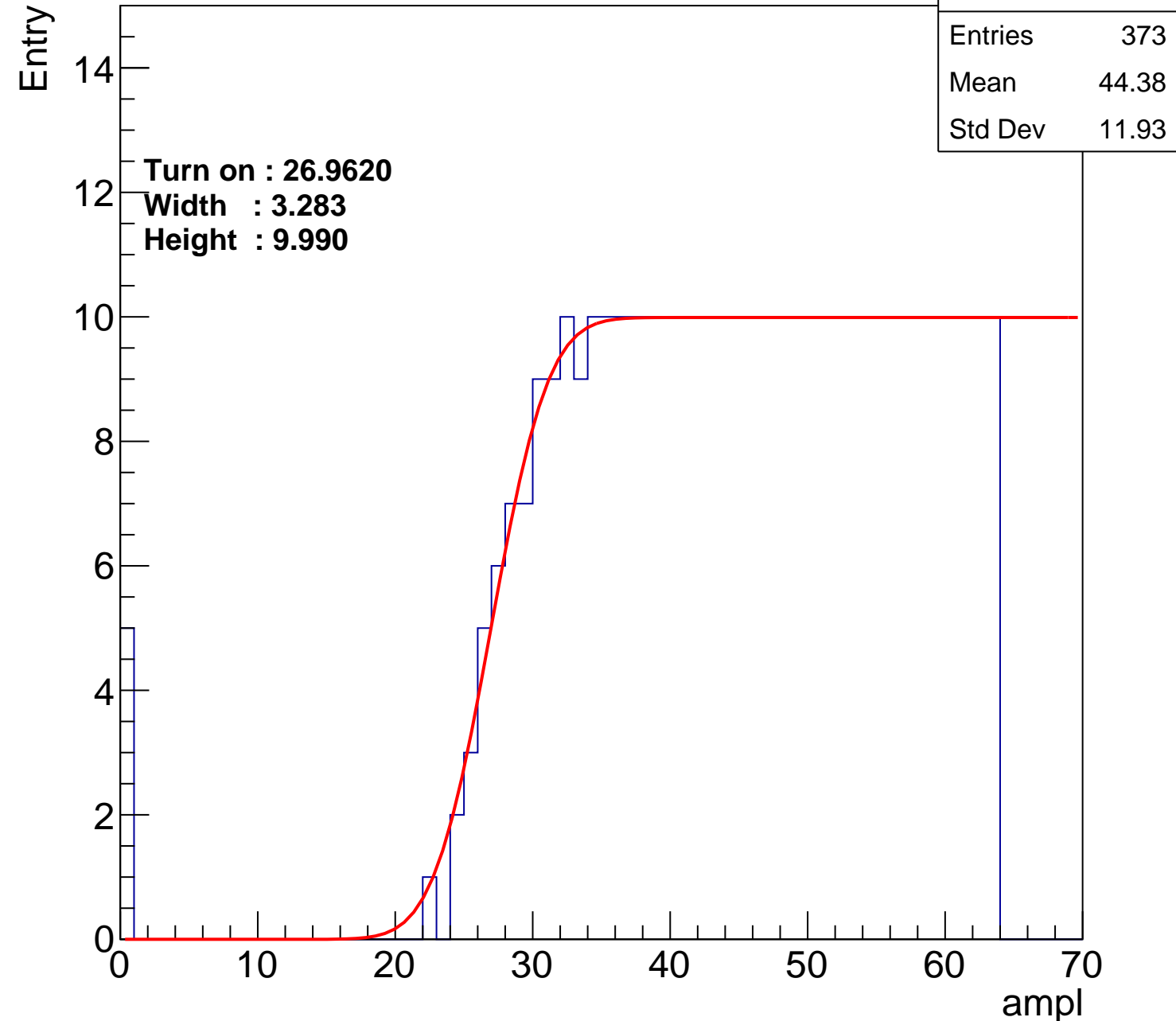
Width : 3.283

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch101

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.77
Std Dev	11.89

Turn on : 28.4211

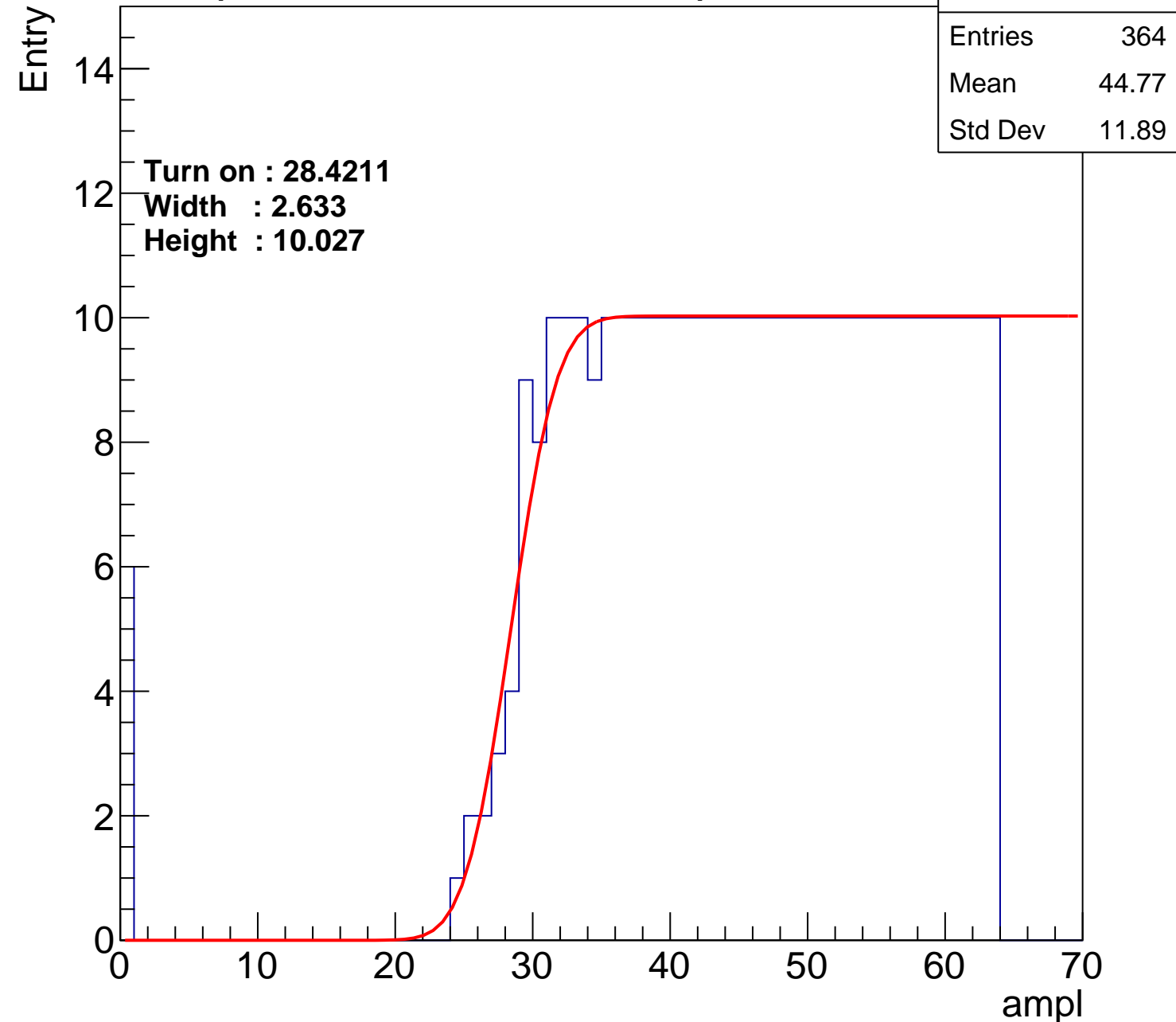
Width : 2.633

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch102

calib_packv5_042523_0143.root, FC#9, port A1

Entries	371
Mean	44.58
Std Dev	11.64

Turn on : 27.5848

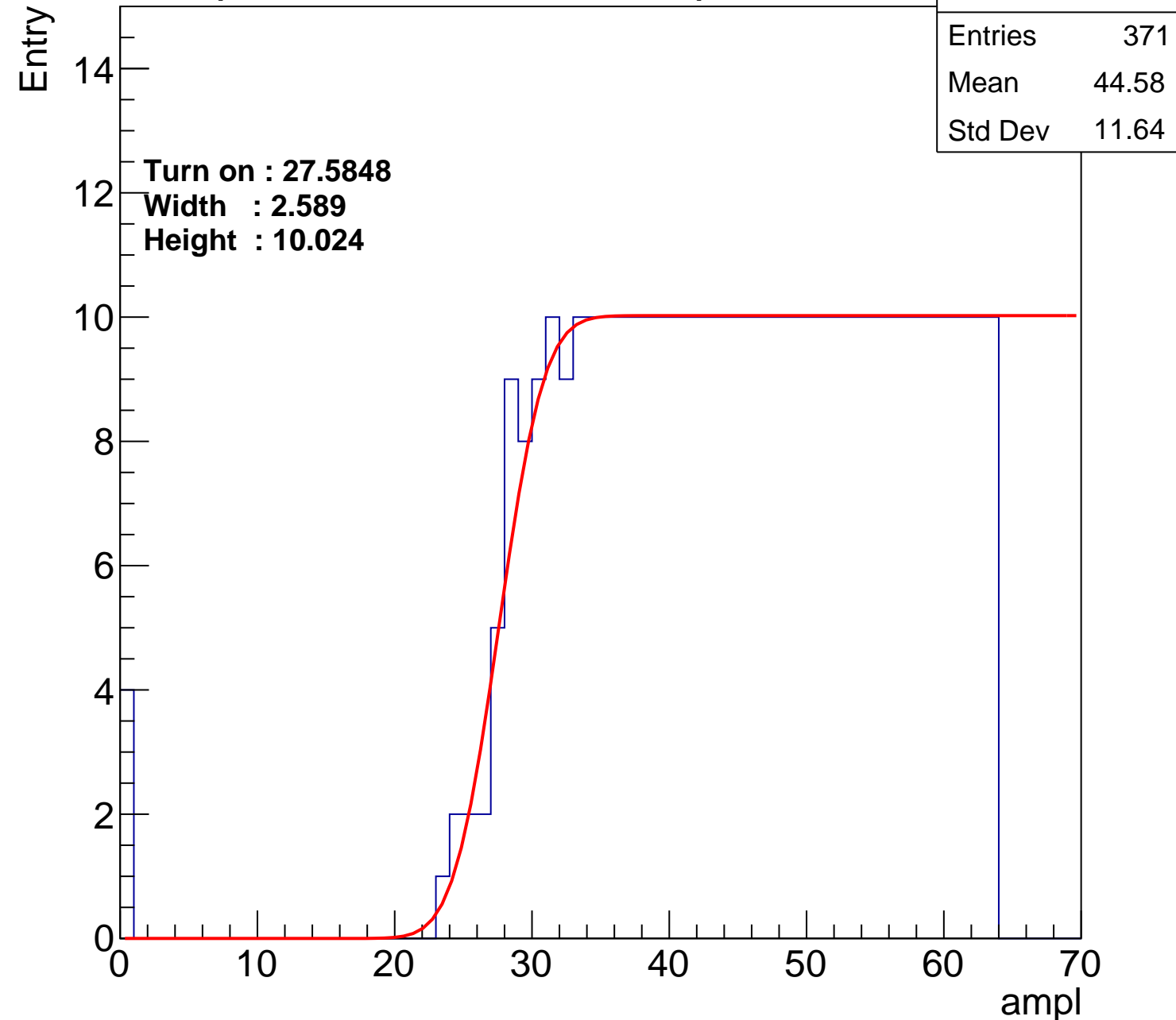
Width : 2.589

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch103

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.84
Std Dev	11.36

Turn on : 27.6655

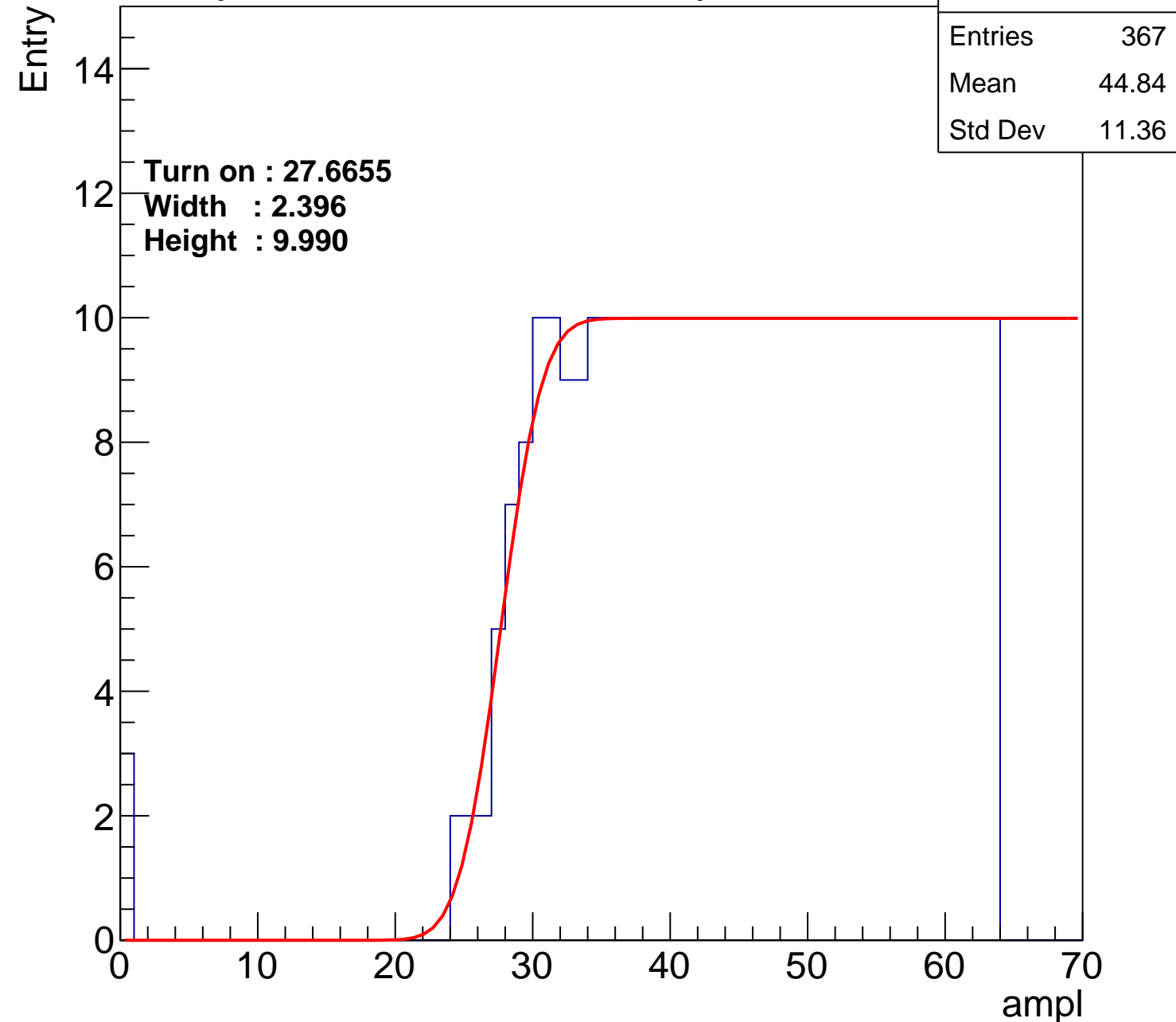
Width : 2.396

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch104

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	45.23
Std Dev	11.24

Turn on : 29.0119

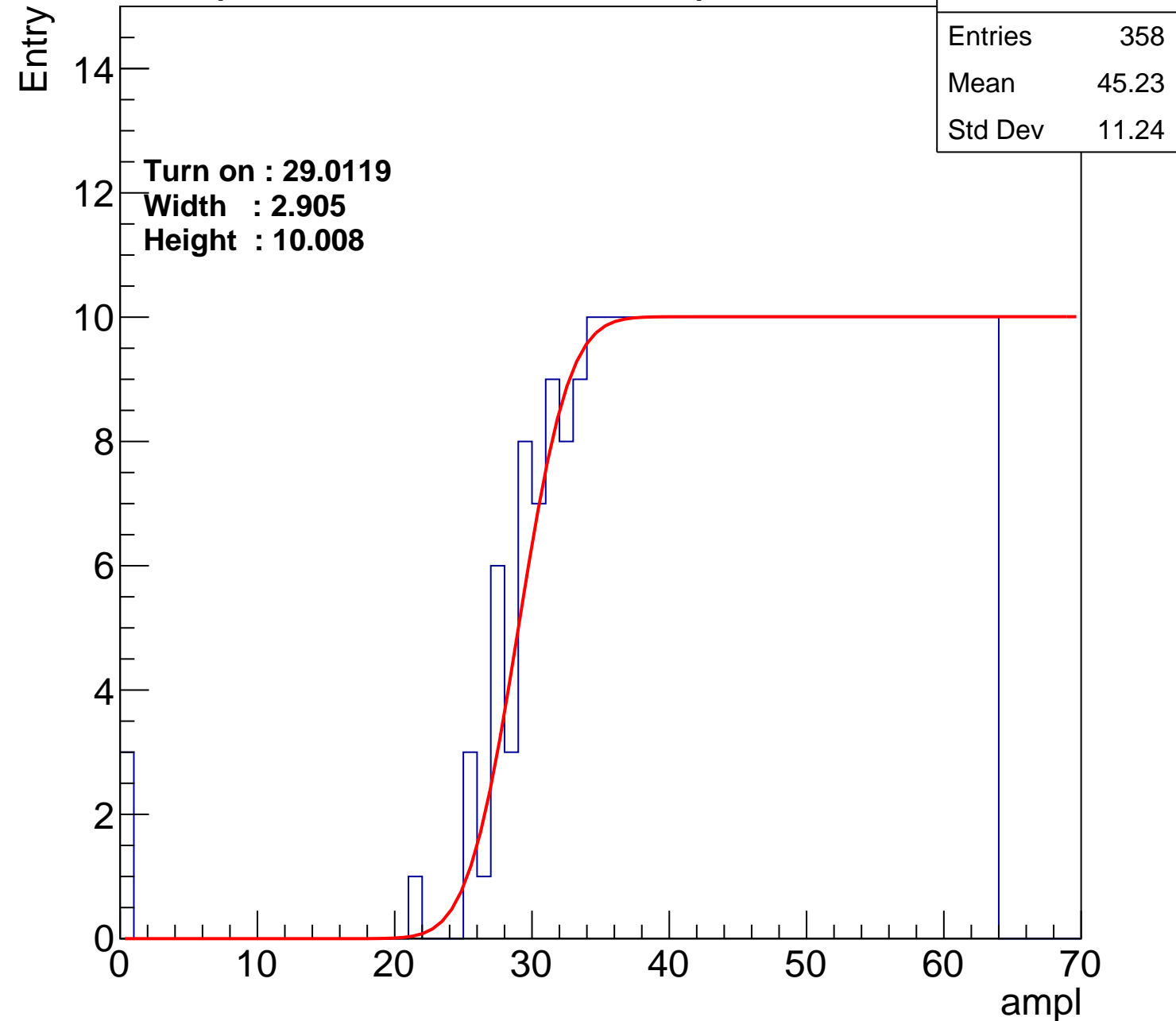
Width : 2.905

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch105

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.13
Std Dev	11.63

Turn on : 29.4046

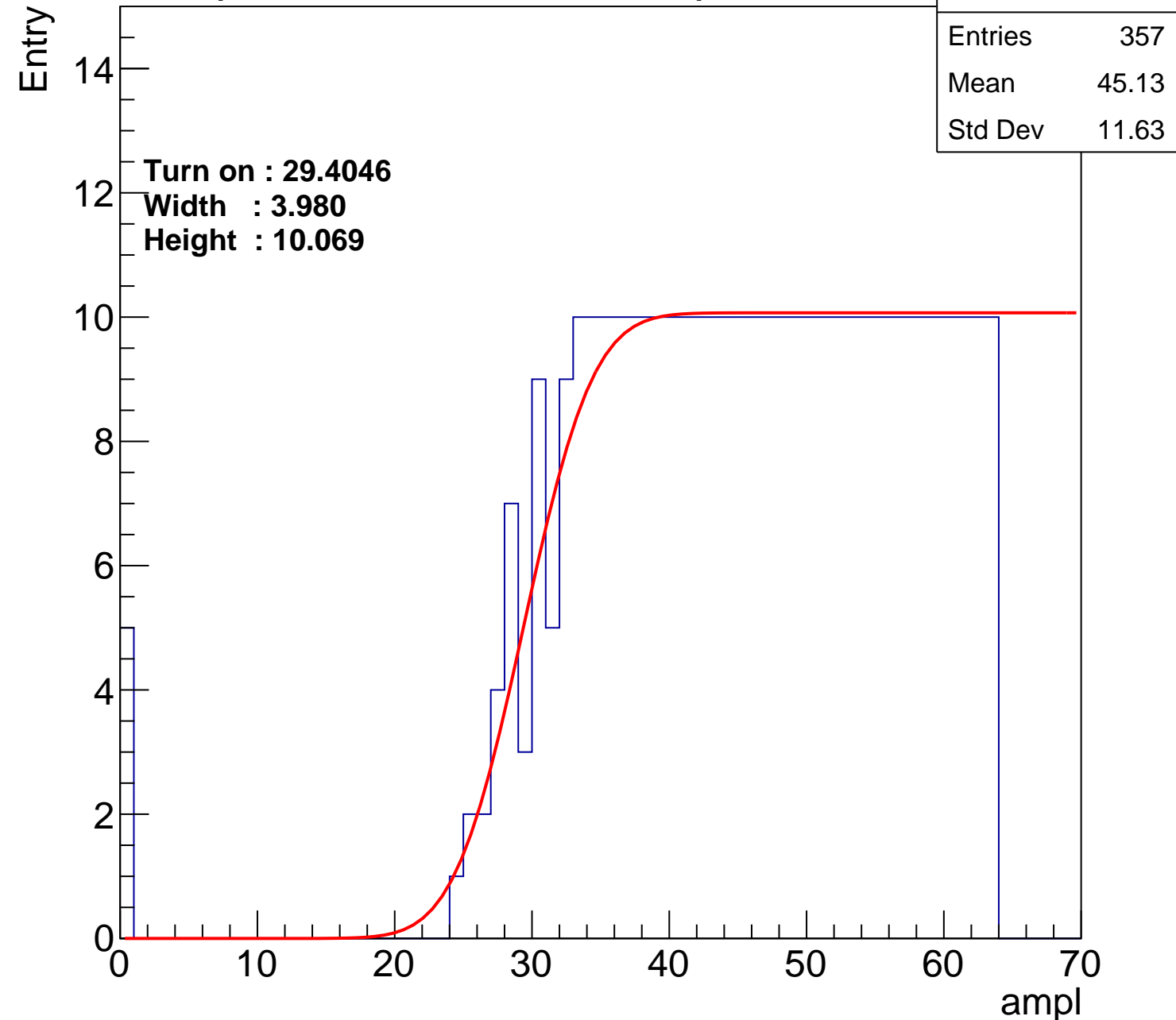
Width : 3.980

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch106

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.65
Std Dev	11.84

Turn on : 28.2386

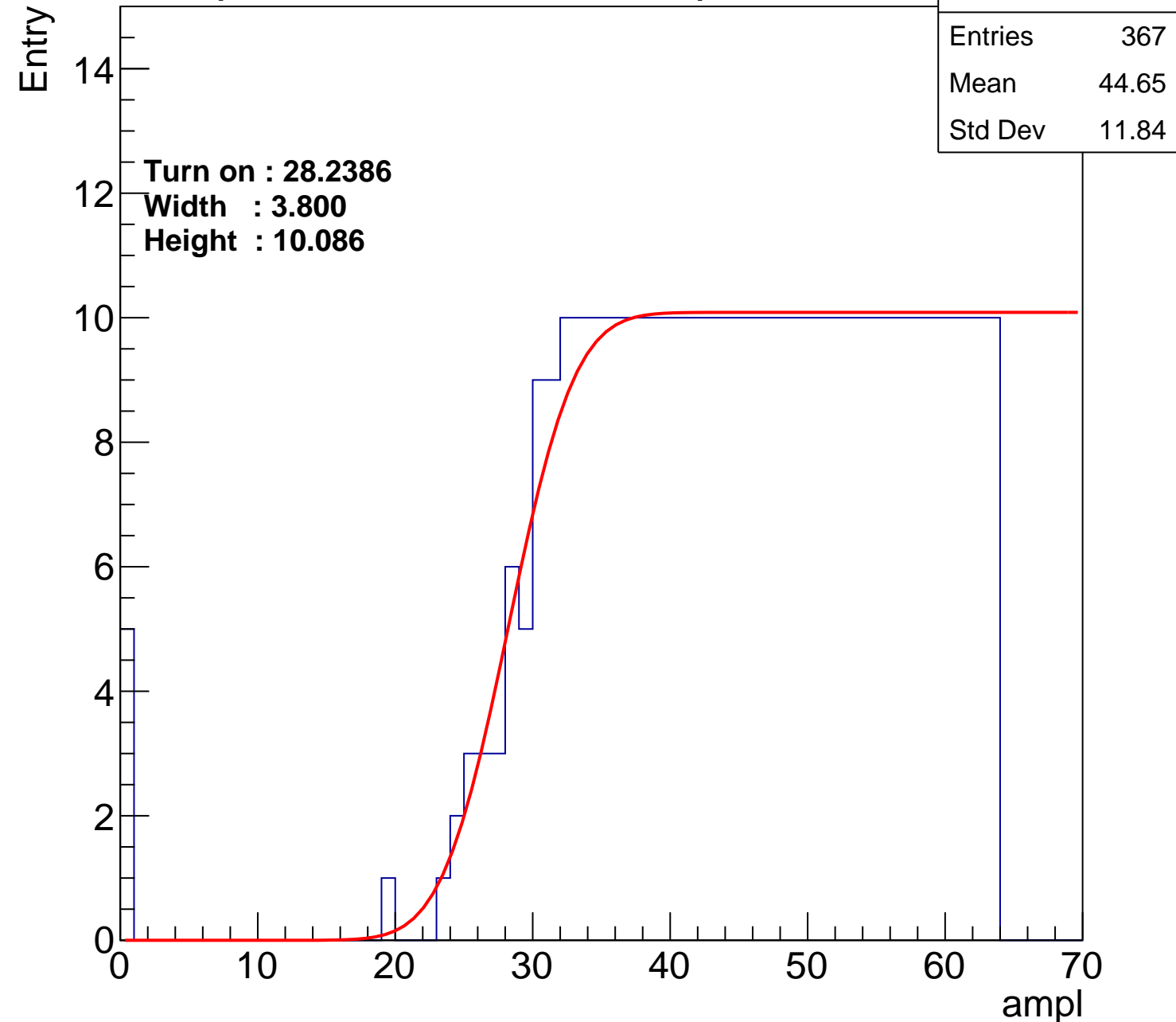
Width : 3.800

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch107

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.7
Std Dev	11.15

Turn on : 27.3927

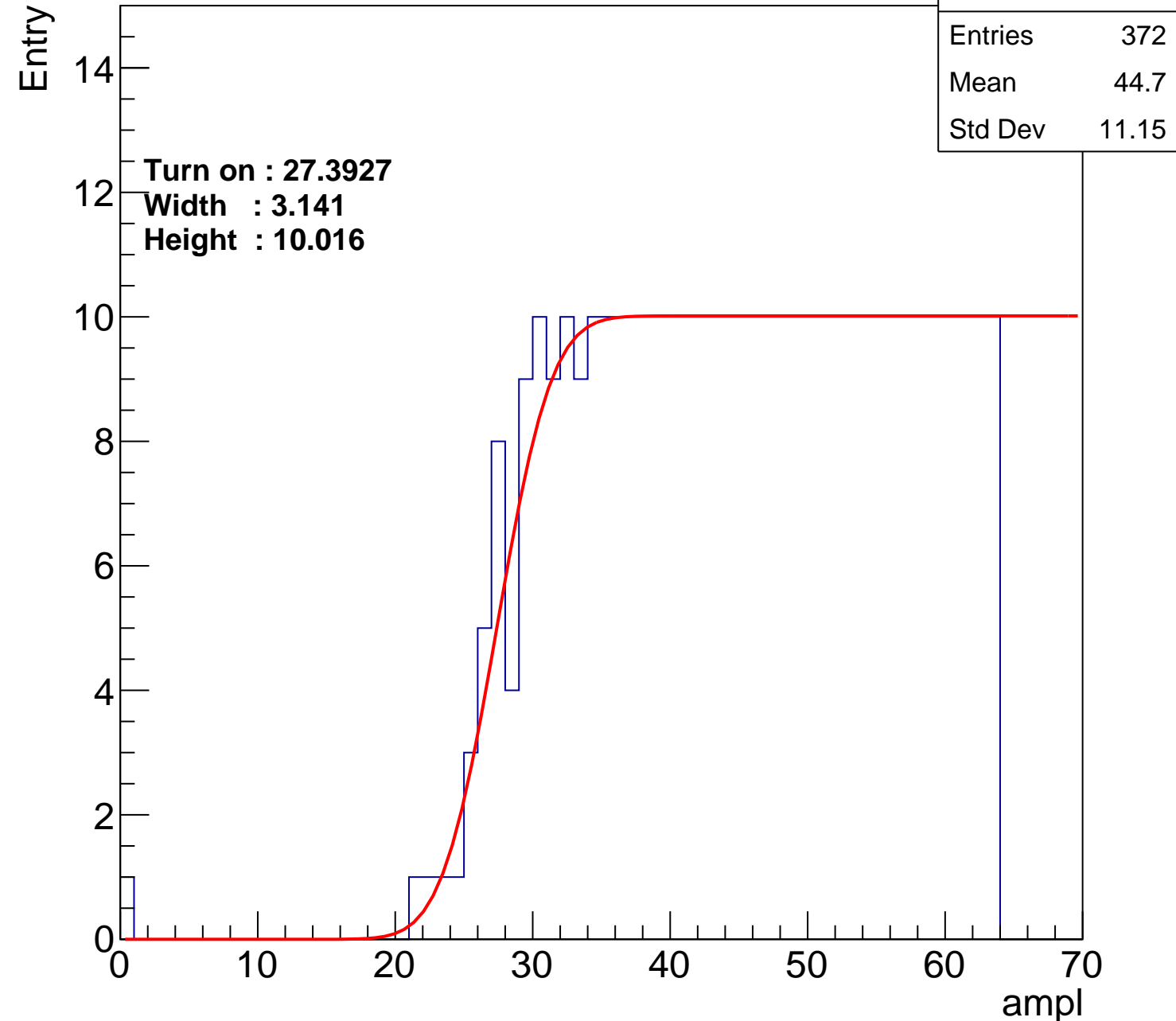
Width : 3.141

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch108

calib_packv5_042523_0143.root, FC#9, port A1

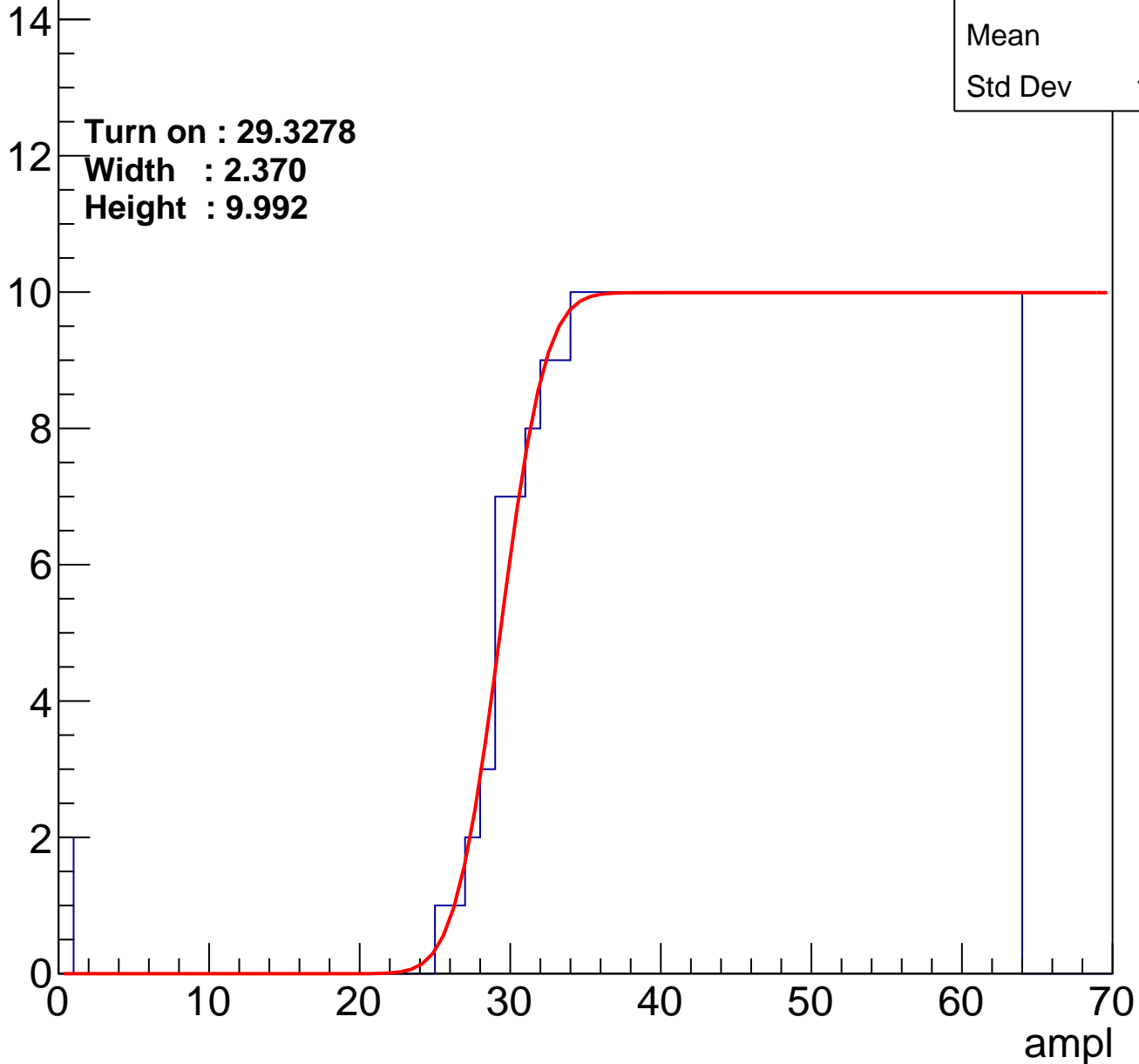
Entries	349
Mean	45.8
Std Dev	10.71

Turn on : 29.3278

Width : 2.370

Height : 9.992

Entry



B0L001S, U26-ch109

calib_packv5_042523_0143.root, FC#9, port A1

Entries	367
Mean	44.72
Std Dev	11.63

Turn on : 27.5198

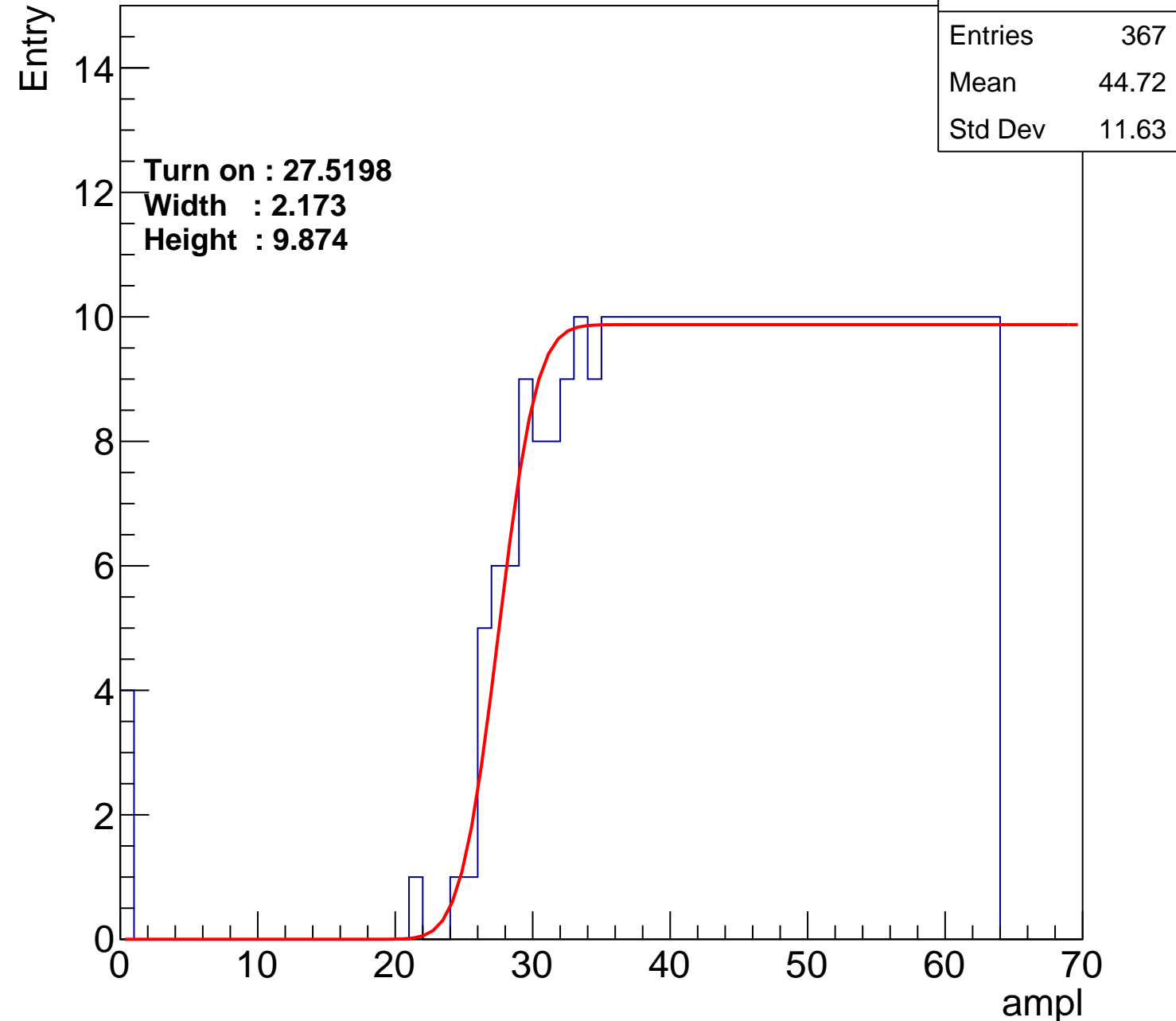
Width : 2.173

Height : 9.874

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch110

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.44
Std Dev	11.67

Turn on : 29.9553

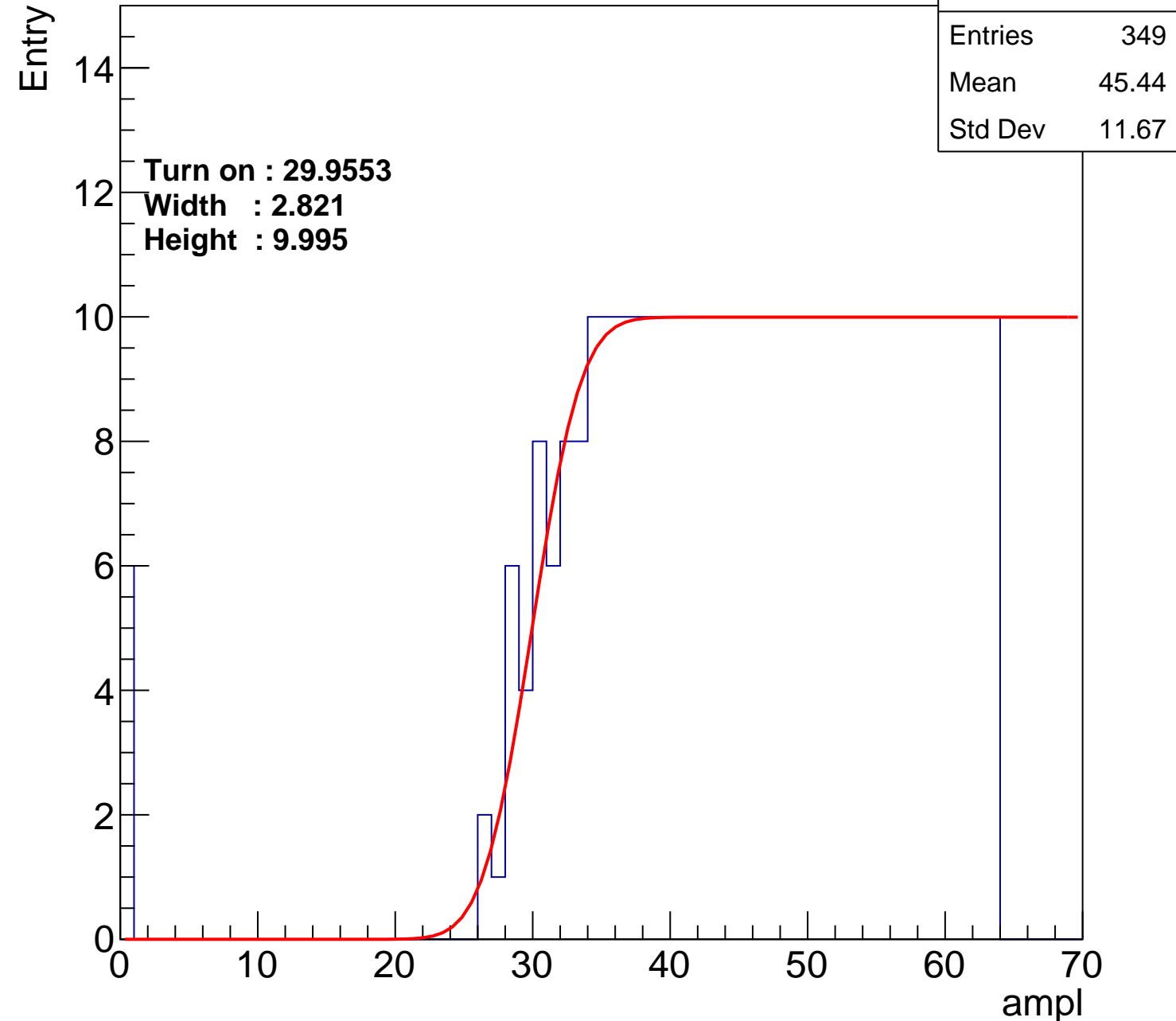
Width : 2.821

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch111

calib_packv5_042523_0143.root, FC#9, port A1

Entries	338
Mean	46.2
Std Dev	10.78

Turn on : 30.9211

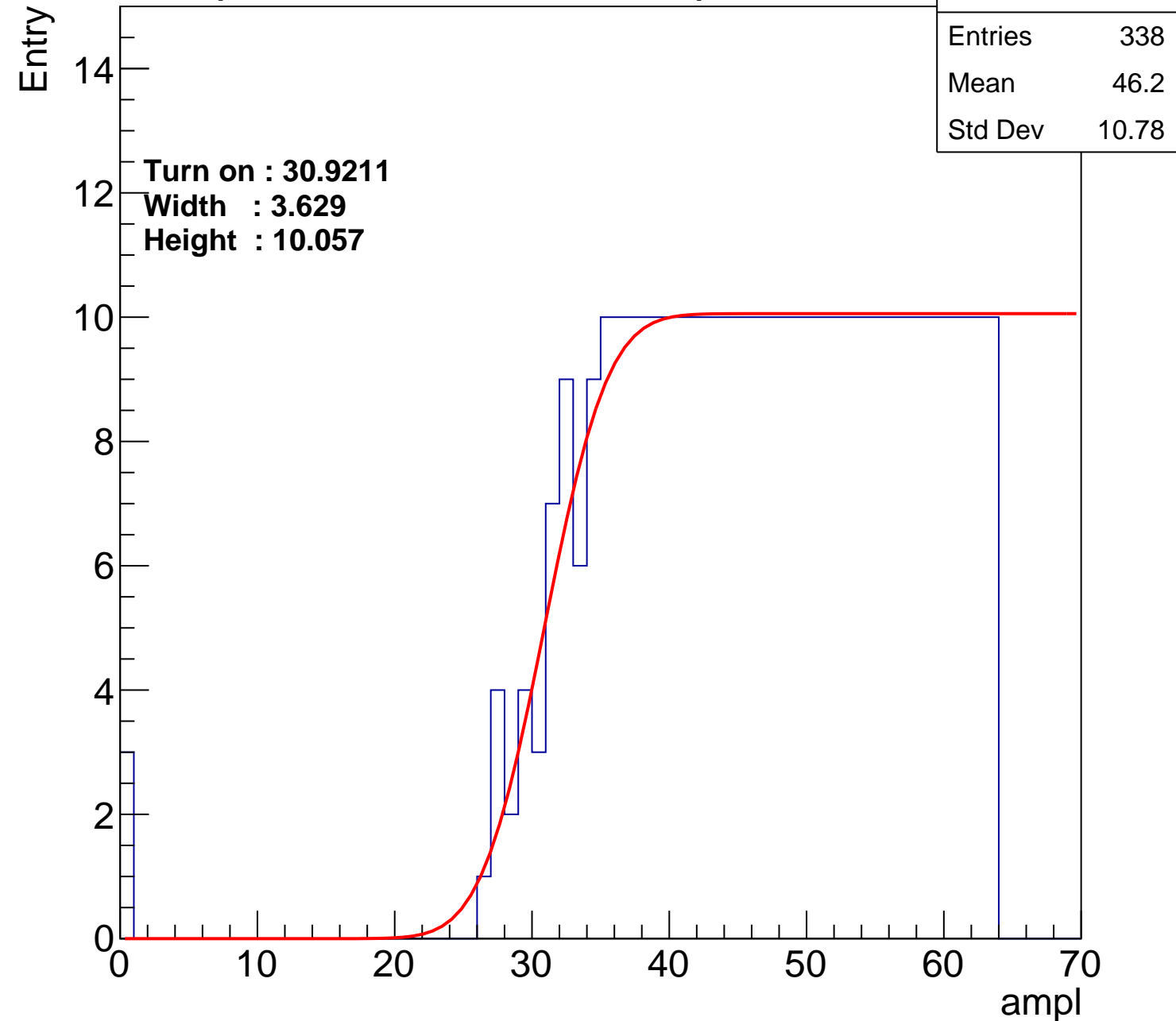
Width : 3.629

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch112

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.54
Std Dev	11.51

Turn on : 30.3604

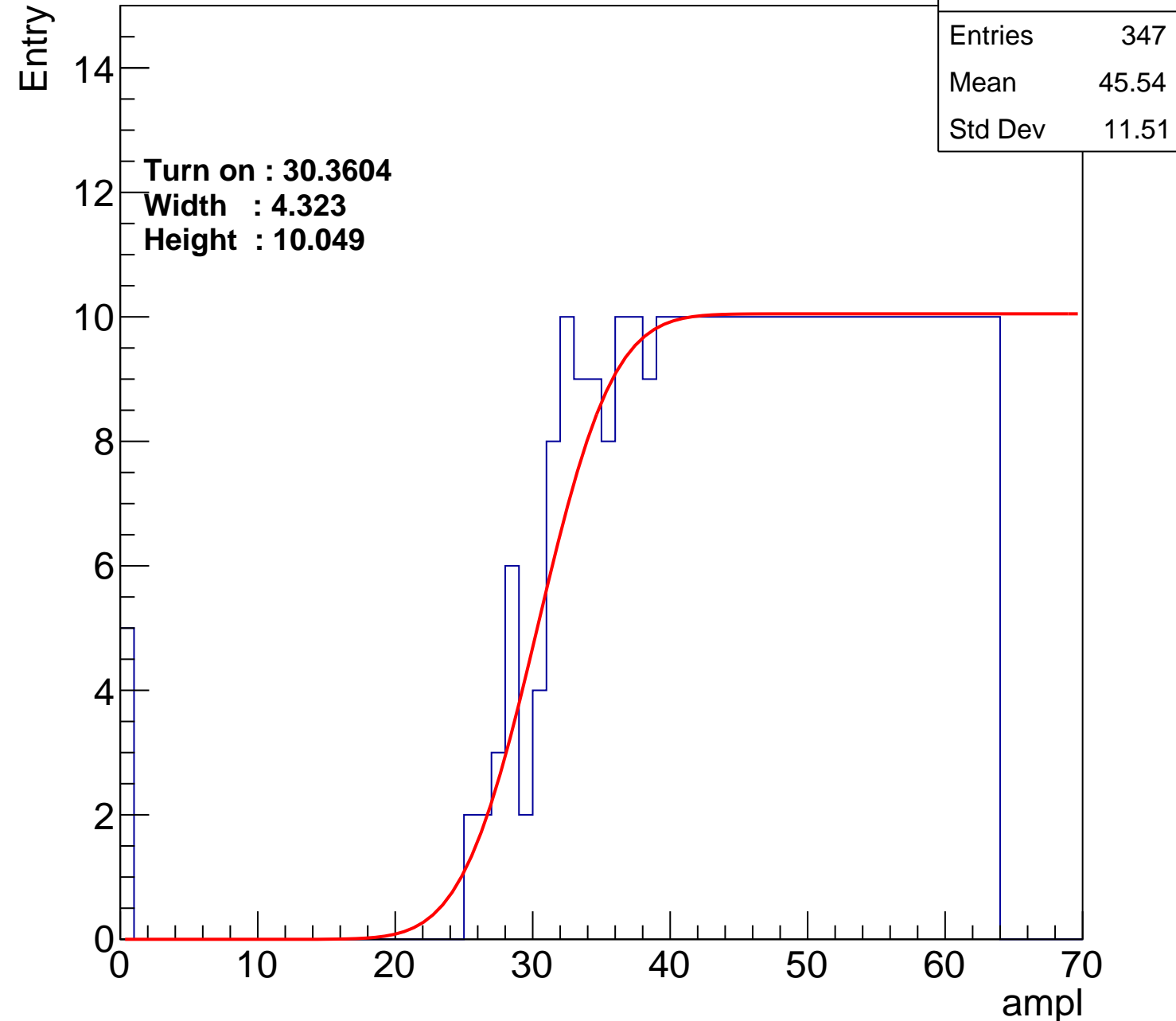
Width : 4.323

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch113

calib_packv5_042523_0143.root, FC#9, port A1

Entries	355
Mean	45.39
Std Dev	11.15

Turn on : 29.5370

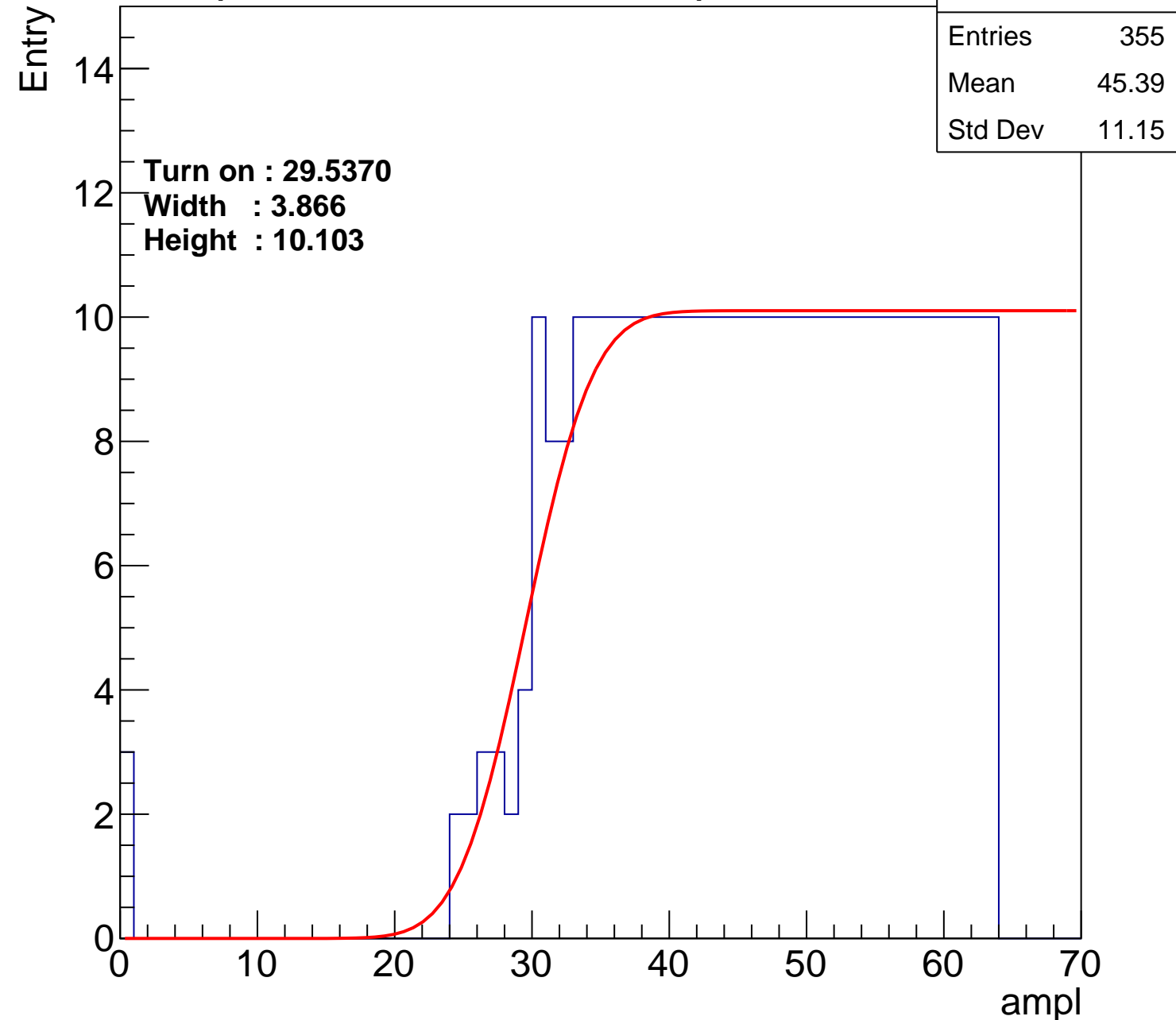
Width : 3.866

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch114

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.6
Std Dev	11.72

Turn on : 27.9191

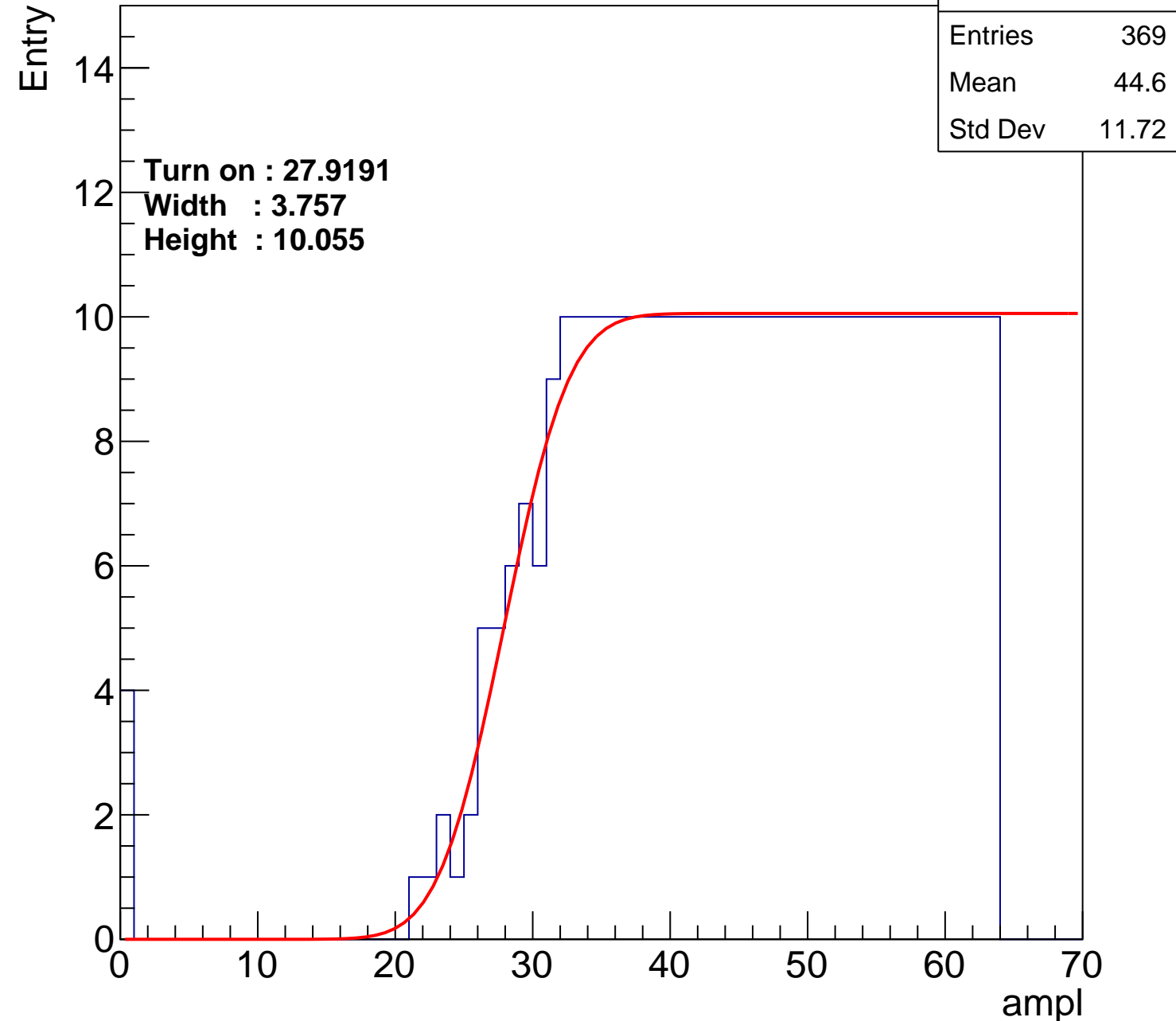
Width : 3.757

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch115

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.09
Std Dev	11.24

Turn on : 28.2531

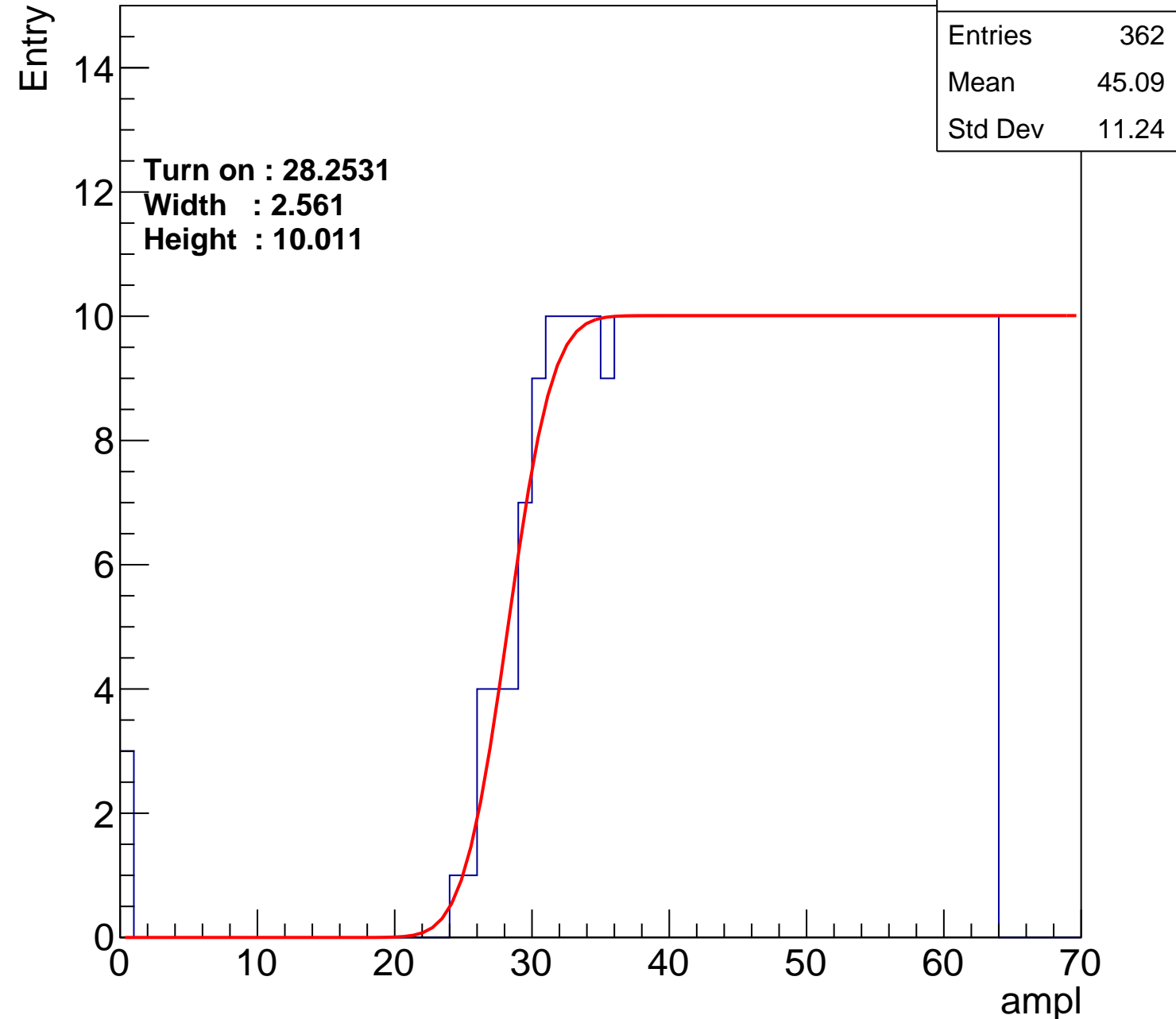
Width : 2.561

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch116

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	44.9
Std Dev	11.71

Turn on : 27.3527

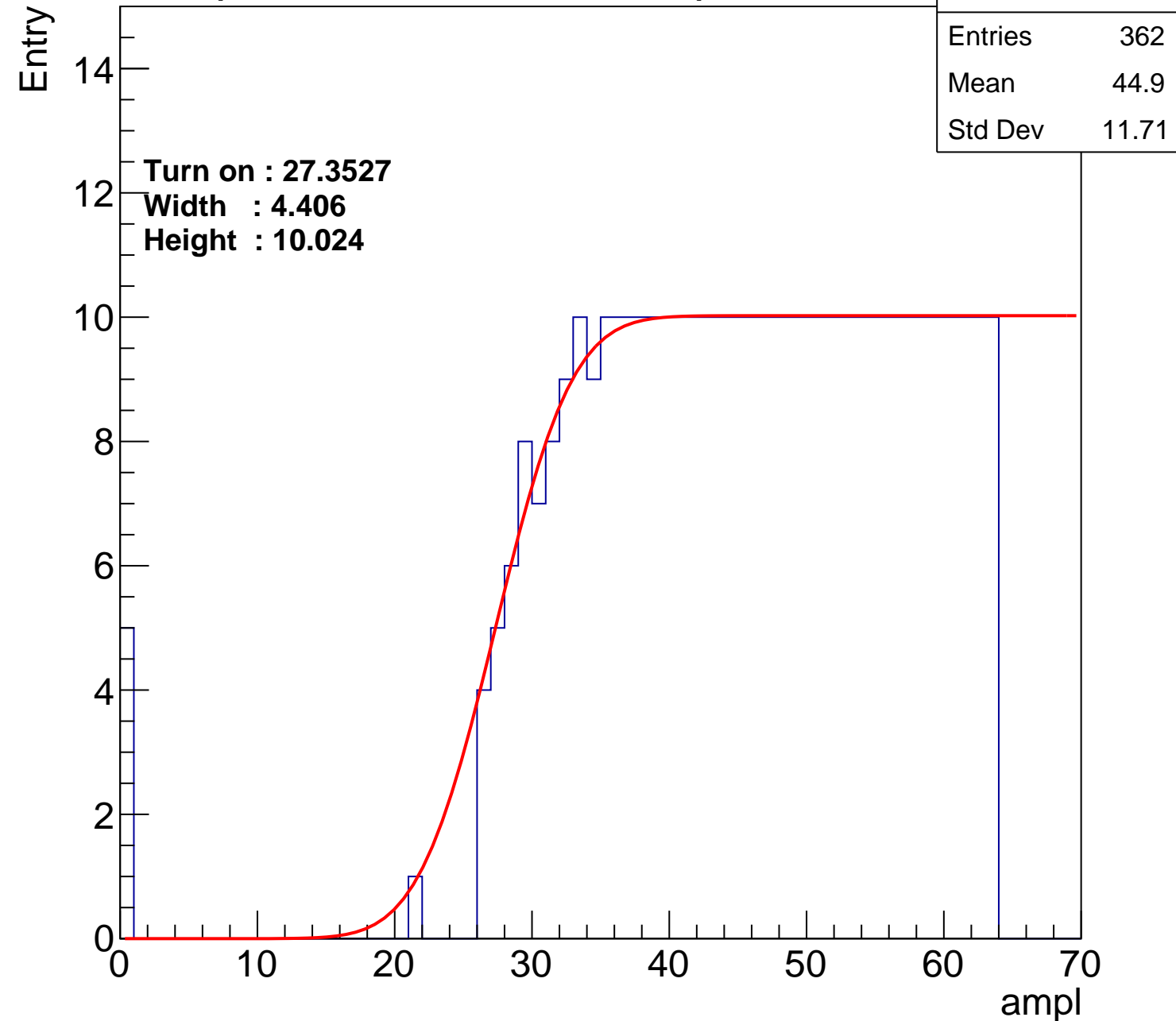
Width : 4.406

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch117

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.41
Std Dev	10.9

Turn on : 28.6095

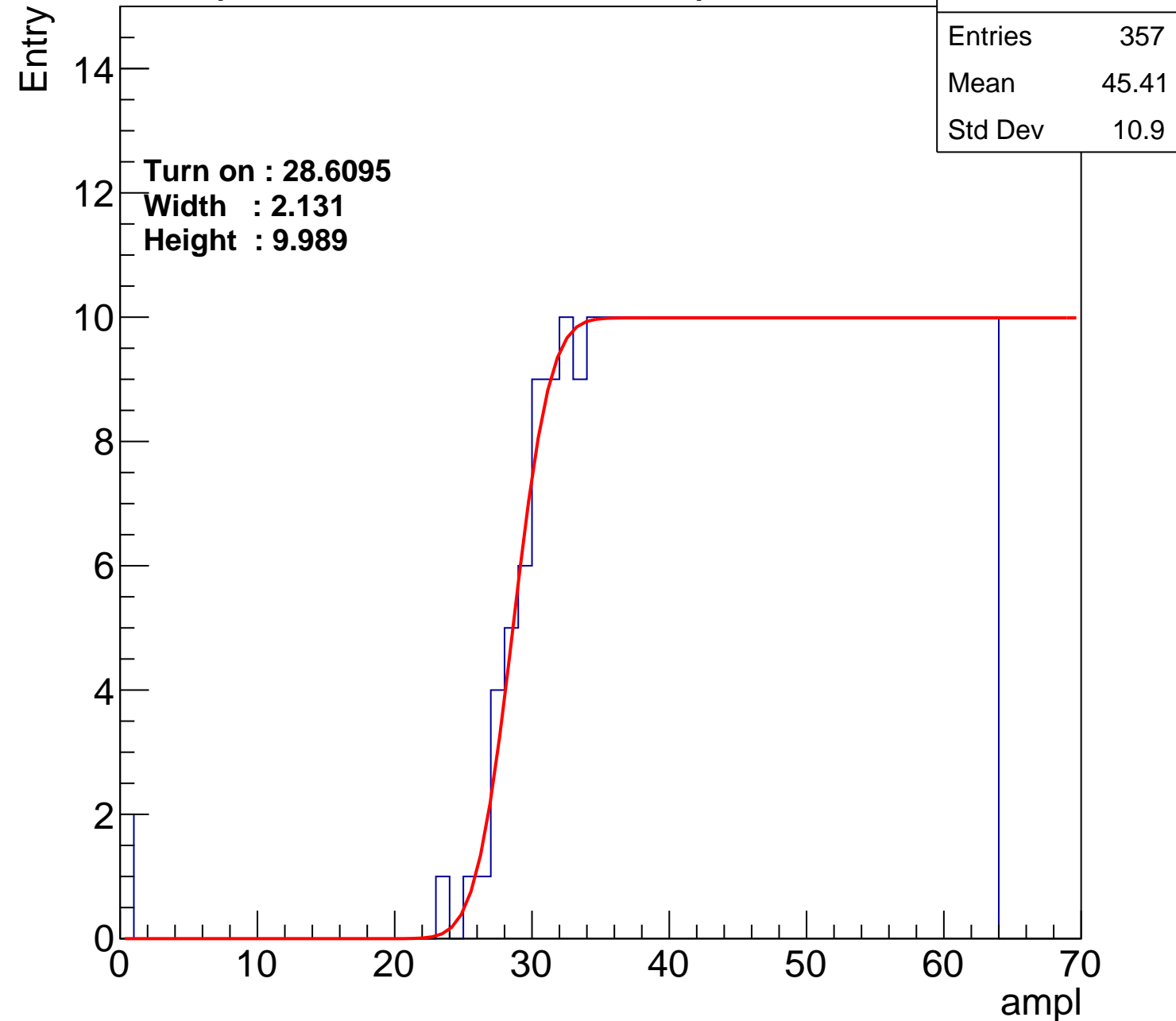
Width : 2.131

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch118

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	45.99
Std Dev	10.5

Turn on : 30.0655

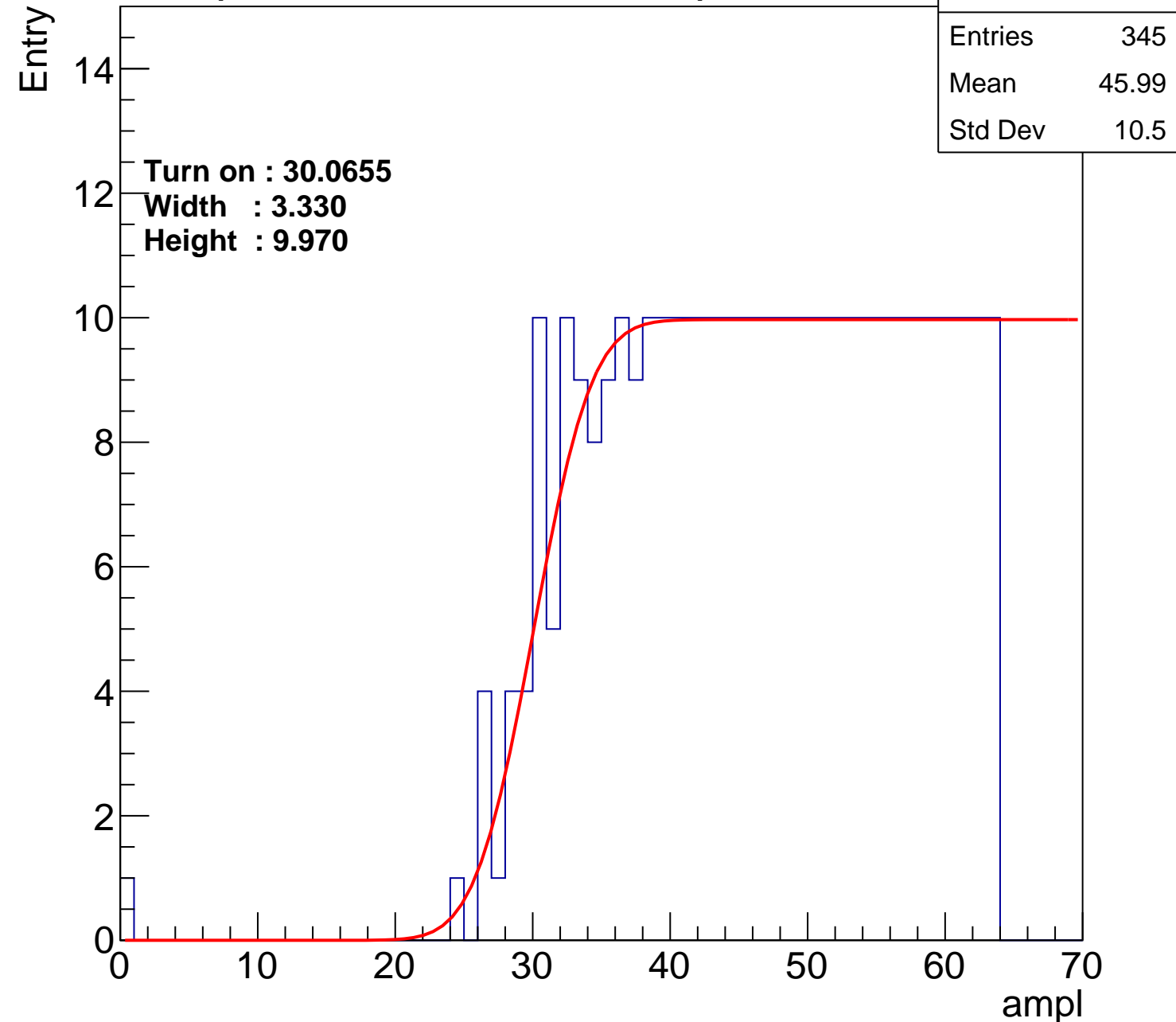
Width : 3.330

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch119

calib_packv5_042523_0143.root, FC#9, port A1

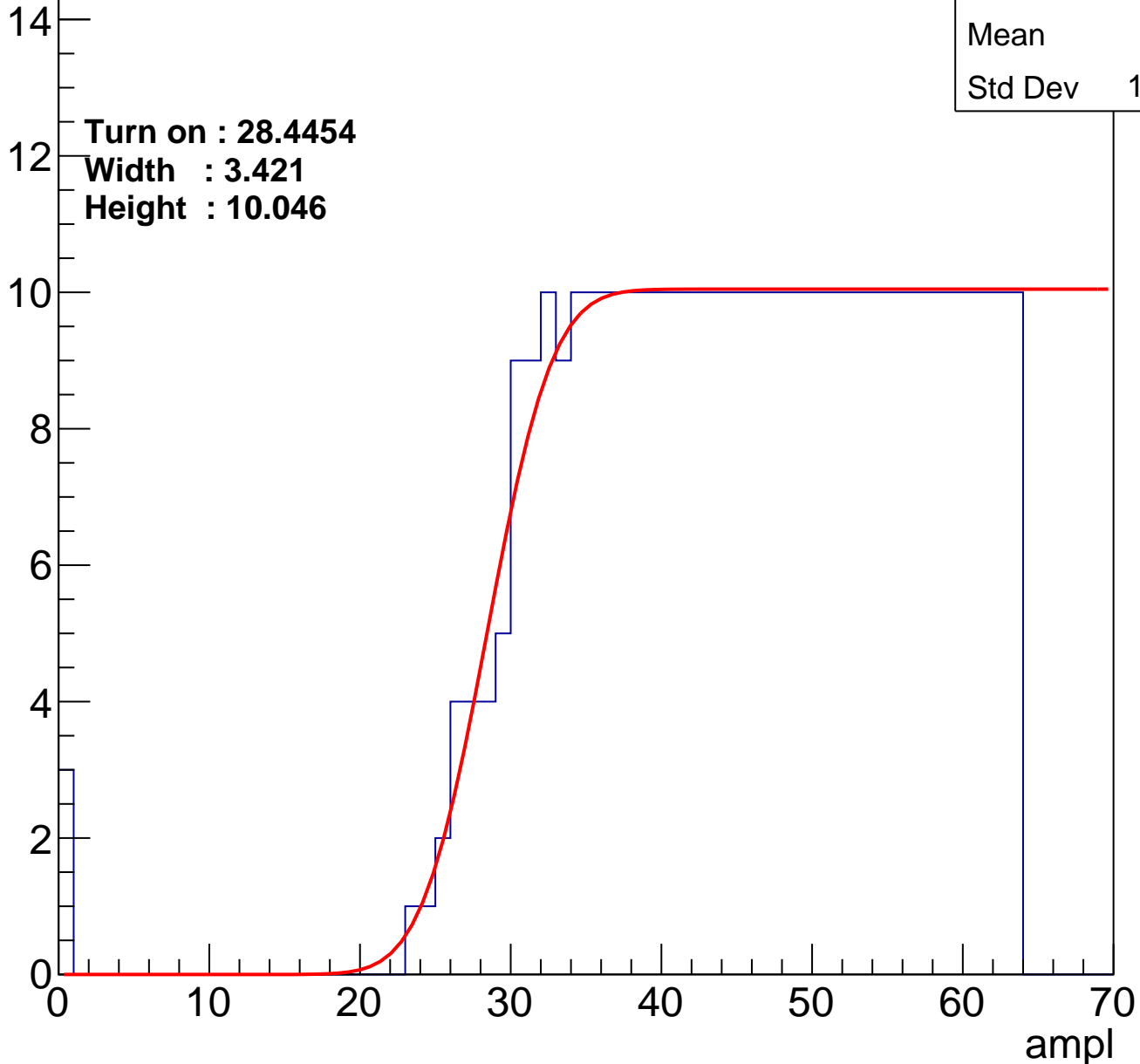
Entries	361
Mean	45.1
Std Dev	11.28

Turn on : 28.4454

Width : 3.421

Height : 10.046

Entry



B0L001S, U26-ch120

calib_packv5_042523_0143.root, FC#9, port A1

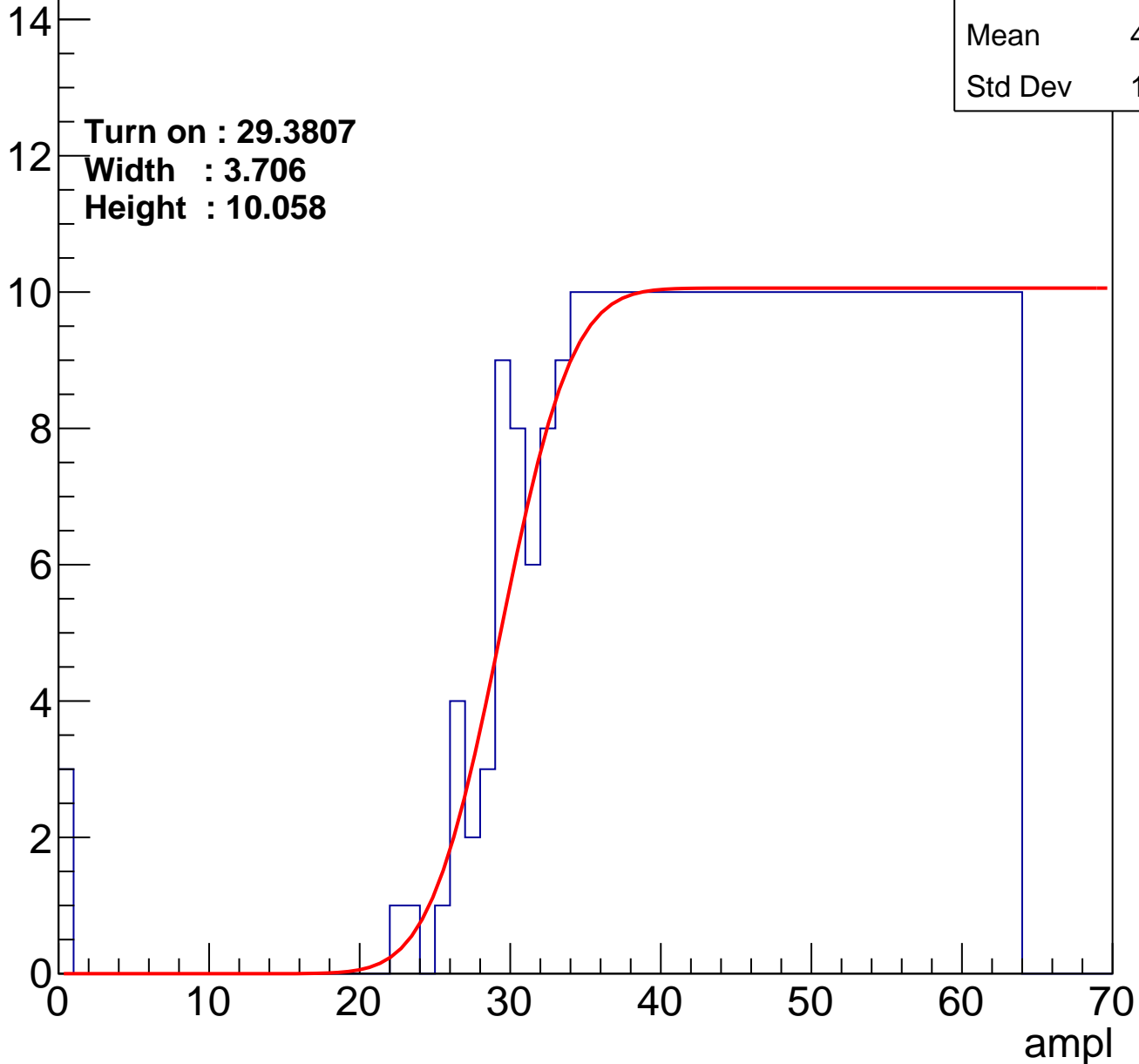
Entries	355
Mean	45.36
Std Dev	11.19

Turn on : 29.3807

Width : 3.706

Height : 10.058

Entry



B0L001S, U26-ch121

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.4
Std Dev	10.98

Turn on : 29.4838

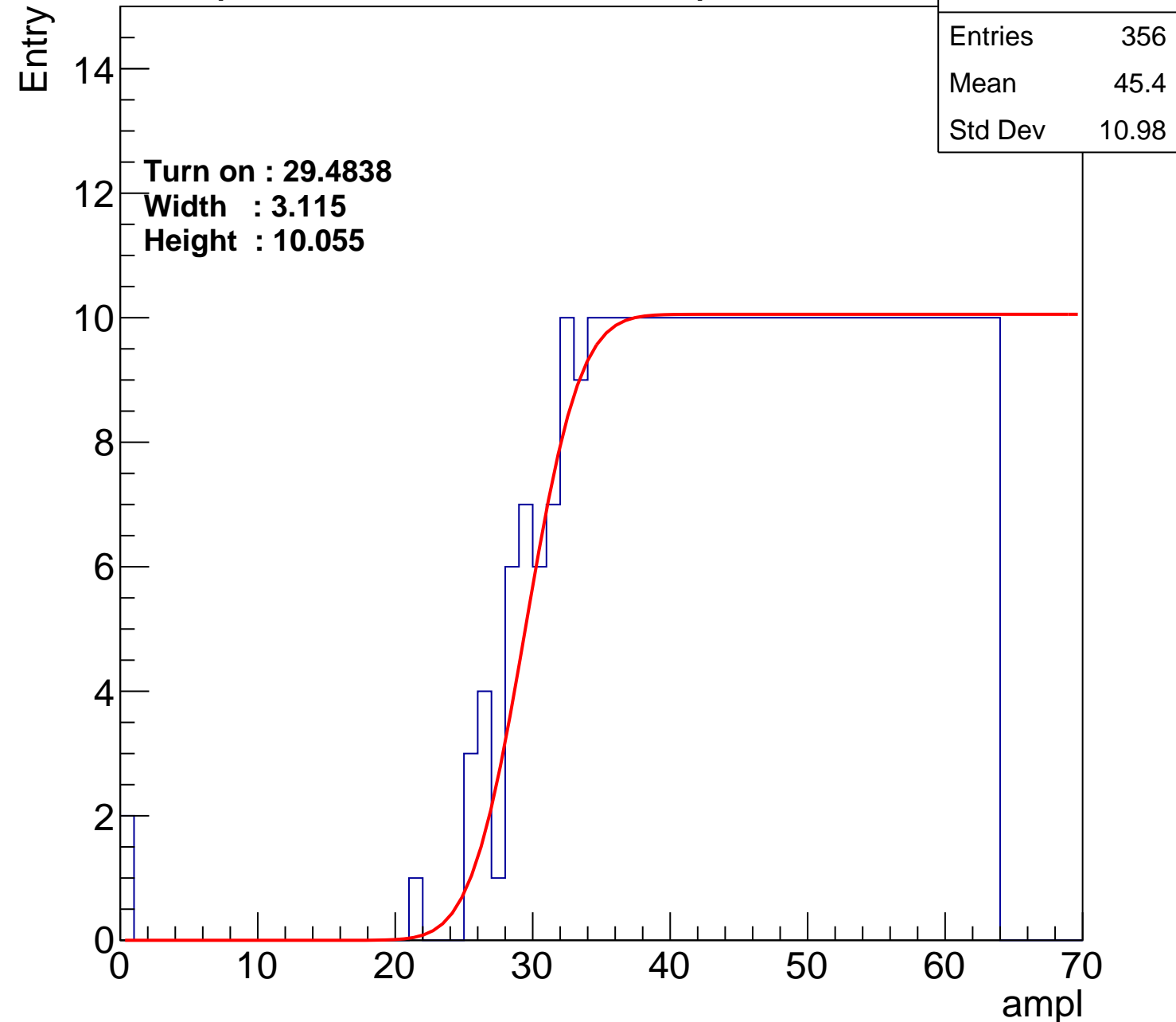
Width : 3.115

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch122

calib_packv5_042523_0143.root, FC#9, port A1

Entries	350
Mean	45.61
Std Dev	11.07

Turn on : 30.0610

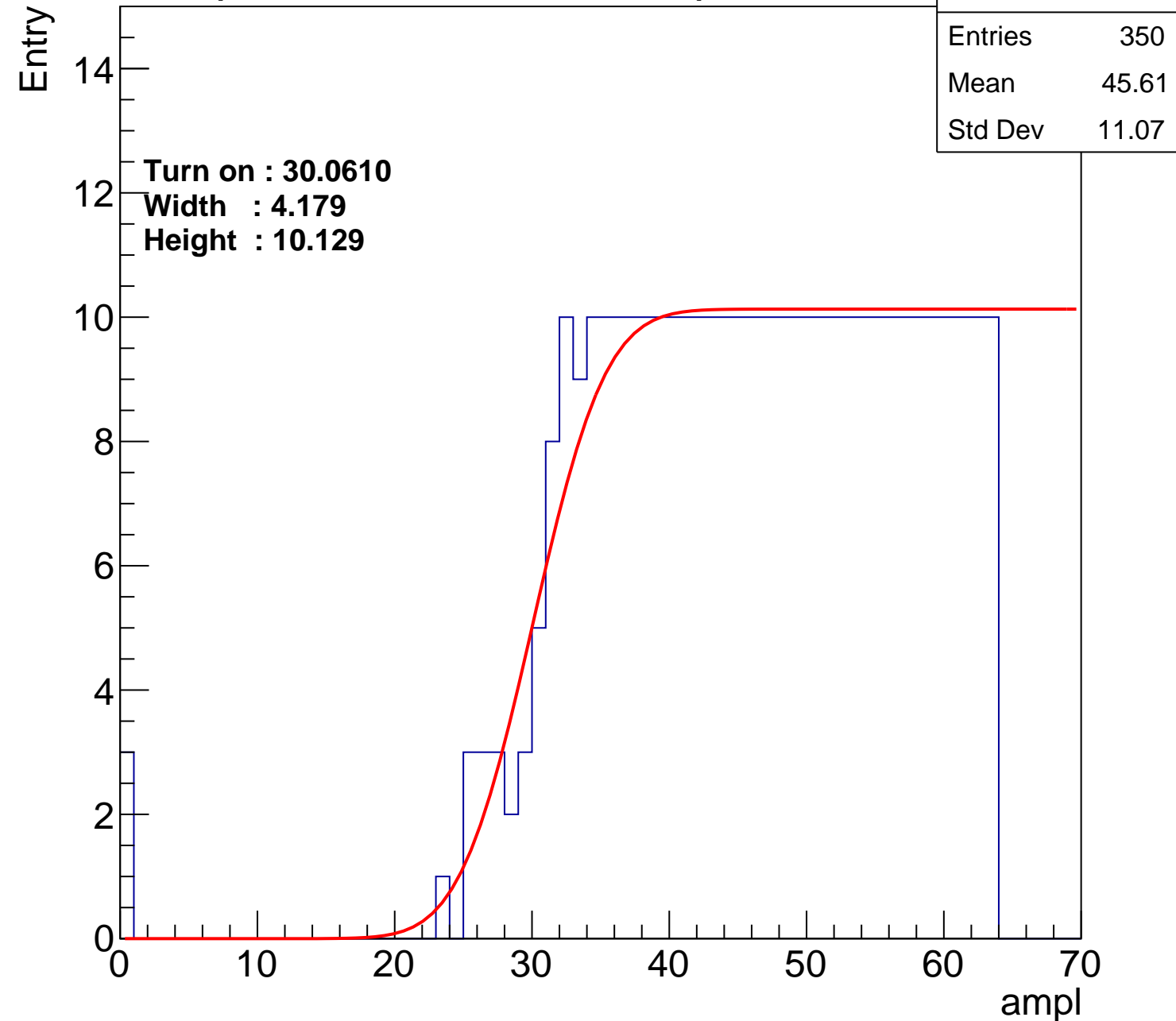
Width : 4.179

Height : 10.129

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch123

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.81
Std Dev	10.58

Turn on : 29.1683

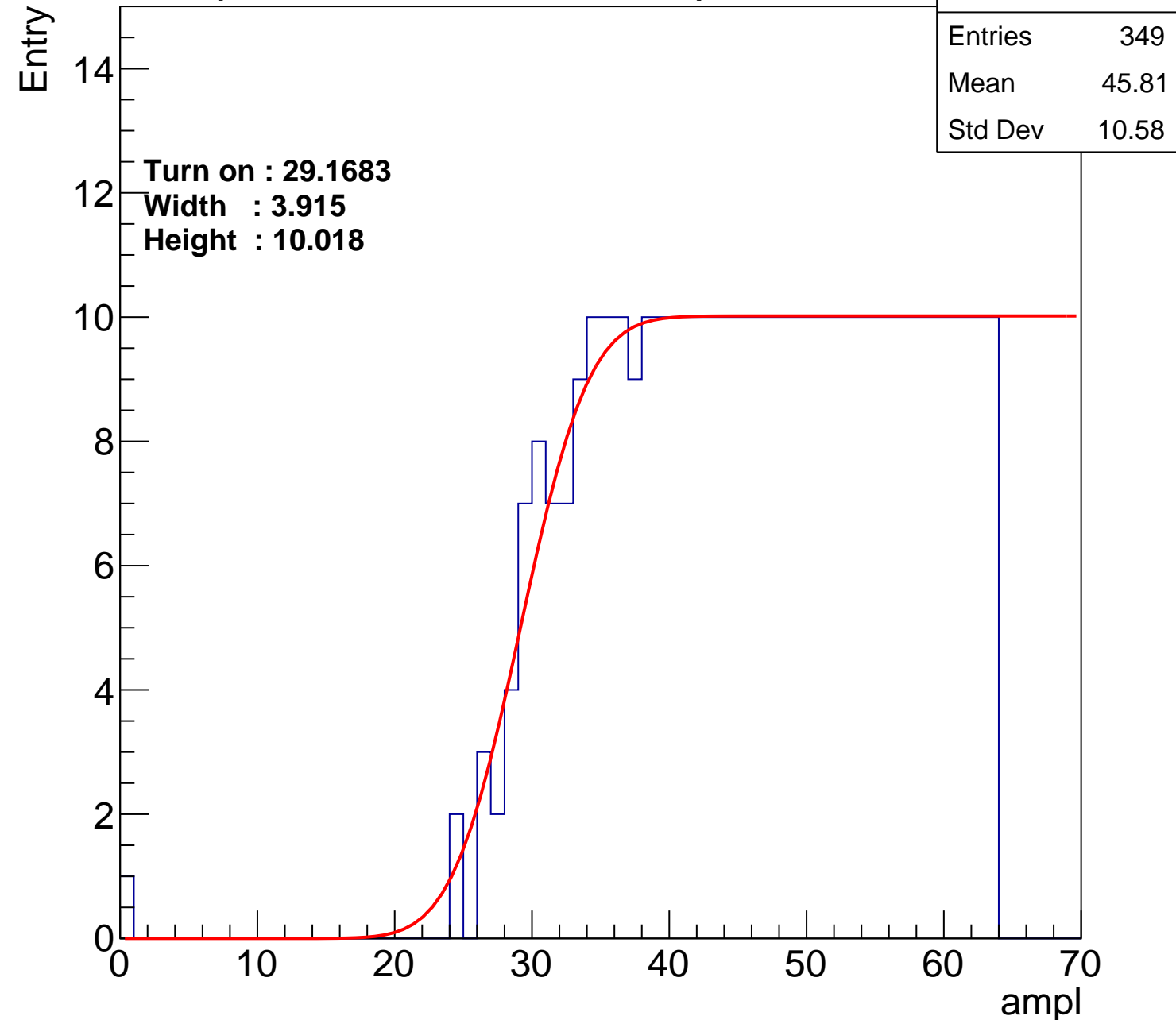
Width : 3.915

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch124

calib_packv5_042523_0143.root, FC#9, port A1

Entries	342
Mean	46.01
Std Dev	10.87

Turn on : 30.3834

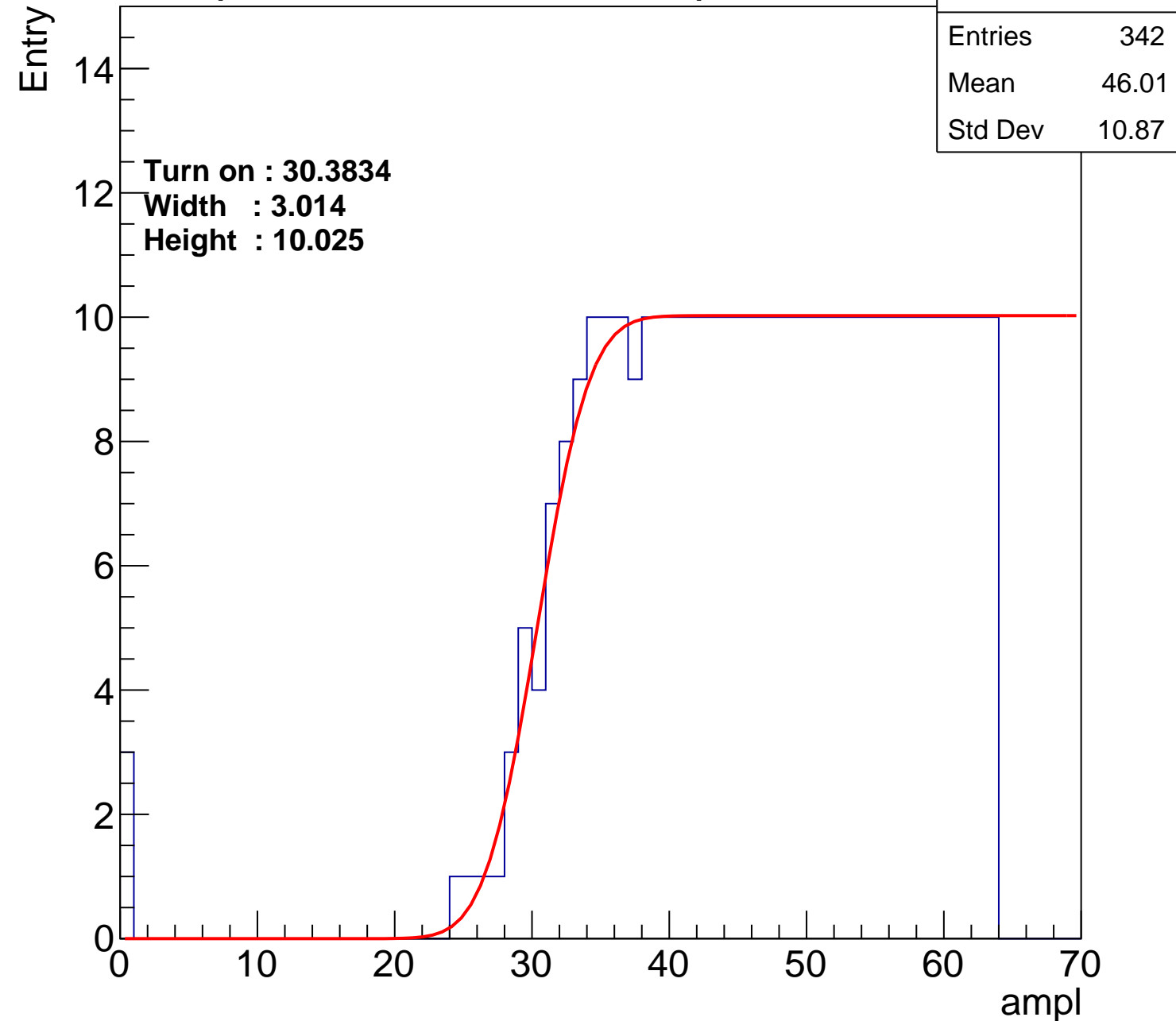
Width : 3.014

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch125

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.28
Std Dev	11

Turn on : 28.6104

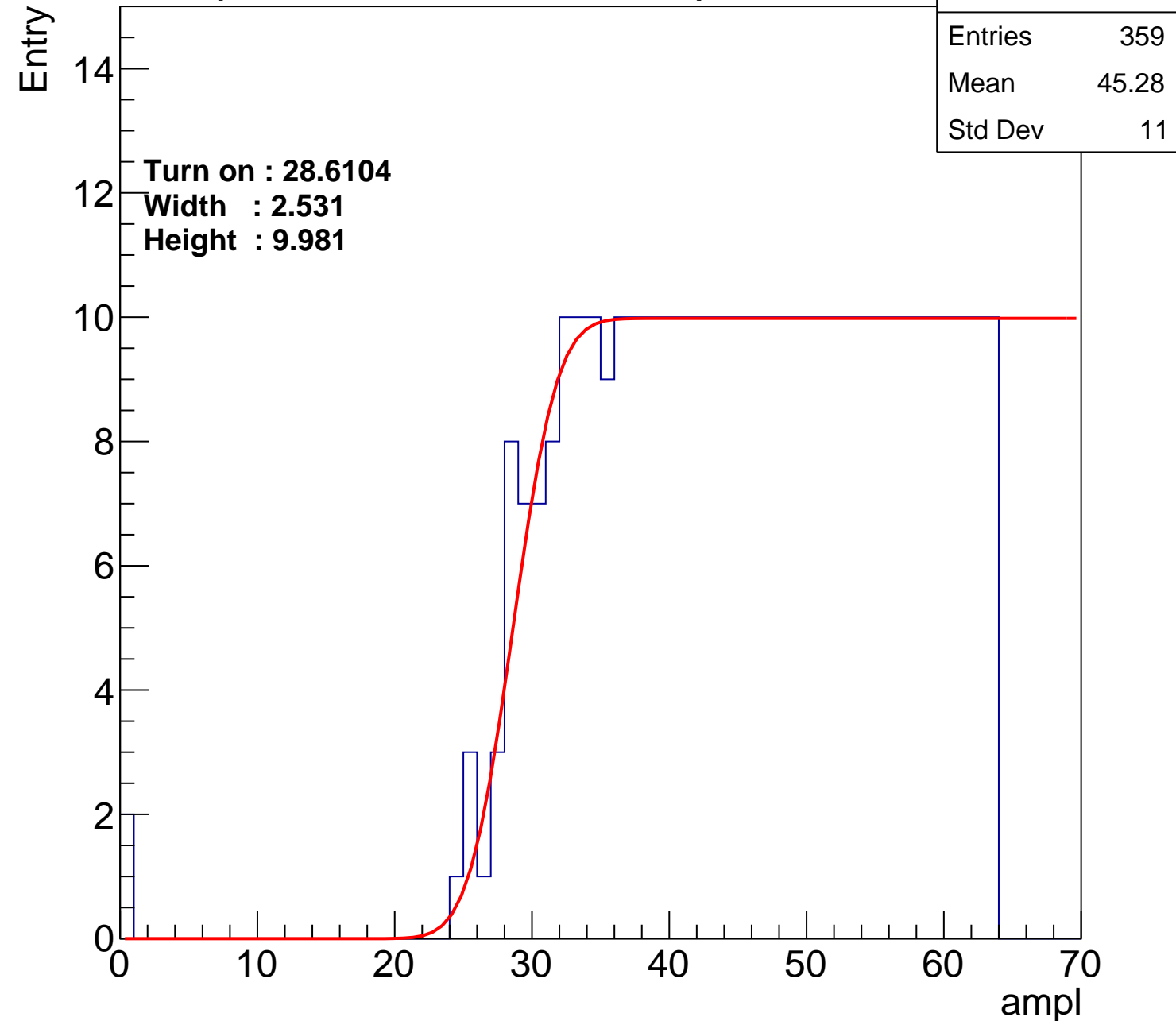
Width : 2.531

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch126

calib_packv5_042523_0143.root, FC#9, port A1

Entries	352
Mean	45.63
Std Dev	10.83

Turn on : 29.2168

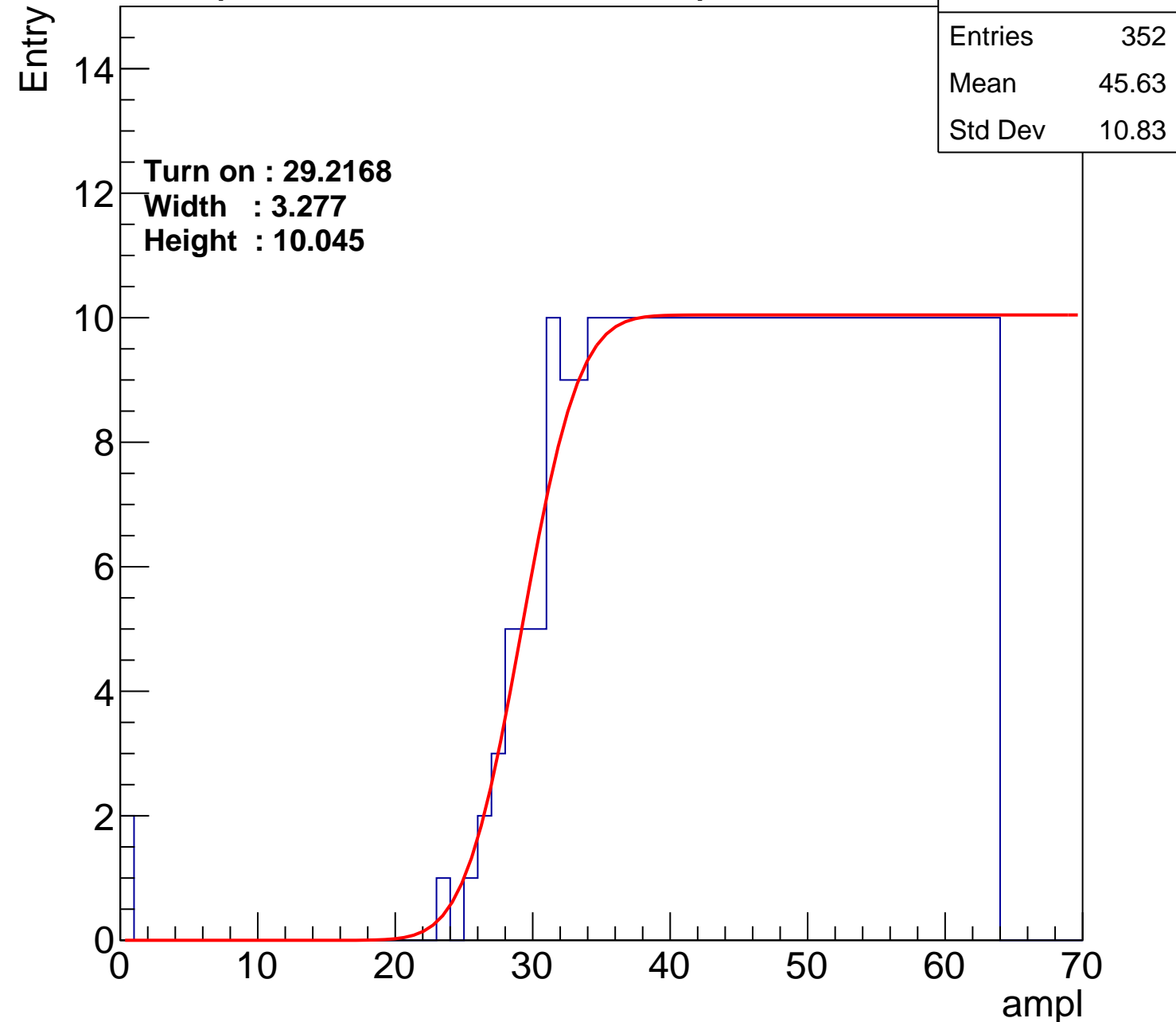
Width : 3.277

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U26-ch127

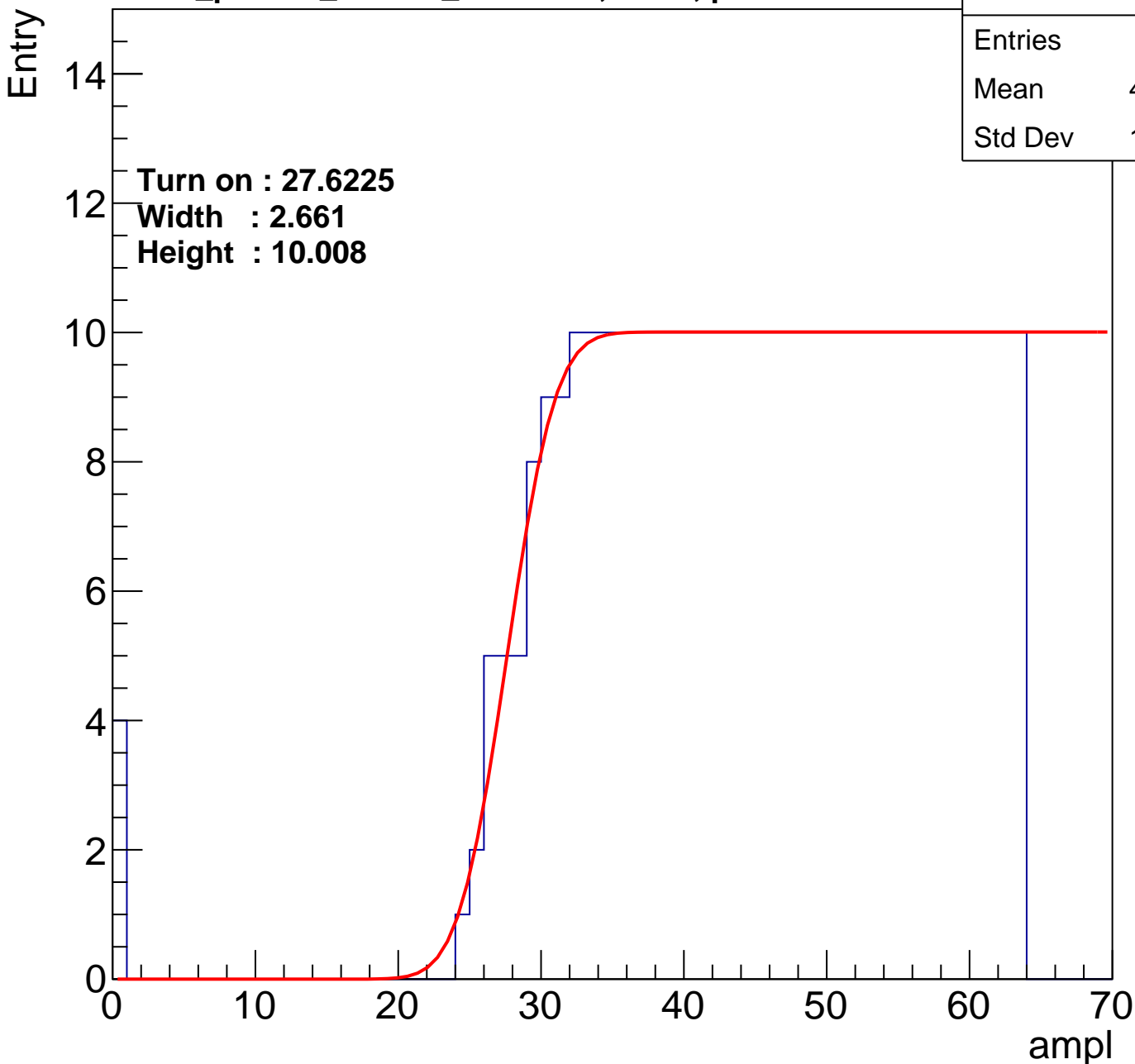
calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.6225

Width : 2.661

Height : 10.008



B0L001S, U26-ch127

calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.6225

Width : 2.661

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl

