

B1L101S, U4-ch0, adc0

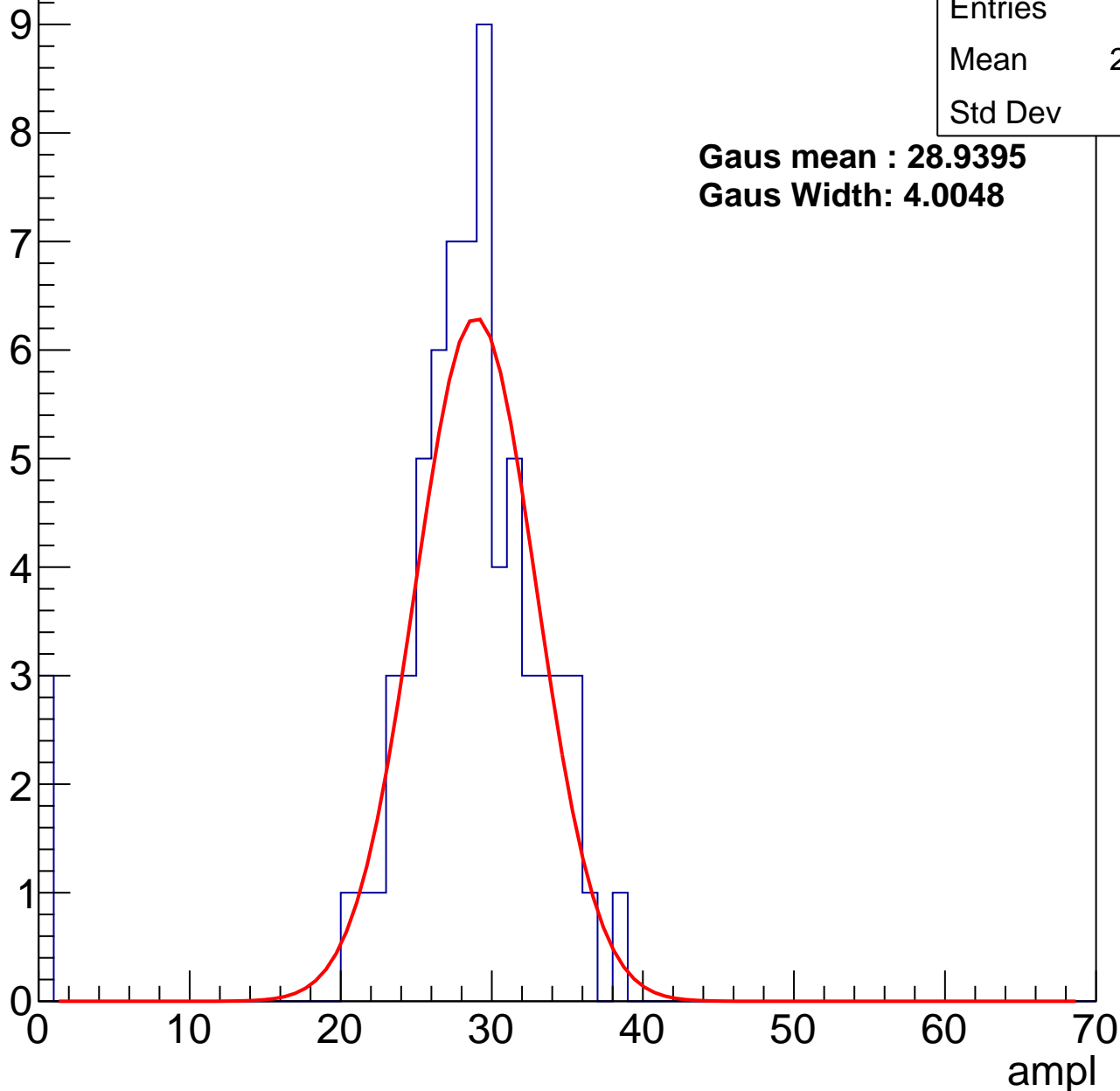
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	27.28
Std Dev	6.89

Gaus mean : 28.9395

Gaus Width: 4.0048



B1L101S, U4-ch0, adc1

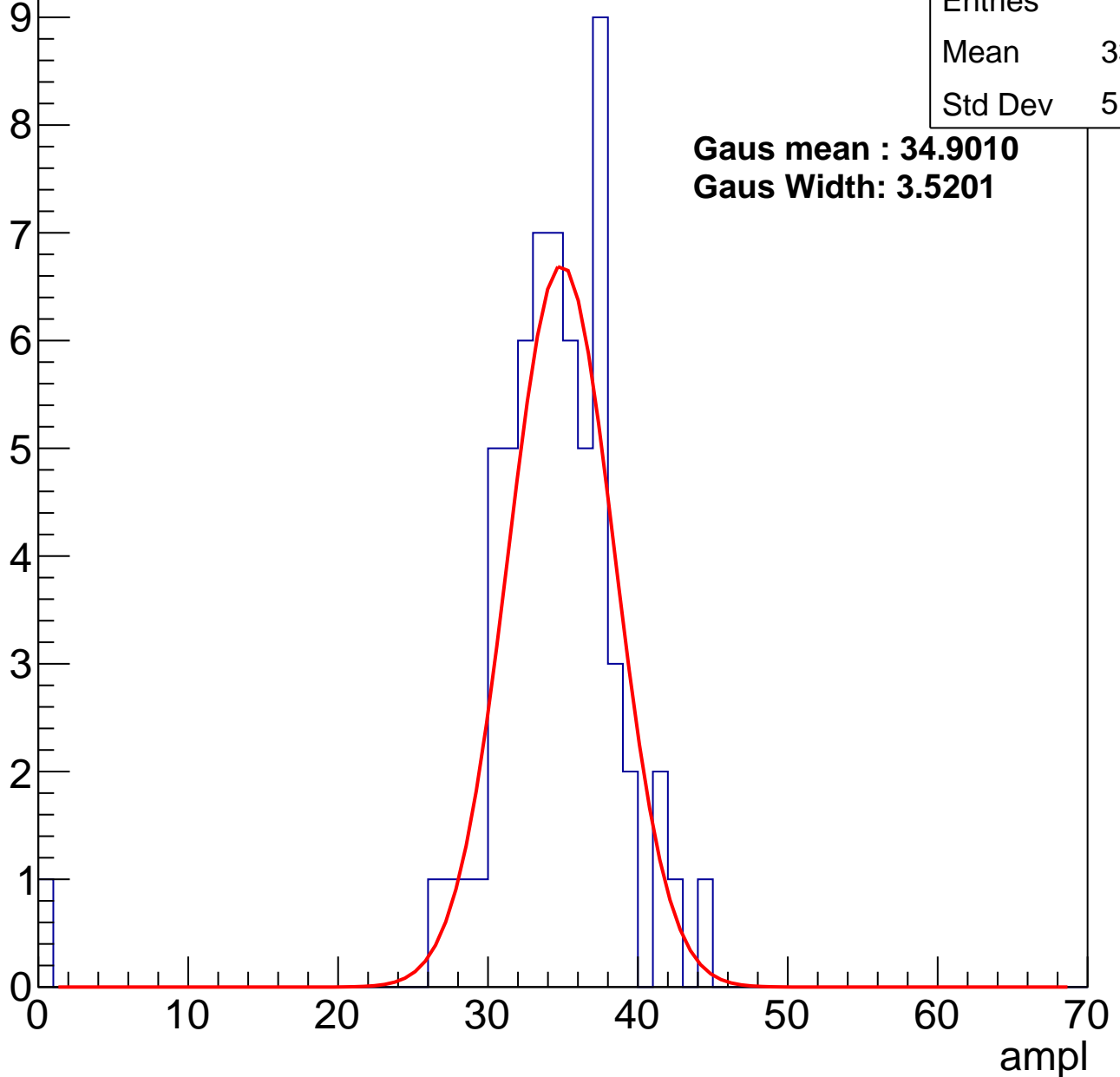
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	33.73
Std Dev	5.518

Gaus mean : 34.9010

Gaus Width: 3.5201



B1L101S, U4-ch0, adc2

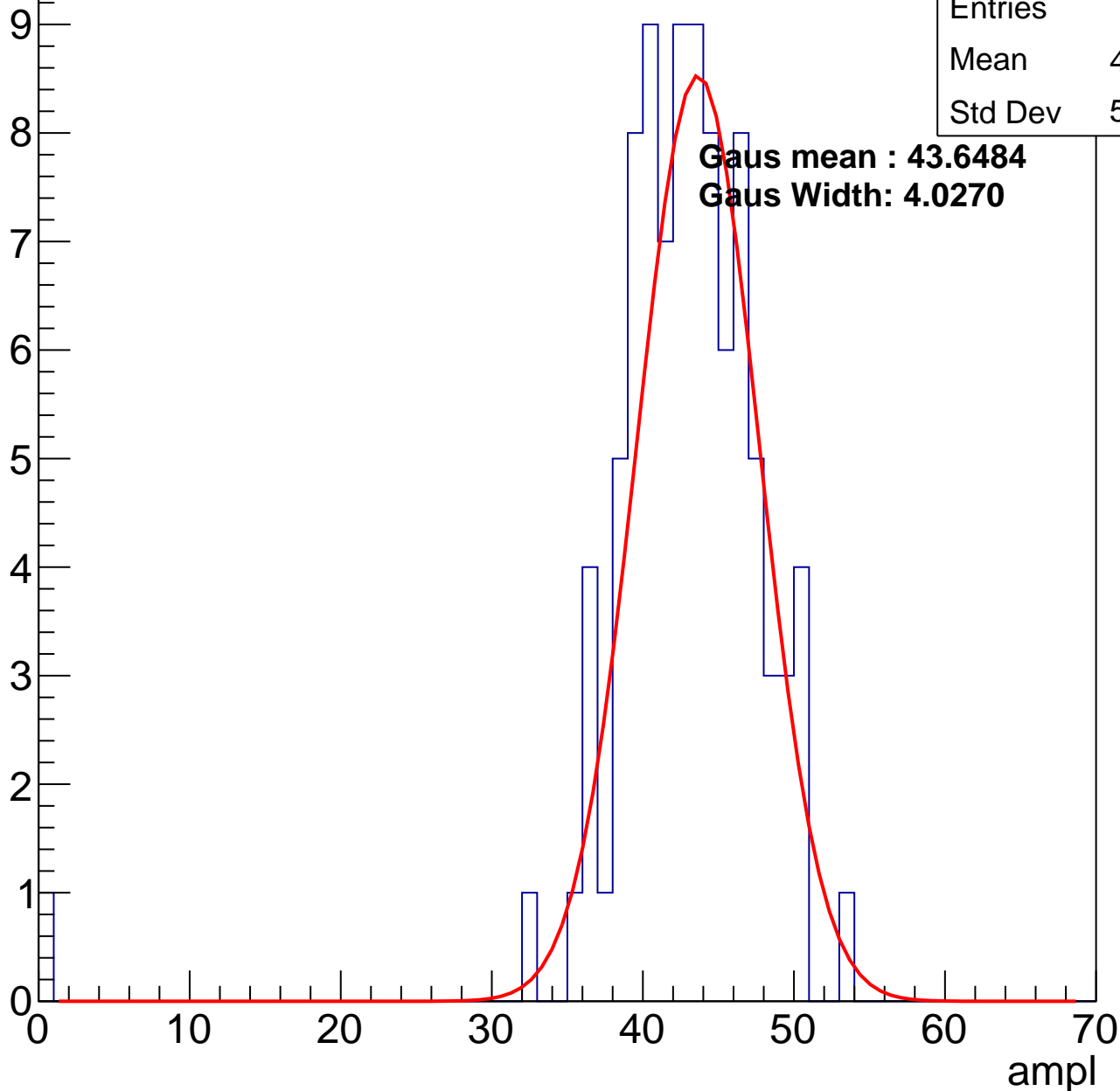
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	42.27
Std Dev	5.912

Gaus mean : 43.6484

Gaus Width: 4.0270

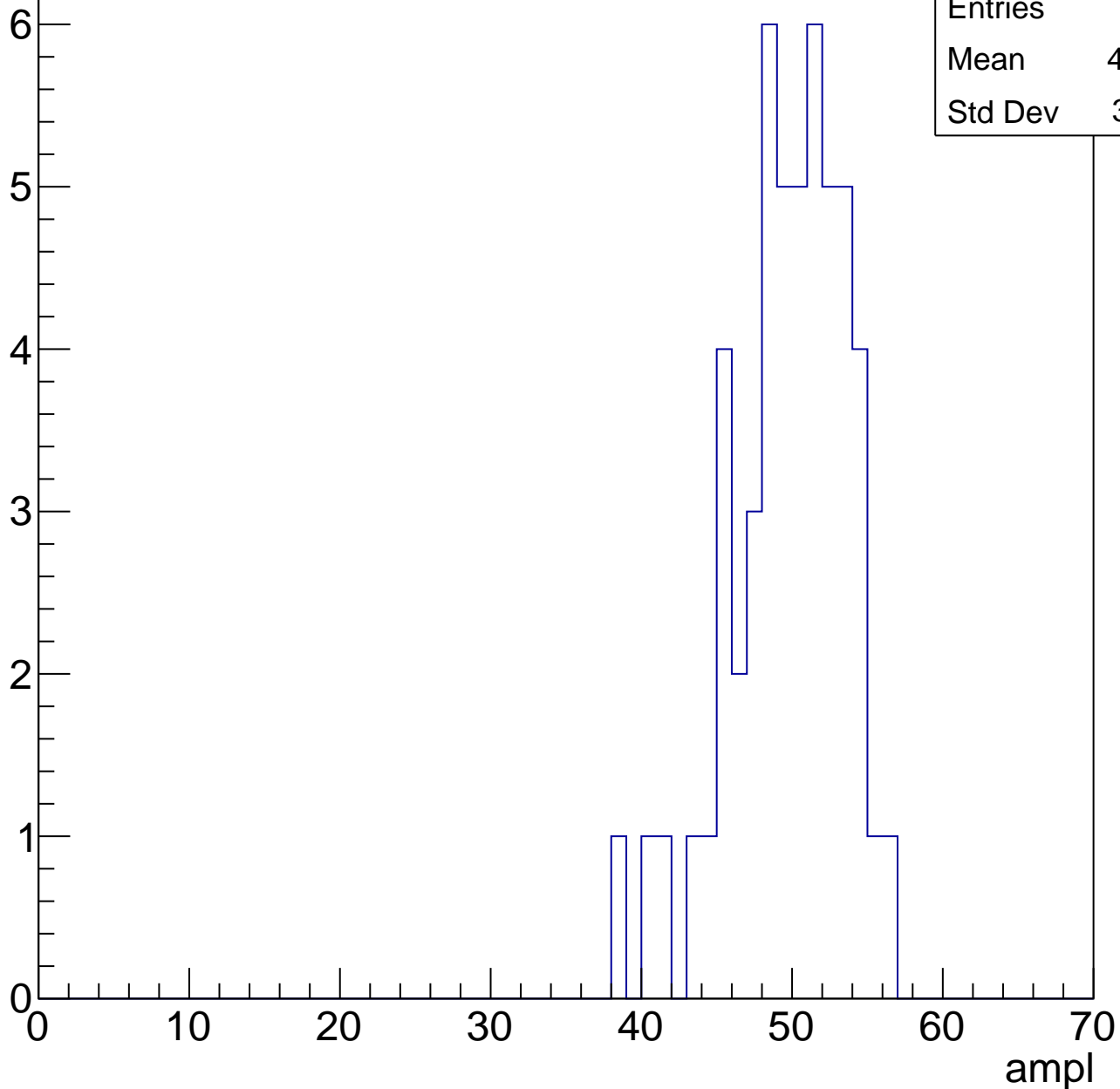


B1L101S, U4-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	49.23
Std Dev	3.831

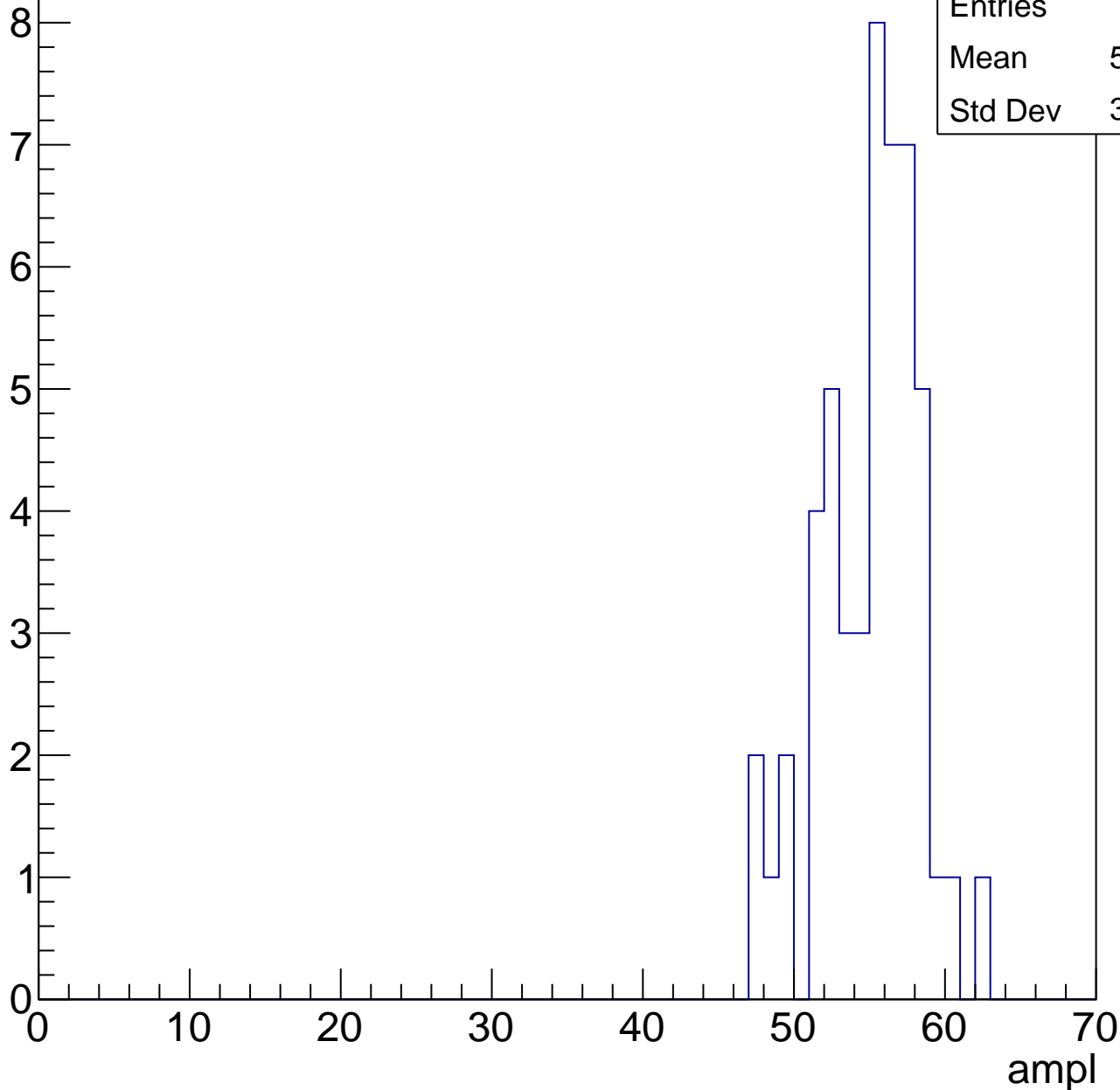


B1L101S, U4-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	54.54
Std Dev	3.263



B1L101S, U4-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	52
Mean	58.1
Std Dev	8.61

Entry

10

8

6

4

2

0

0

10

20

30

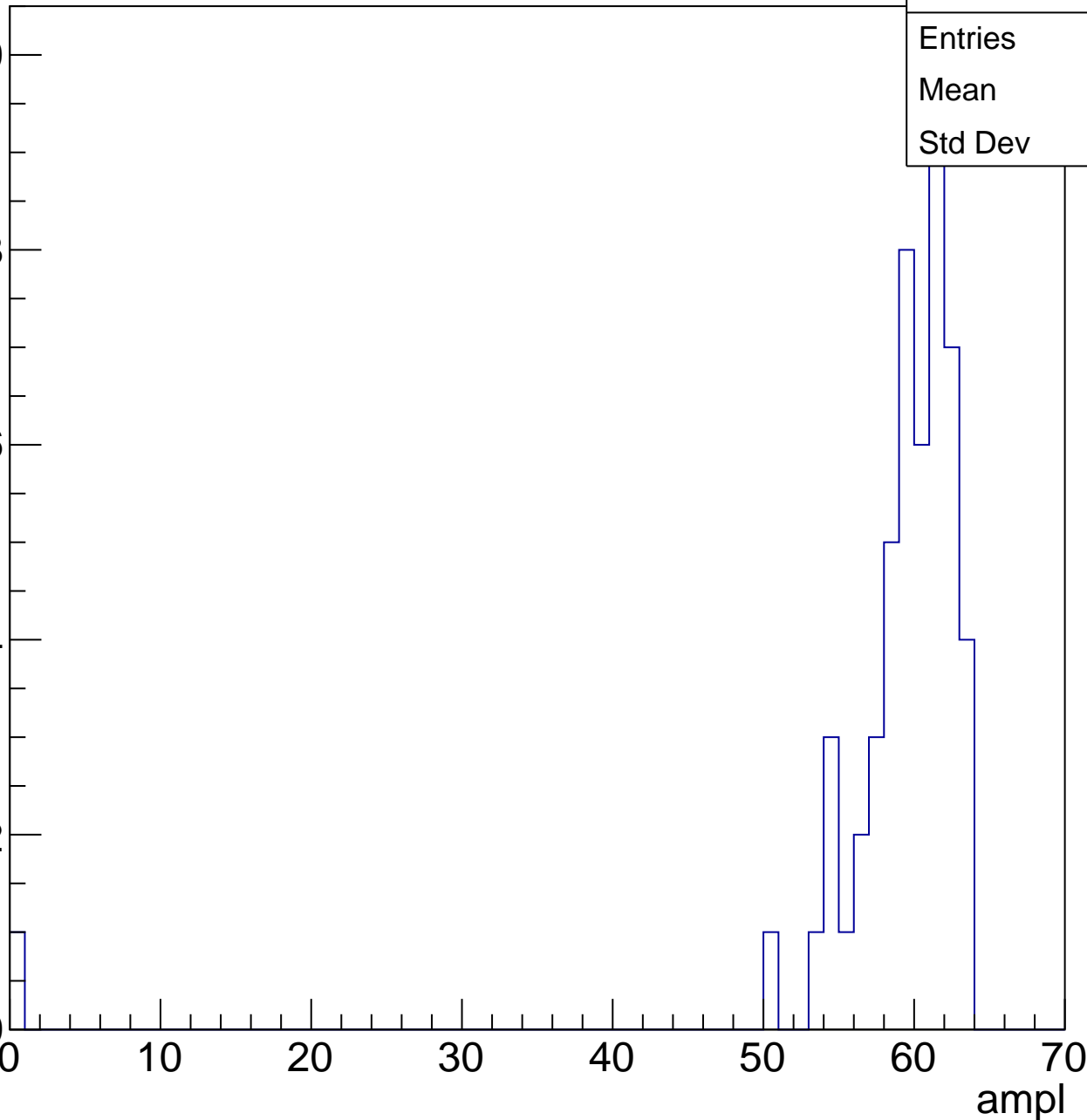
40

50

60

70

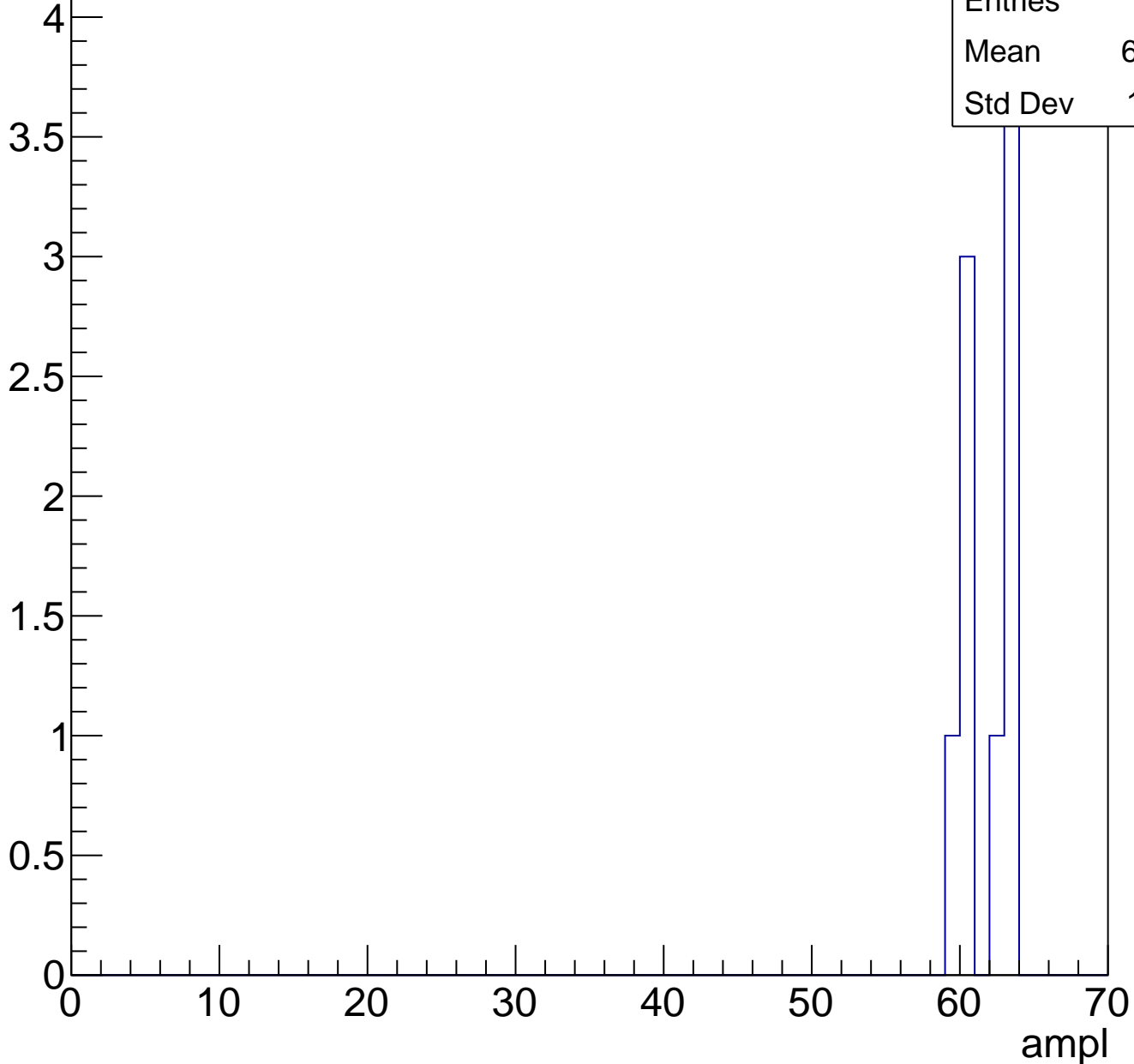
ampl



B1L101S, U4-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

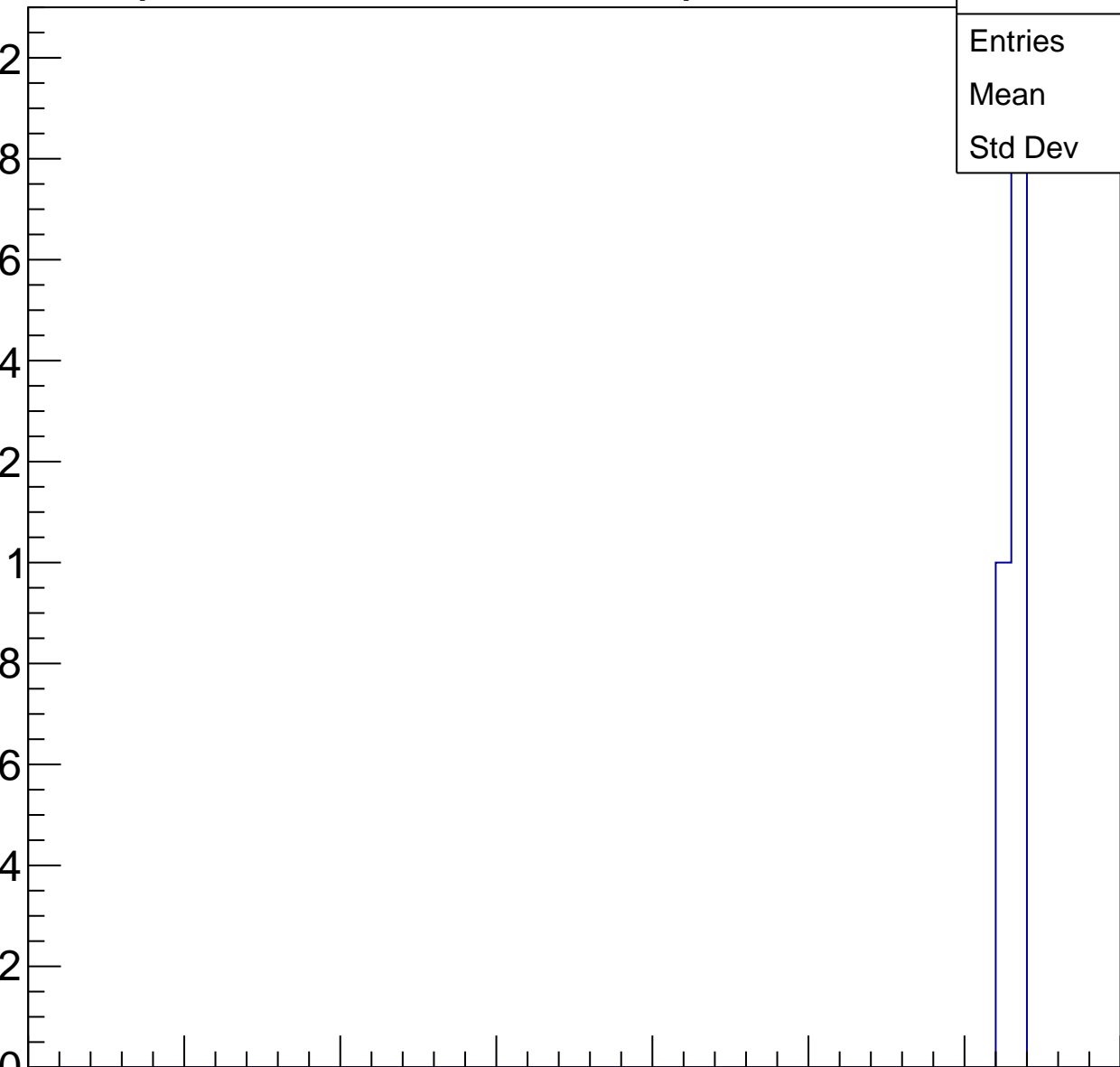
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch1, adc0

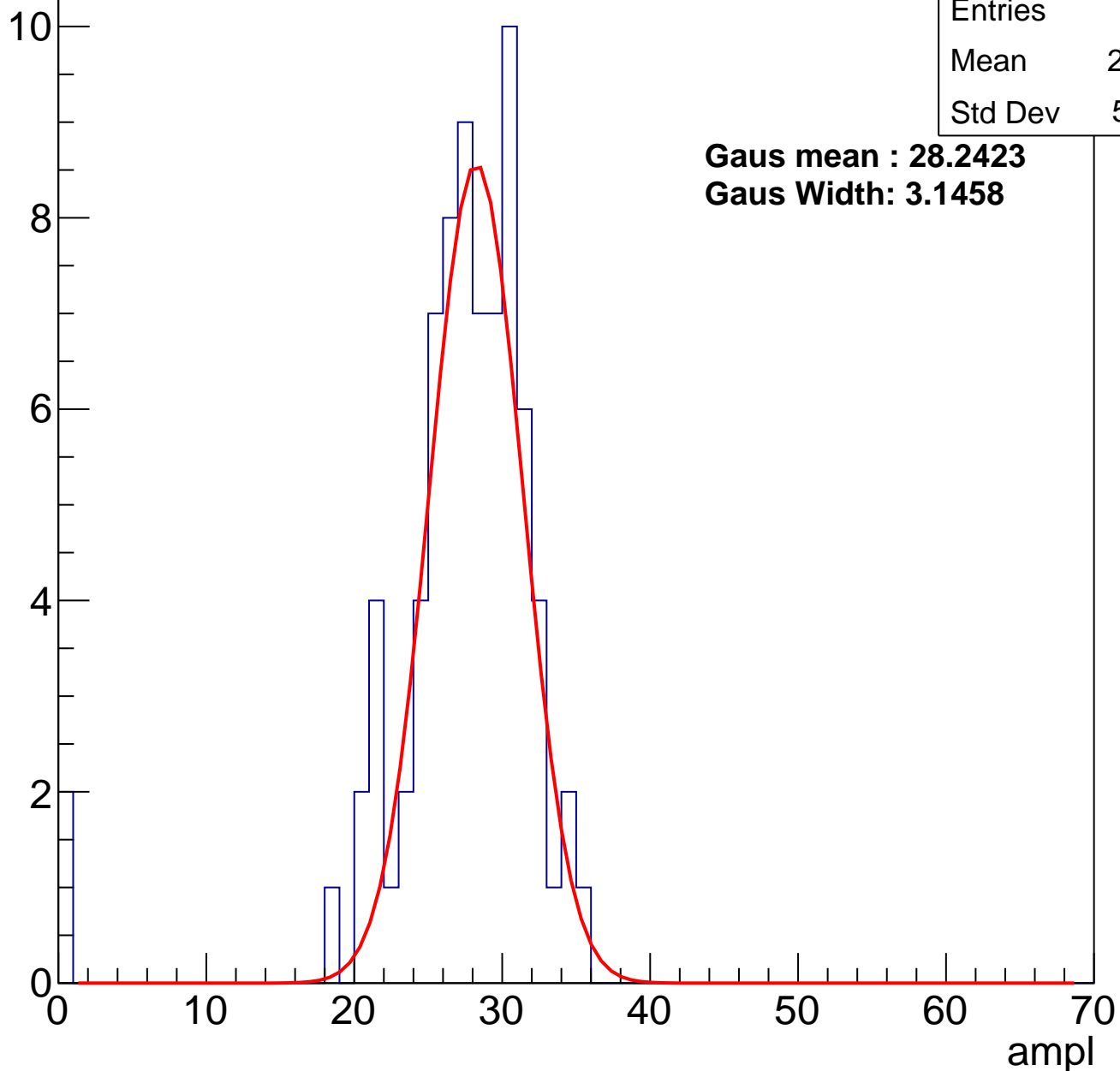
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	26.68
Std Dev	5.571

Gaus mean : 28.2423

Gaus Width: 3.1458

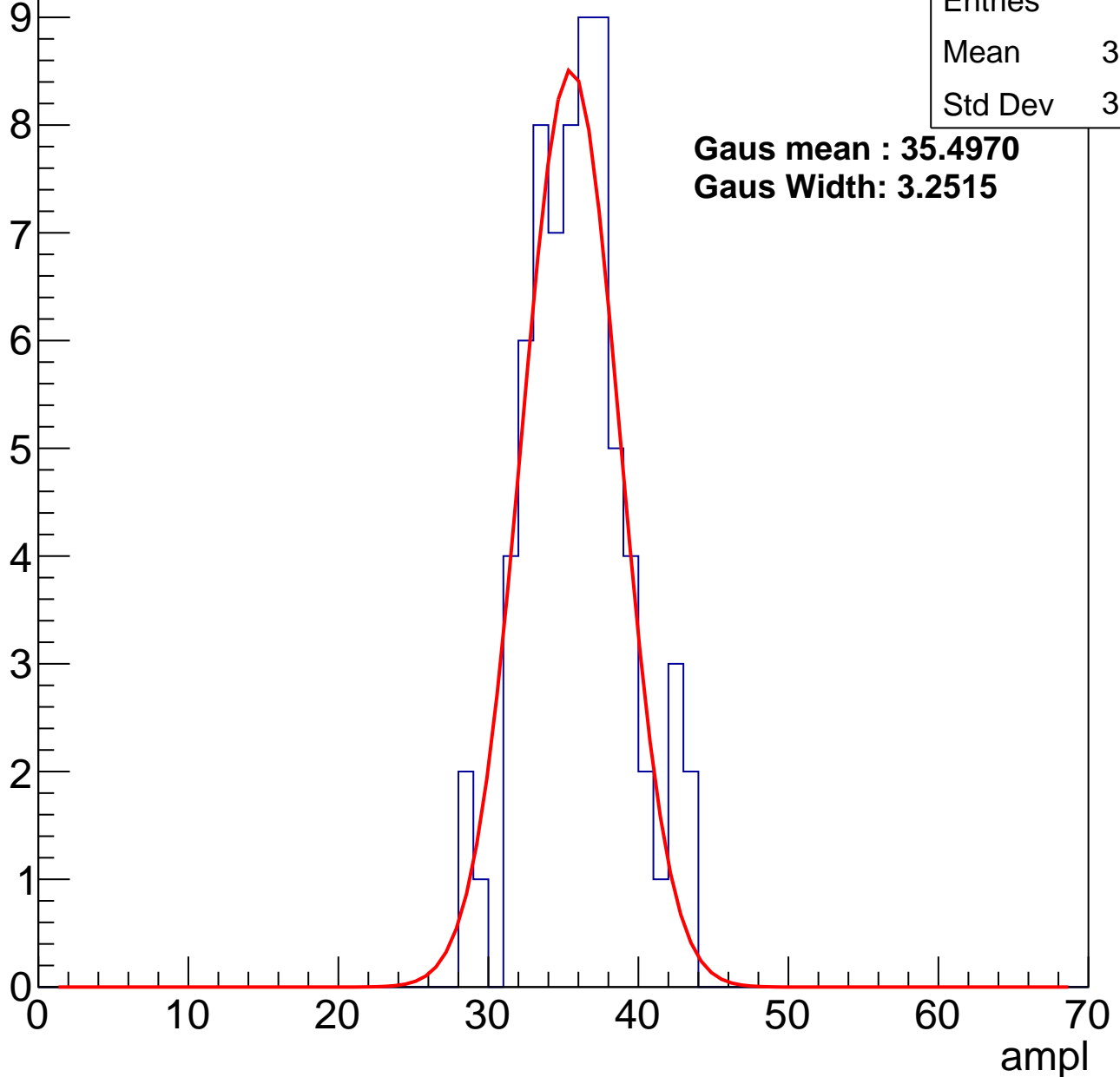
Entry



B1L101S, U4-ch1, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch1, adc2

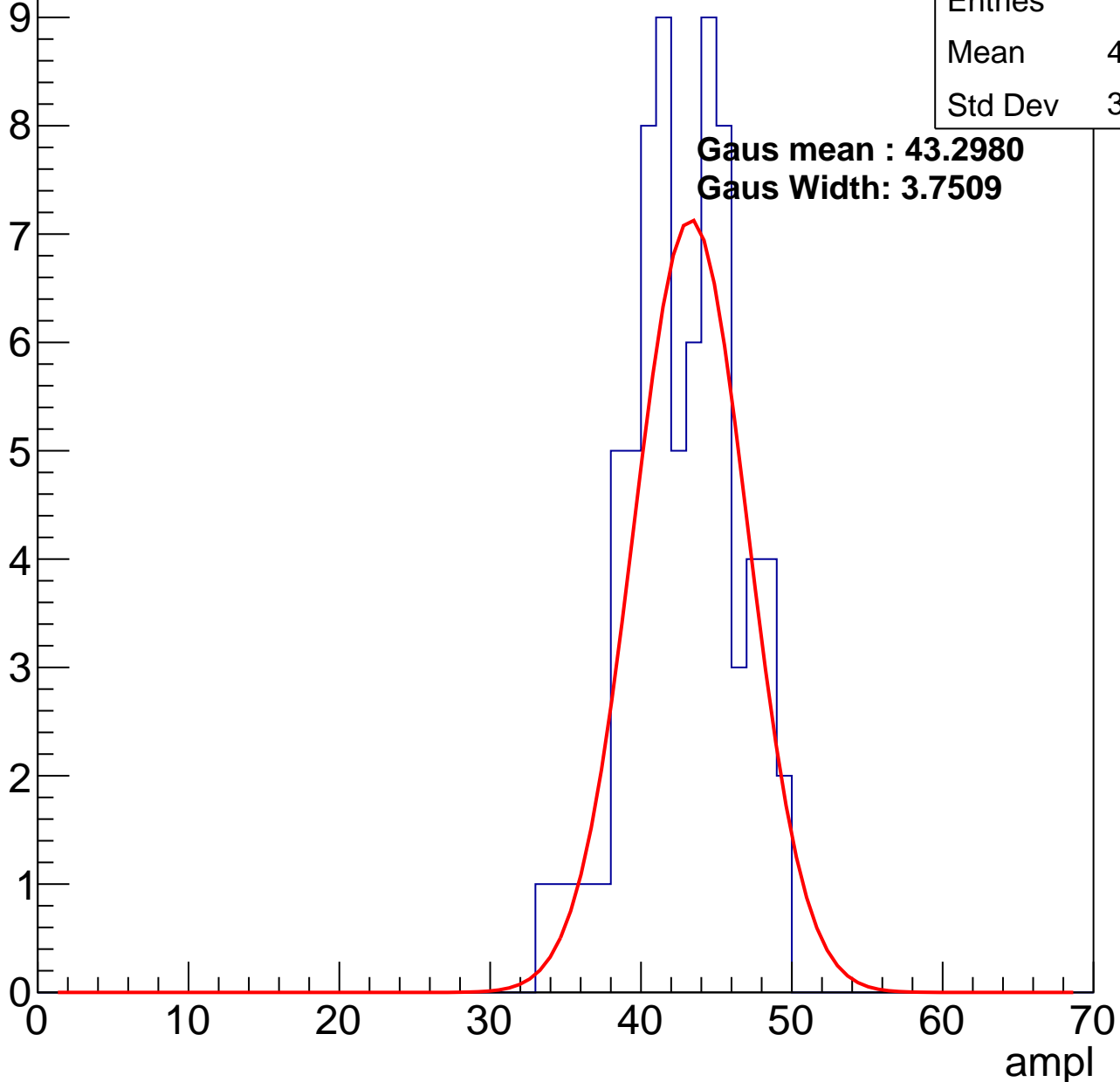
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	42.32
Std Dev	3.546

Gaus mean : 43.2980

Gaus Width: 3.7509

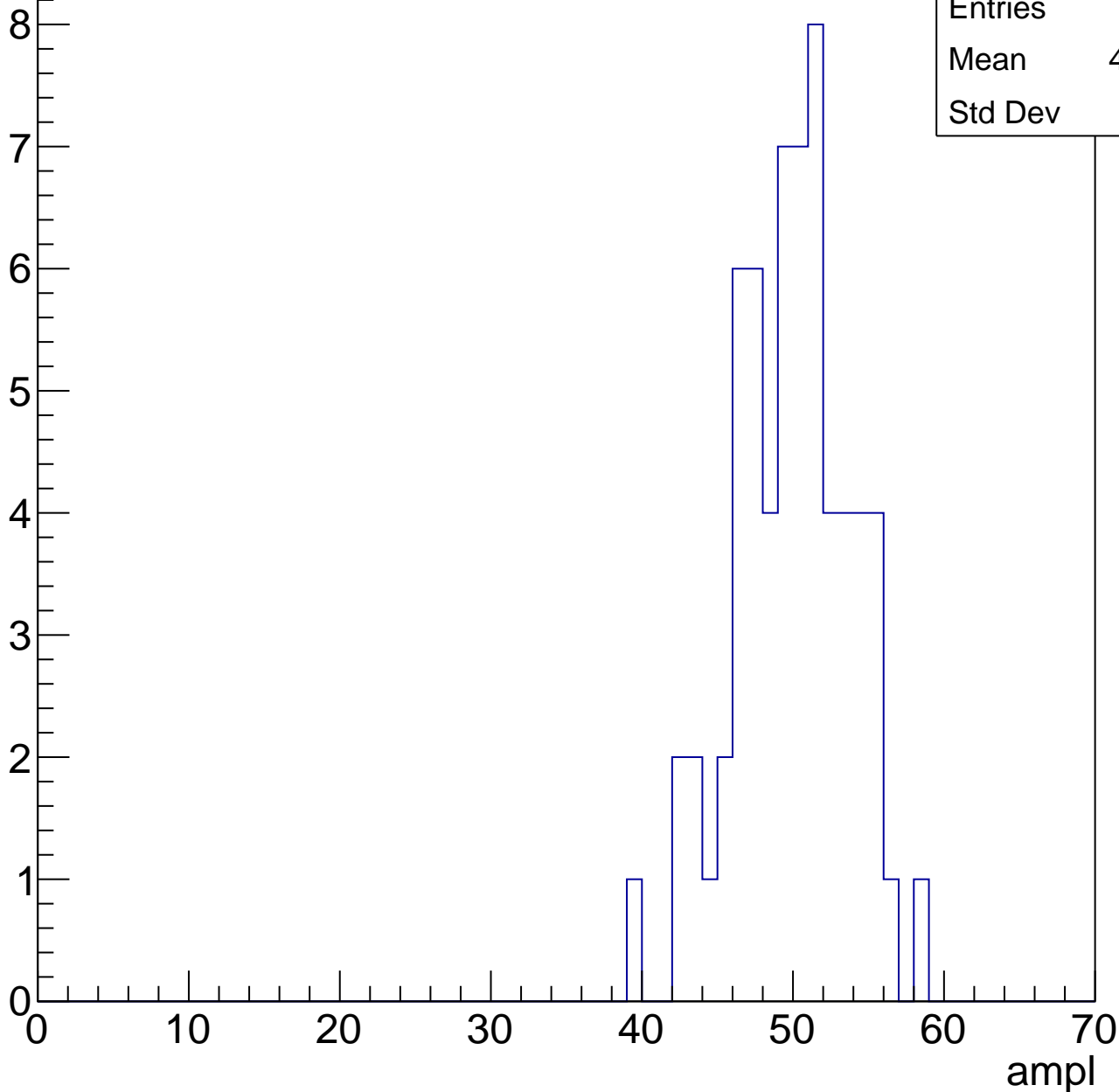


B1L101S, U4-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.44
Std Dev	3.77

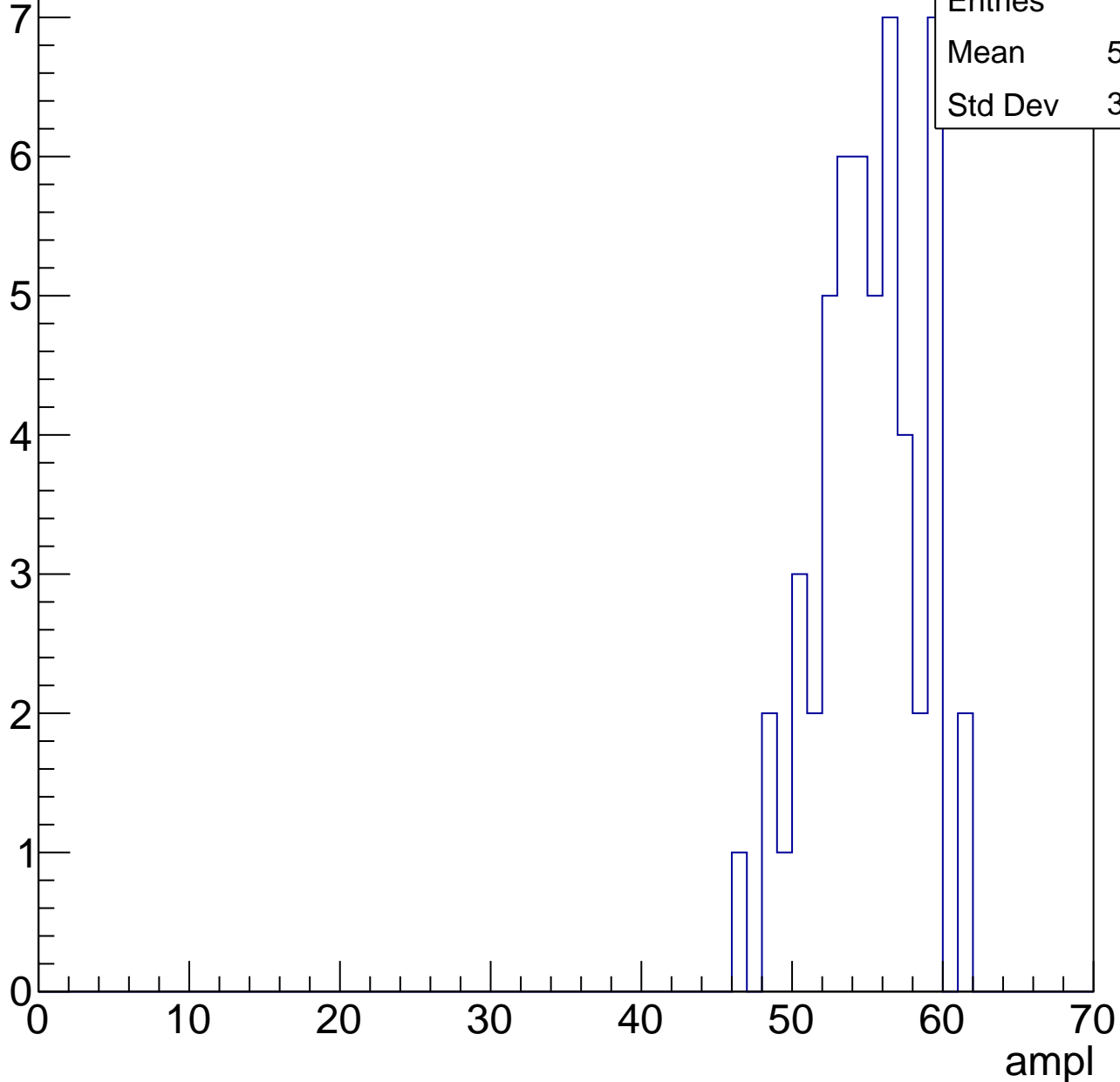


B1L101S, U4-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	54.55
Std Dev	3.396



B1L101S, U4-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	42
Mean	58.1
Std Dev	9.408

Entry

10

8

6

4

2

0

0

10

20

30

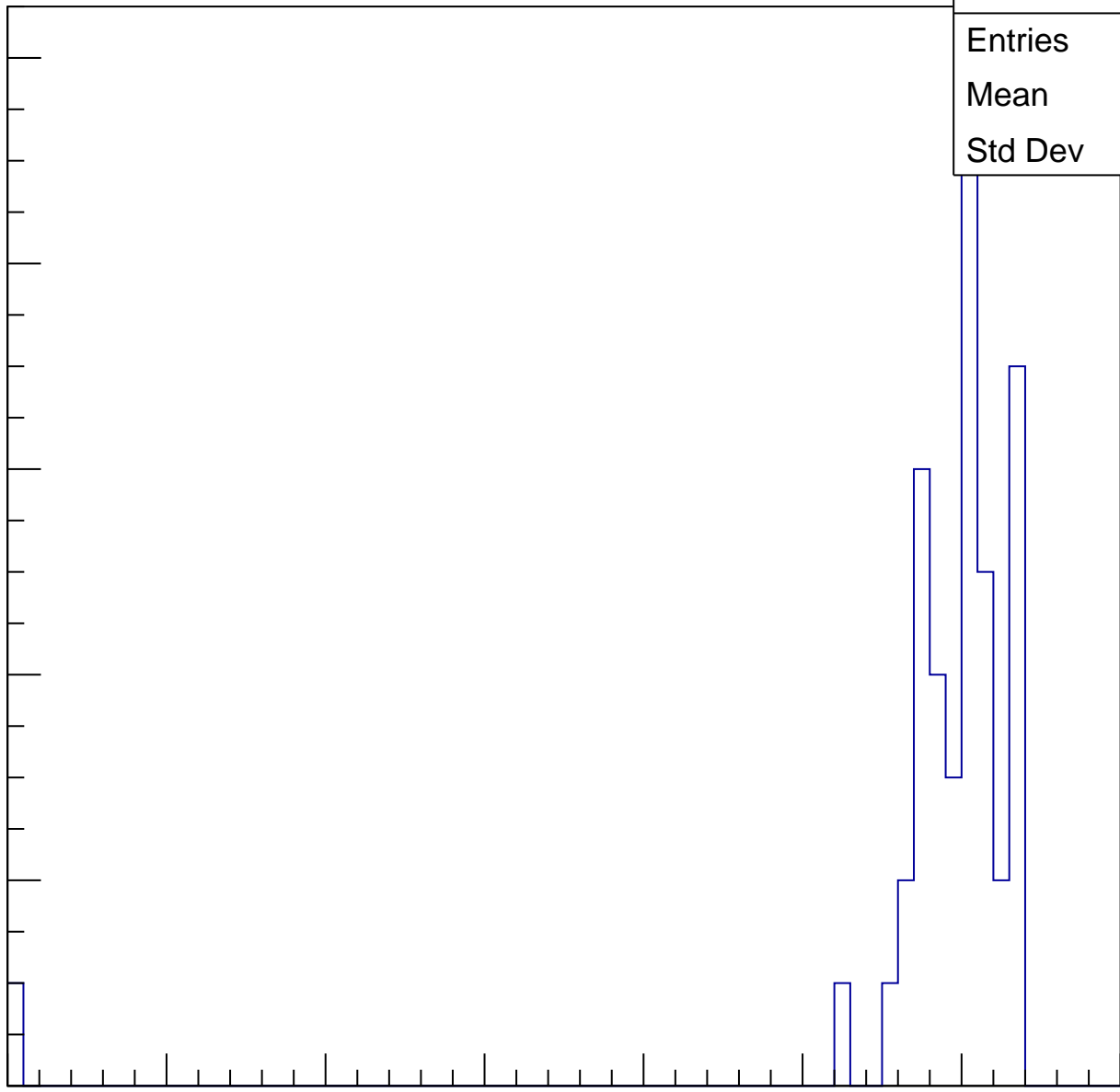
40

50

60

70

ampl

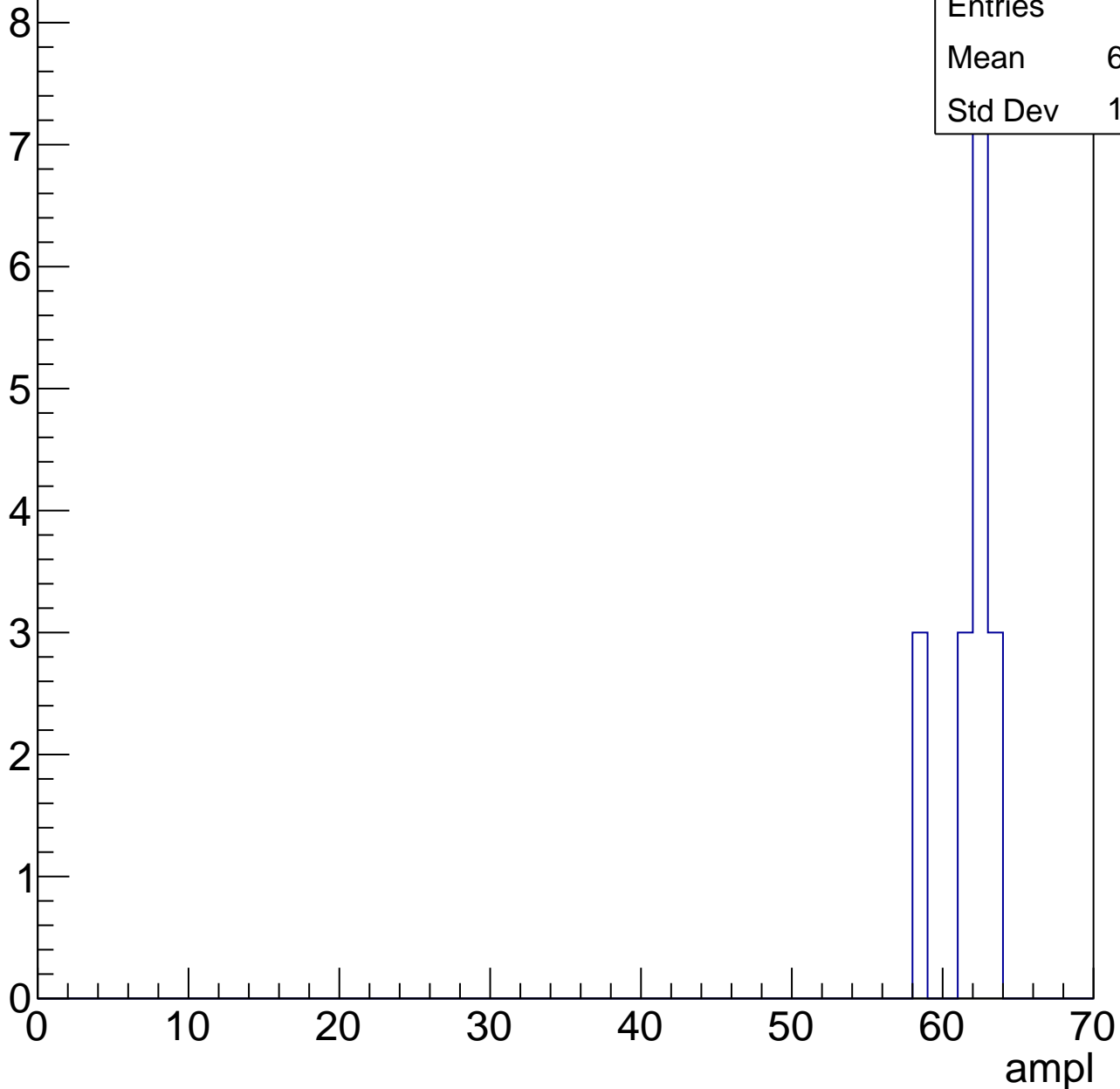


B1L101S, U4-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61.29
Std Dev	1.637



B1L101S, U4-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

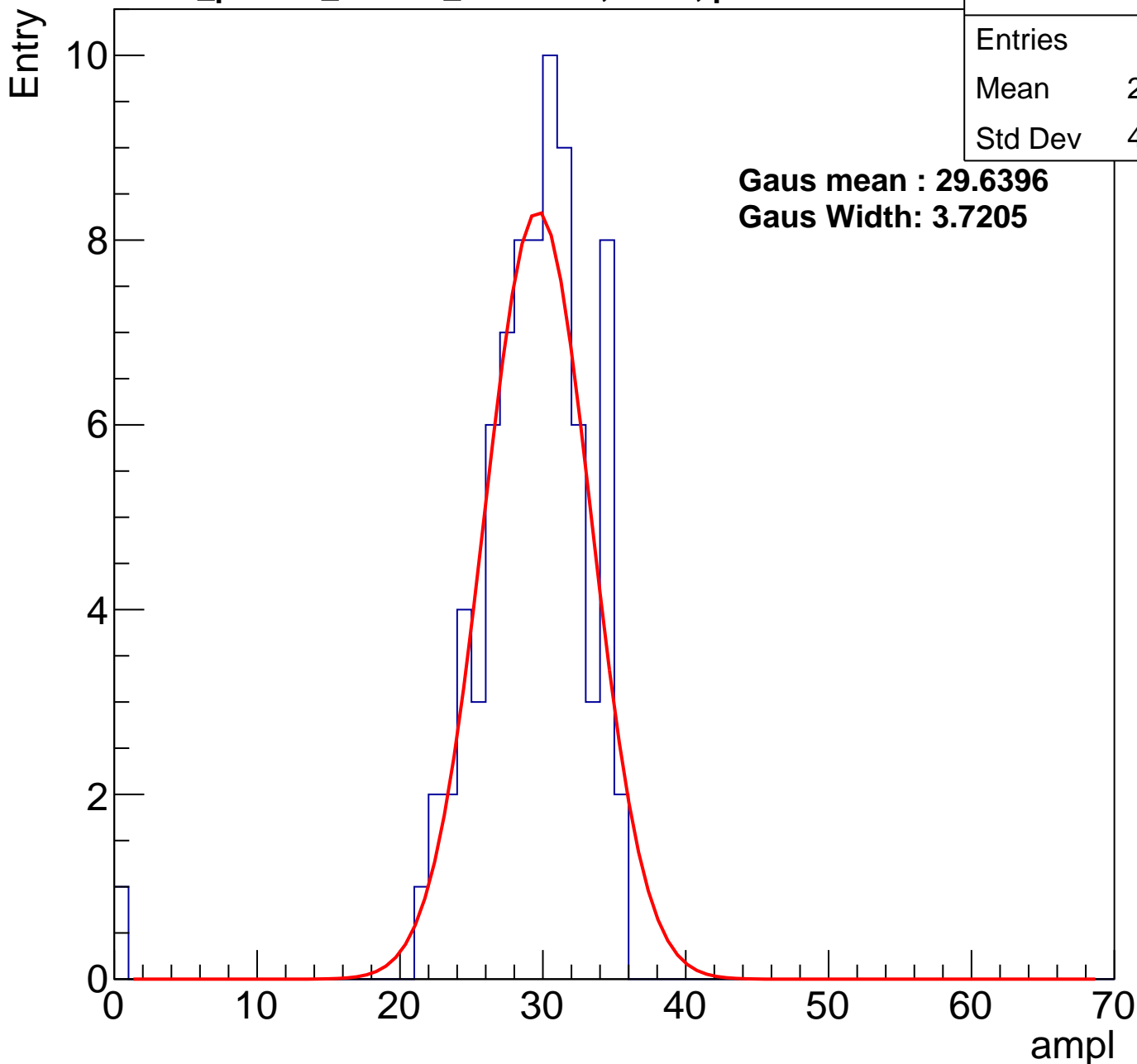
B1L101S, U4-ch2, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	28.69
Std Dev	4.644

Gaus mean : 29.6396

Gaus Width: 3.7205

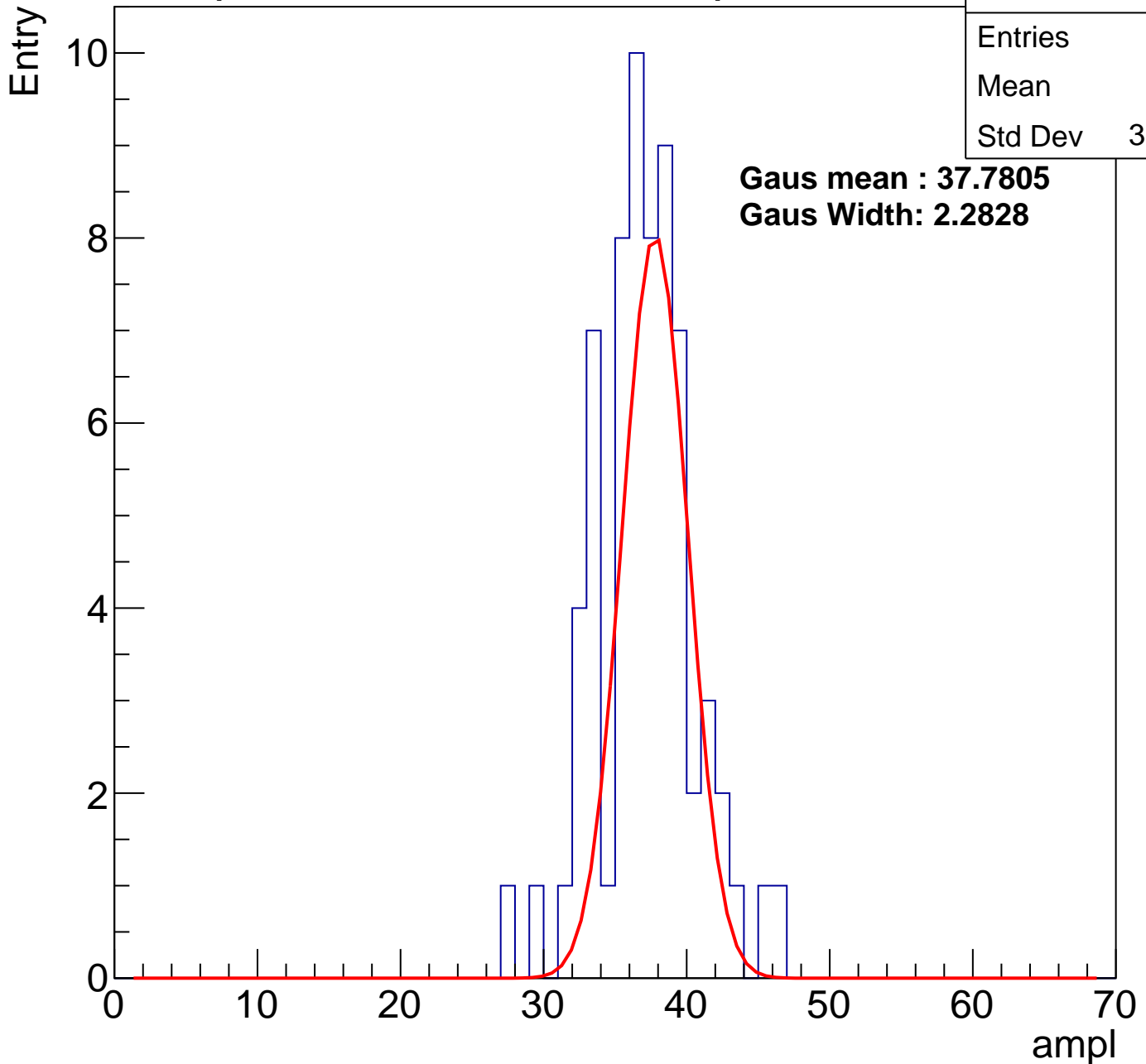


B1L101S, U4-ch2, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	36.6
Std Dev	3.425

Gaus mean : 37.7805
Gaus Width: 2.2828



B1L101S, U4-ch2, adc2

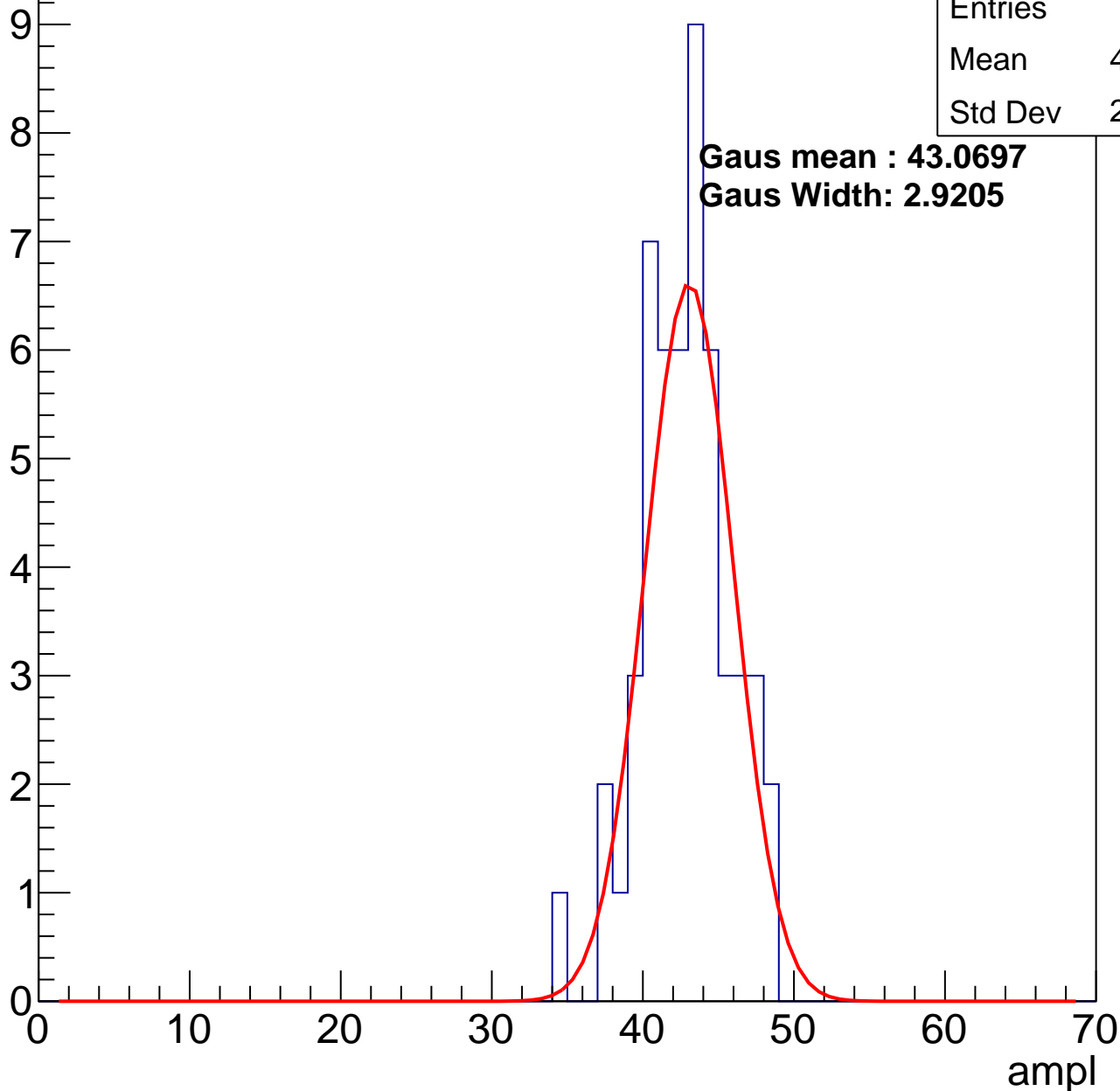
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	42.35
Std Dev	2.908

Gaus mean : 43.0697

Gaus Width: 2.9205

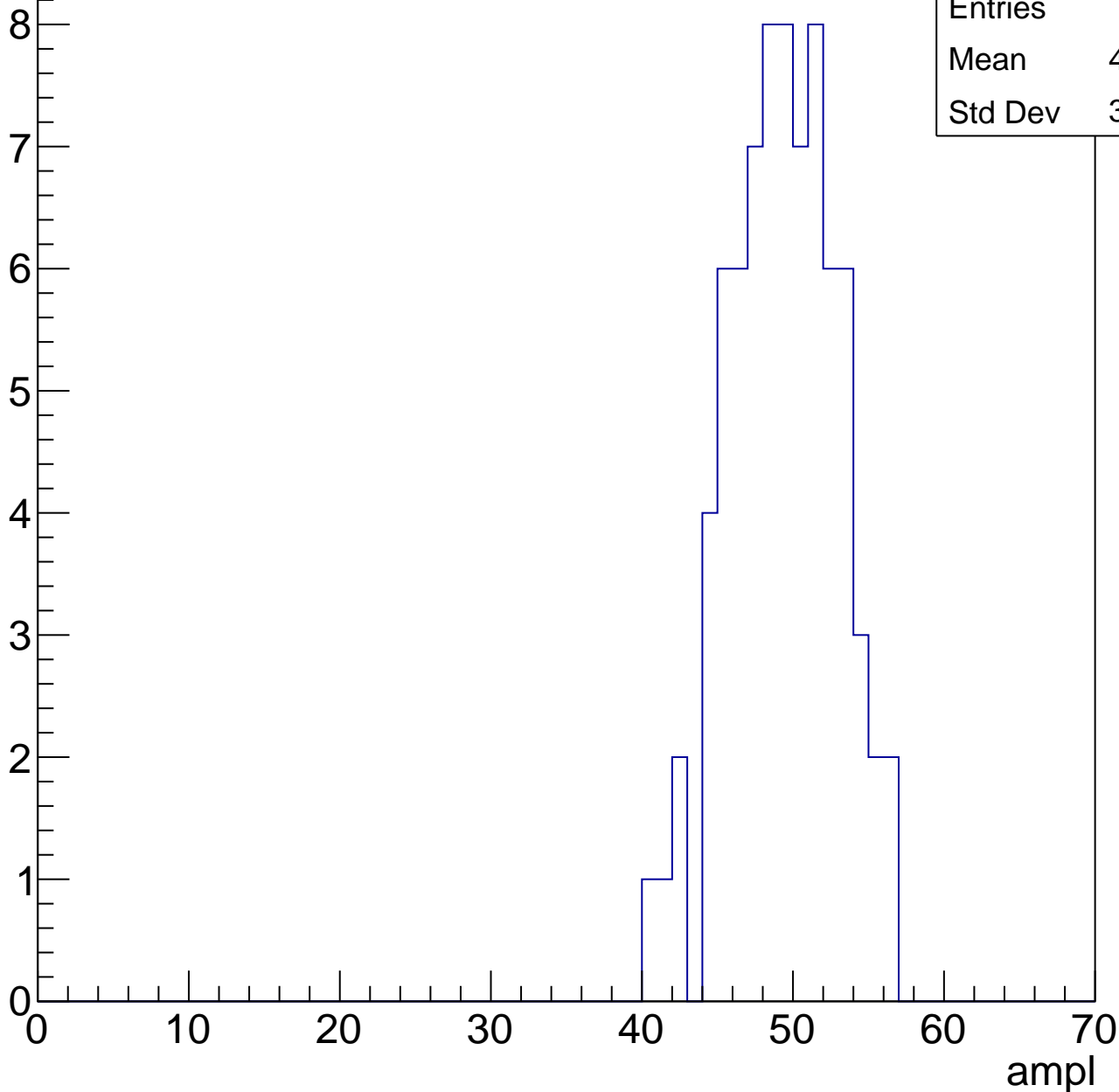


B1L101S, U4-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

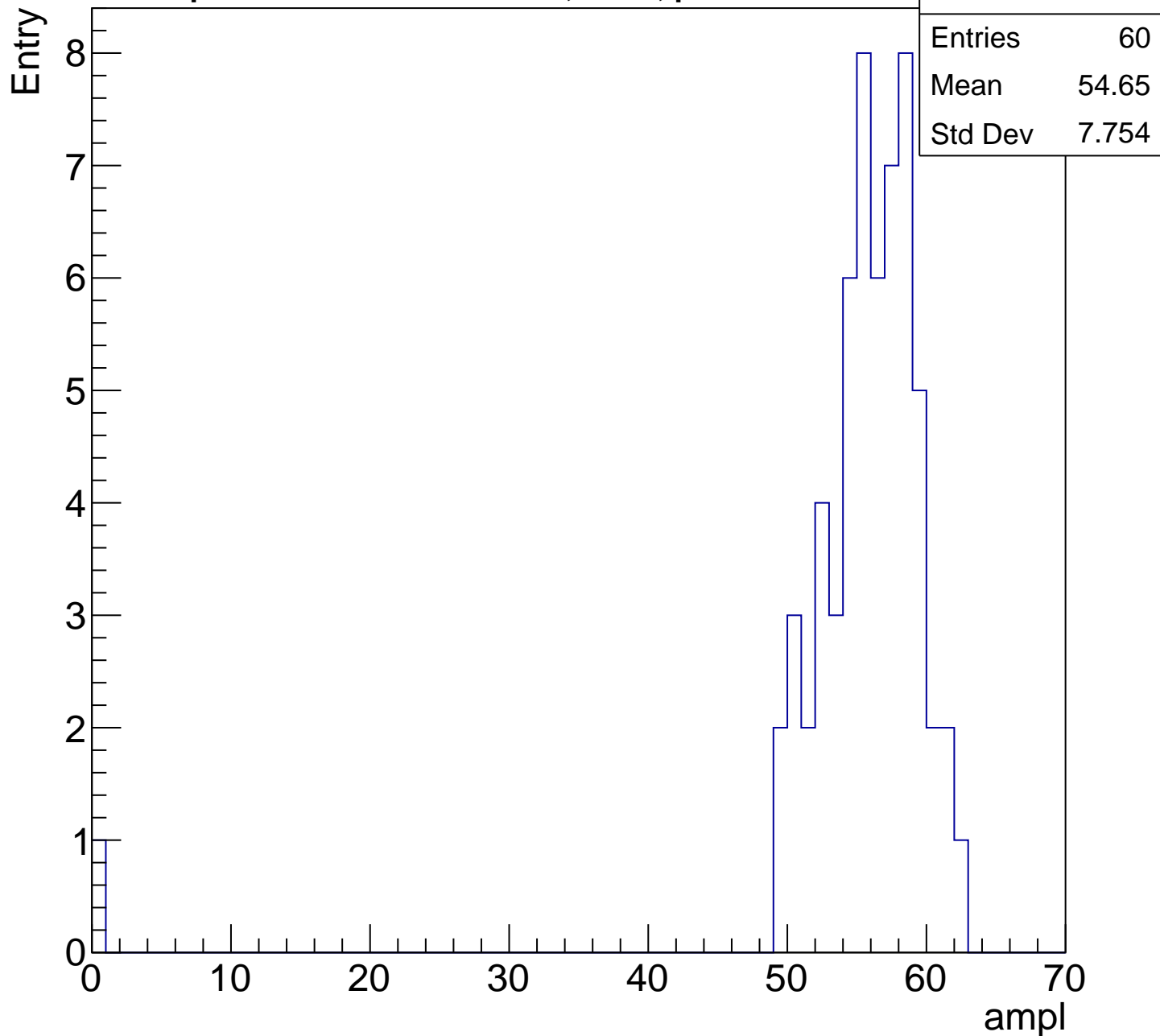
Entry

Entries	77
Mean	48.88
Std Dev	3.535



B1L101S, U4-ch2, adc4

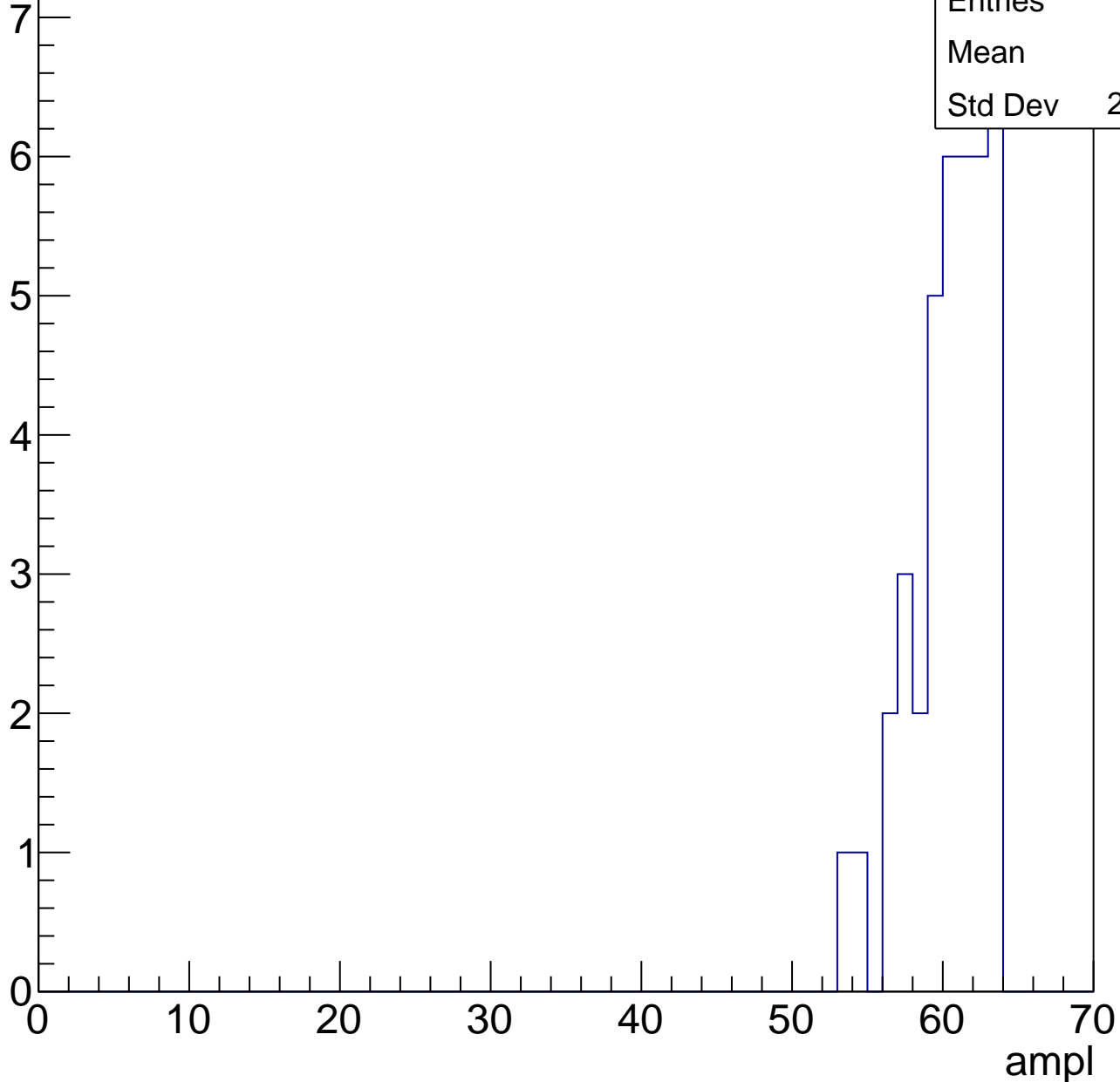
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

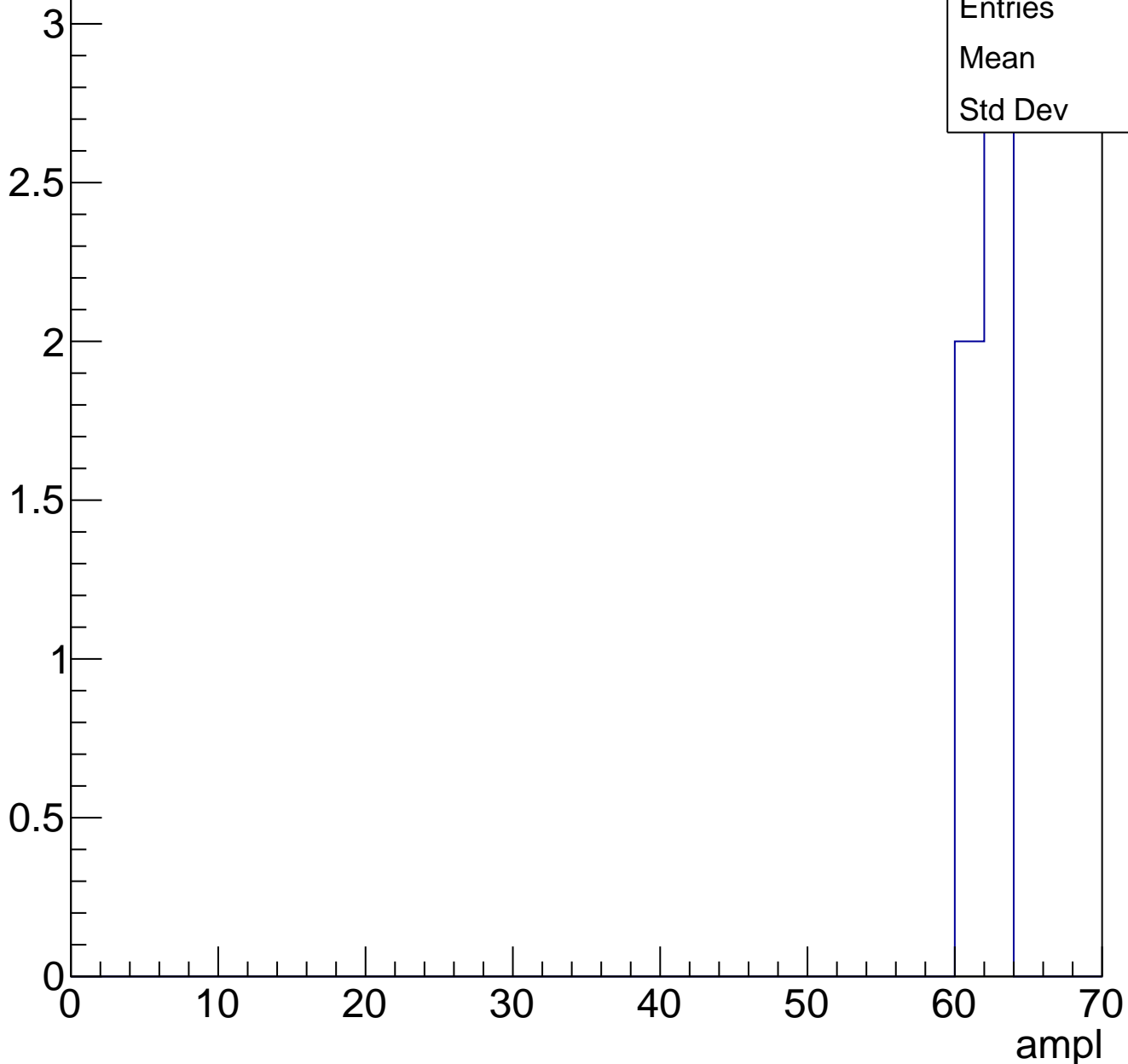
Entry



B1L101S, U4-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	61.7
Std Dev	1.1

B1L101S, U4-ch2, adc7

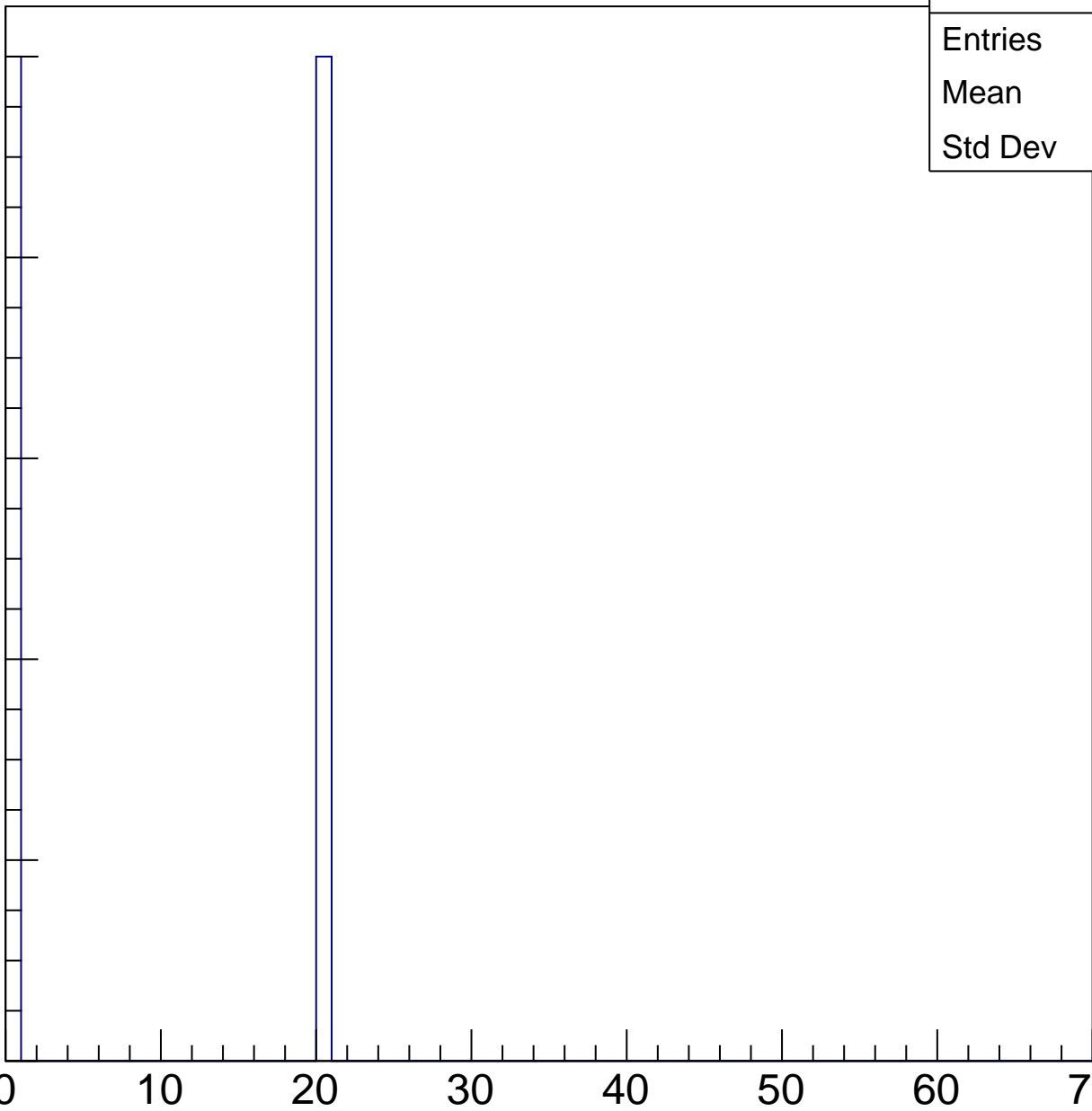
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	10
Std Dev	10

ampl



B1L101S, U4-ch3, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	26.97
Std Dev	7.622

Gaus mean : 29.0338

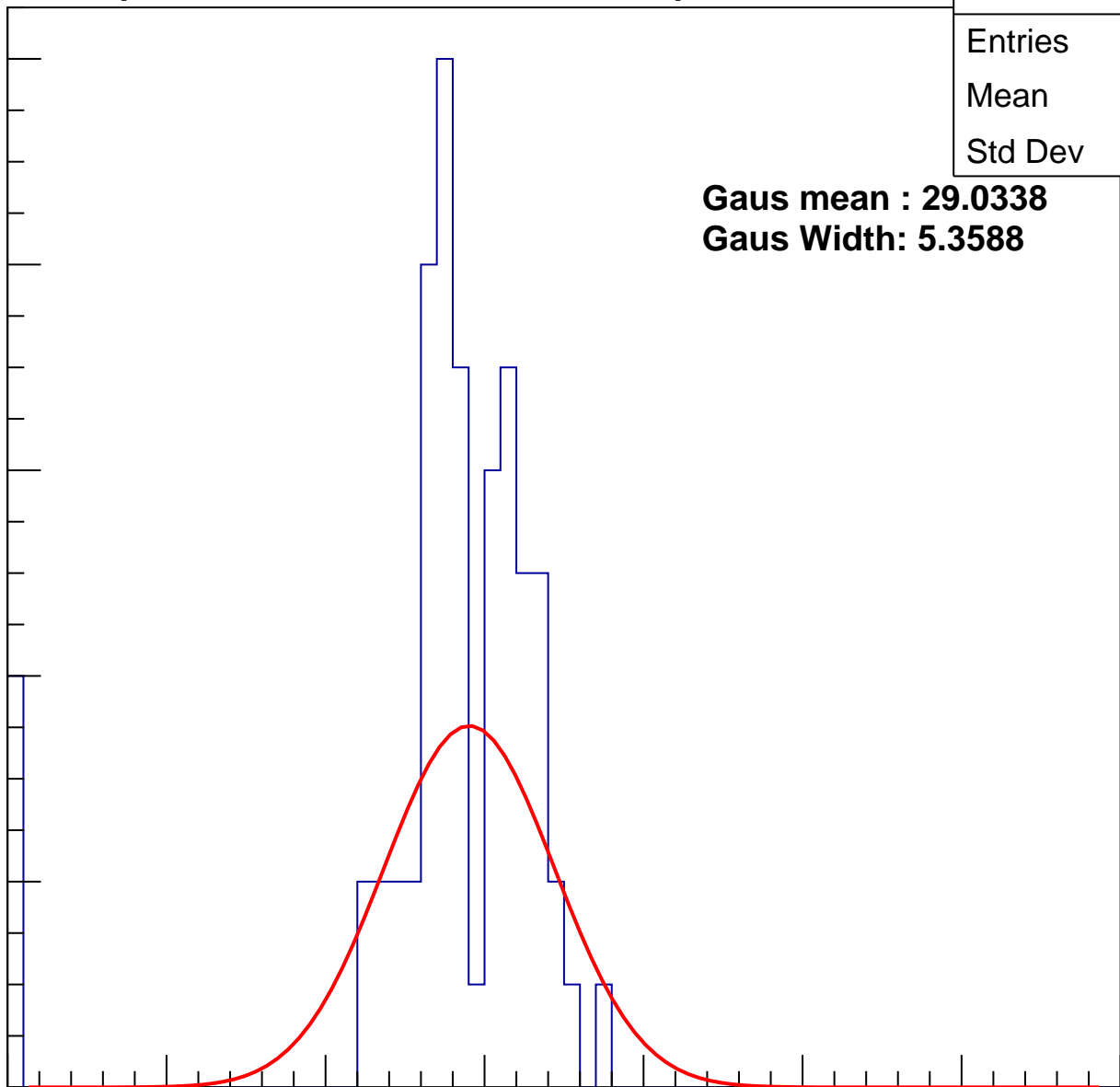
Gaus Width: 5.3588

Entry

10
8
6
4
2
0

ampl

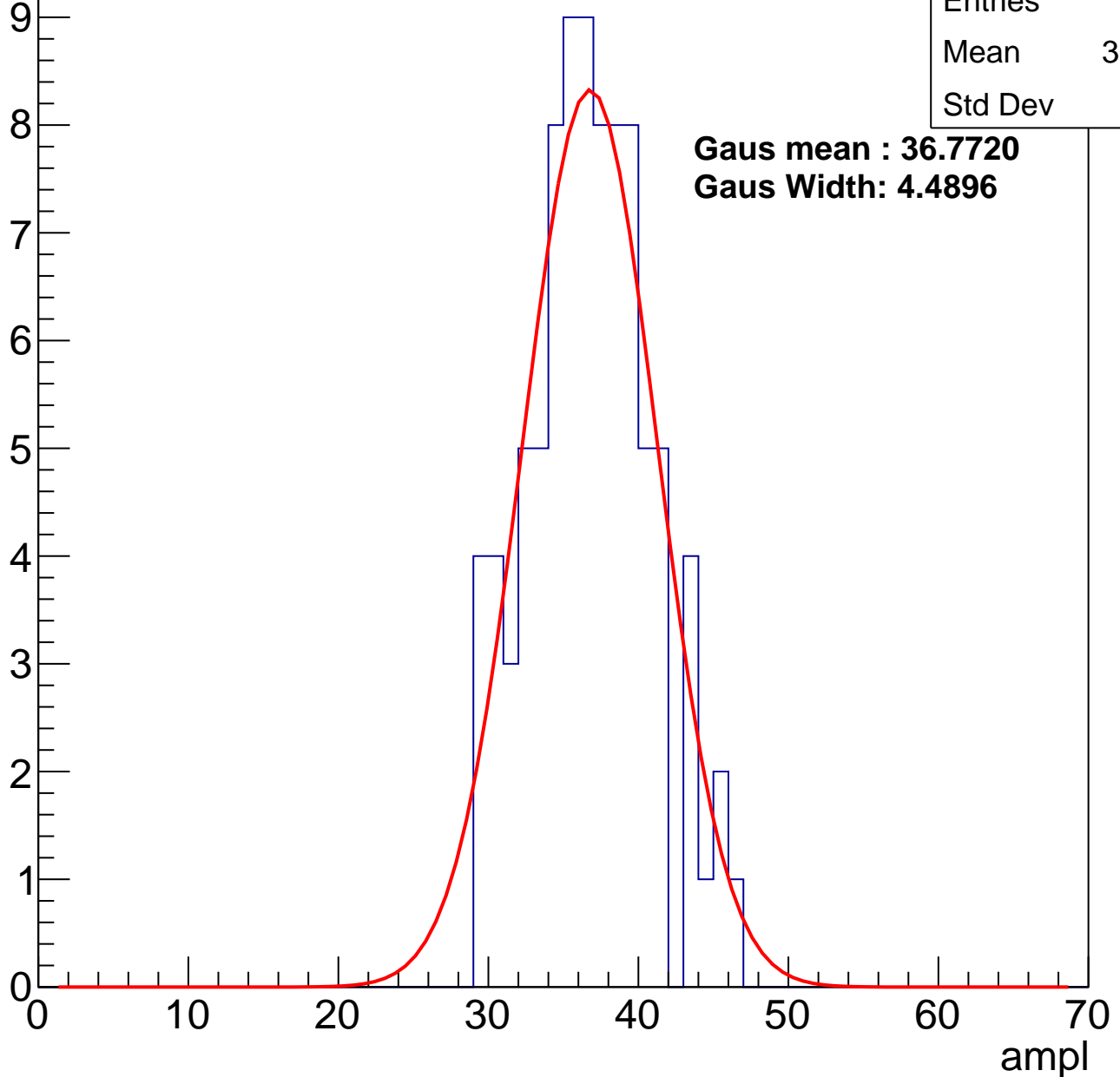
0 10 20 30 40 50 60 70



B1L101S, U4-ch3, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	89
Mean	36.34
Std Dev	3.98

B1L101S, U4-ch3, adc2

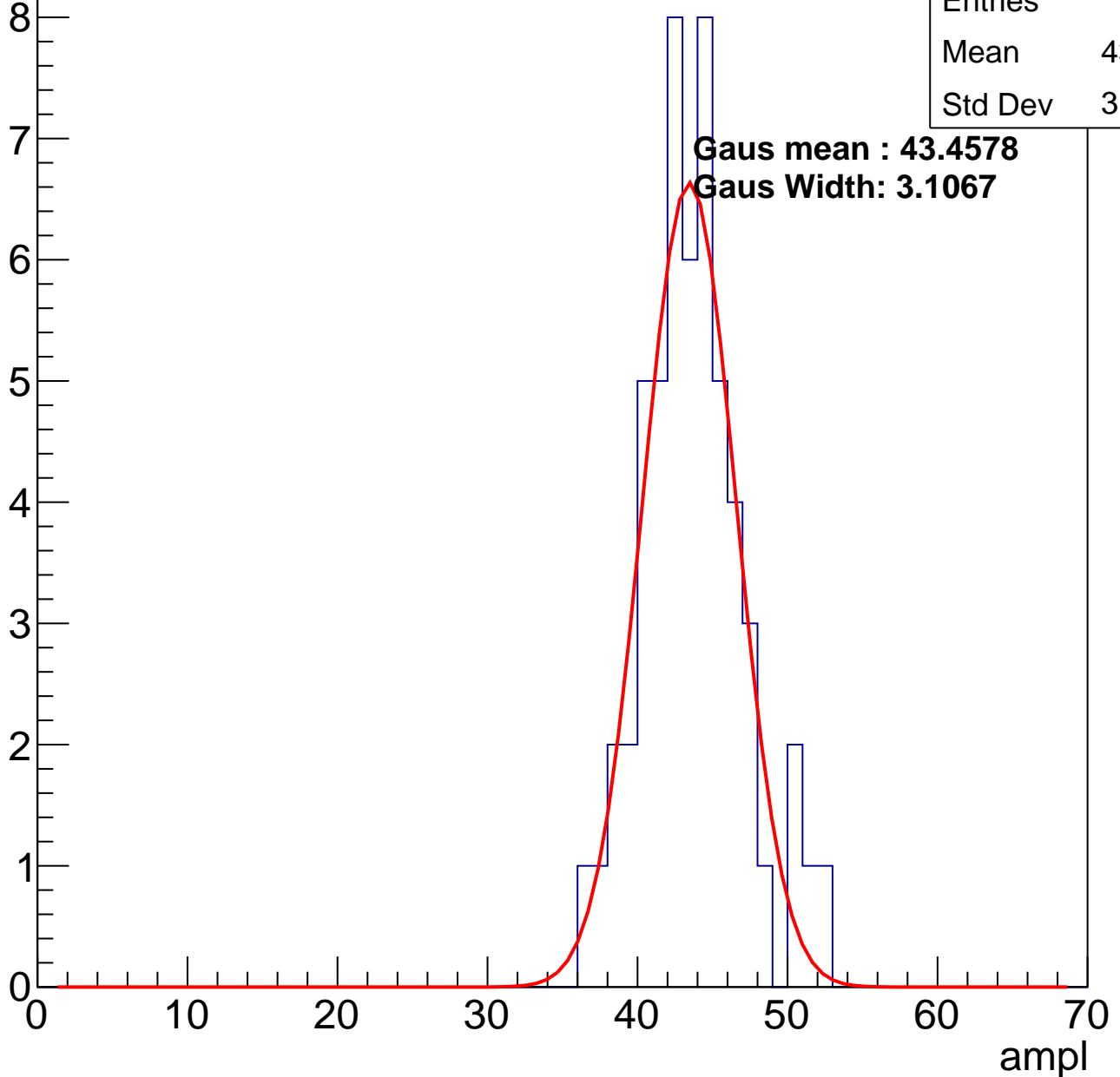
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.25
Std Dev	3.348

Gaus mean : 43.4578

Gaus Width: 3.1067

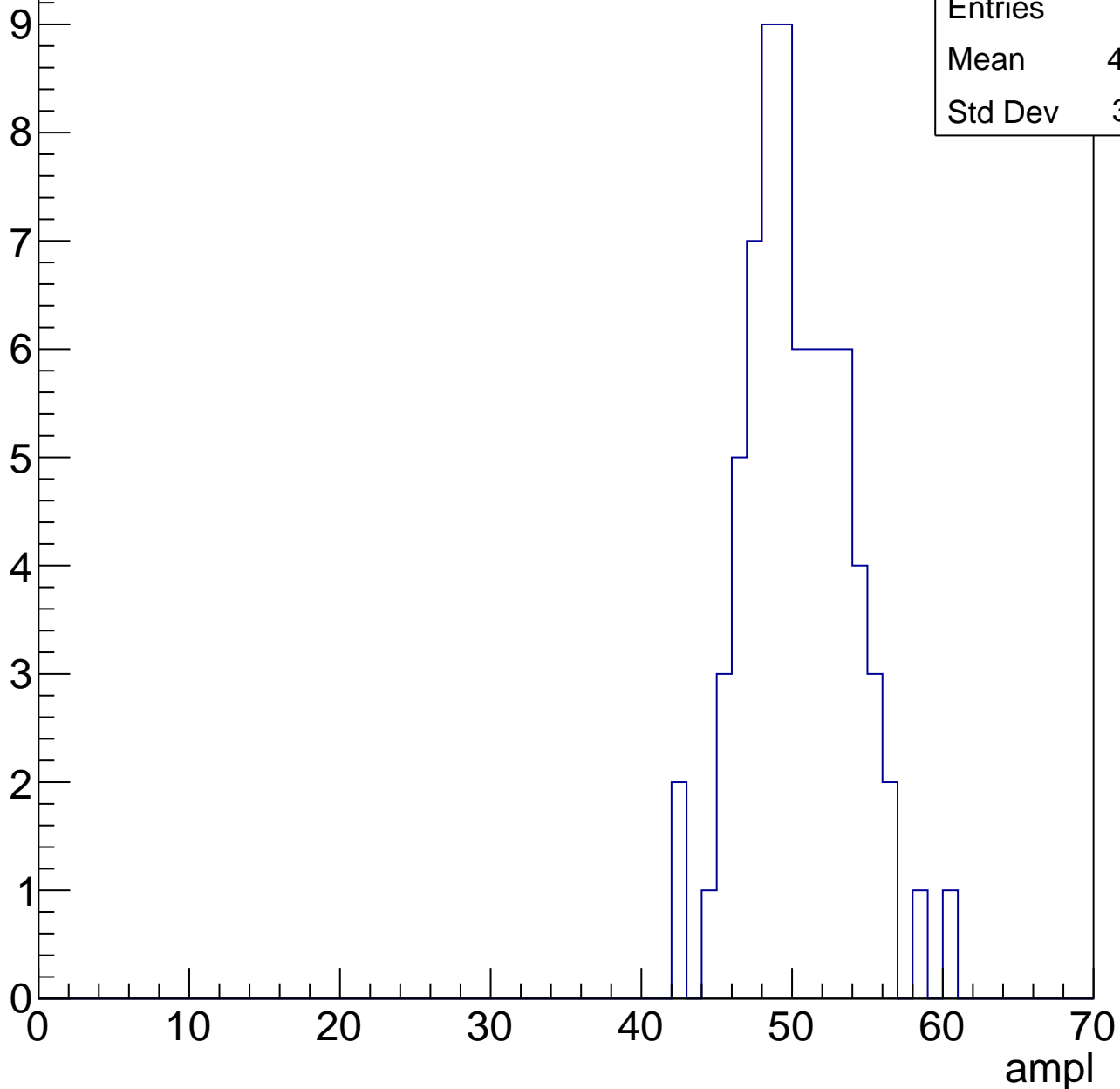


B1L101S, U4-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

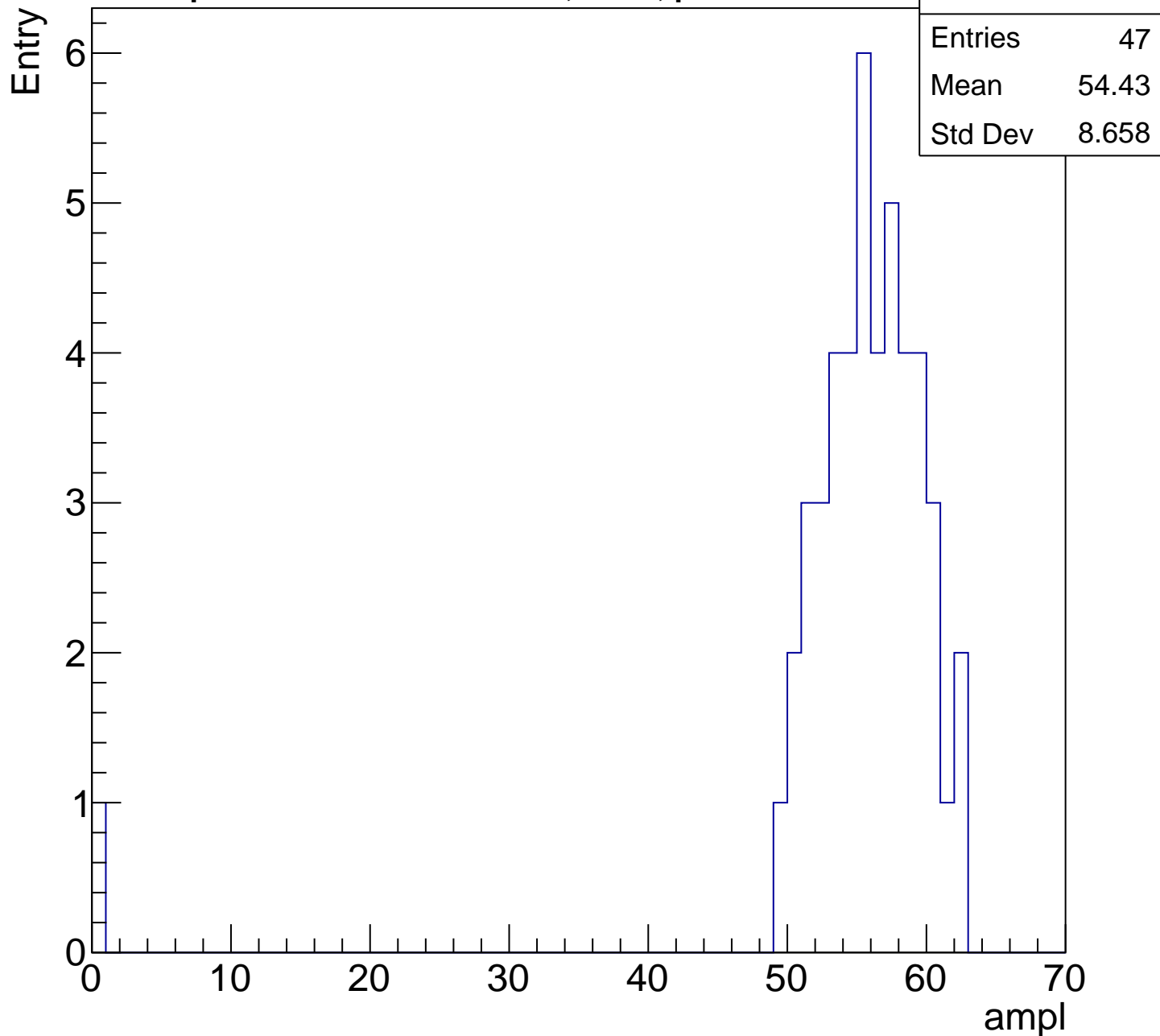
Entry

Entries	71
Mean	49.89
Std Dev	3.531



B1L101S, U4-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

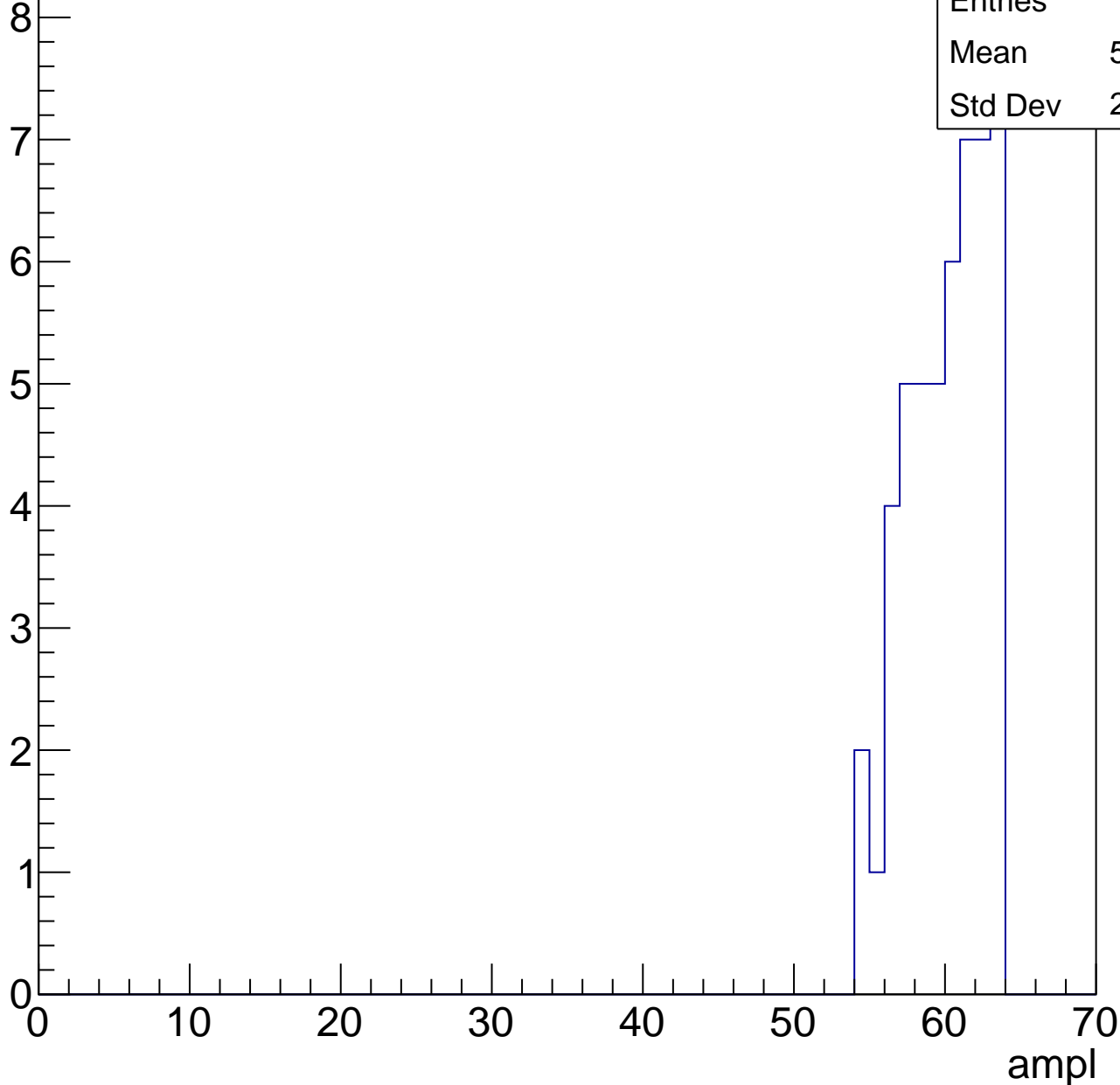


B1L101S, U4-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	59.64
Std Dev	2.575



B1L101S, U4-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.384

ampl

0 10 20 30 40 50 60 70

B1L101S, U4-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch4, adc0

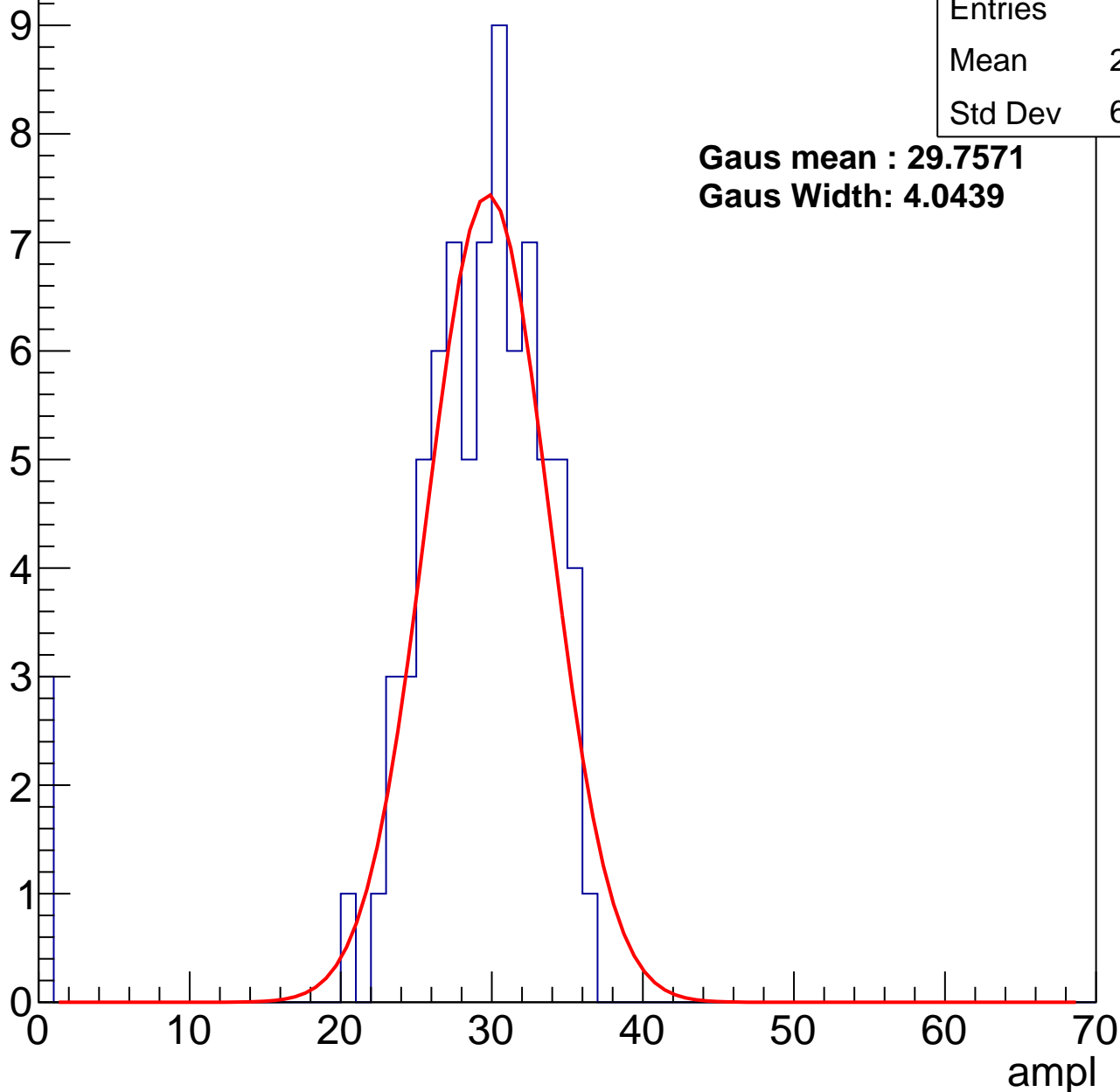
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.04
Std Dev	6.626

Gaus mean : 29.7571

Gaus Width: 4.0439



B1L101S, U4-ch4, adc1

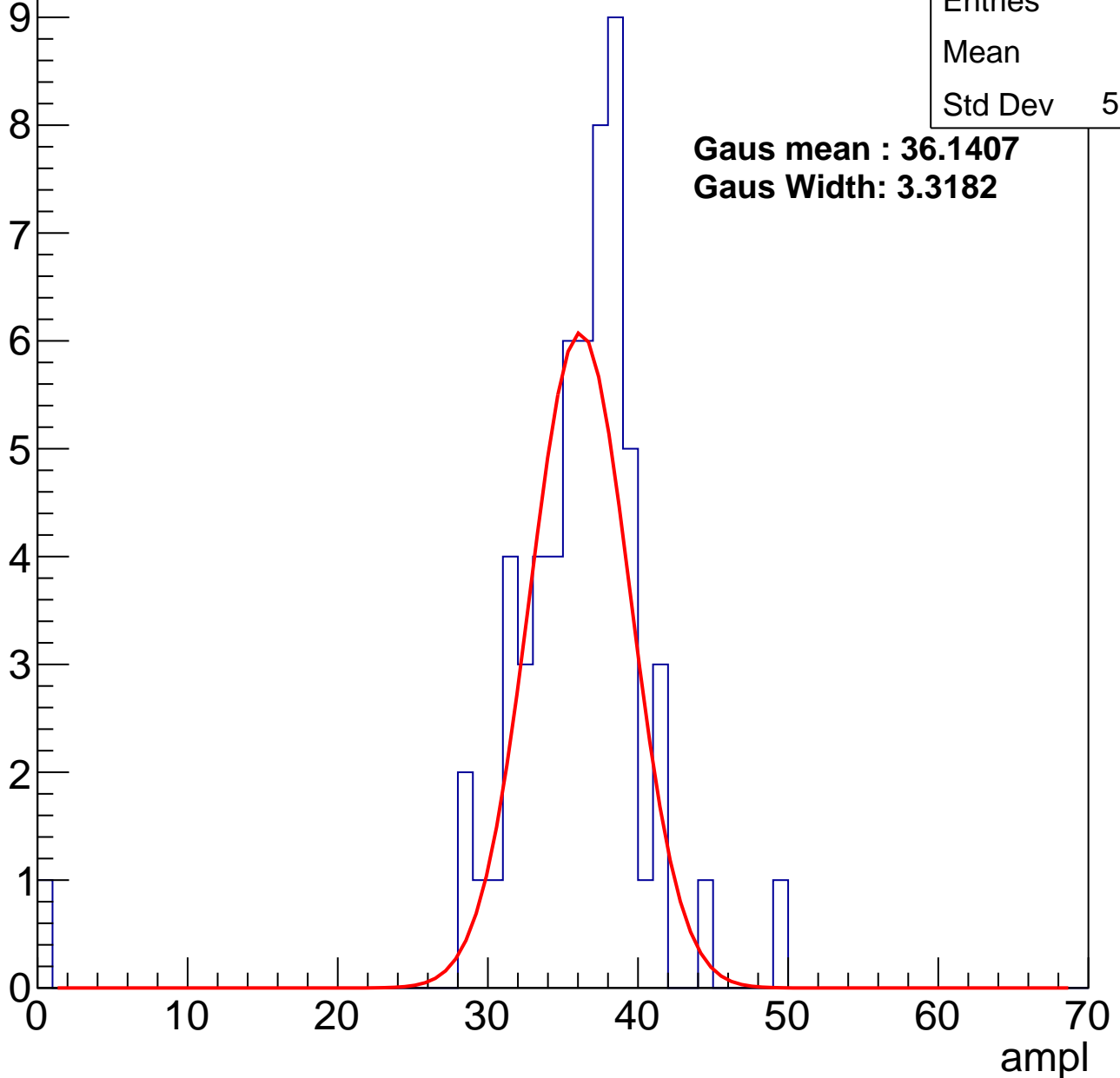
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.3
Std Dev	5.914

Gaus mean : 36.1407

Gaus Width: 3.3182



B1L101S, U4-ch4, adc2

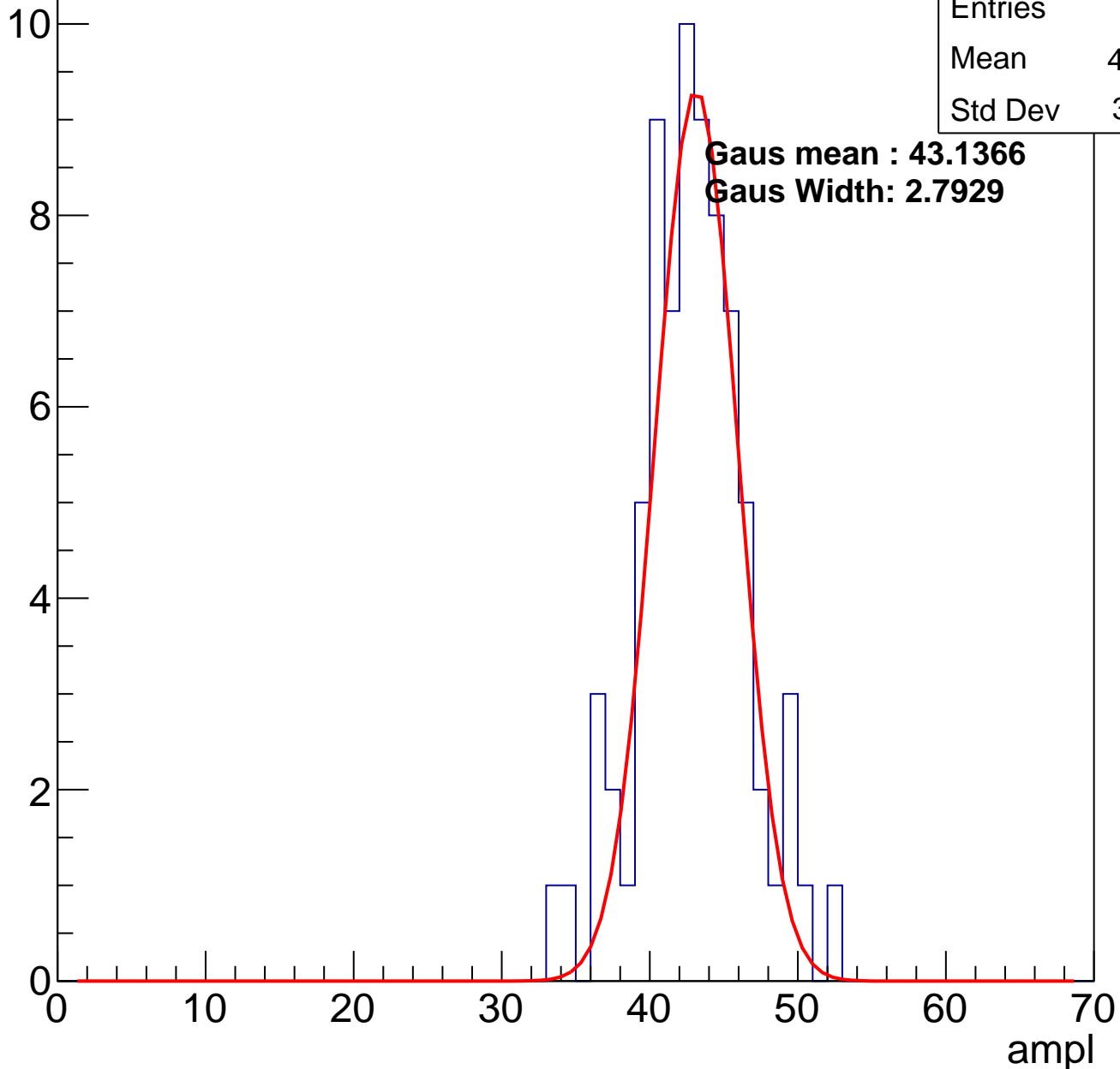
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	42.42
Std Dev	3.581

Gaus mean : 43.1366

Gaus Width: 2.7929

Entry

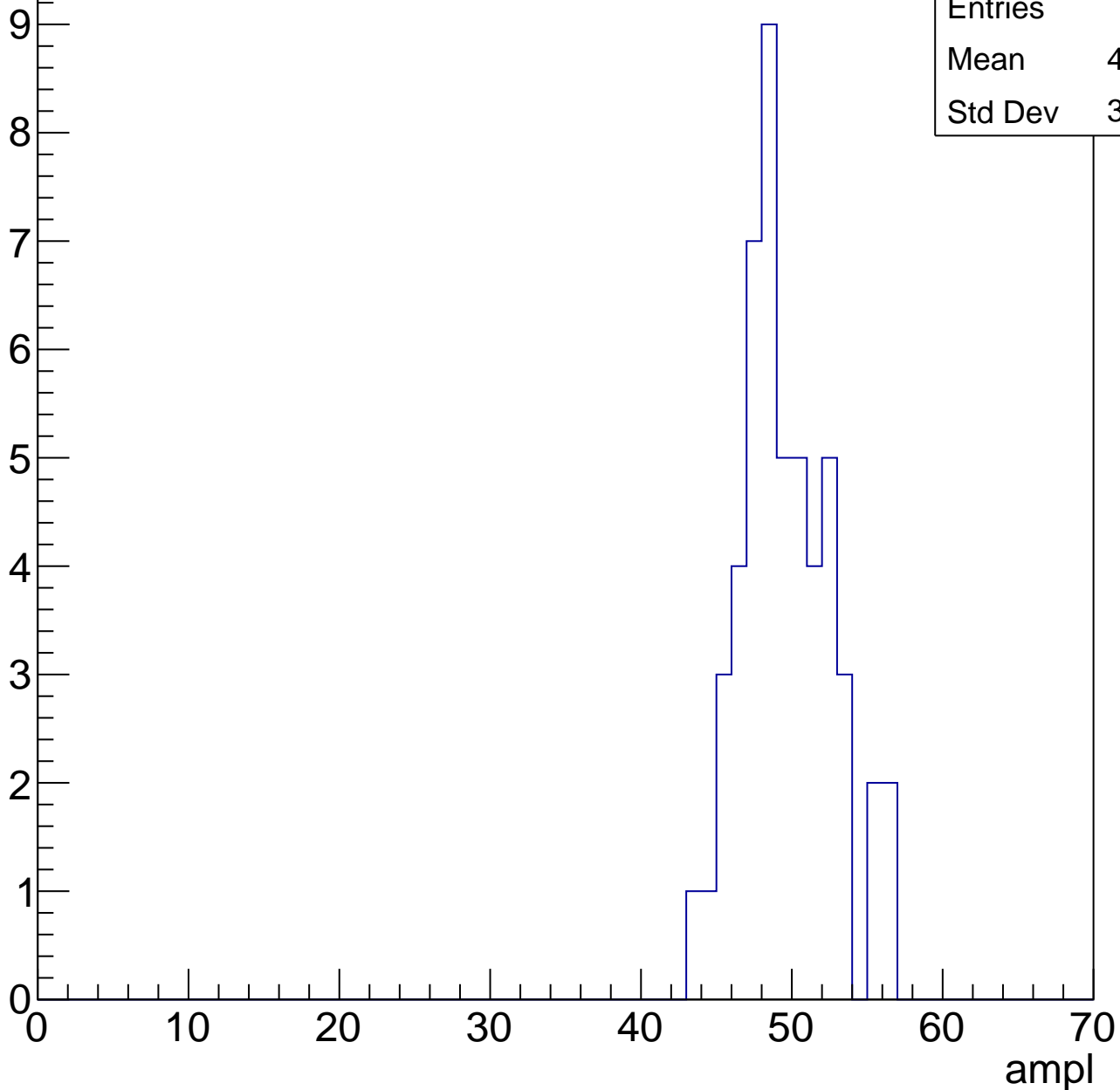


B1L101S, U4-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	49.16
Std Dev	3.019

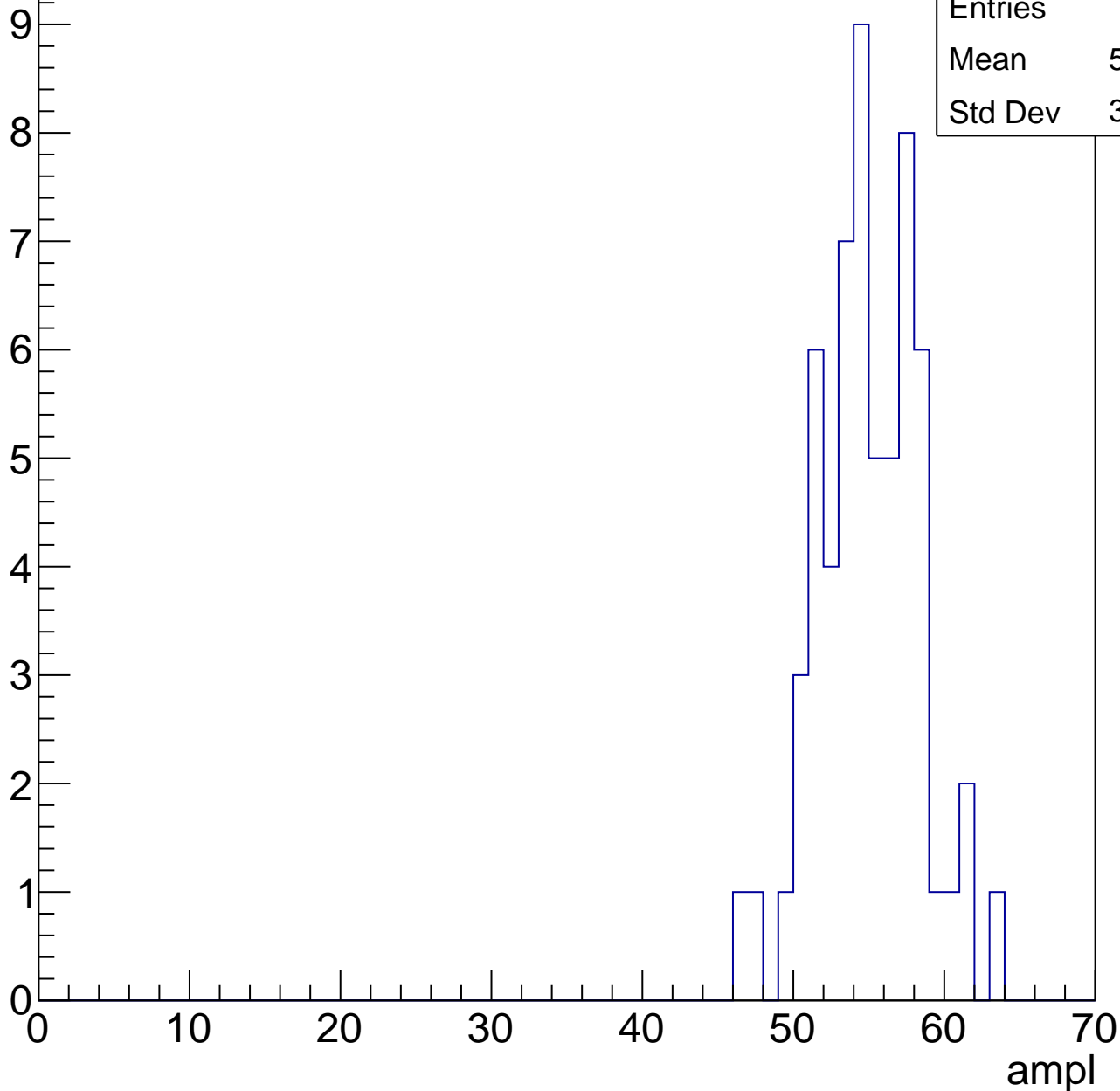


B1L101S, U4-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

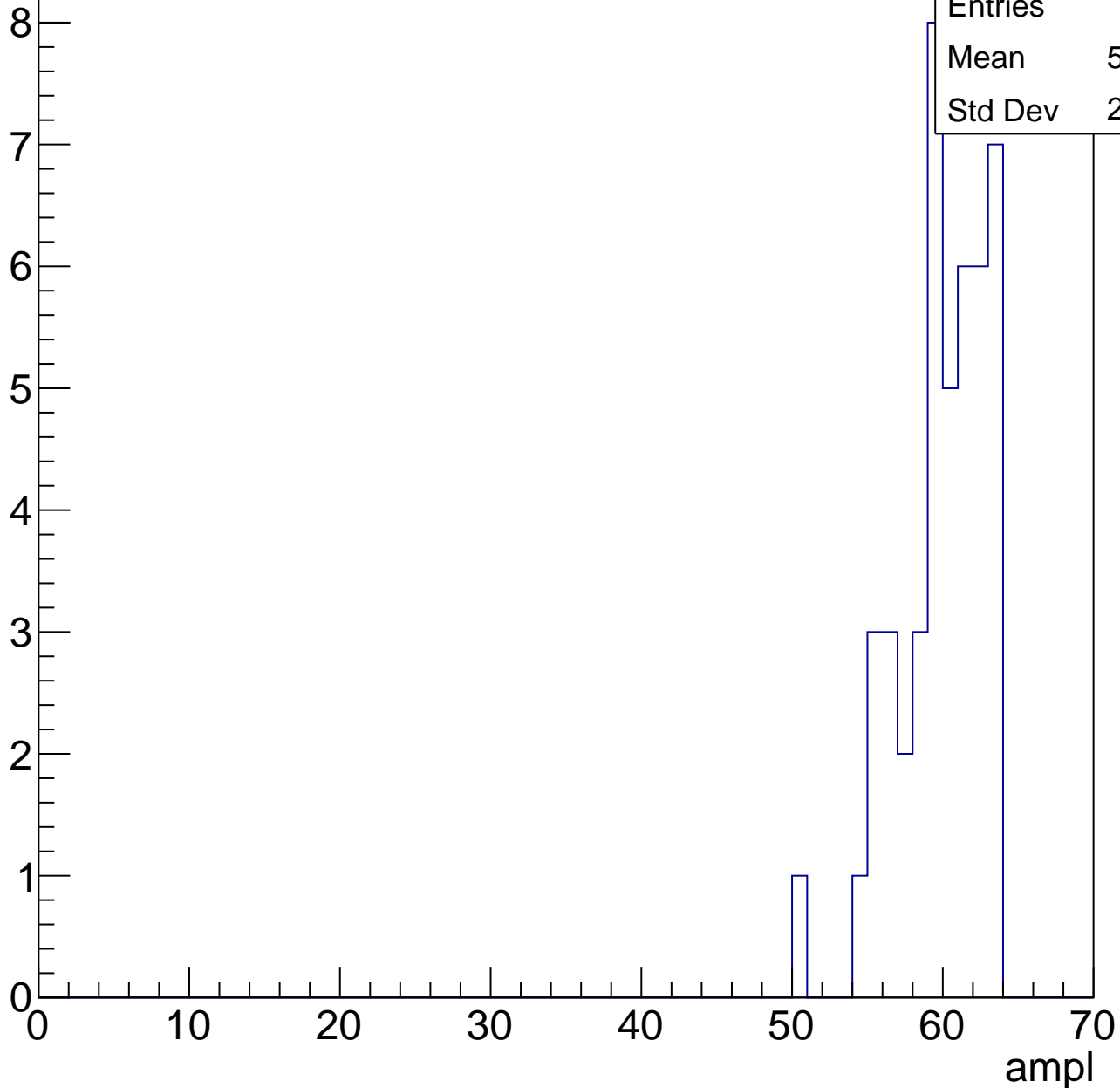
Entries	61
Mean	54.52
Std Dev	3.337



B1L101S, U4-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

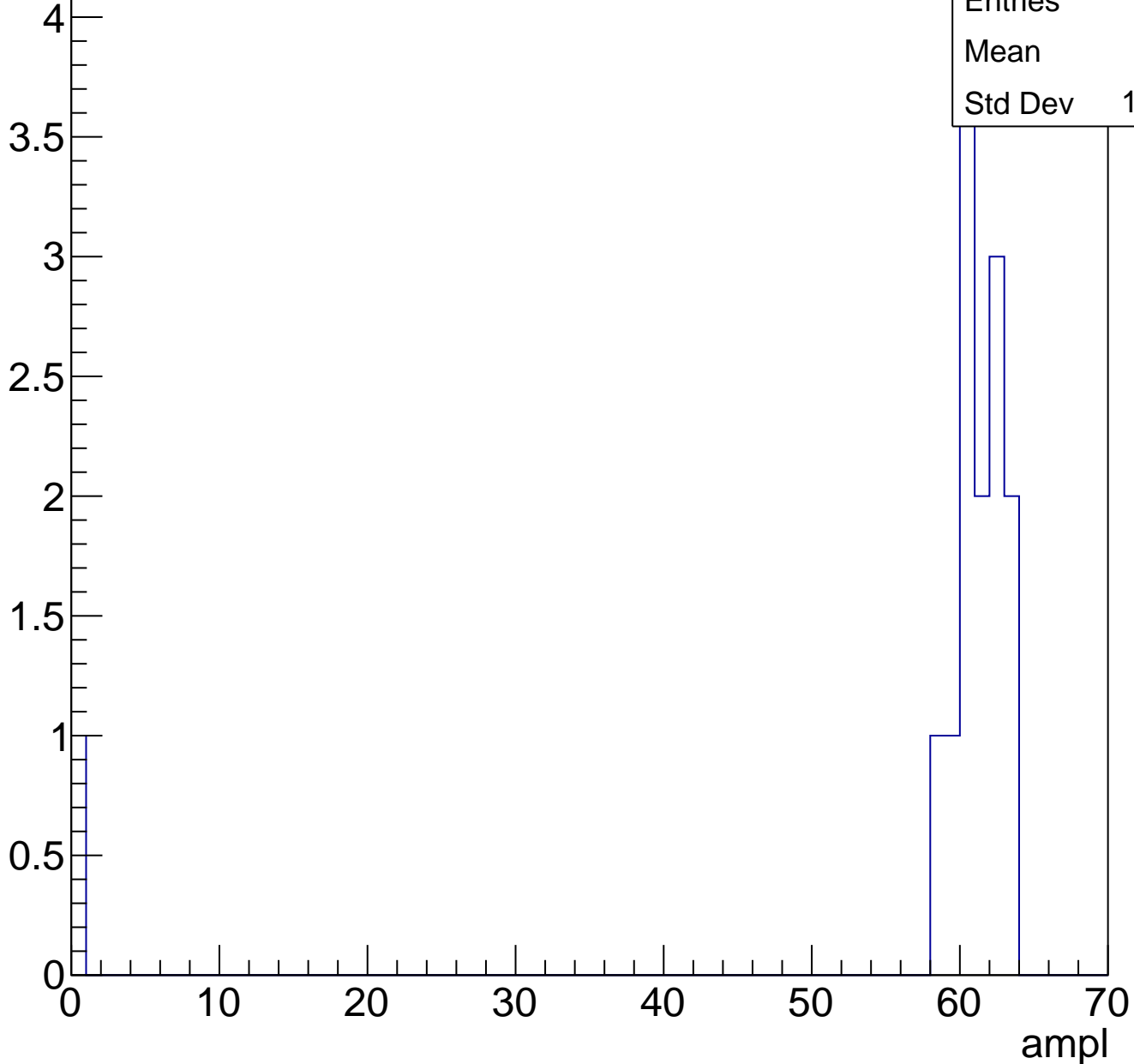


Entries	45
Mean	59.47
Std Dev	2.895

B1L101S, U4-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch5, adc0

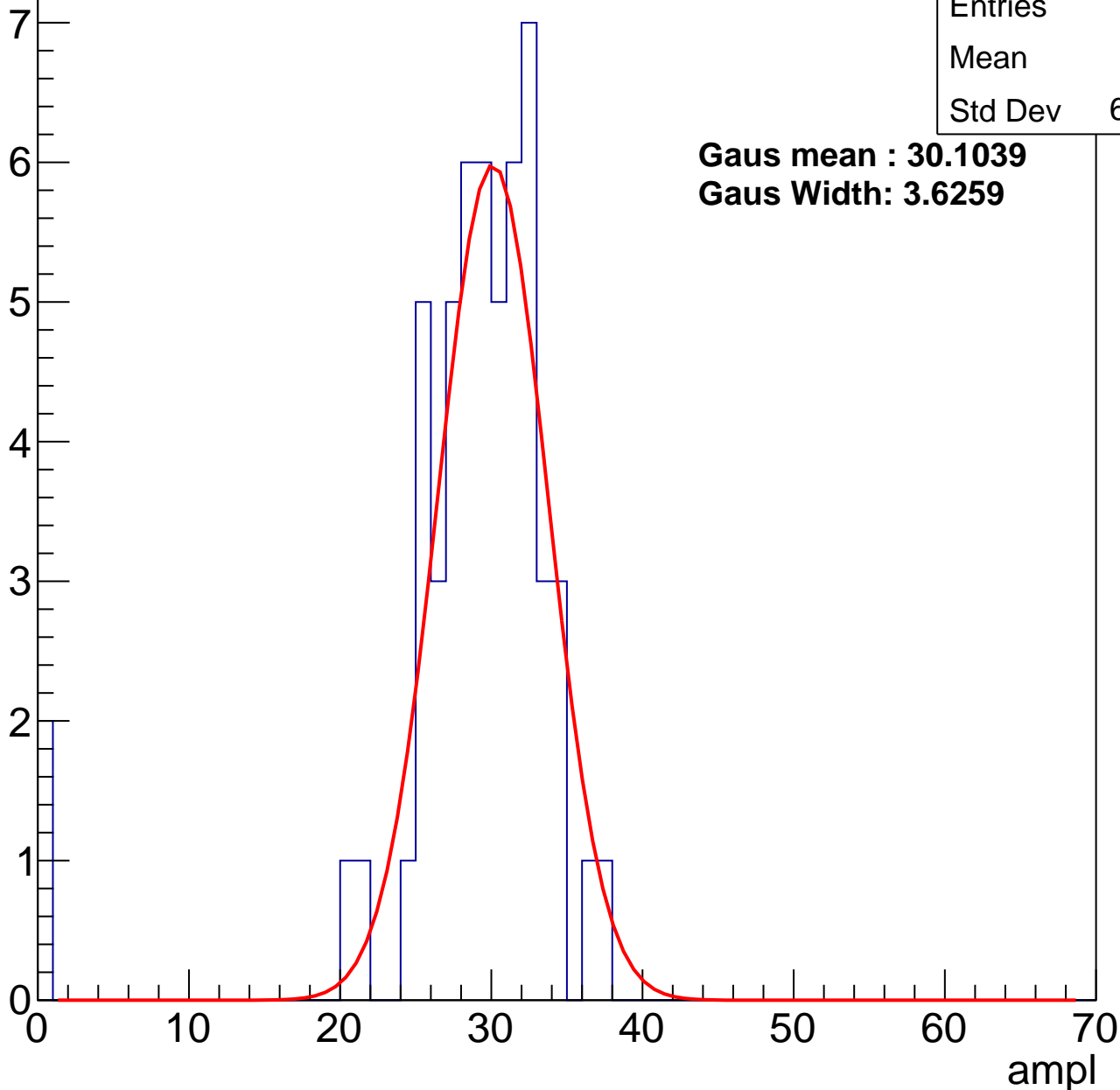
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.2
Std Dev	6.376

Gaus mean : 30.1039

Gaus Width: 3.6259



B1L101S, U4-ch5, adc1

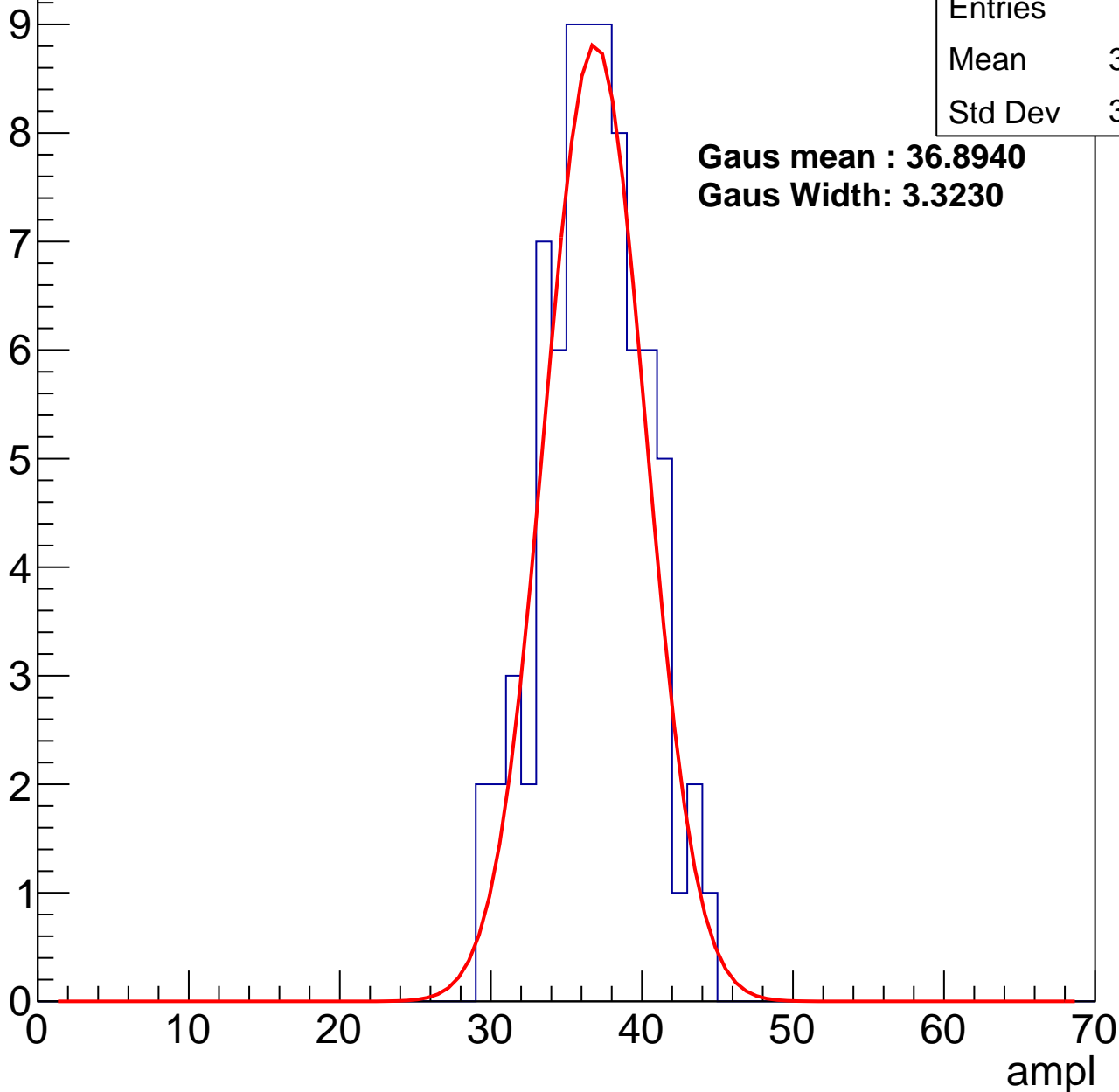
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.37
Std Dev	3.352

Gaus mean : 36.8940

Gaus Width: 3.3230



B1L101S, U4-ch5, adc2

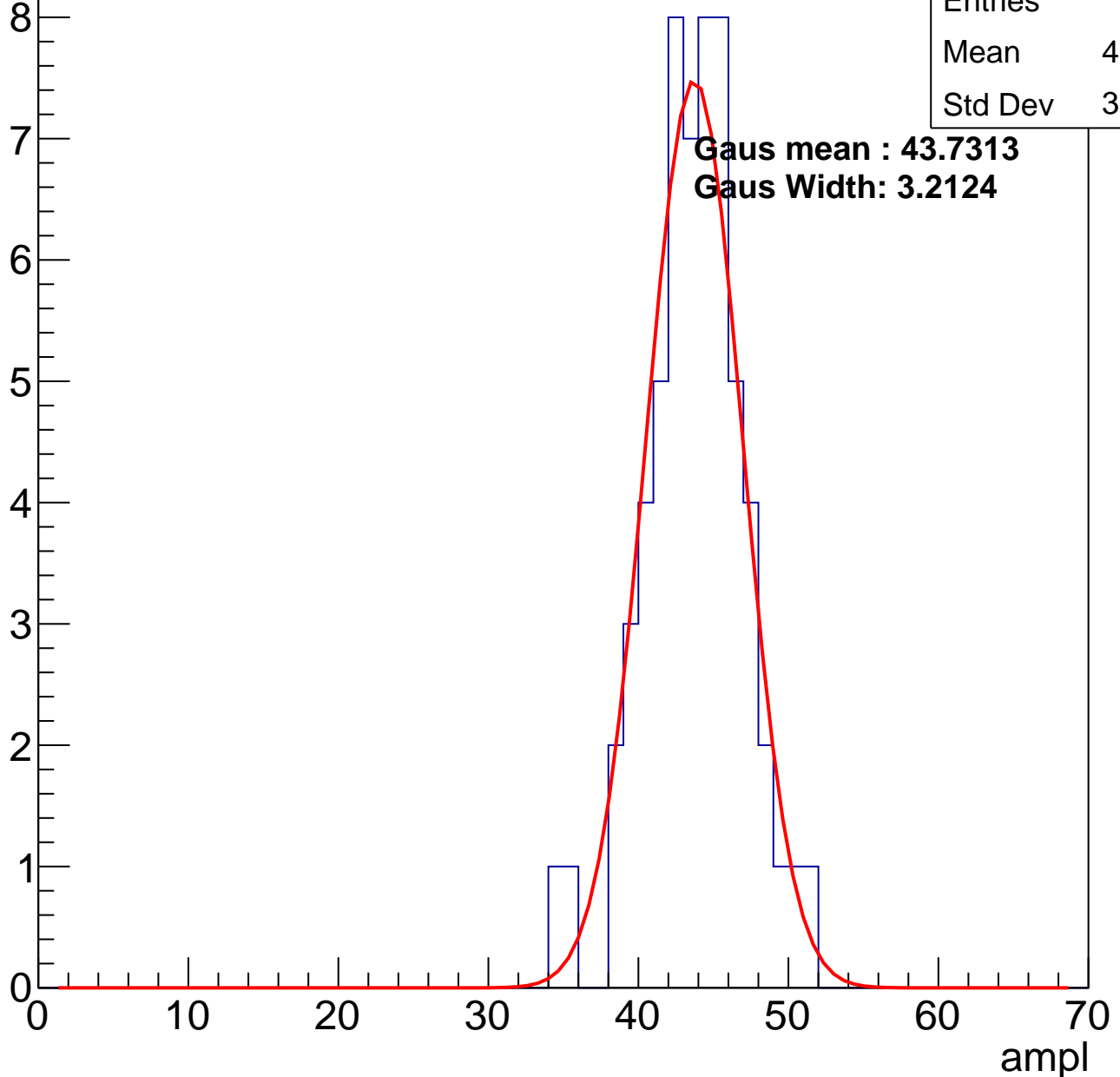
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.28
Std Dev	3.265

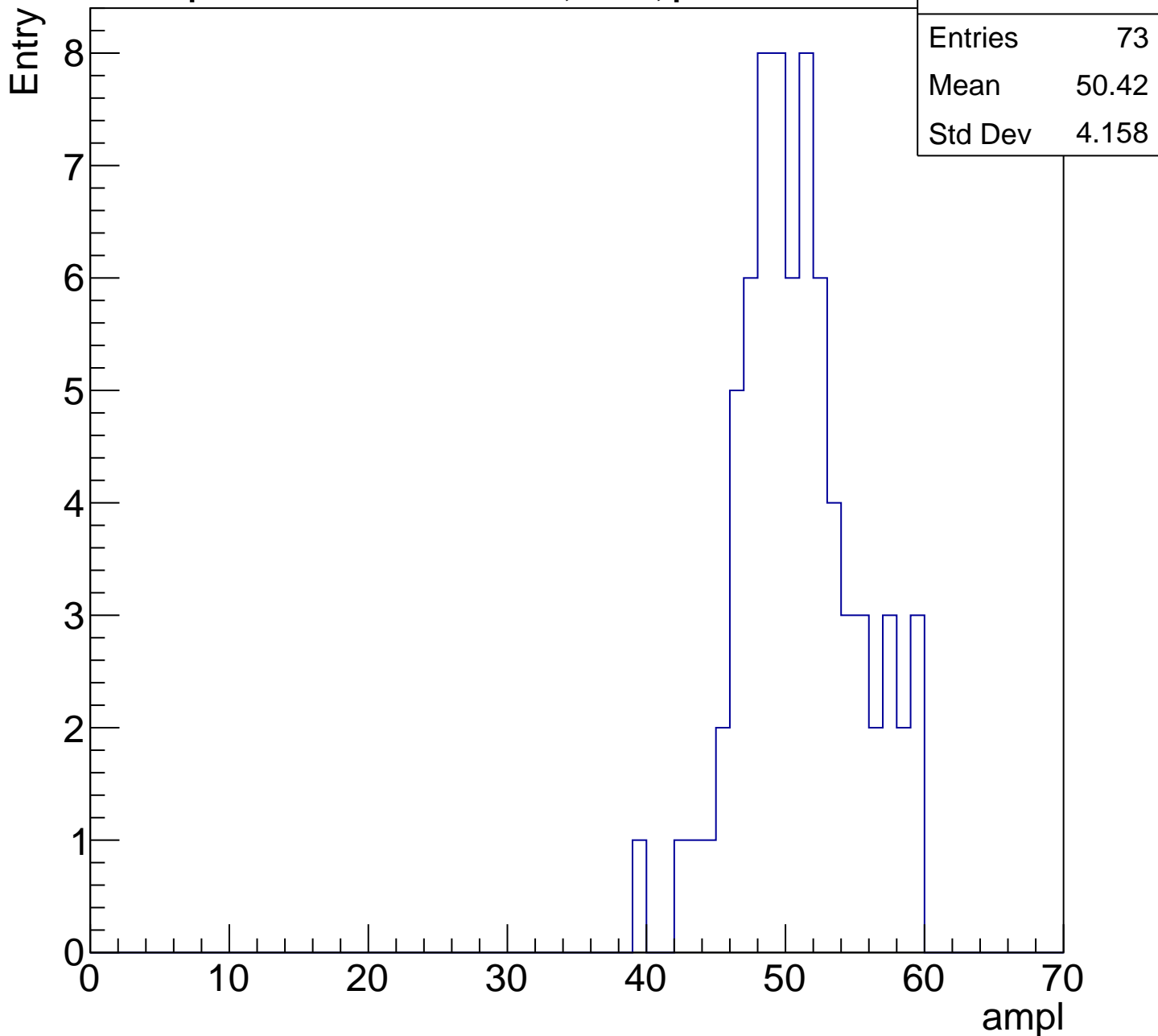
Gaus mean : 43.7313

Gaus Width: 3.2124



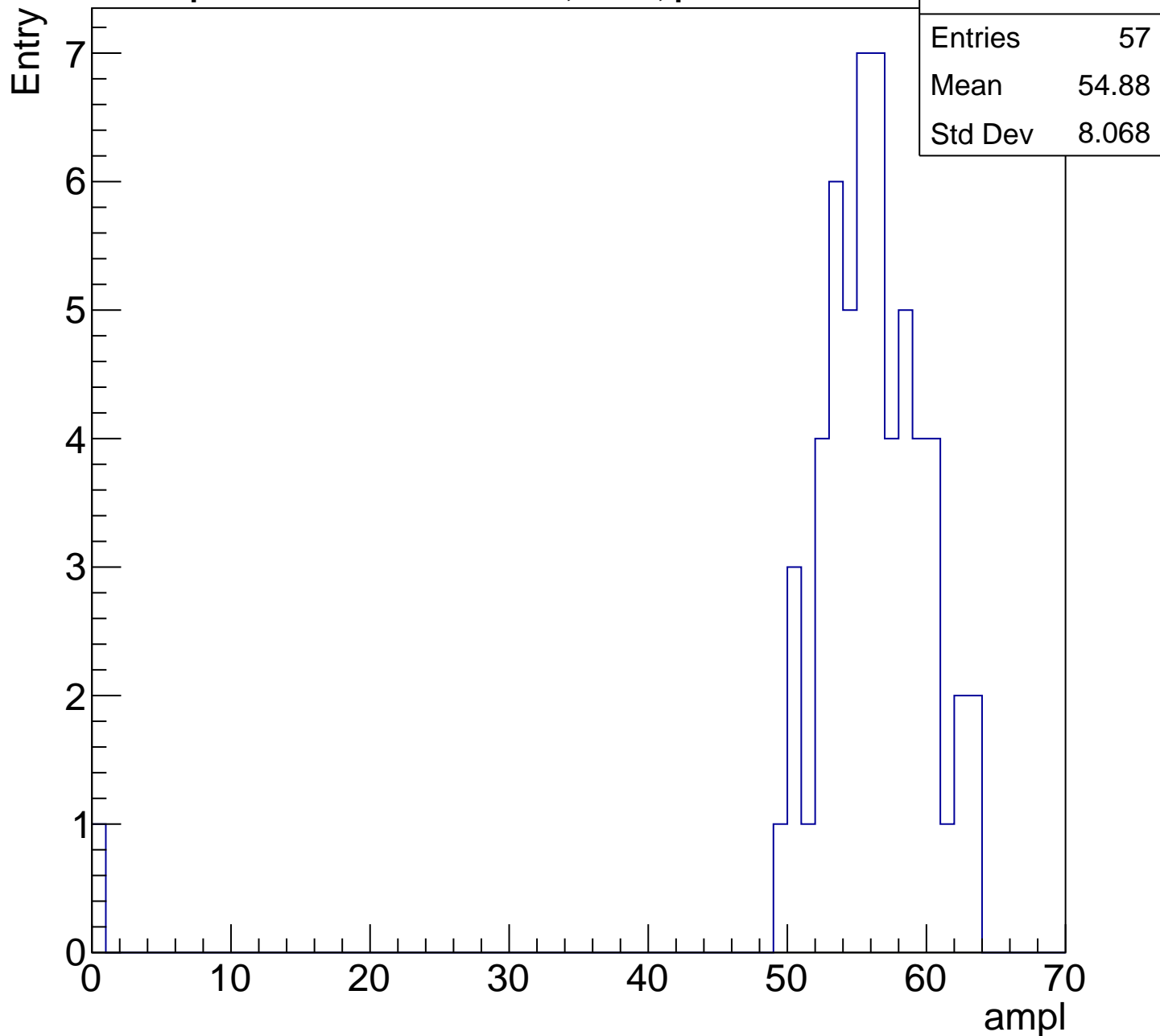
B1L101S, U4-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U4-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch6, adc0

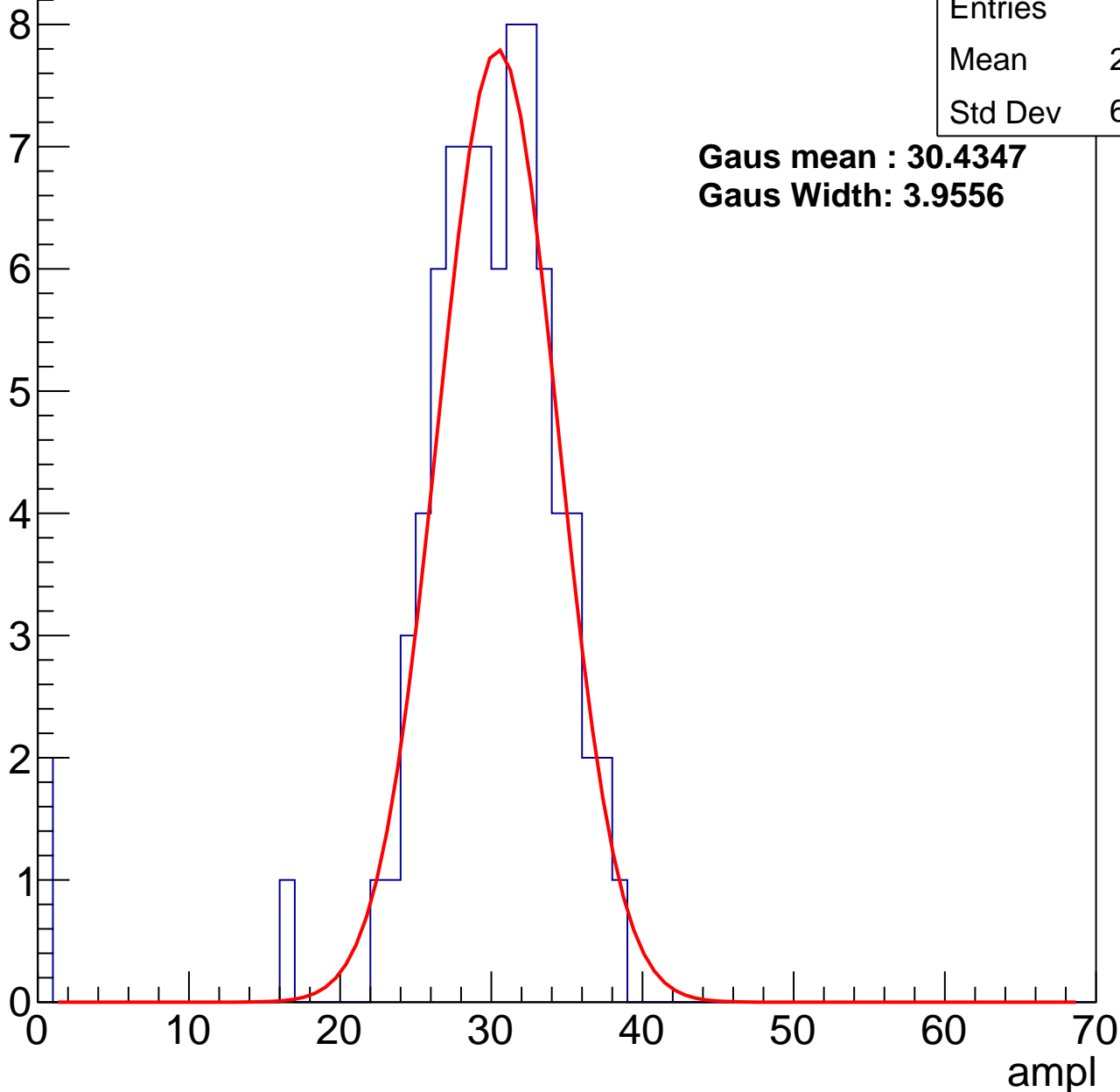
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	28.99
Std Dev	6.043

Gaus mean : 30.4347

Gaus Width: 3.9556



B1L101S, U4-ch6, adc1

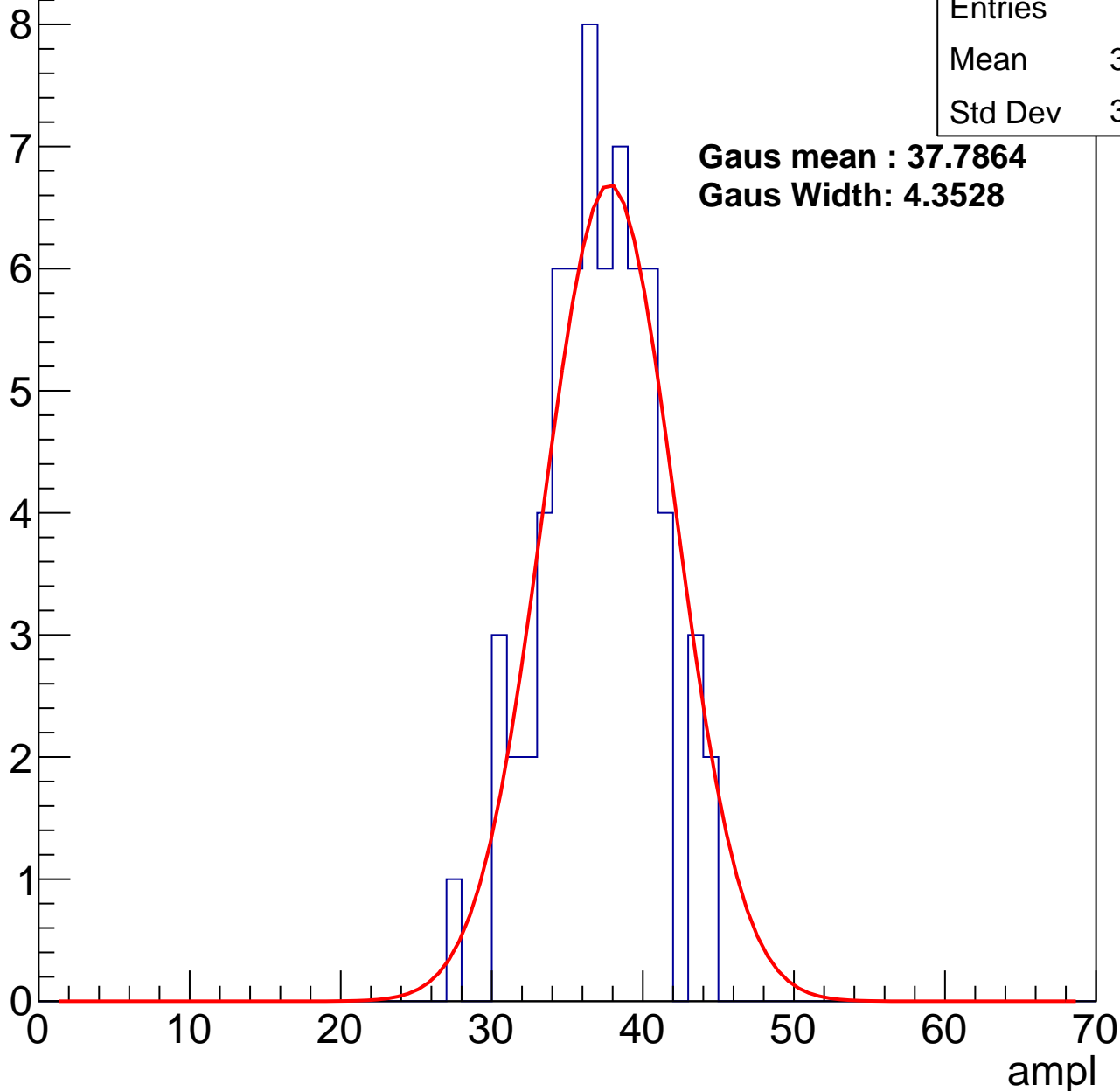
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.67
Std Dev	3.624

Gaus mean : 37.7864

Gaus Width: 4.3528



B1L101S, U4-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	43.37
Std Dev	3.169

Gaus mean : 43.6496

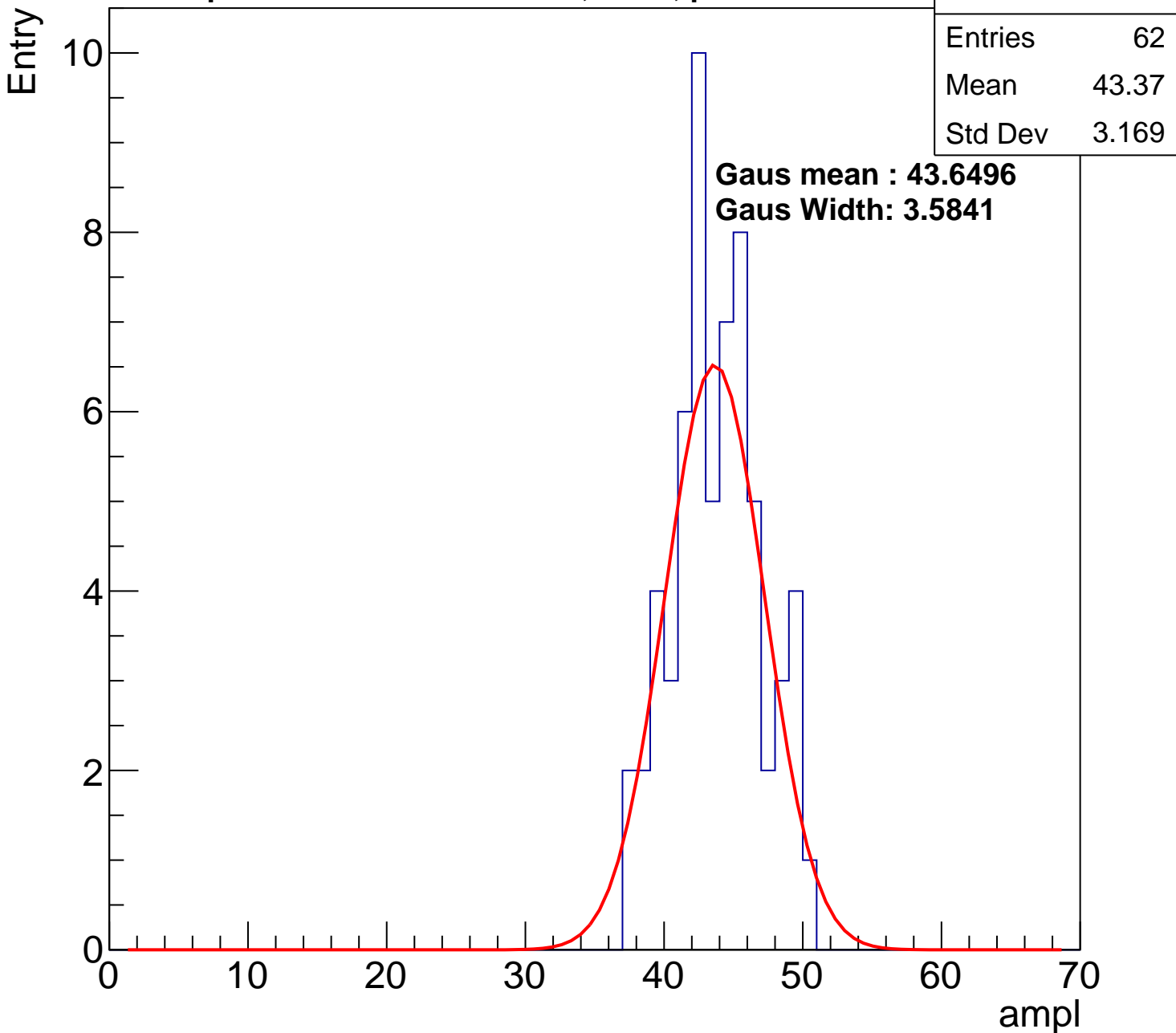
Gaus Width: 3.5841

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

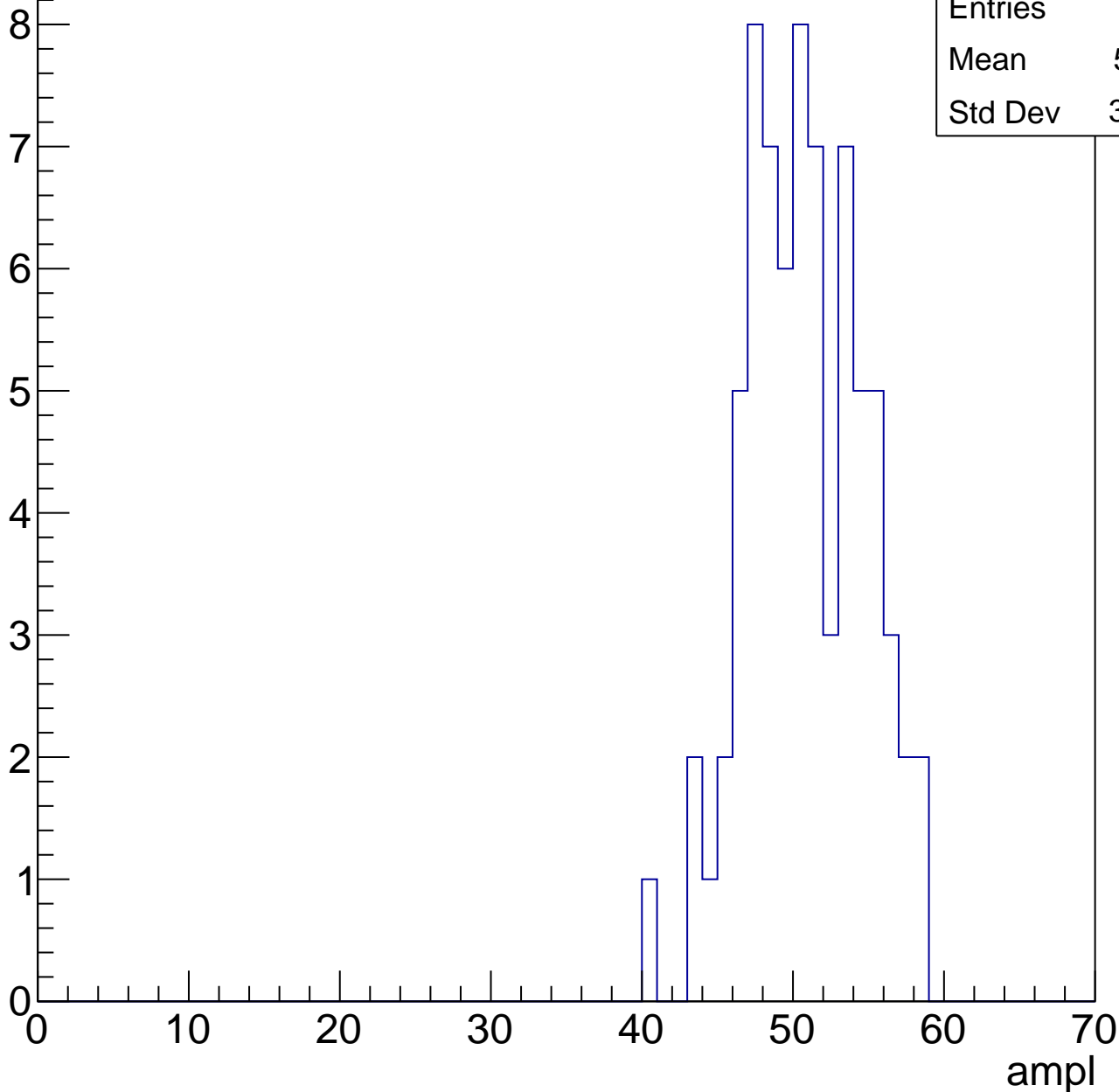


B1L101S, U4-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	50.31
Std Dev	3.827

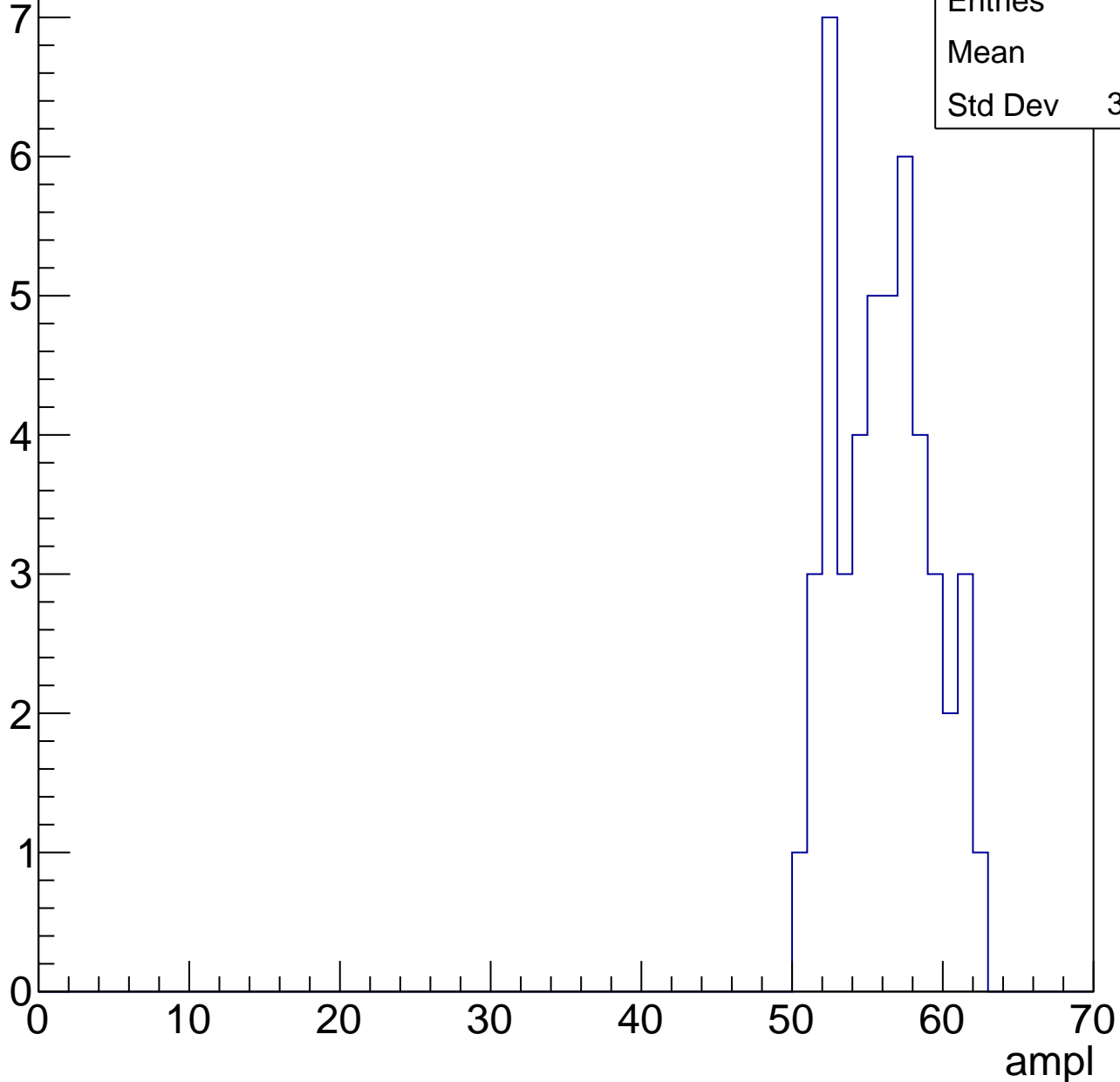


B1L101S, U4-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	55.6
Std Dev	3.112



B1L101S, U4-ch6, adc5

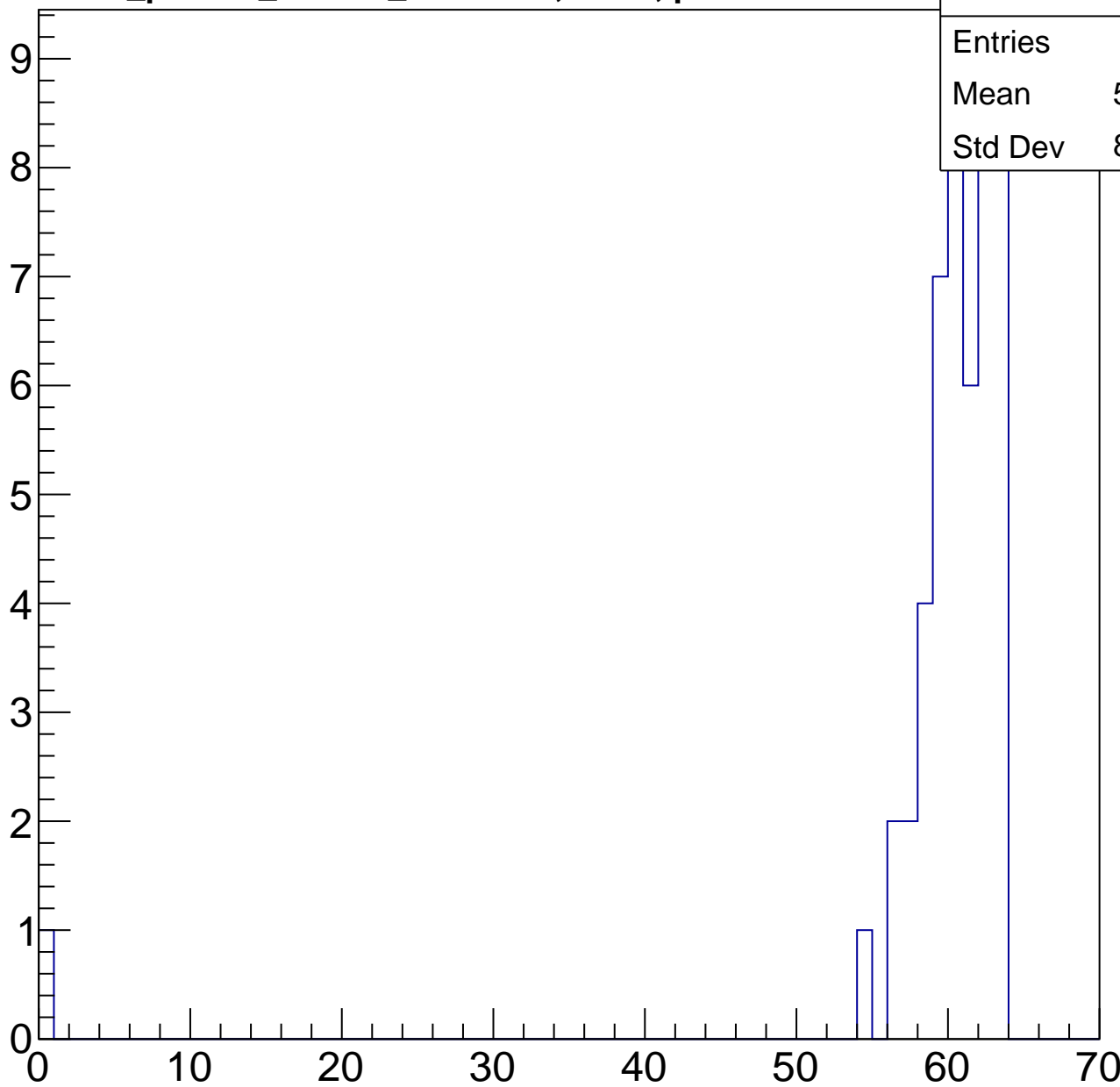
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.04
Std Dev	8.879

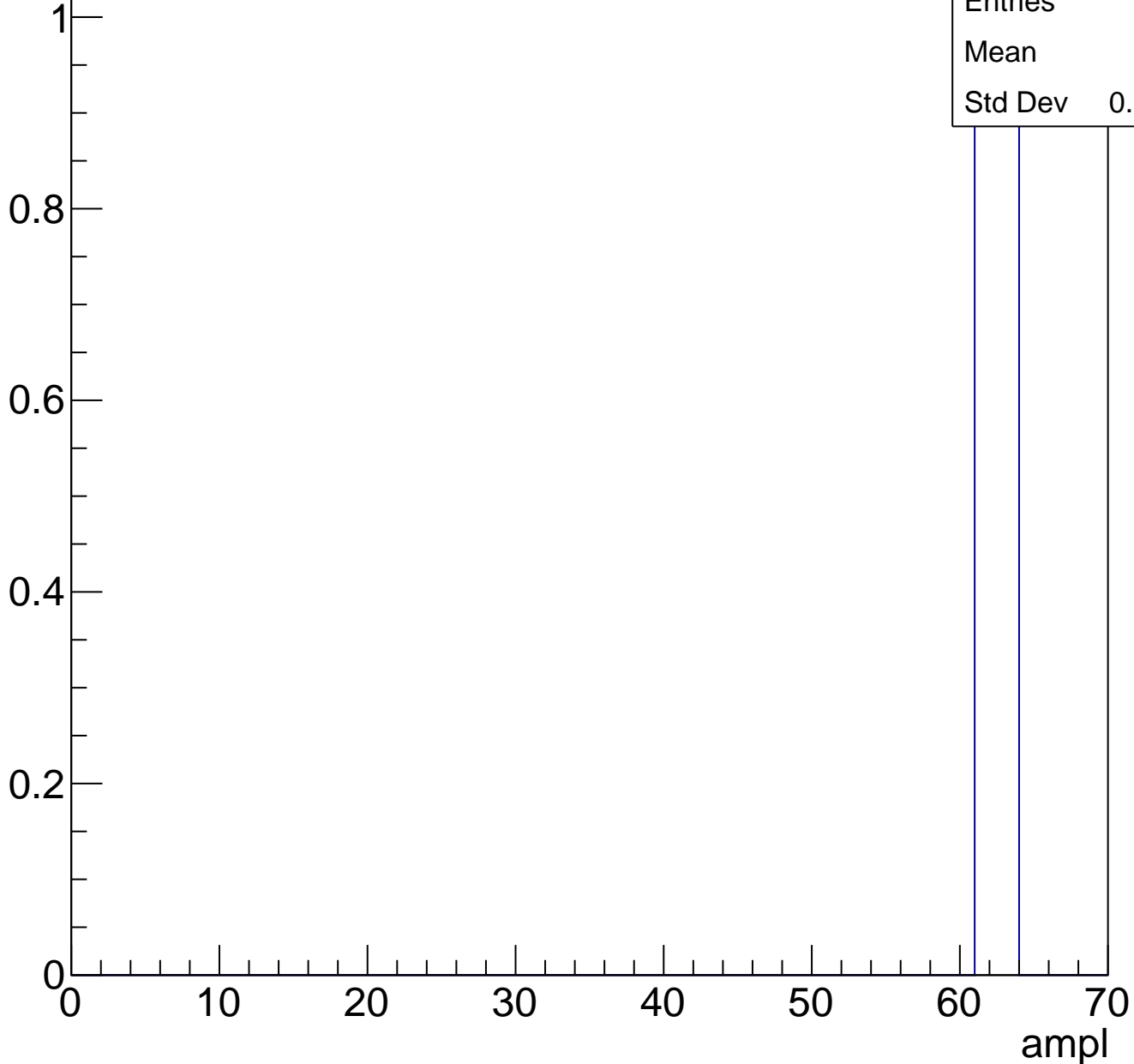
ampl



B1L101S, U4-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch7, adc0

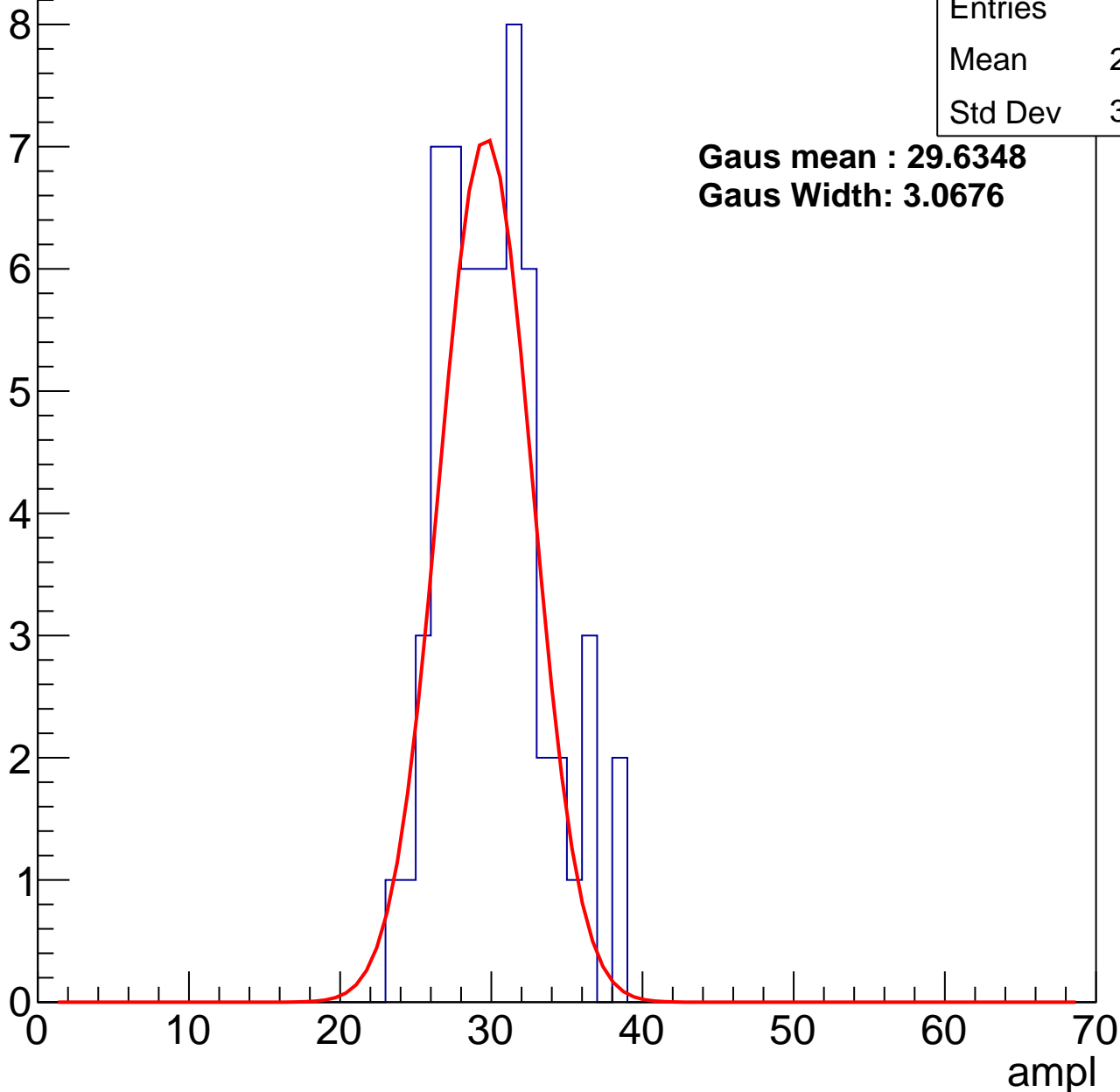
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.64
Std Dev	3.388

Gaus mean : 29.6348

Gaus Width: 3.0676



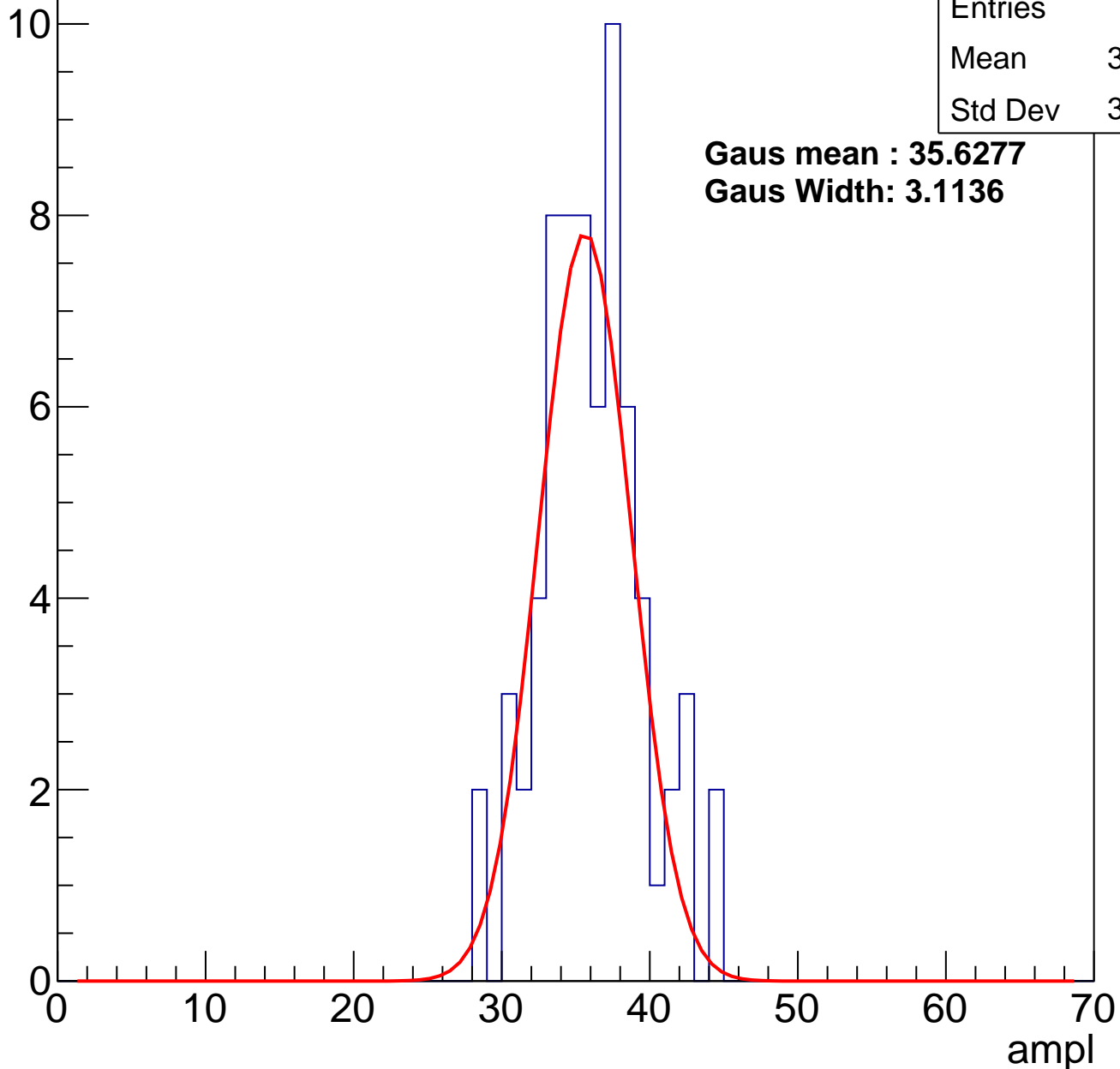
B1L101S, U4-ch7, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	35.62
Std Dev	3.456

Gaus mean : 35.6277
Gaus Width: 3.1136

Entry



B1L101S, U4-ch7, adc2

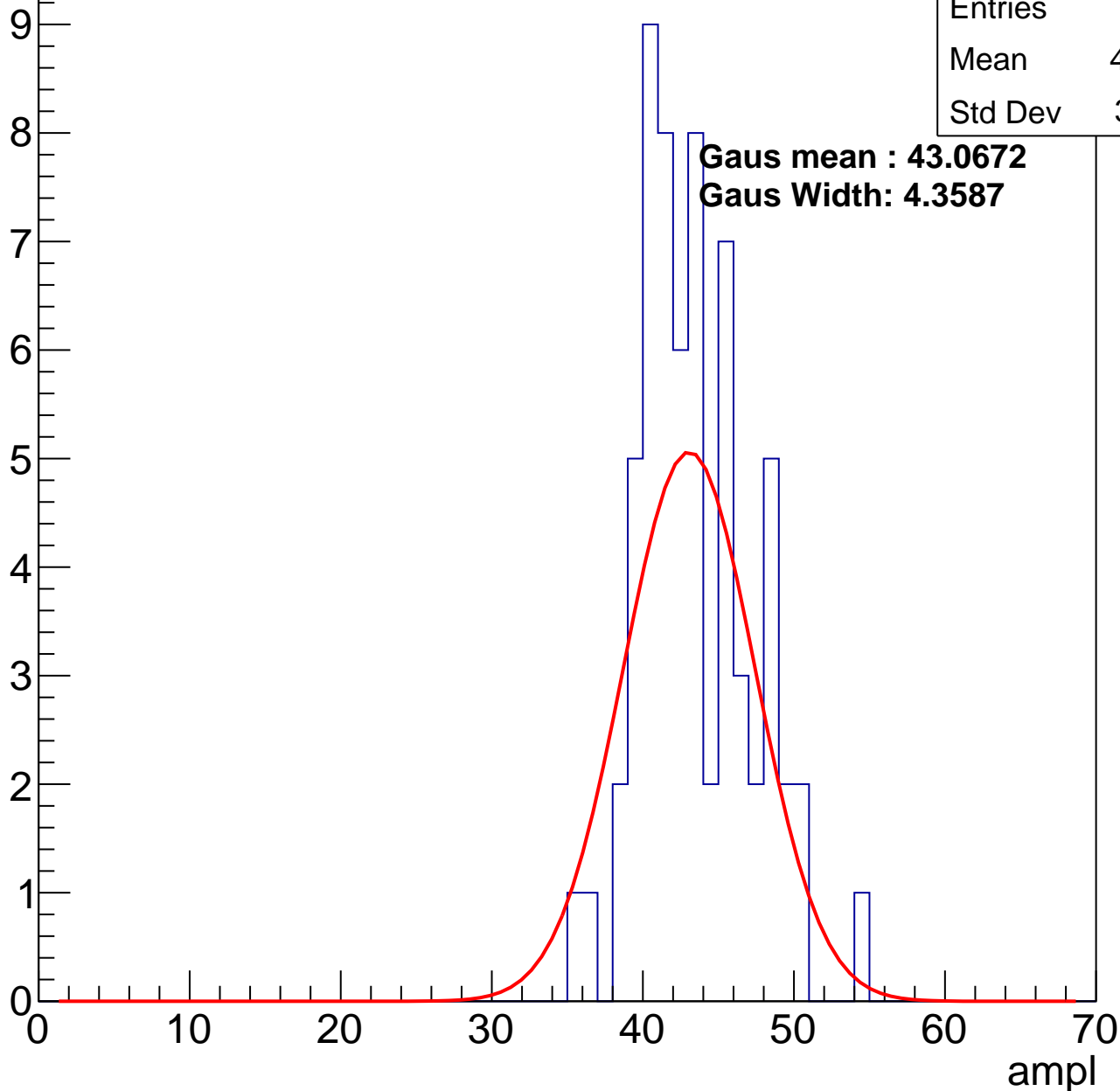
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.02
Std Dev	3.681

Gaus mean : 43.0672

Gaus Width: 4.3587

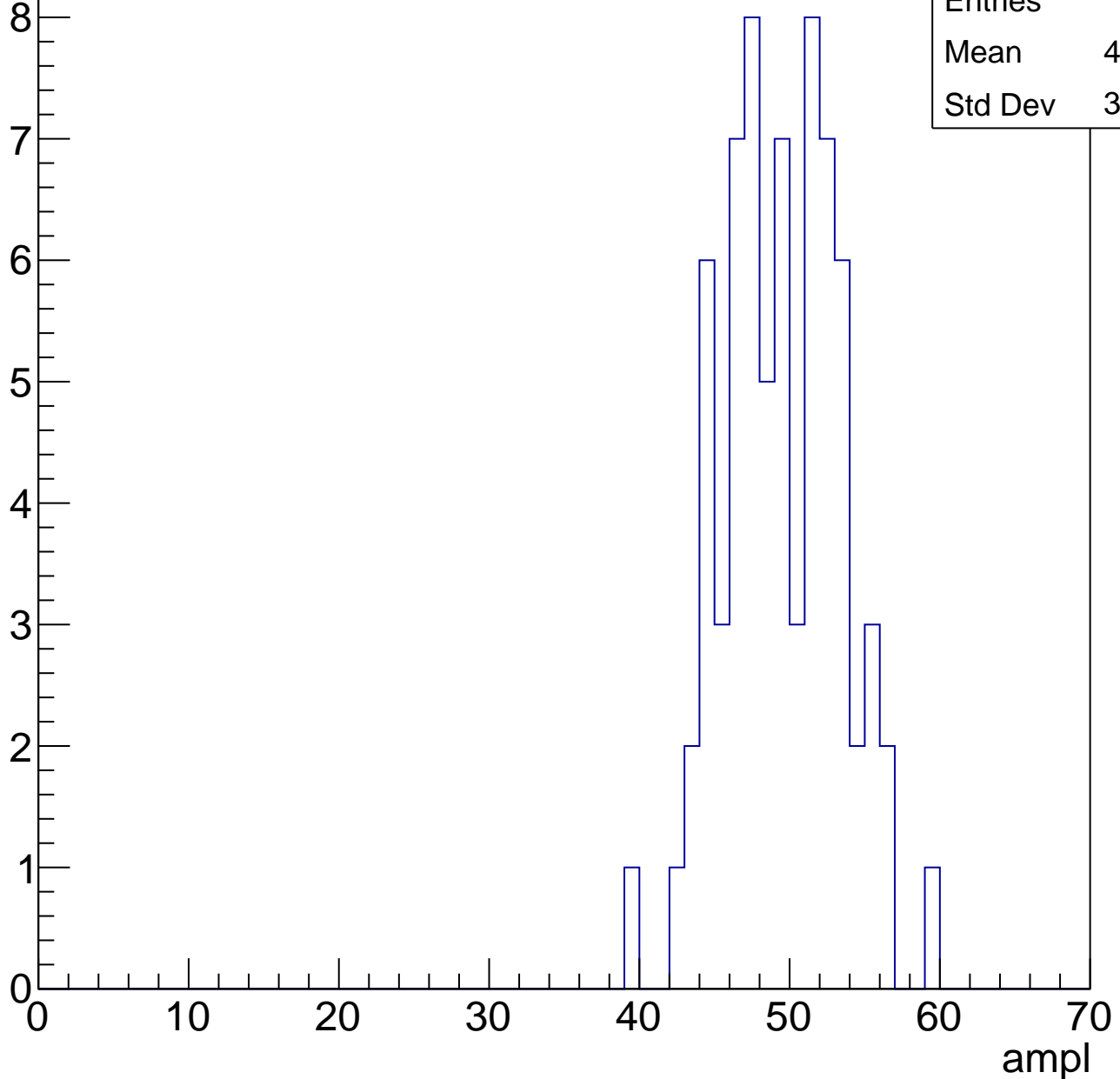


B1L101S, U4-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	49.04
Std Dev	3.853

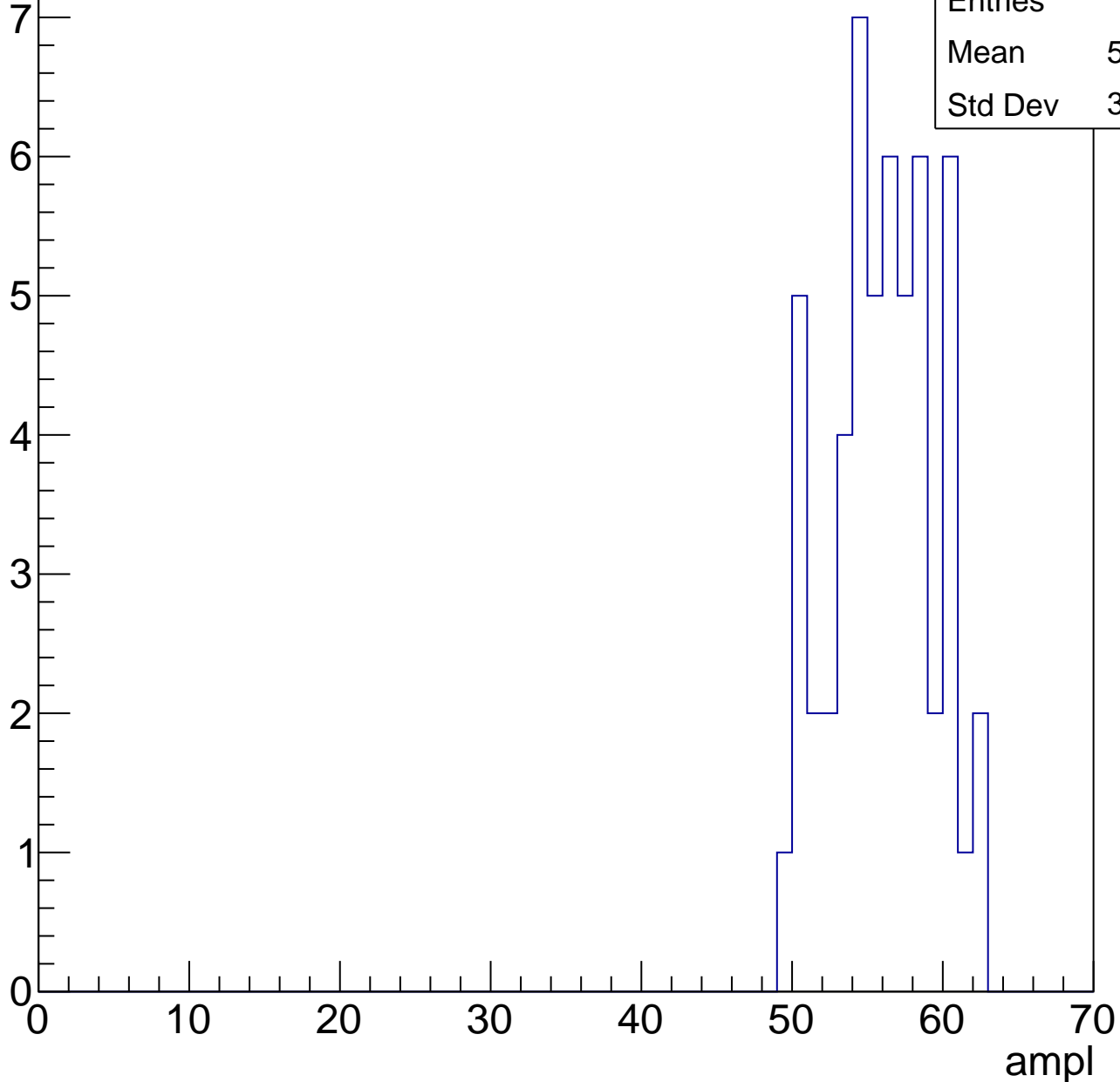


B1L101S, U4-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

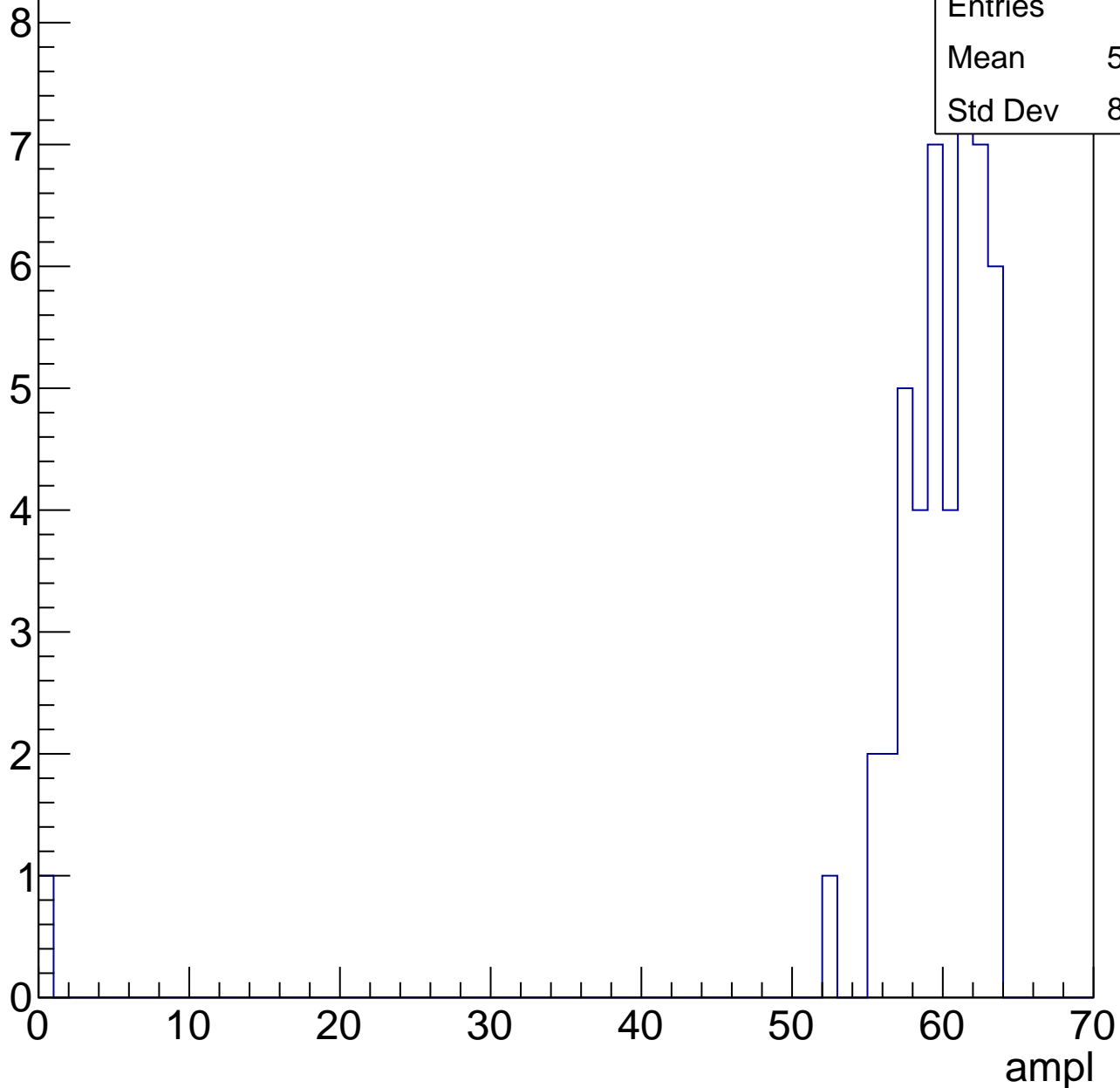
Entries	54
Mean	55.59
Std Dev	3.364



B1L101S, U4-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl

B1L101S, U4-ch8, adc0

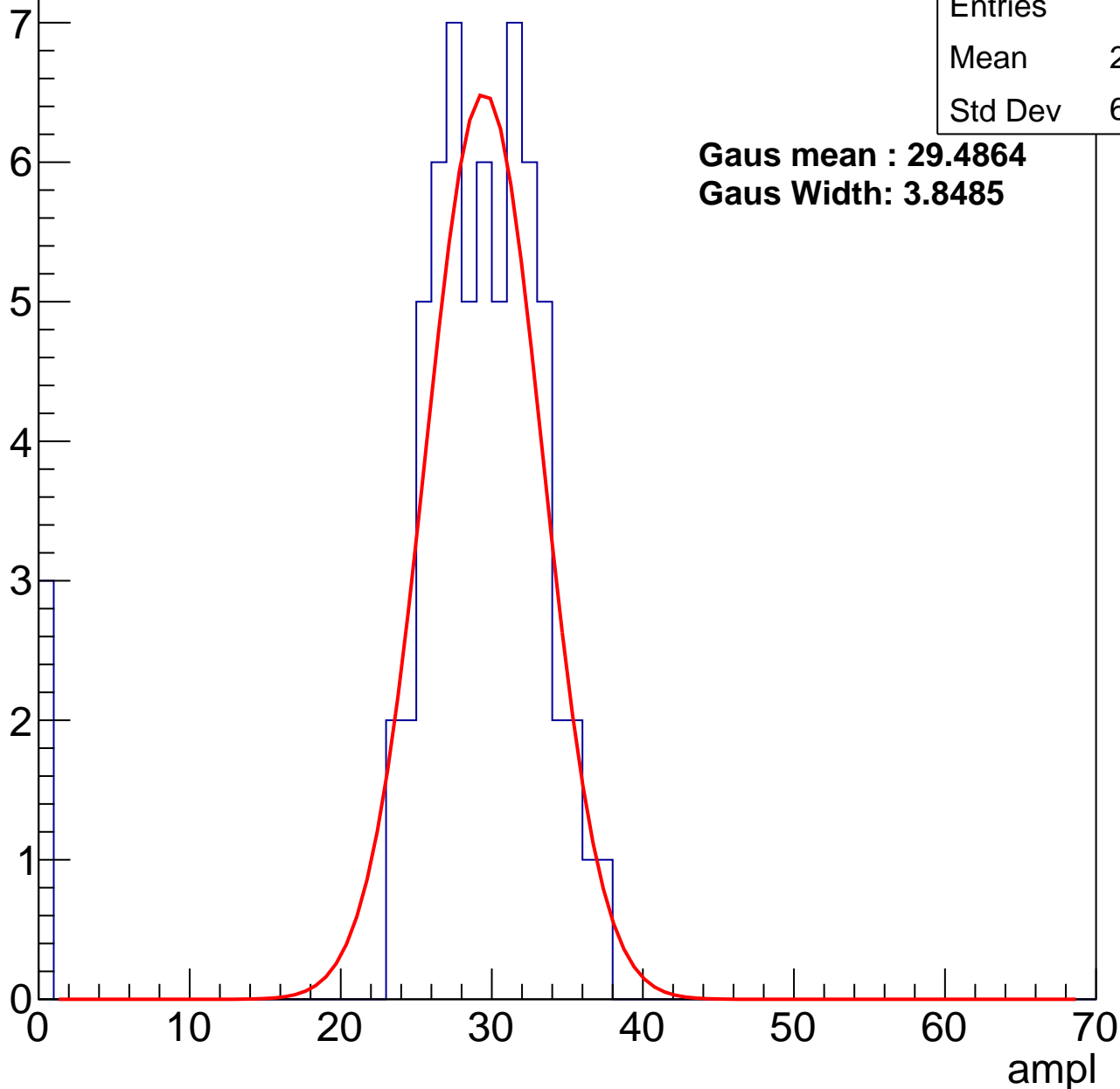
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	27.89
Std Dev	6.944

Gaus mean : 29.4864

Gaus Width: 3.8485



B1L101S, U4-ch8, adc1

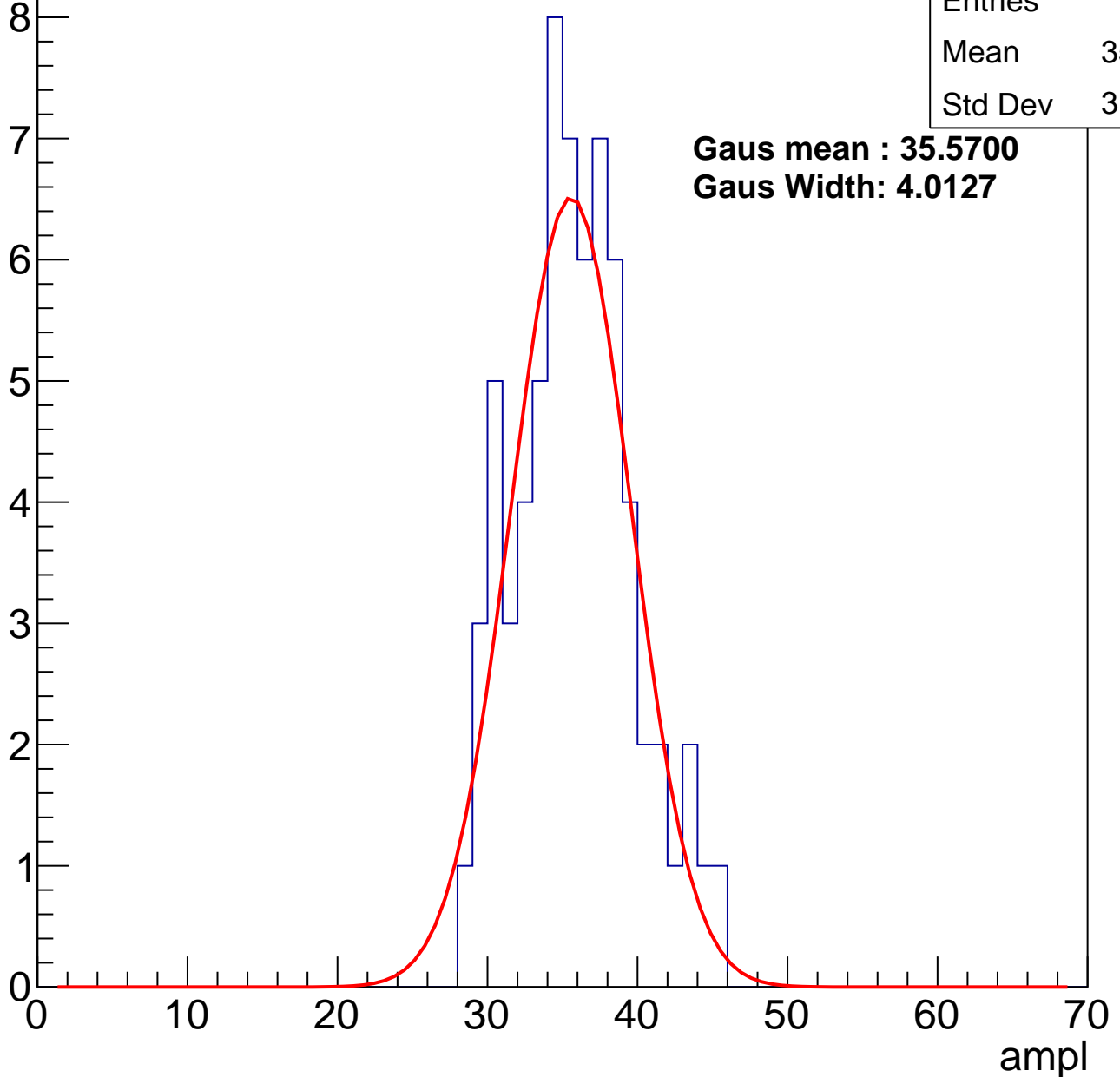
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.38
Std Dev	3.865

Gaus mean : 35.5700

Gaus Width: 4.0127



B1L101S, U4-ch8, adc2

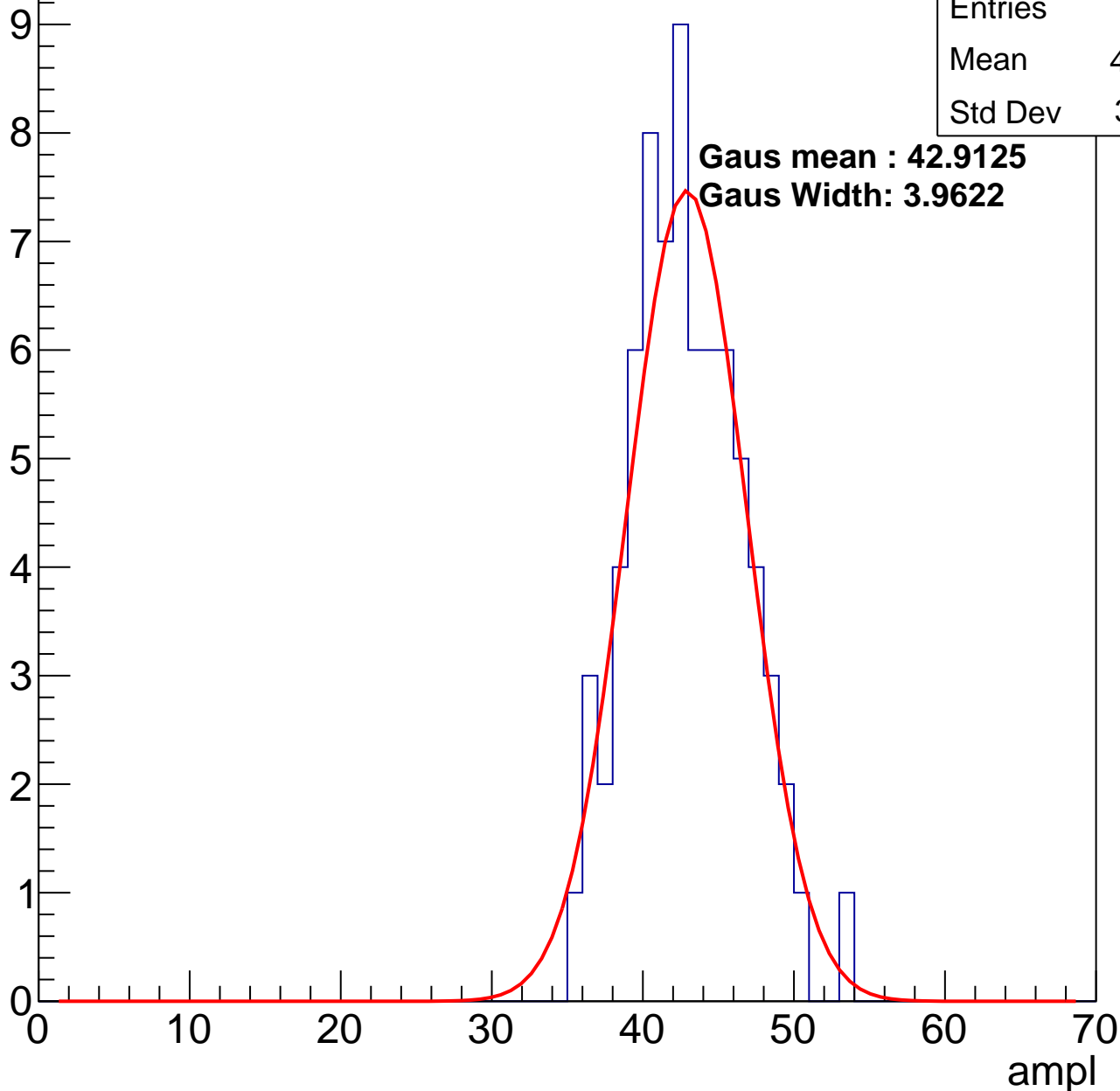
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.47
Std Dev	3.681

Gaus mean : 42.9125

Gaus Width: 3.9622

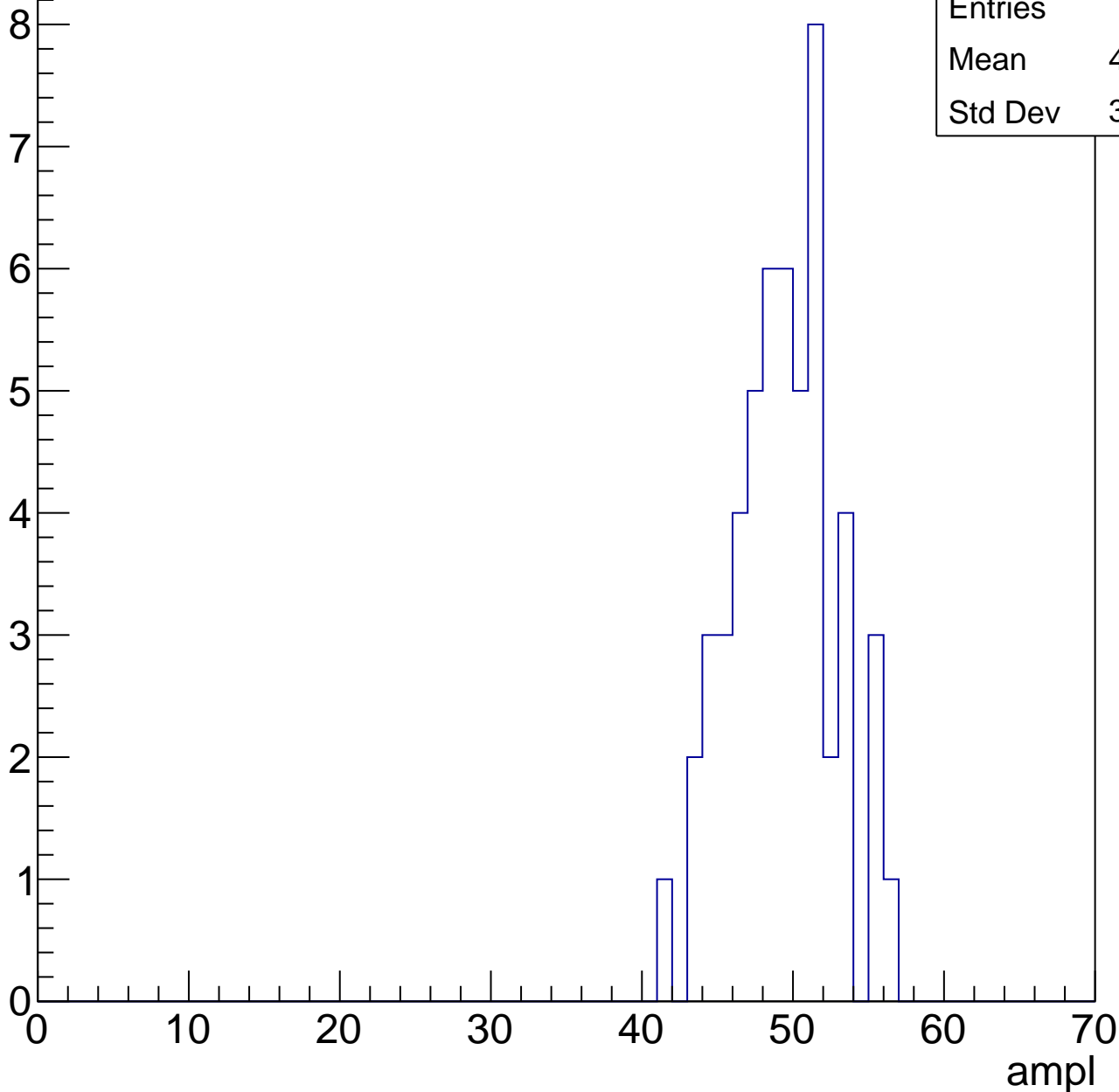


B1L101S, U4-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	48.87
Std Dev	3.354

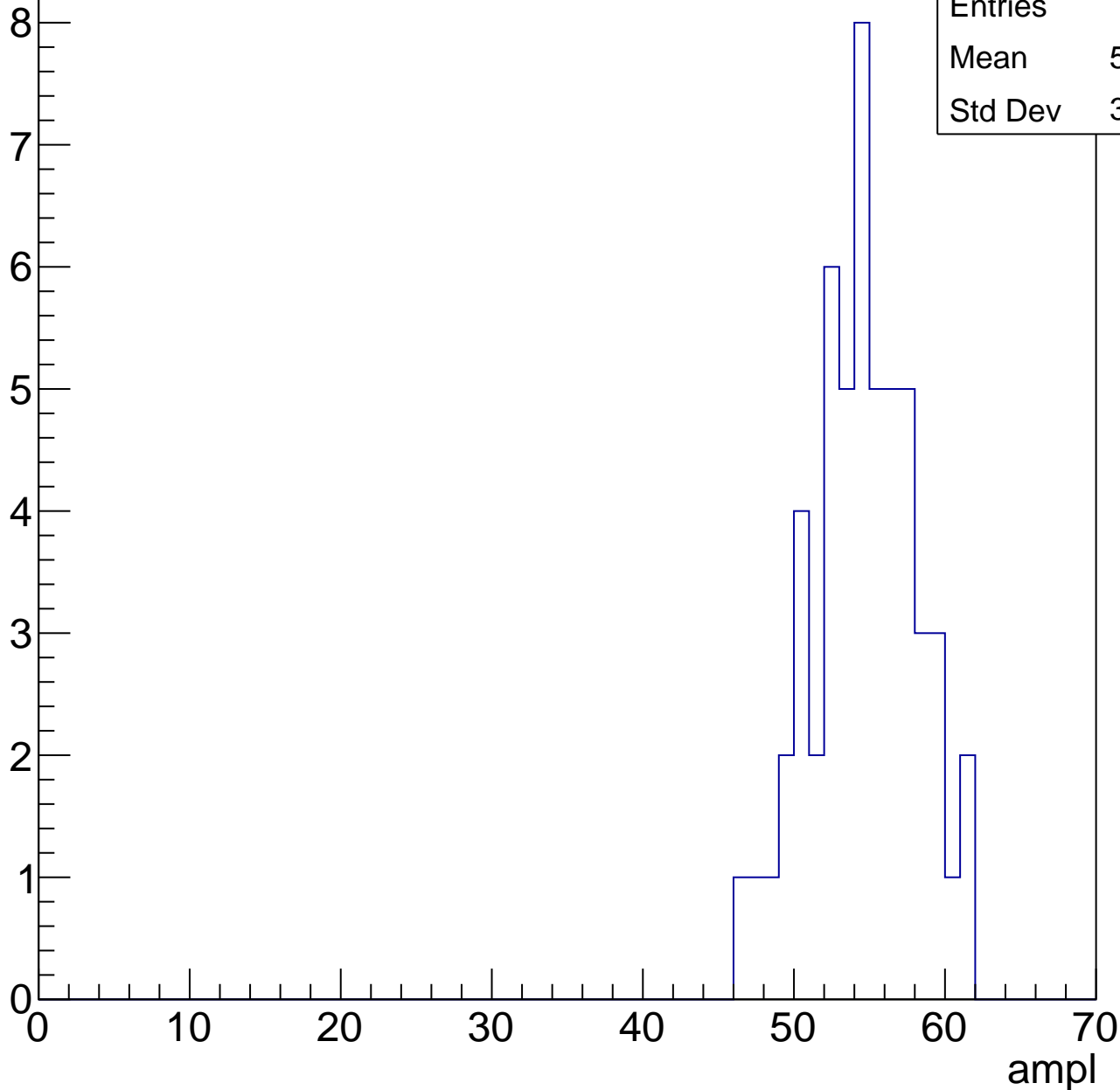


B1L101S, U4-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

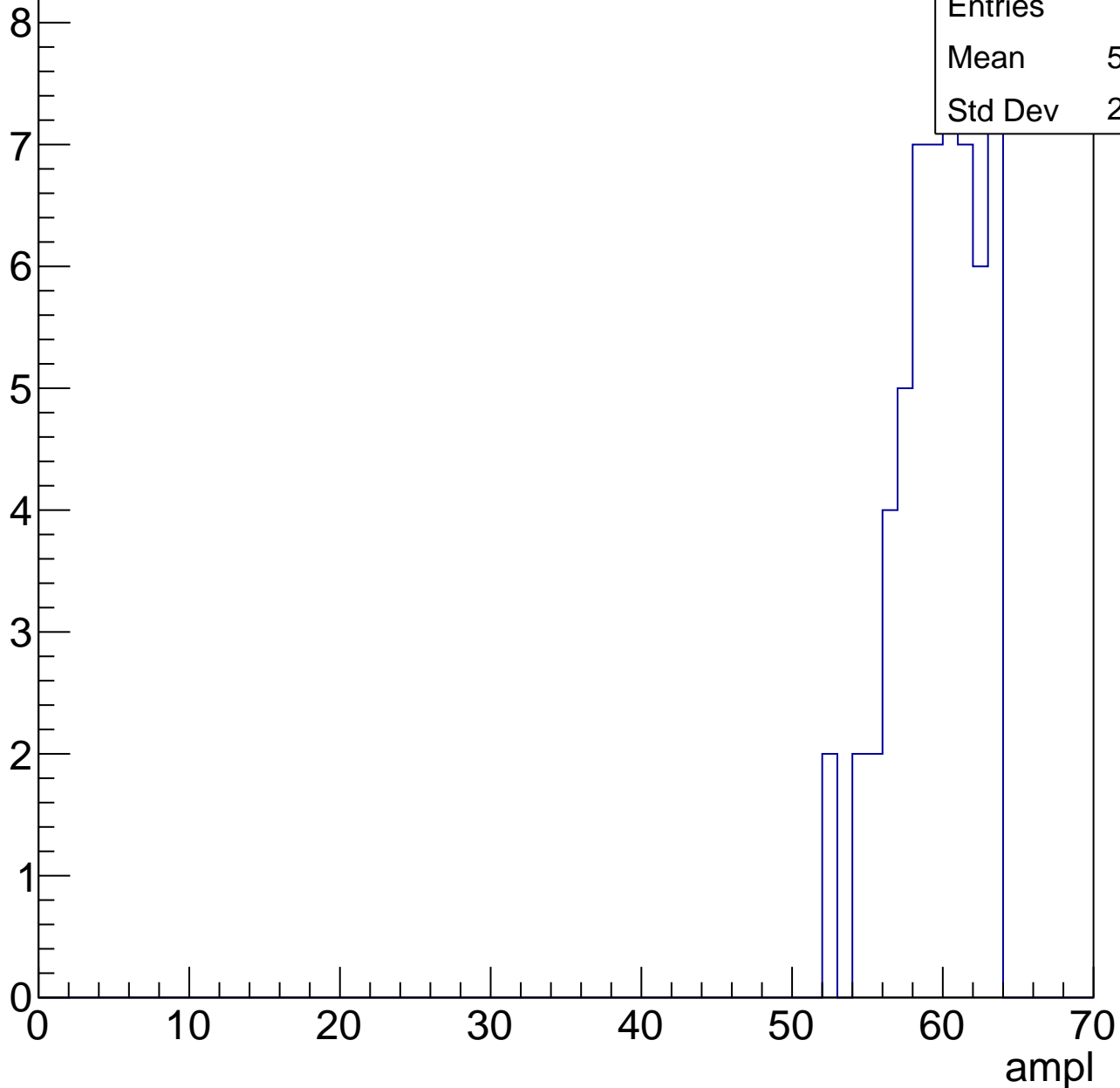
Entries	54
Mean	54.13
Std Dev	3.432



B1L101S, U4-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

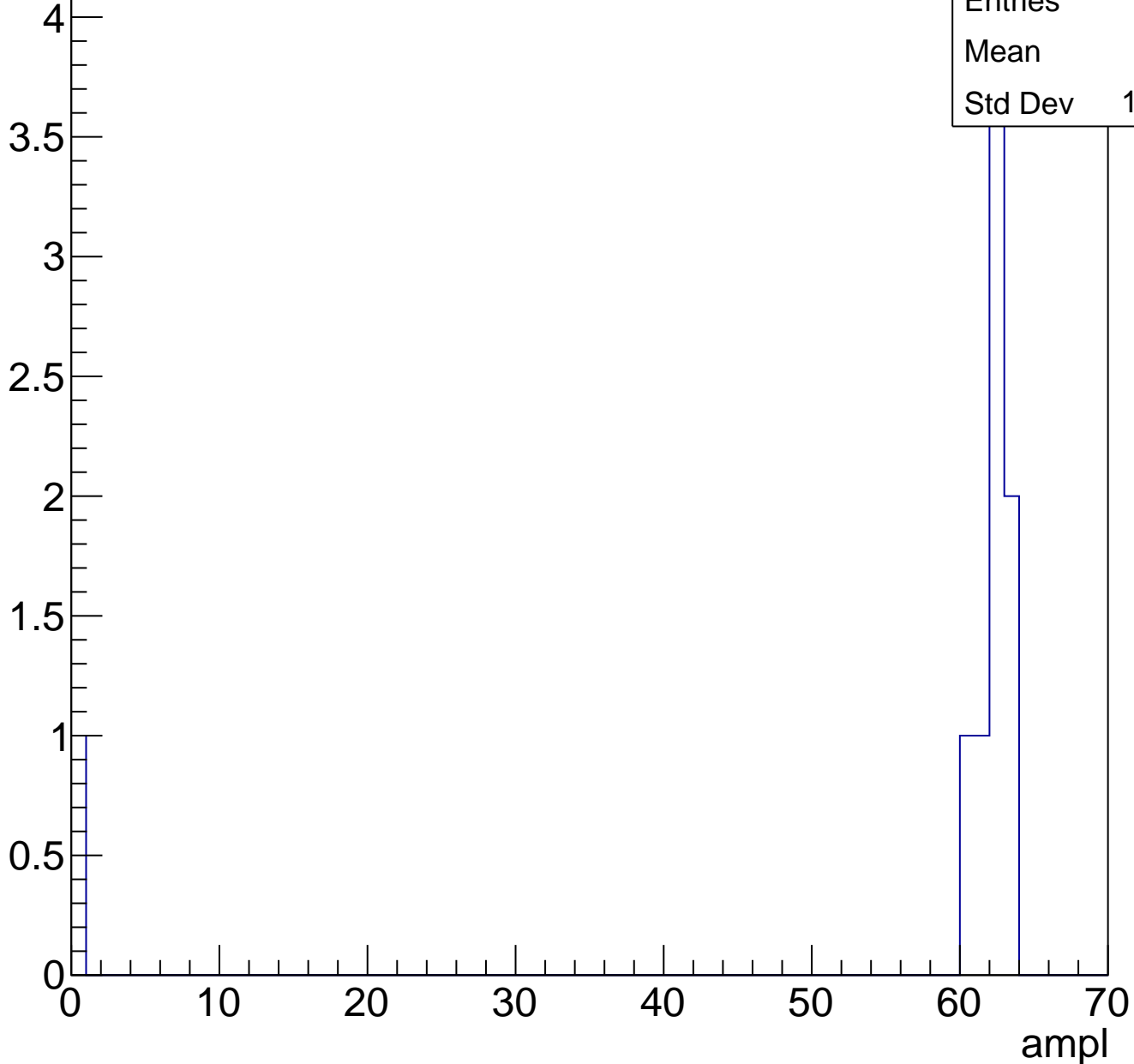
Entry



B1L101S, U4-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U4-ch9, adc0

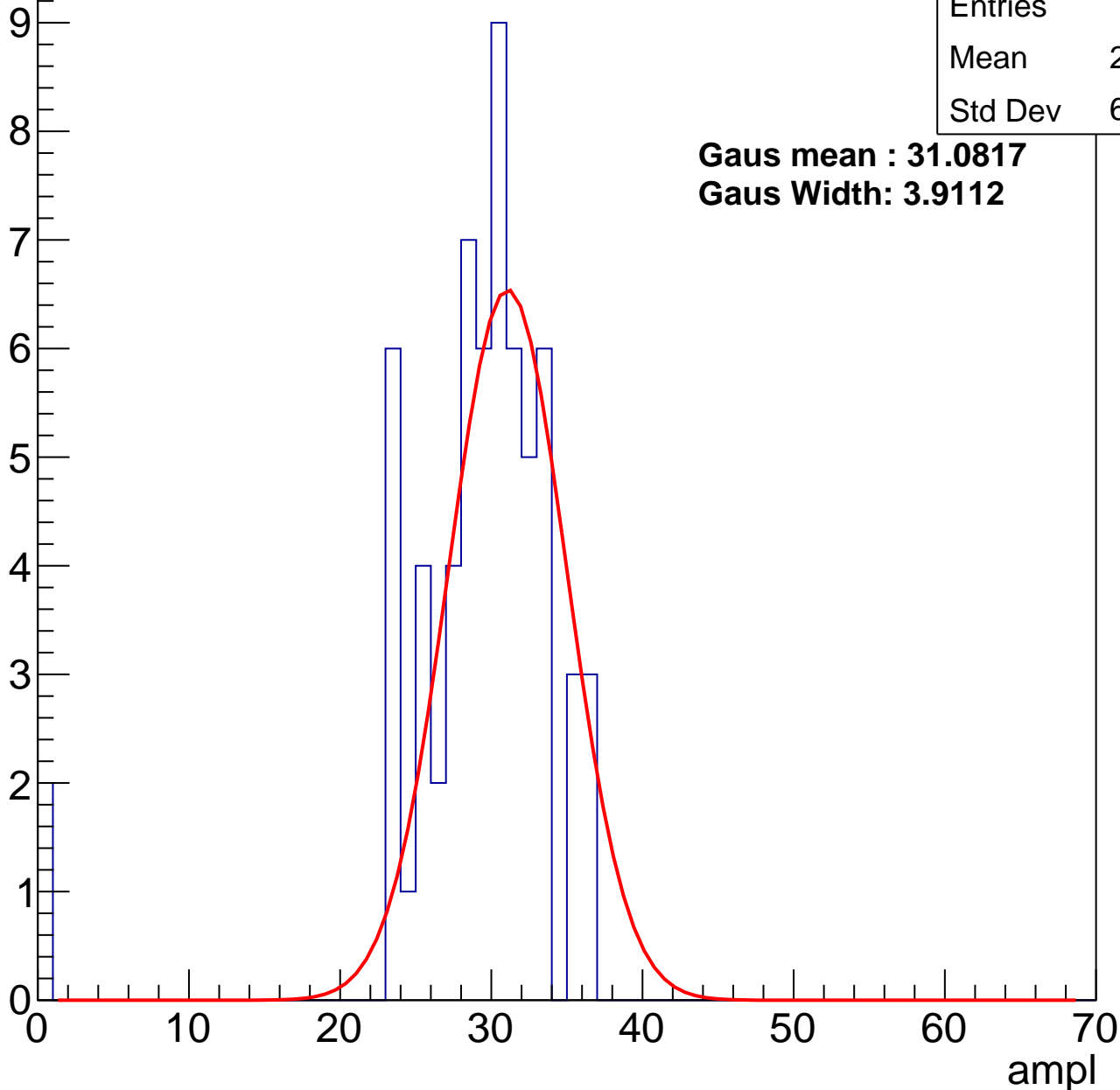
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	28.42
Std Dev	6.169

Gaus mean : 31.0817

Gaus Width: 3.9112



B1L101S, U4-ch9, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	35.96
Std Dev	2.96

Gaus mean : 36.5223

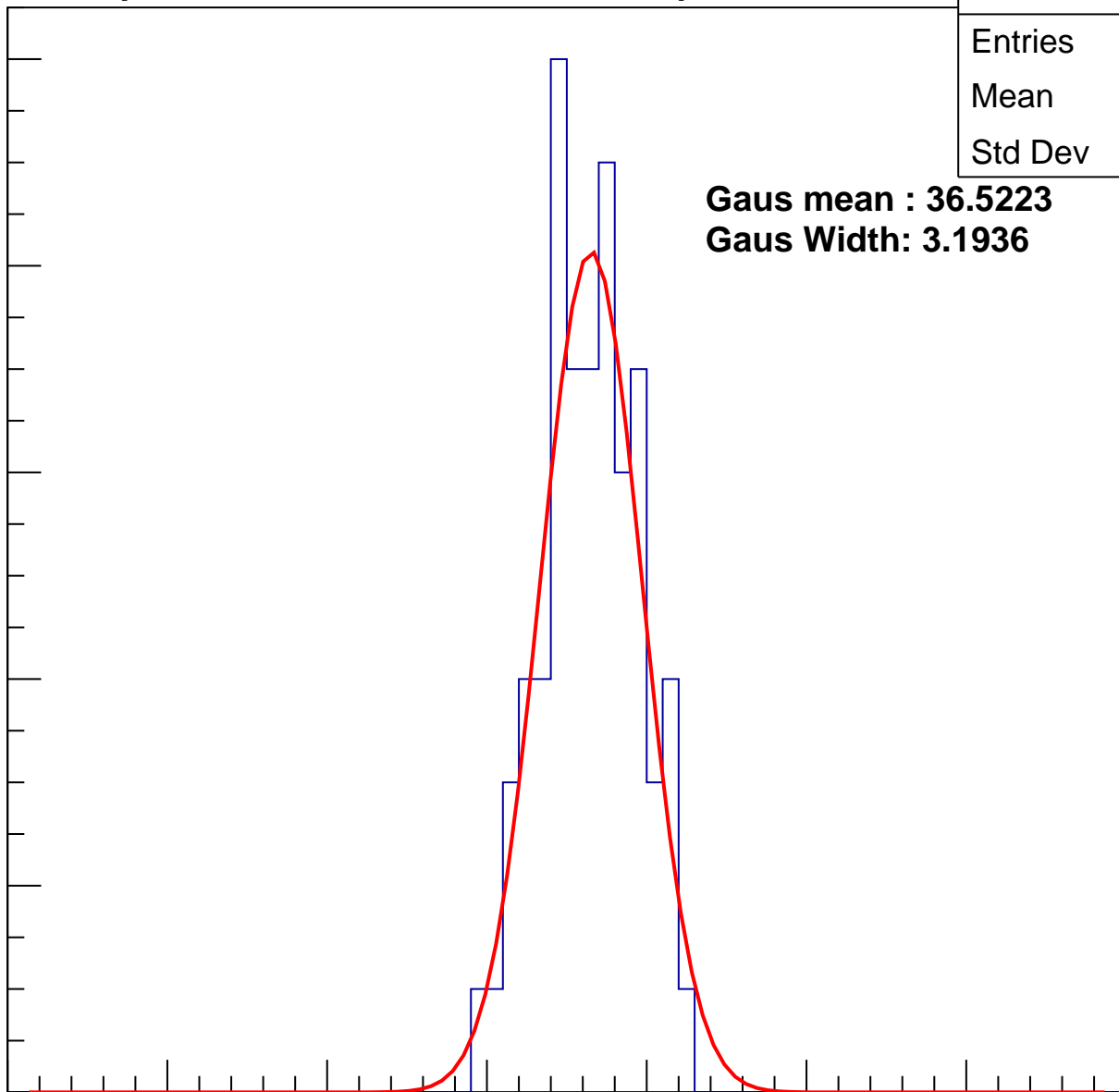
Gaus Width: 3.1936

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch9, adc2

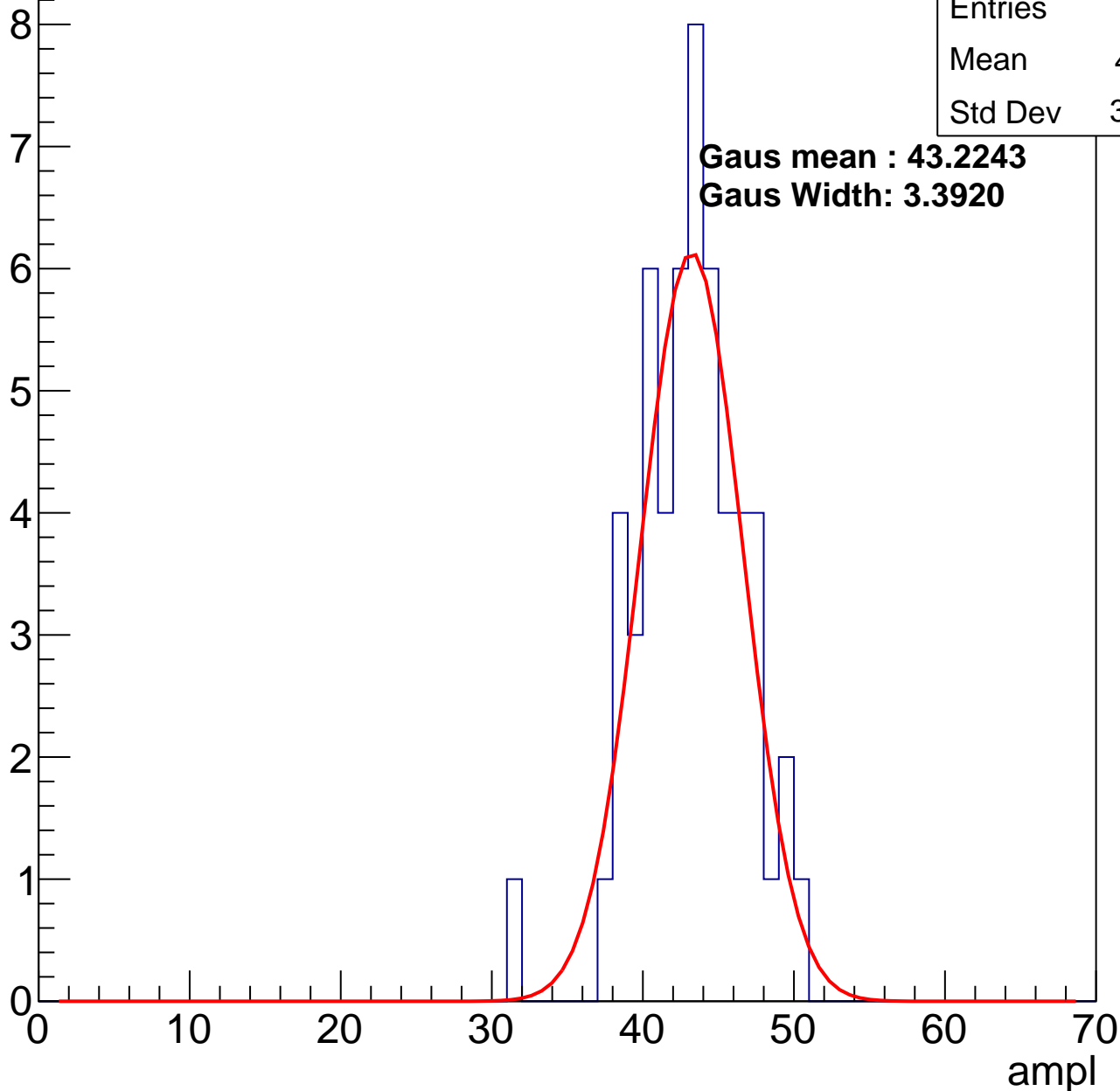
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	42.71
Std Dev	3.483

Gaus mean : 43.2243

Gaus Width: 3.3920

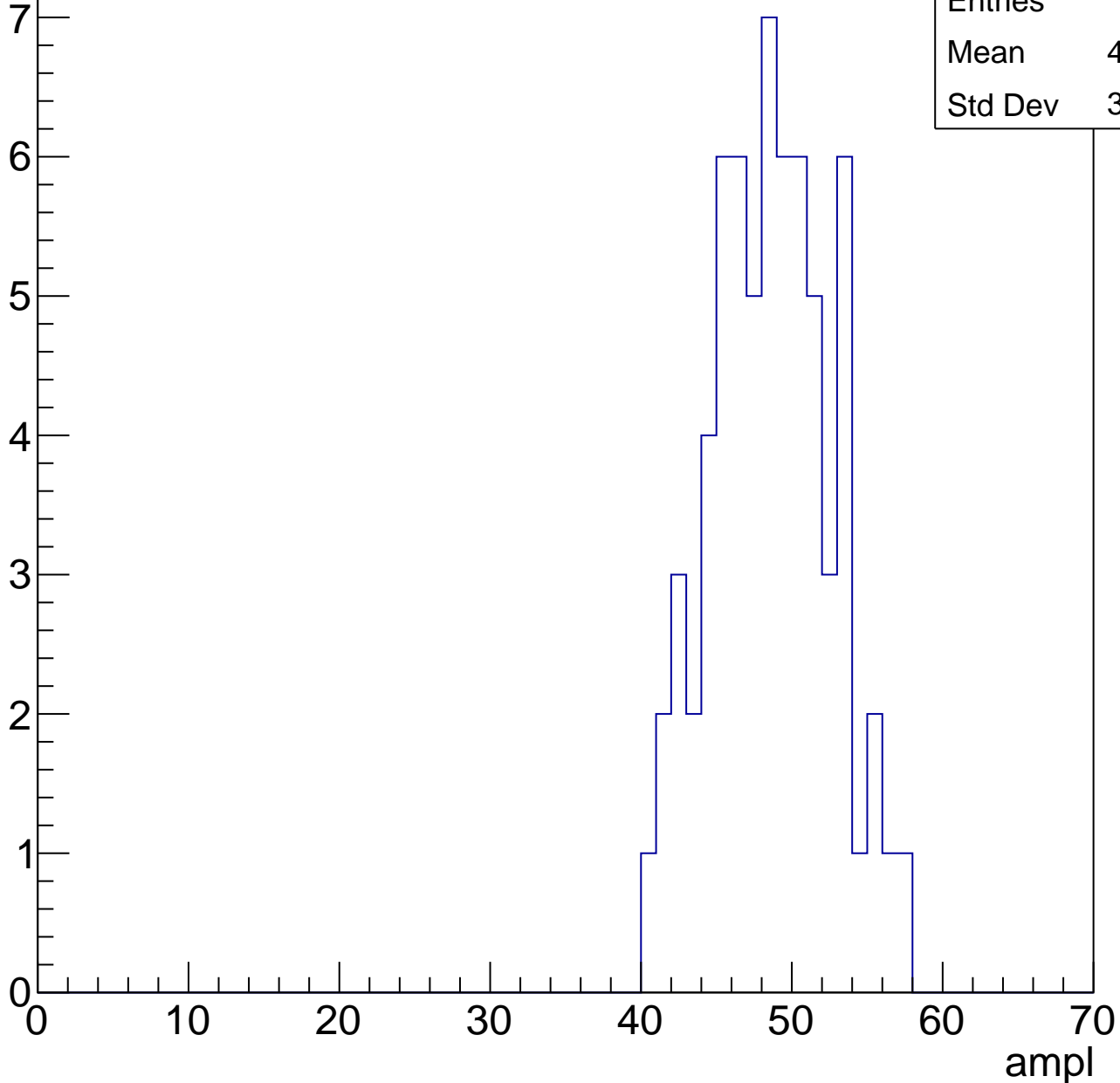


B1L101S, U4-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	48.16
Std Dev	3.896

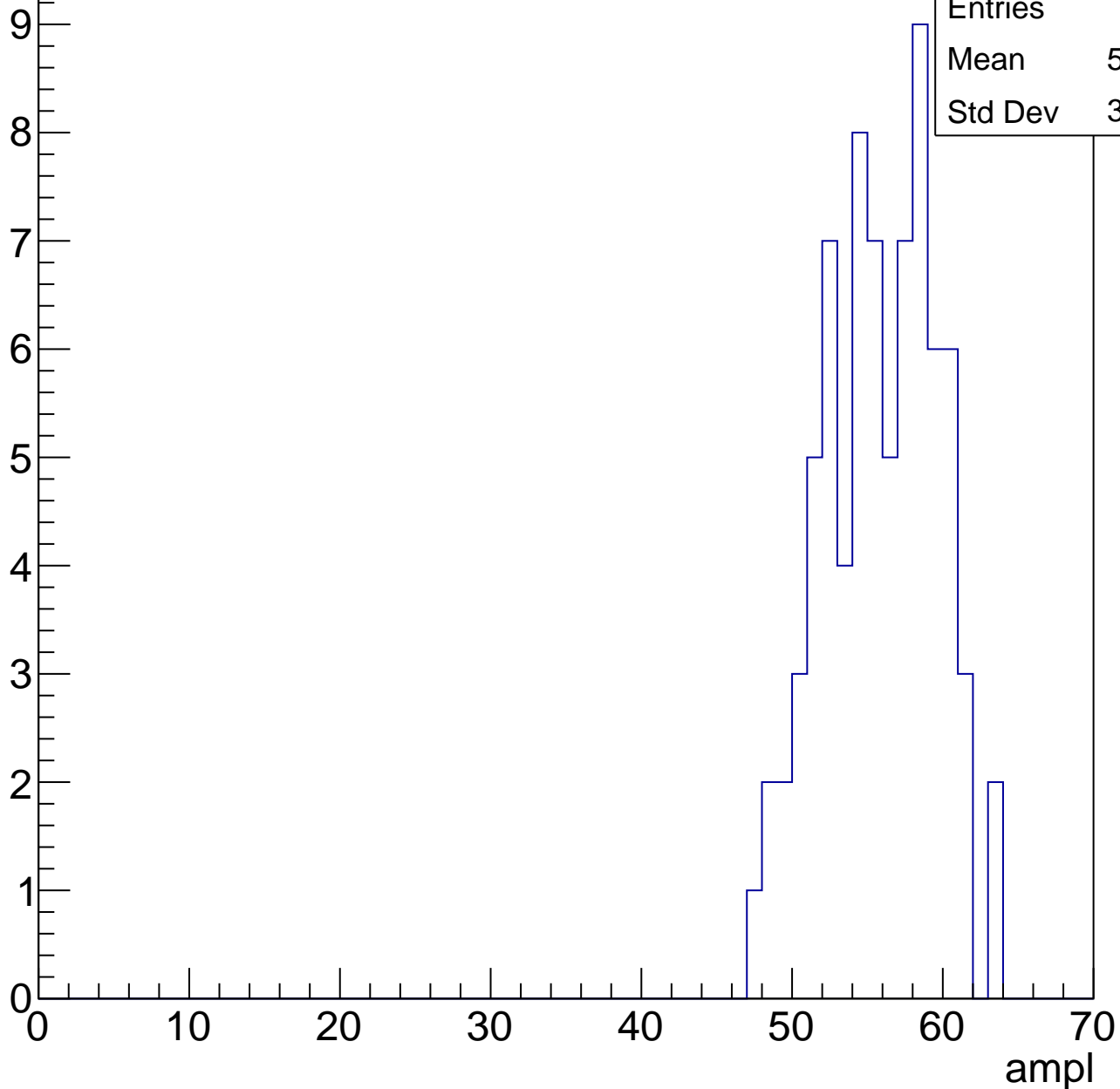


B1L101S, U4-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	55.36
Std Dev	3.713

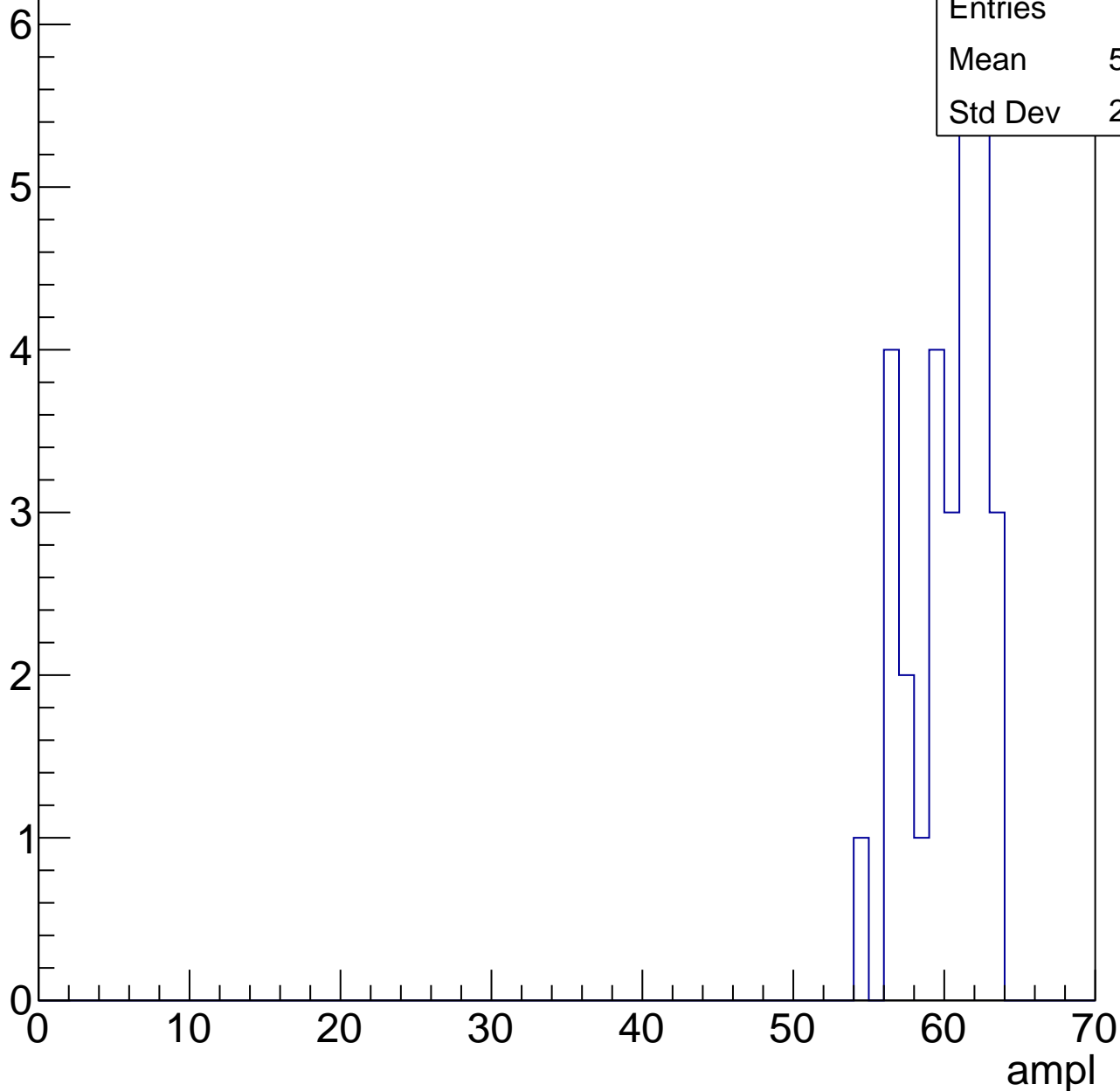


B1L101S, U4-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	59.77
Std Dev	2.459

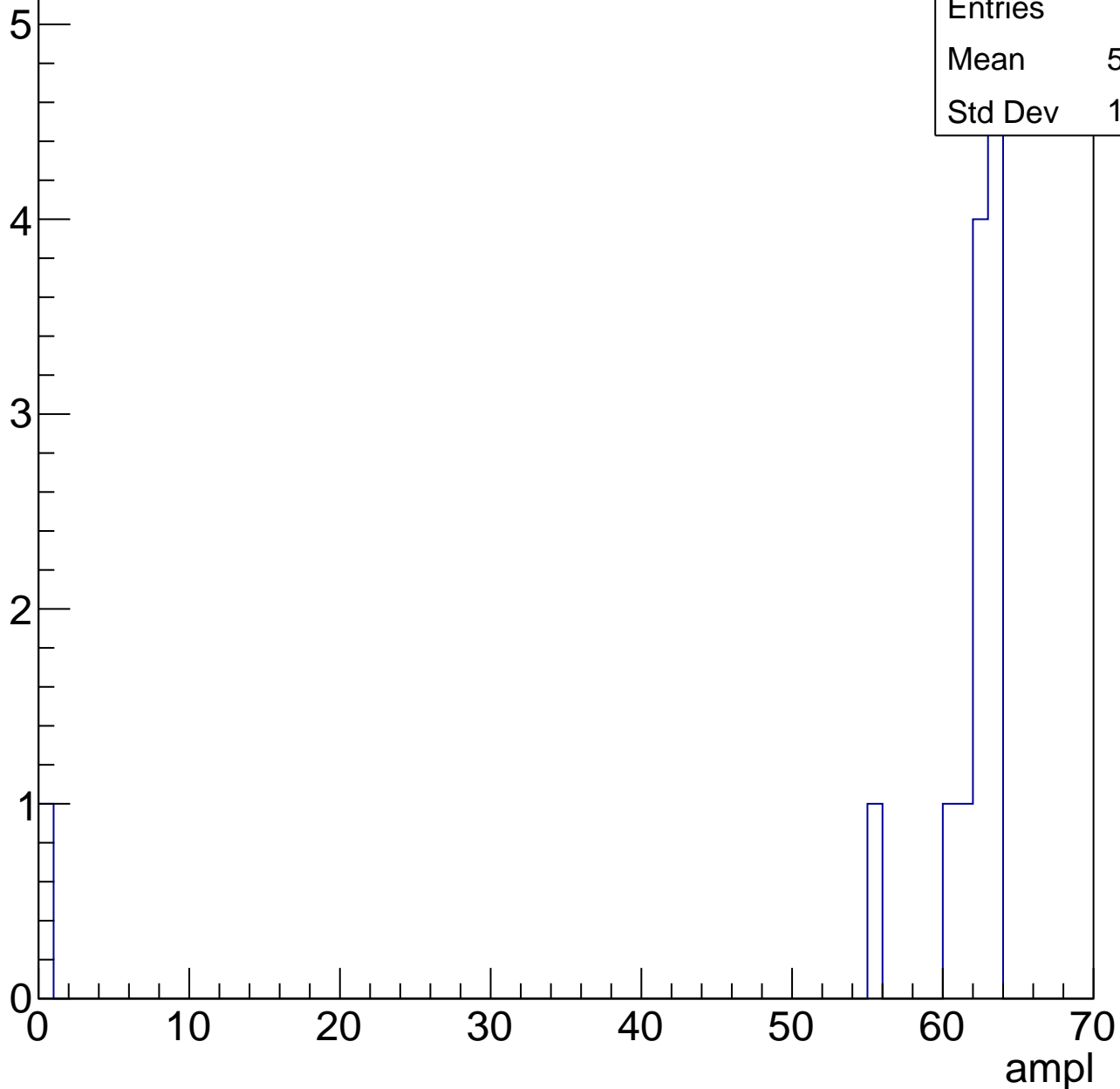


B1L101S, U4-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	56.85
Std Dev	16.54



B1L101S, U4-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch10, adc0

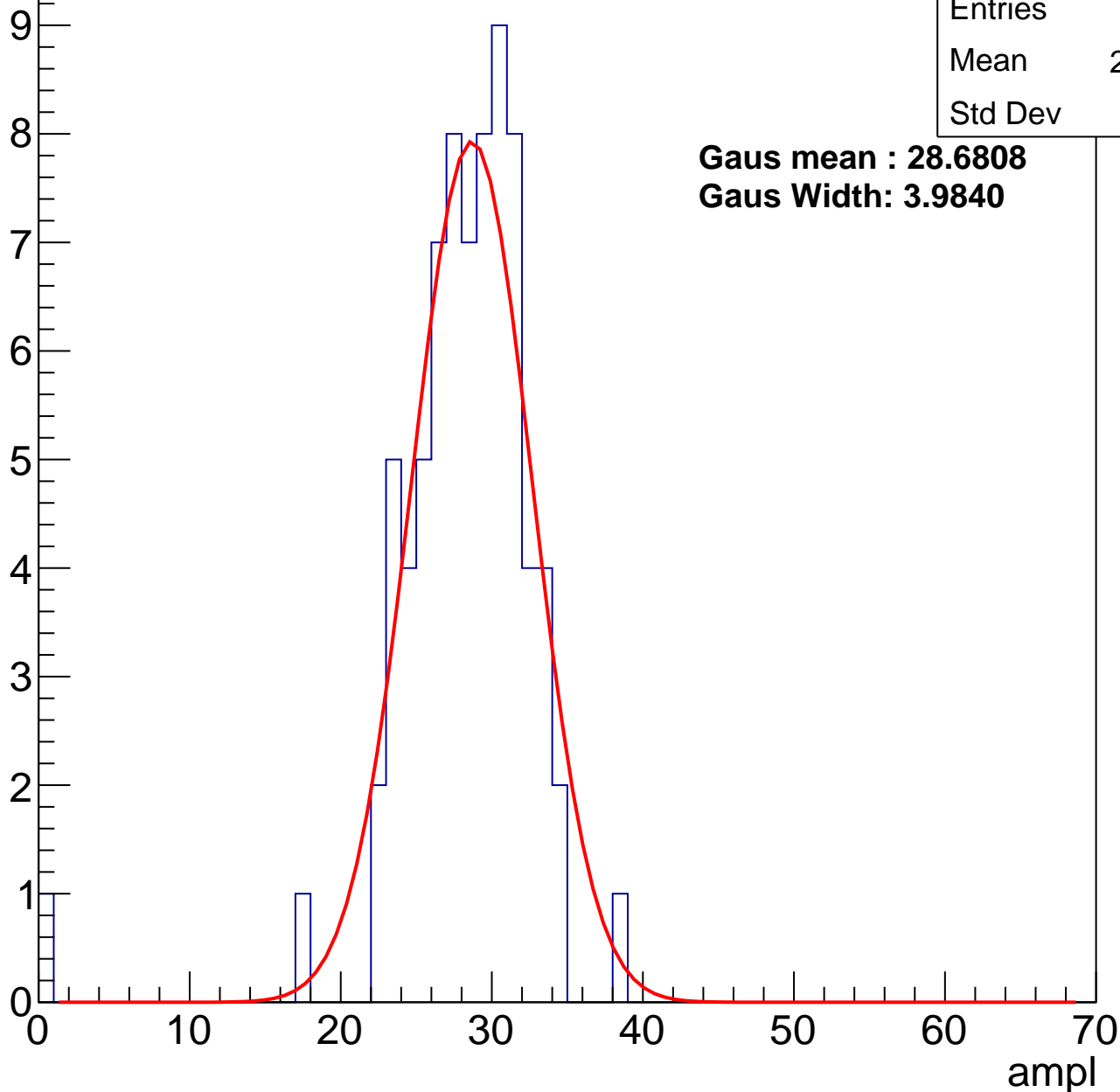
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	27.72
Std Dev	4.72

Gaus mean : 28.6808

Gaus Width: 3.9840



B1L101S, U4-ch10, adc1

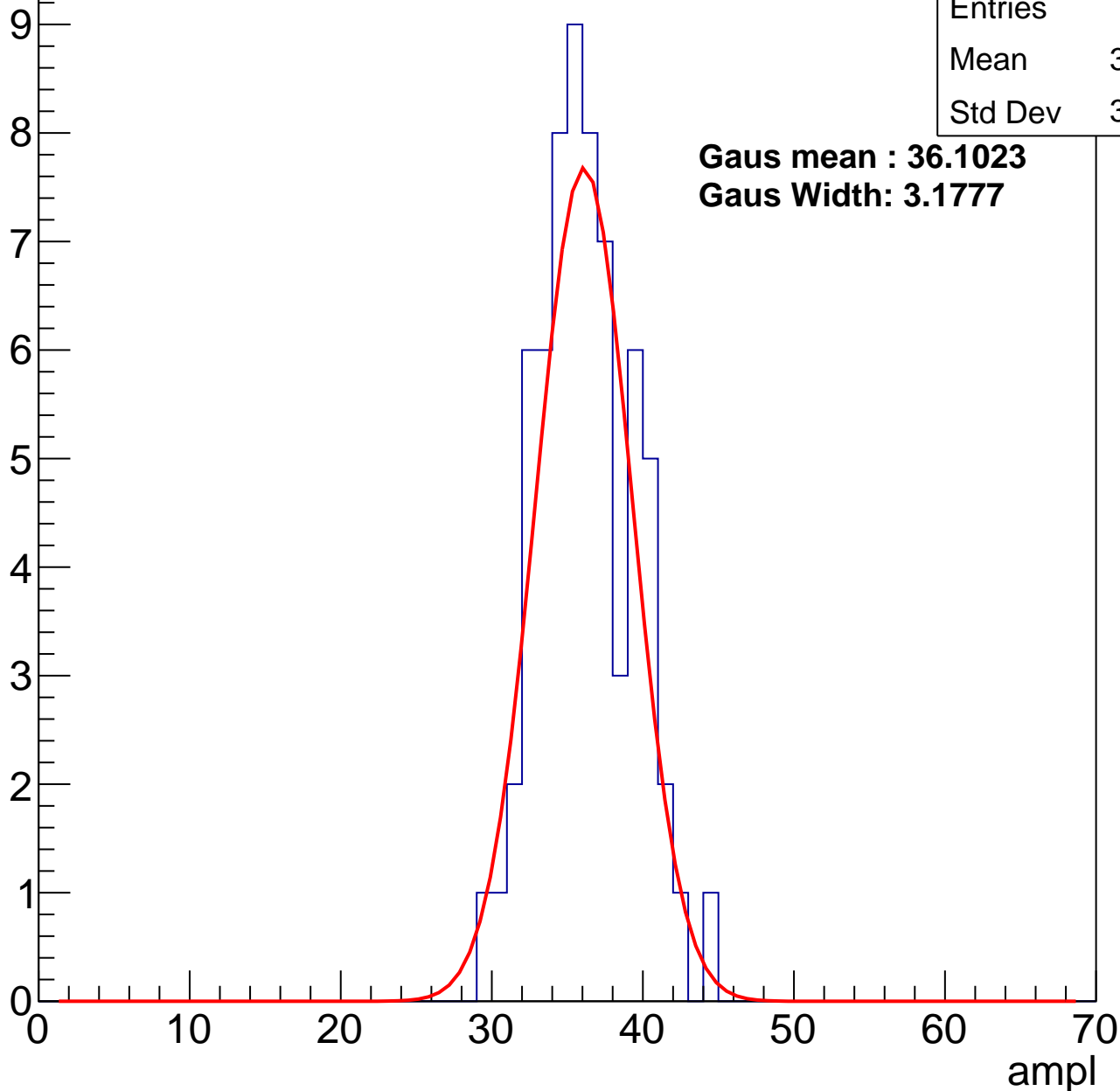
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.77
Std Dev	3.079

Gaus mean : 36.1023

Gaus Width: 3.1777



B1L101S, U4-ch10, adc2

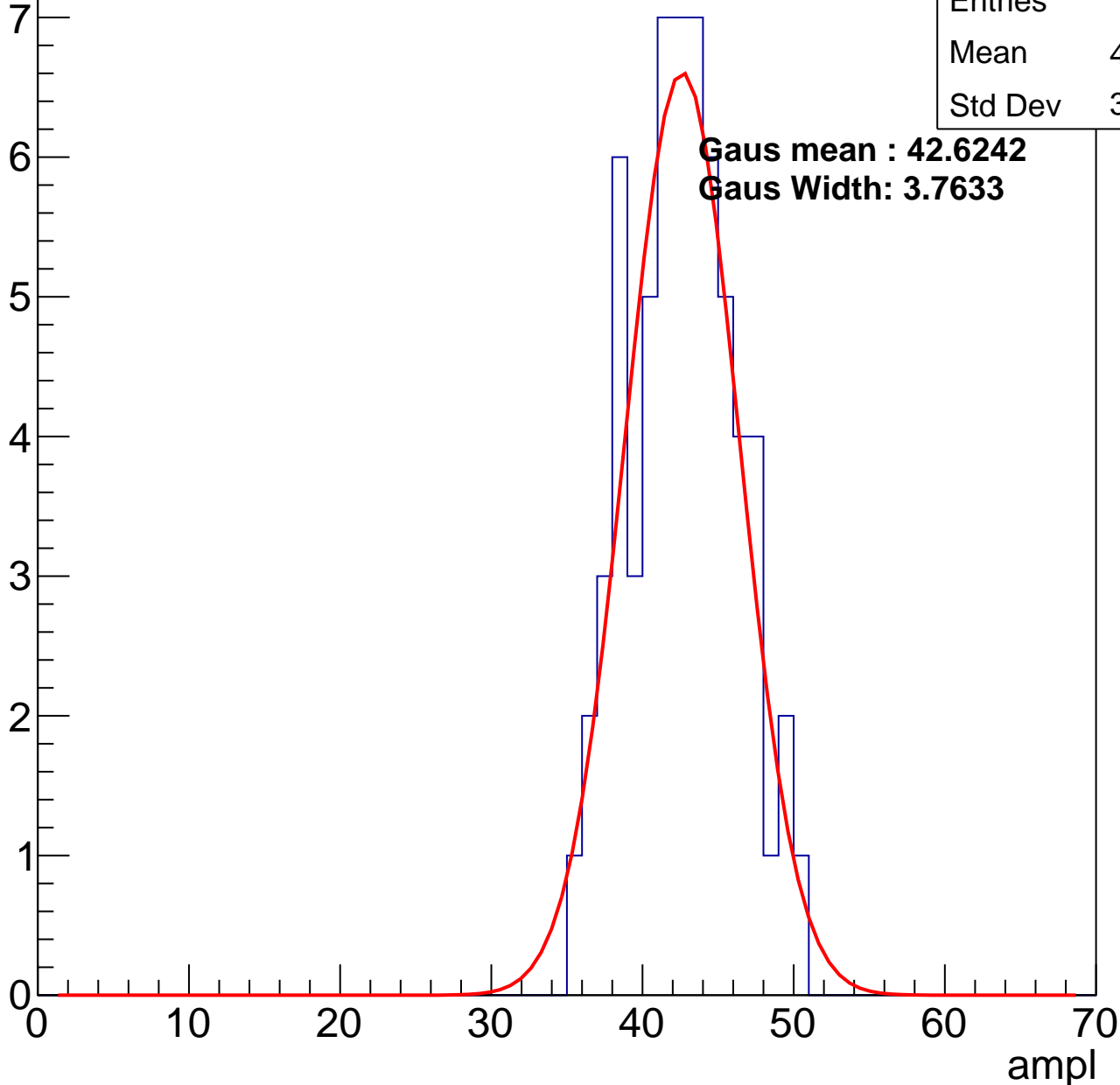
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.22
Std Dev	3.493

Gaus mean : 42.6242

Gaus Width: 3.7633

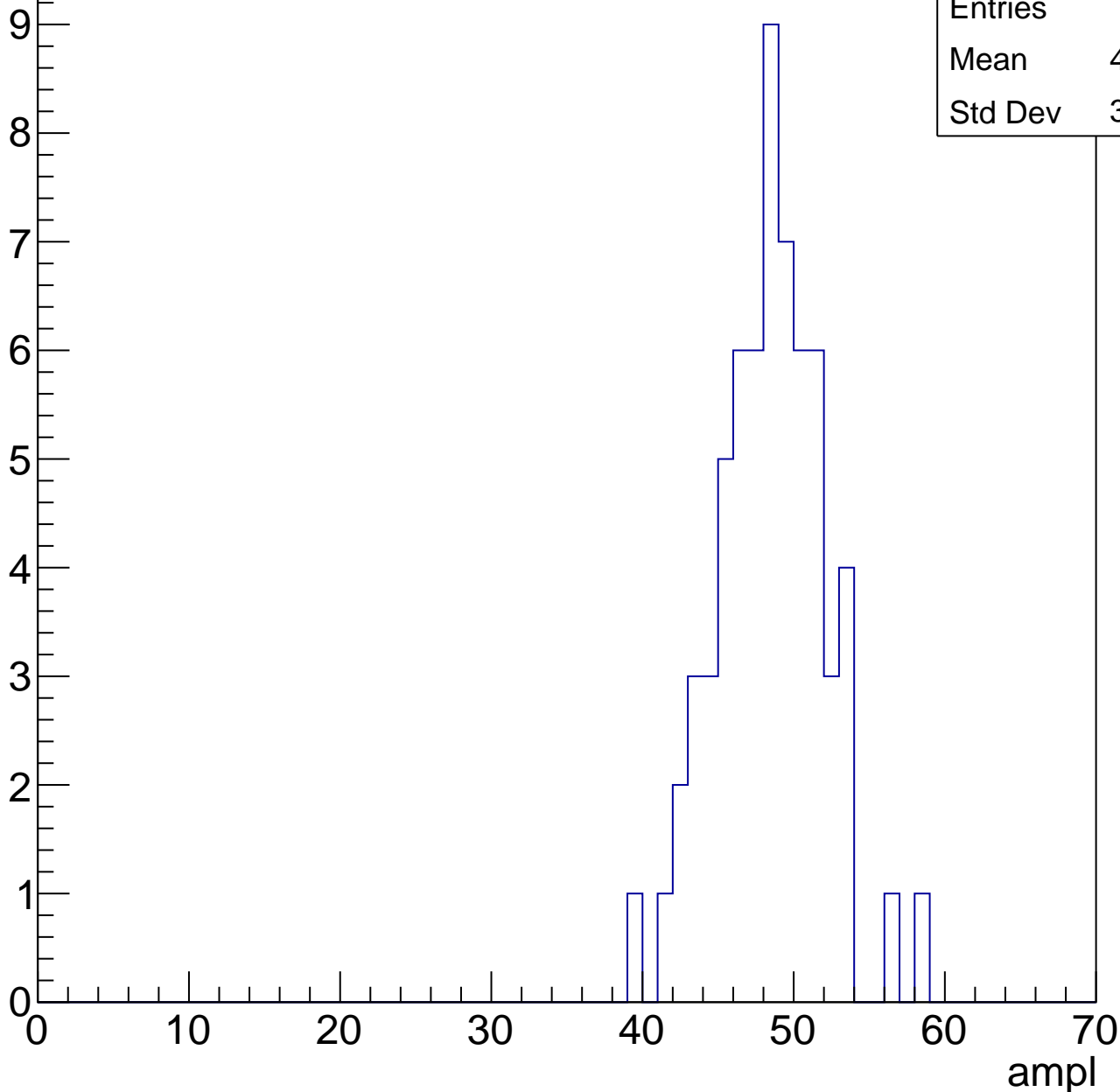


B1L101S, U4-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	47.98
Std Dev	3.529

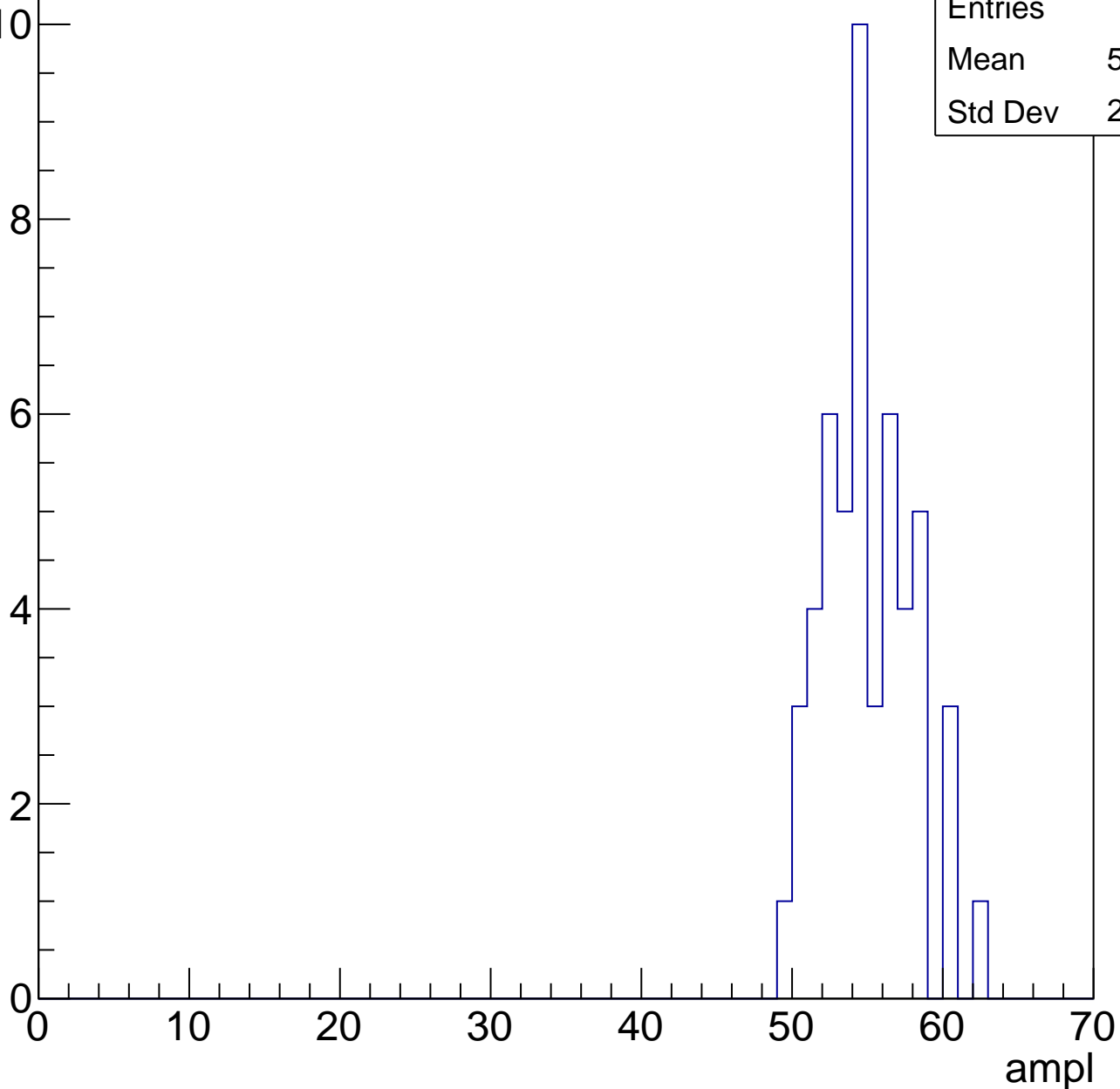


B1L101S, U4-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

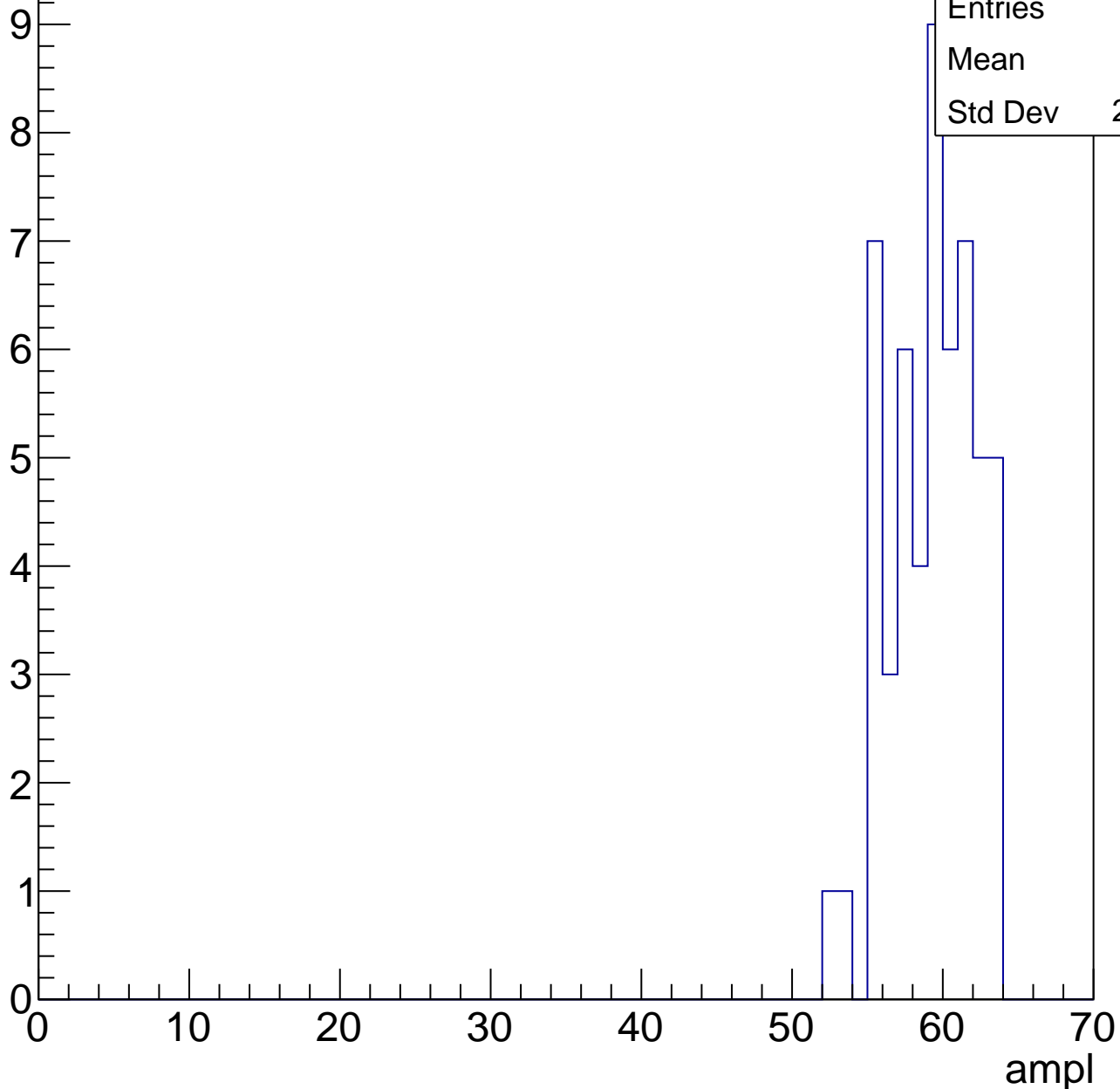
Entries	51
Mean	54.53
Std Dev	2.933



B1L101S, U4-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

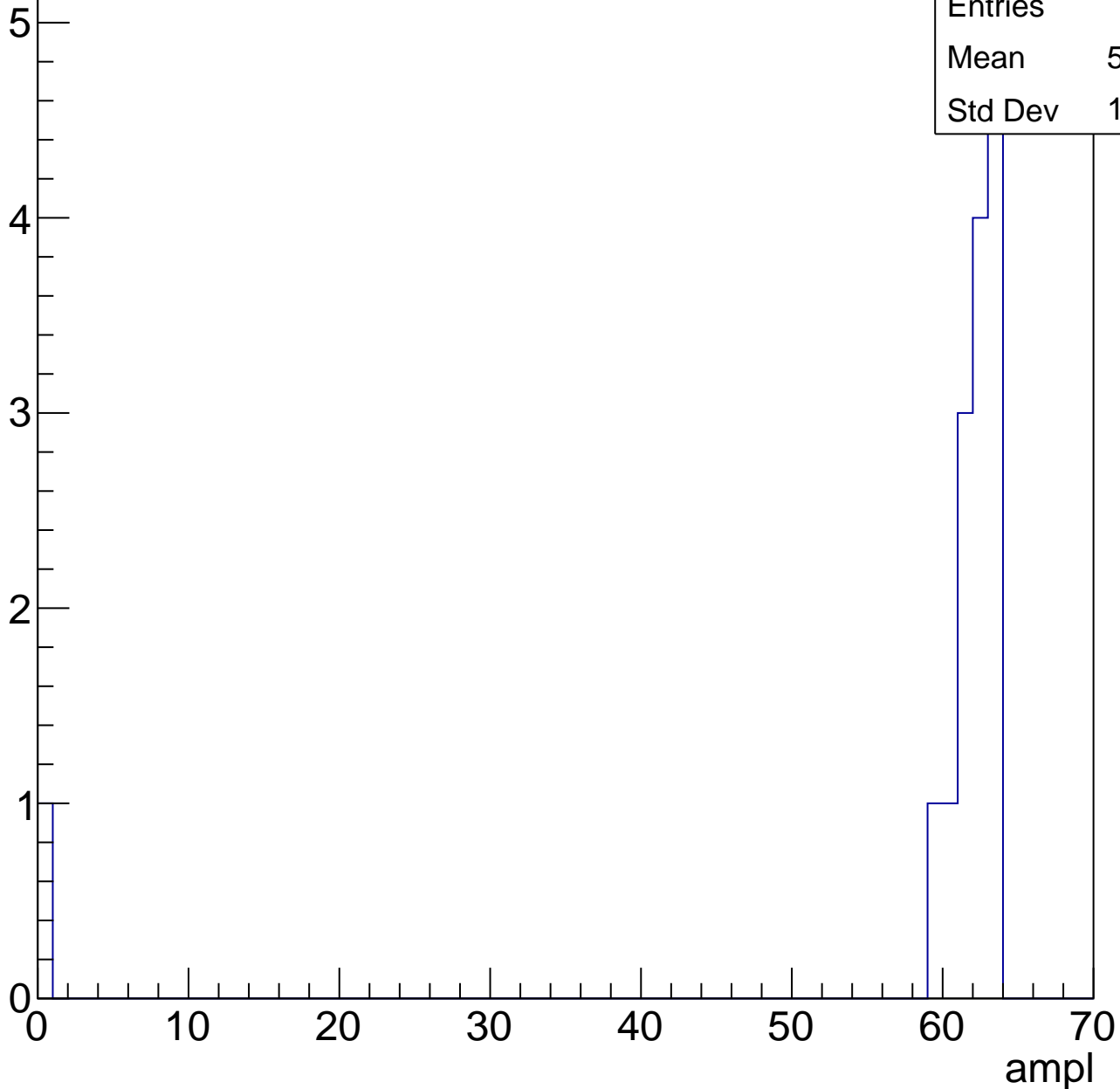


B1L101S, U4-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.67
Std Dev	15.46



B1L101S, U4-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch11, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	27.21
Std Dev	6.346

Gaus mean : 29.1009

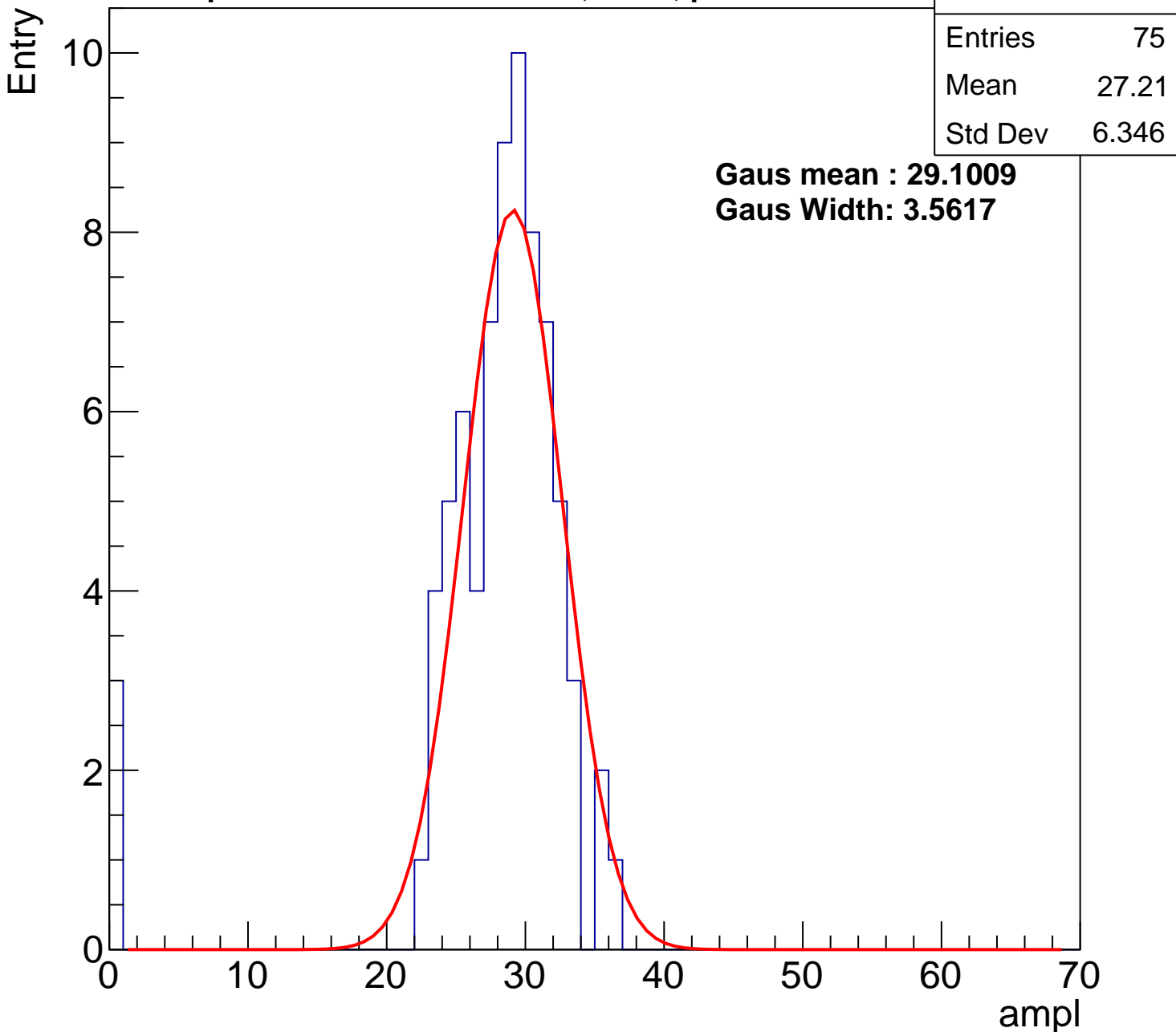
Gaus Width: 3.5617

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch11, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	35.7
Std Dev	3.43

Gaus mean : 36.0980

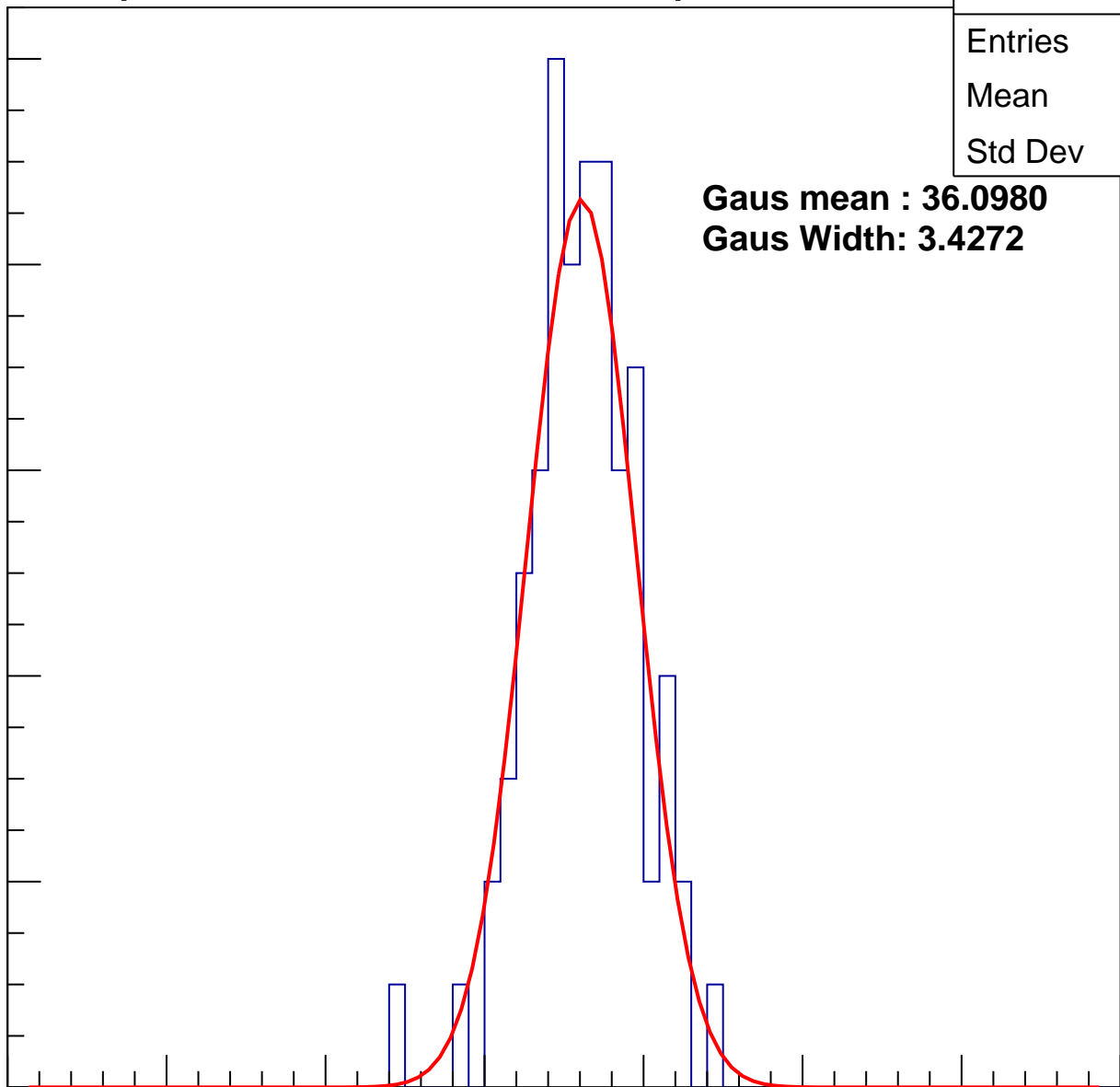
Gaus Width: 3.4272

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch11, adc2

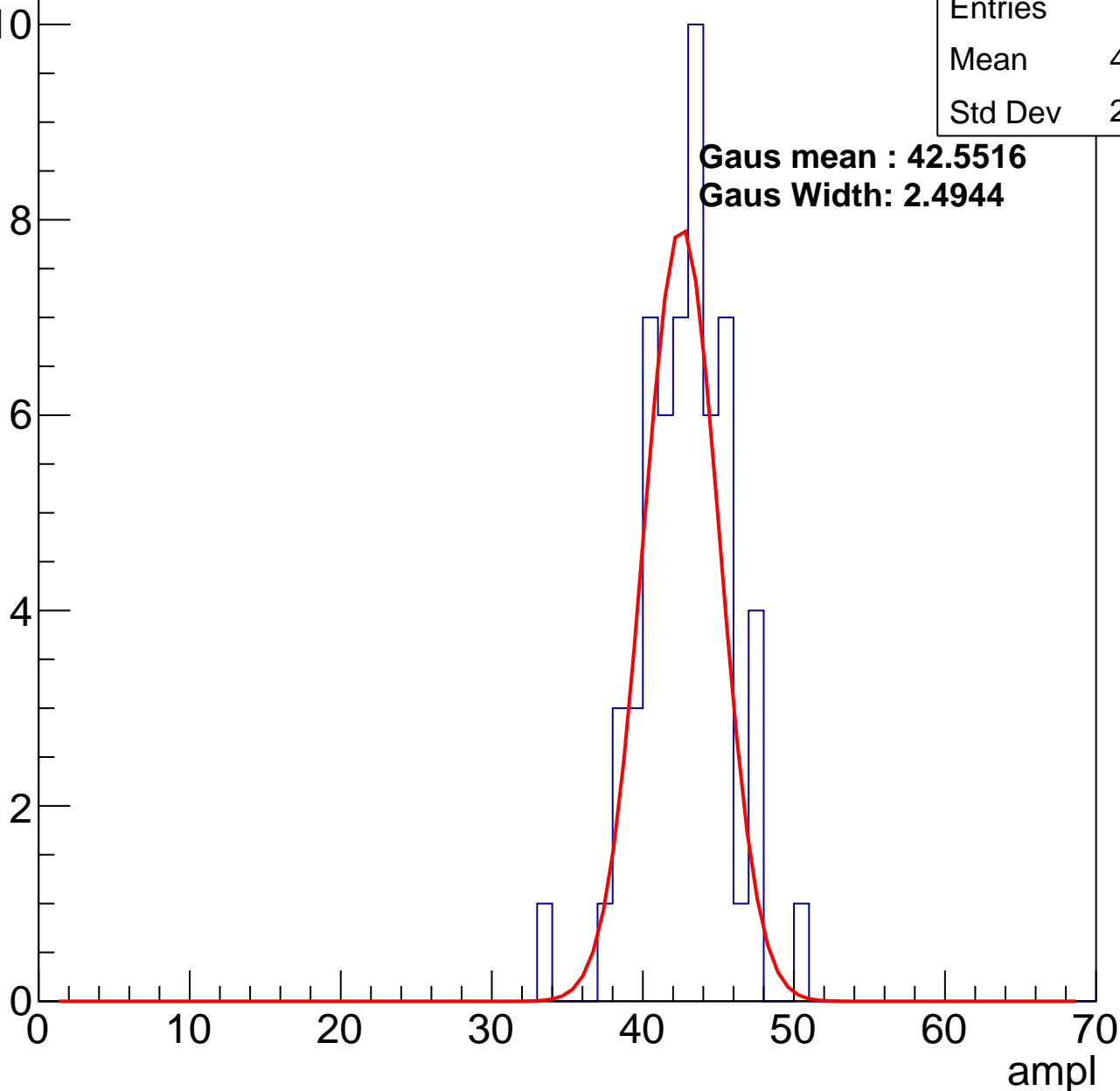
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.35
Std Dev	2.917

Gaus mean : 42.5516

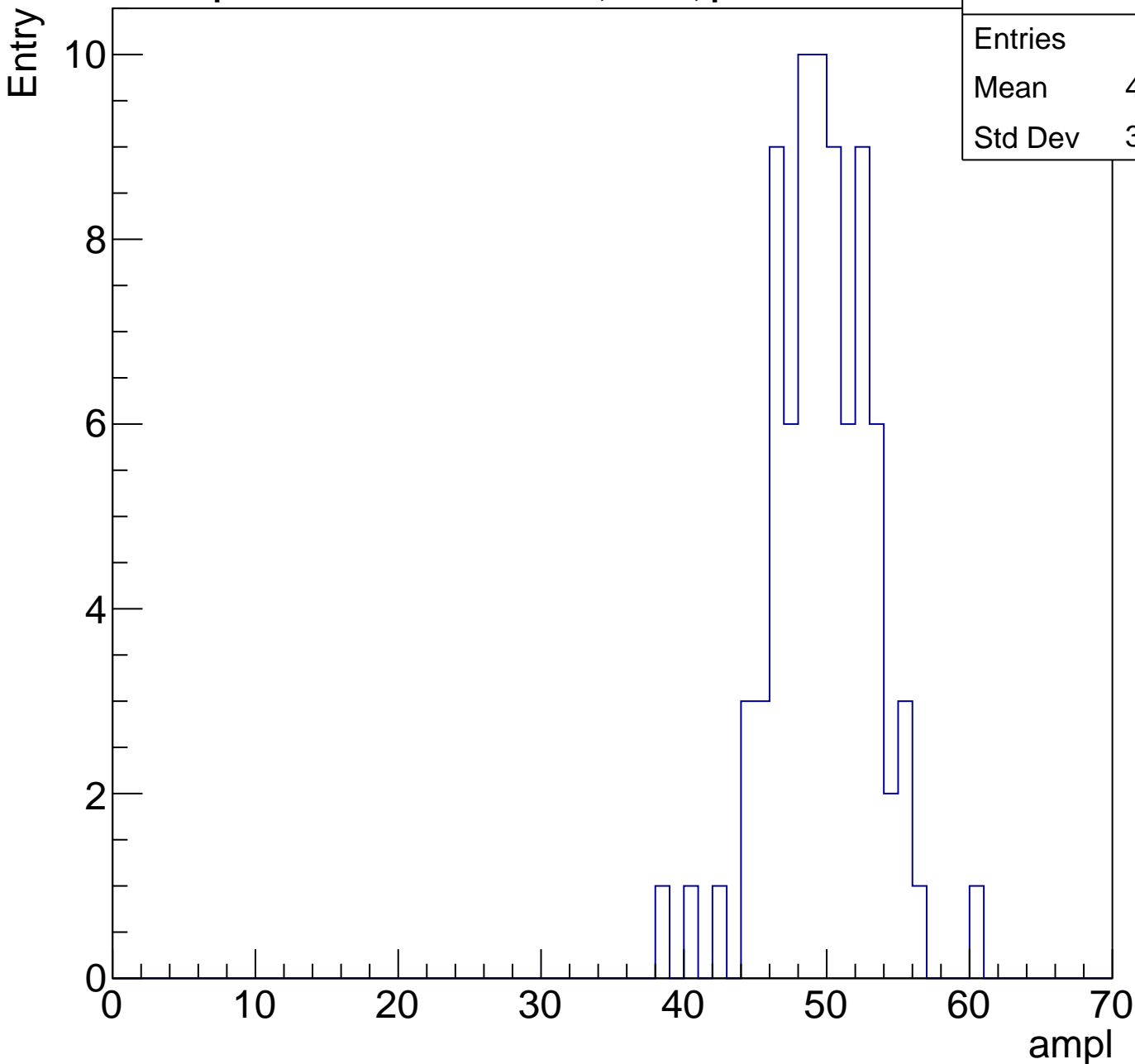
Gaus Width: 2.4944



B1L101S, U4-ch11, adc3

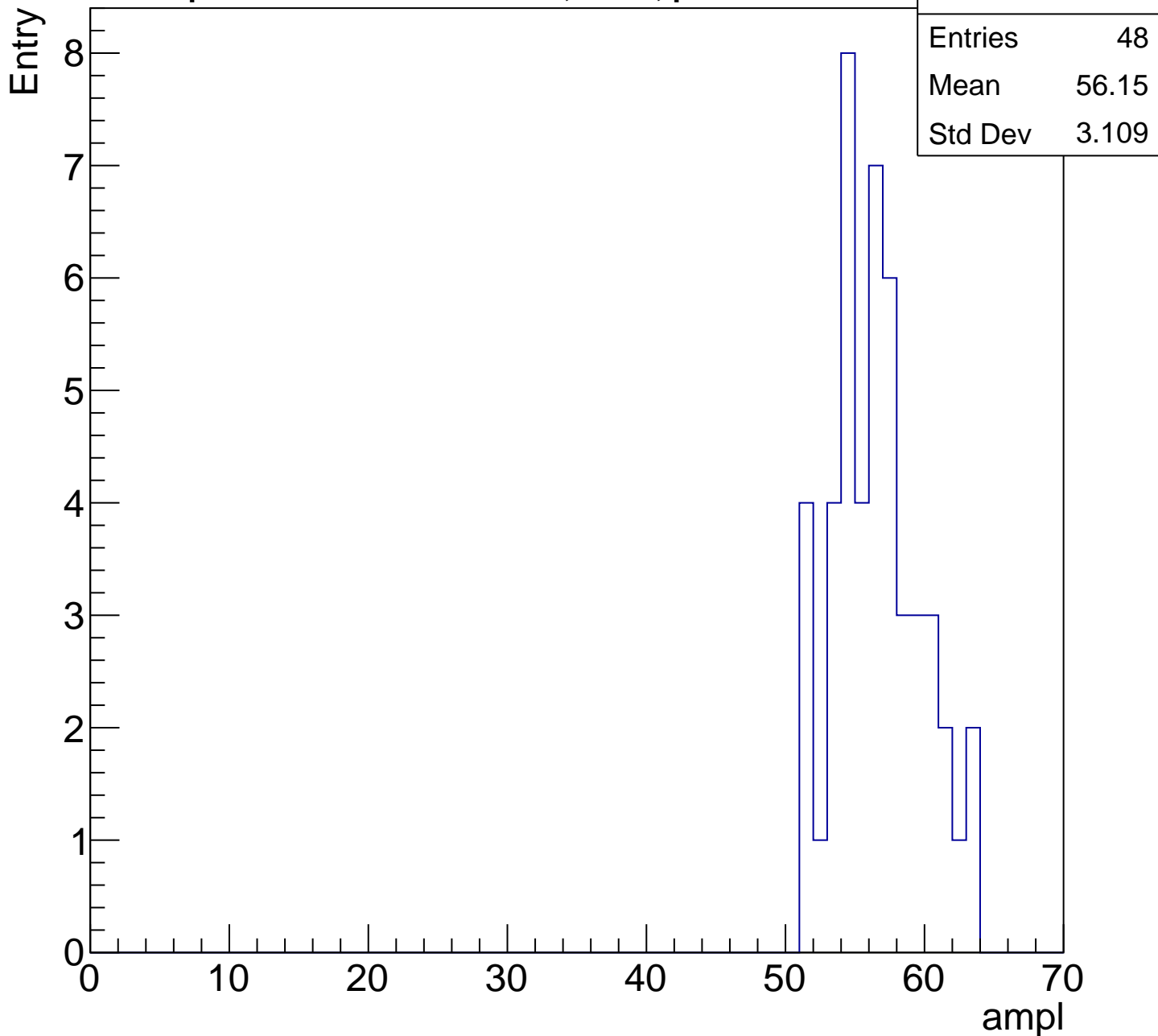
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	49.19
Std Dev	3.556



B1L101S, U4-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

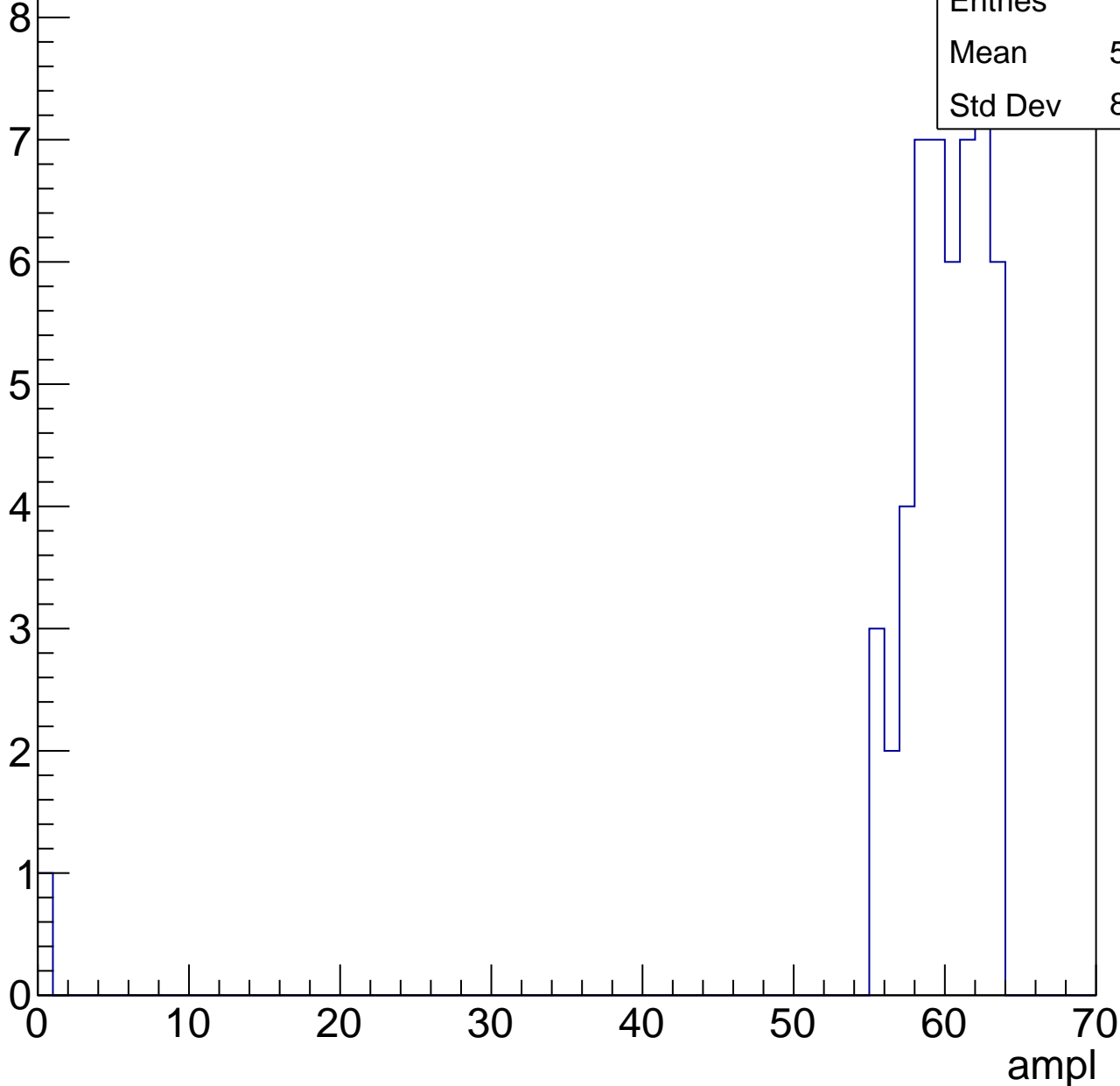


B1L101S, U4-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

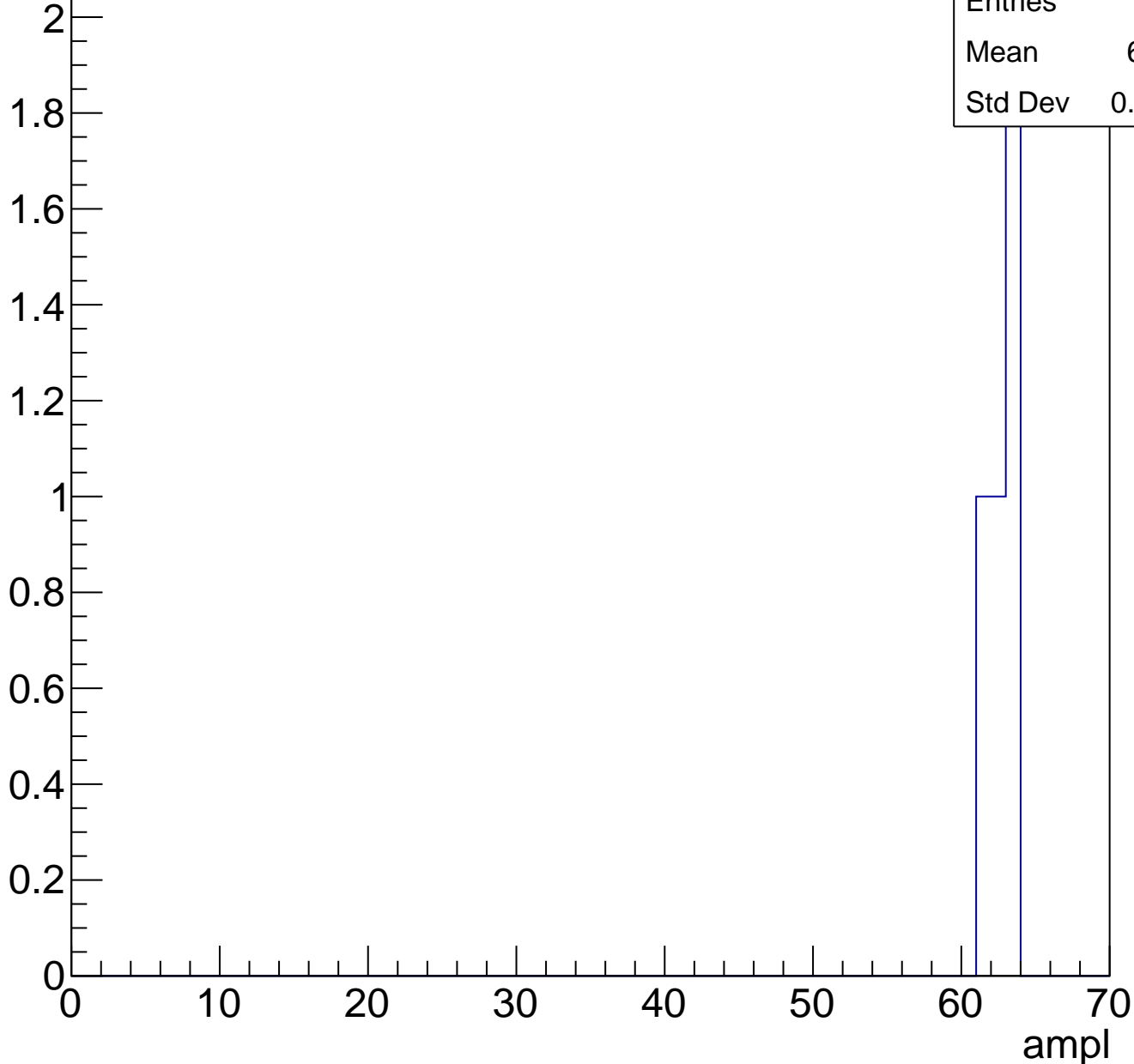
Entries	51
Mean	58.53
Std Dev	8.587



B1L101S, U4-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

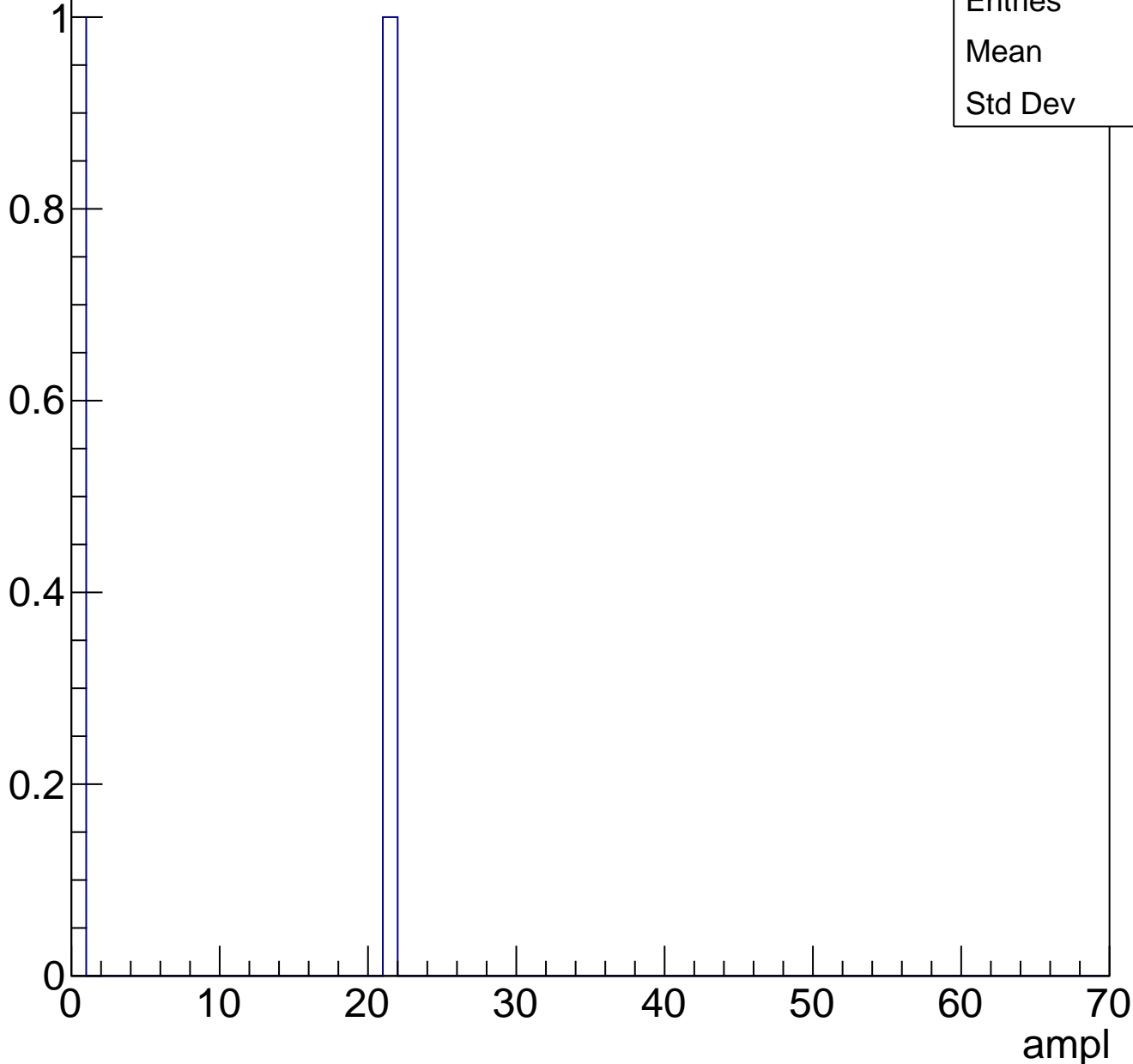
Entry



B1L101S, U4-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch12, adc0

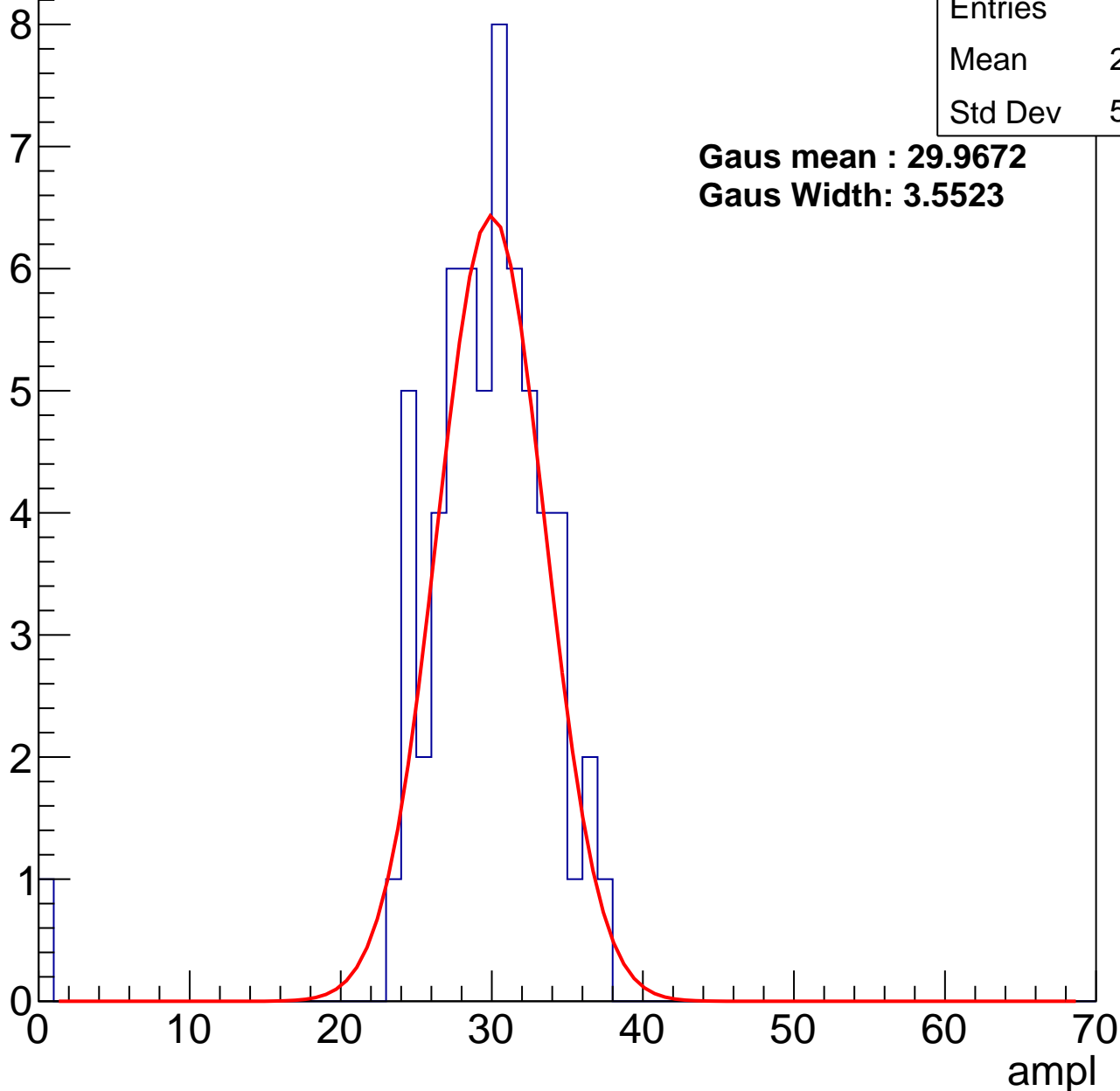
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.02
Std Dev	5.023

Gaus mean : 29.9672

Gaus Width: 3.5523



B1L101S, U4-ch12, adc1

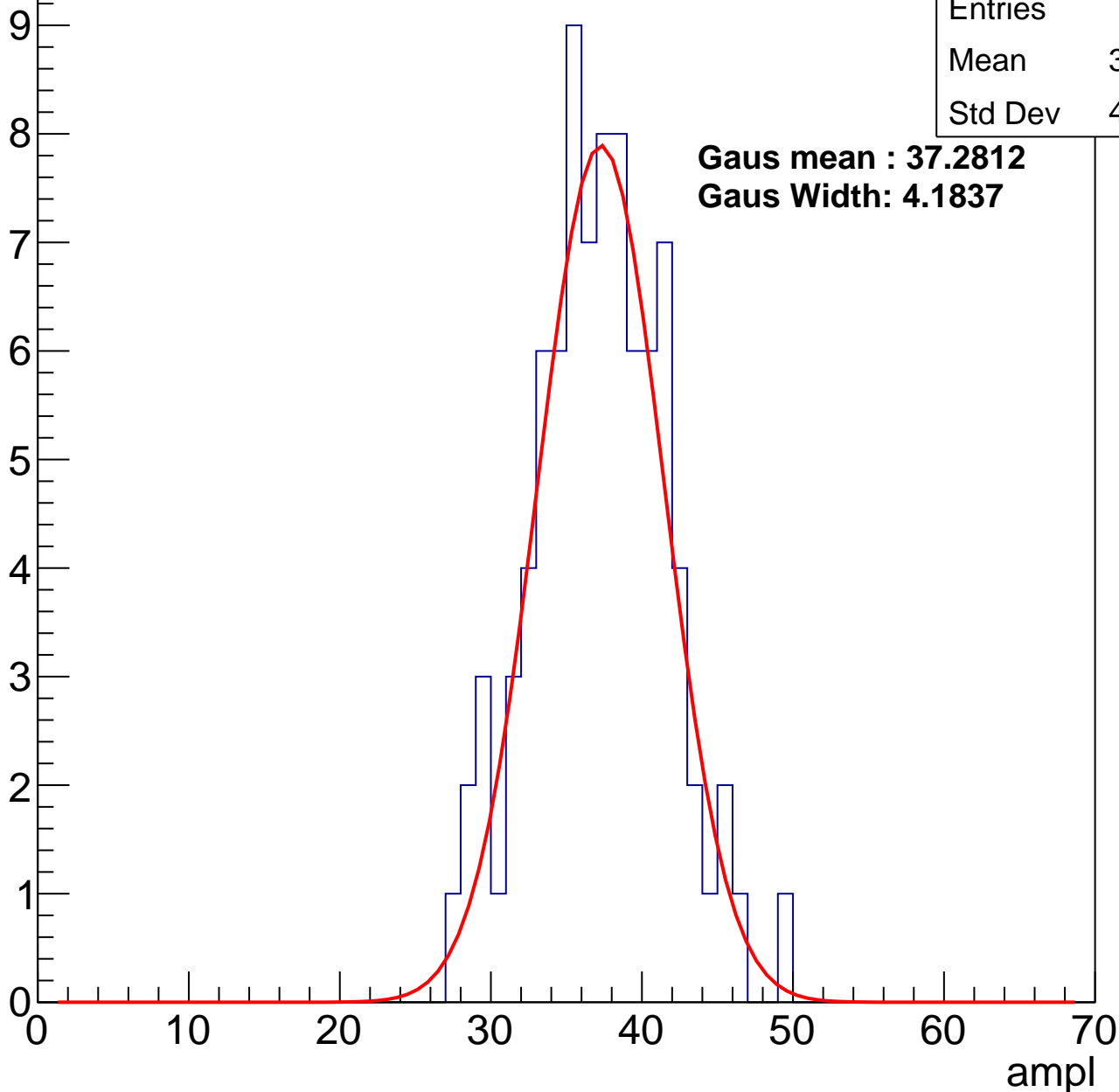
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	36.75
Std Dev	4.357

Gaus mean : 37.2812

Gaus Width: 4.1837



B1L101S, U4-ch12, adc2

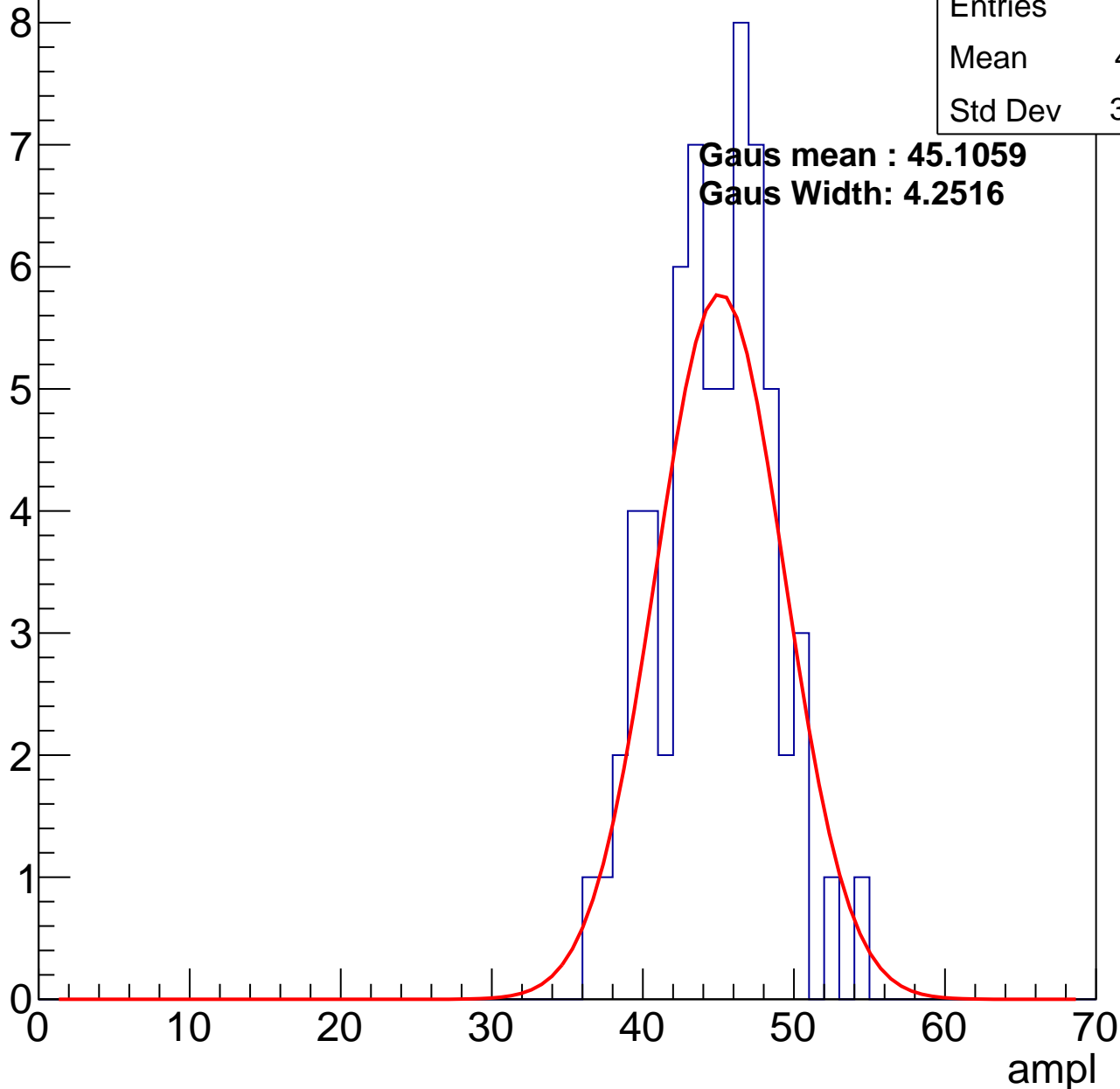
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.31
Std Dev	3.737

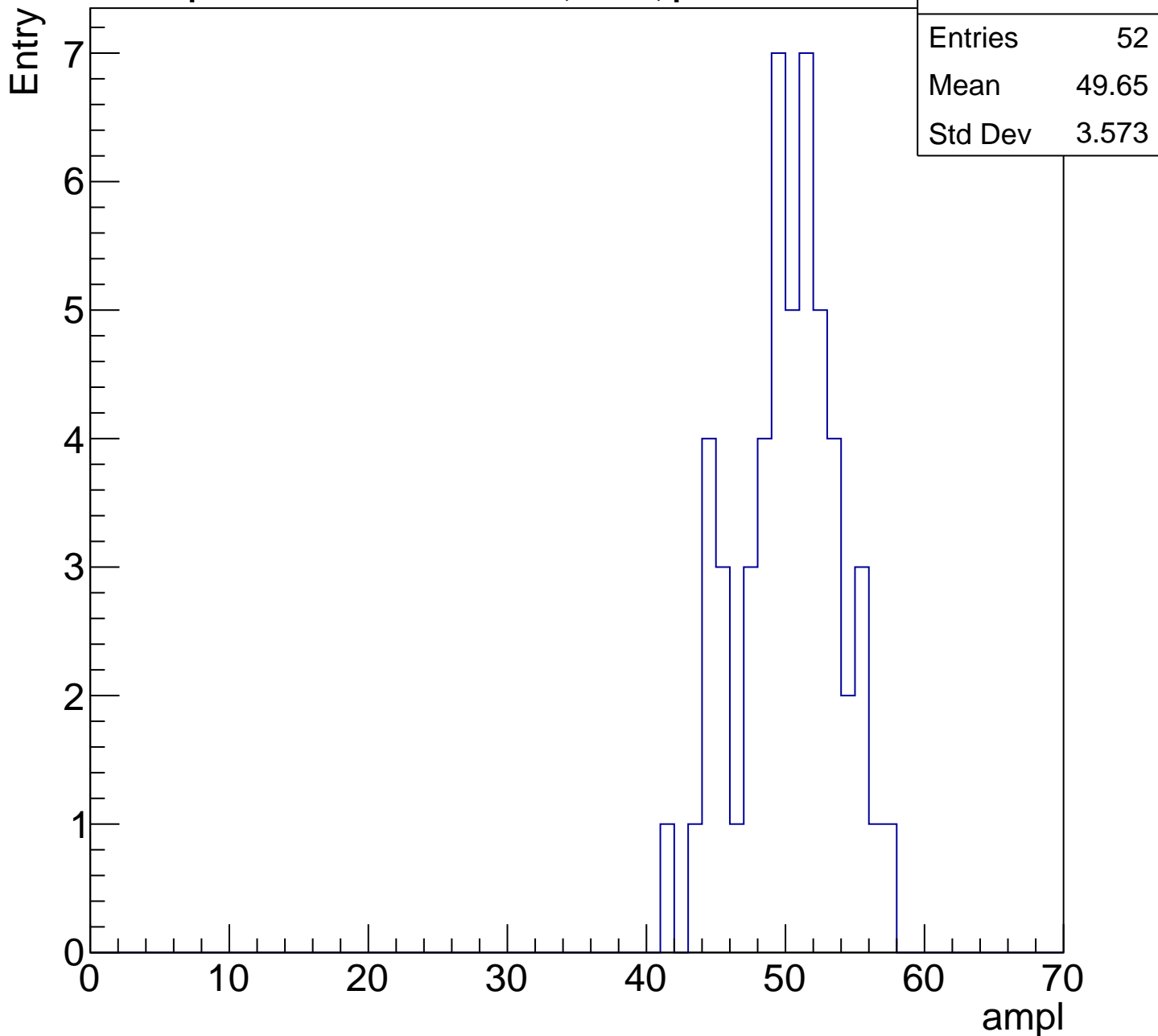
Gaus mean : 45.1059

Gaus Width: 4.2516



B1L101S, U4-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

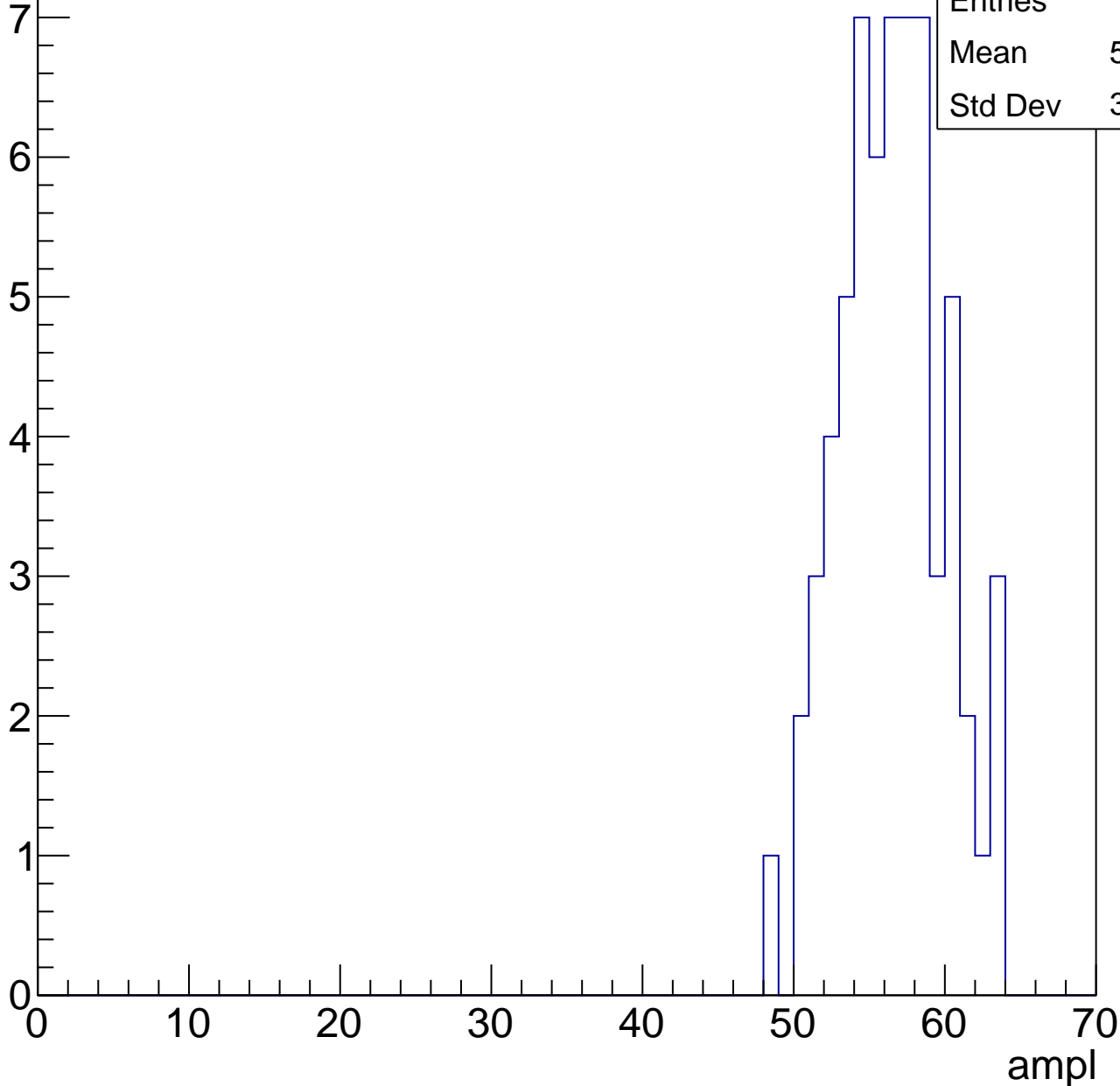


B1L101S, U4-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	56.02
Std Dev	3.402

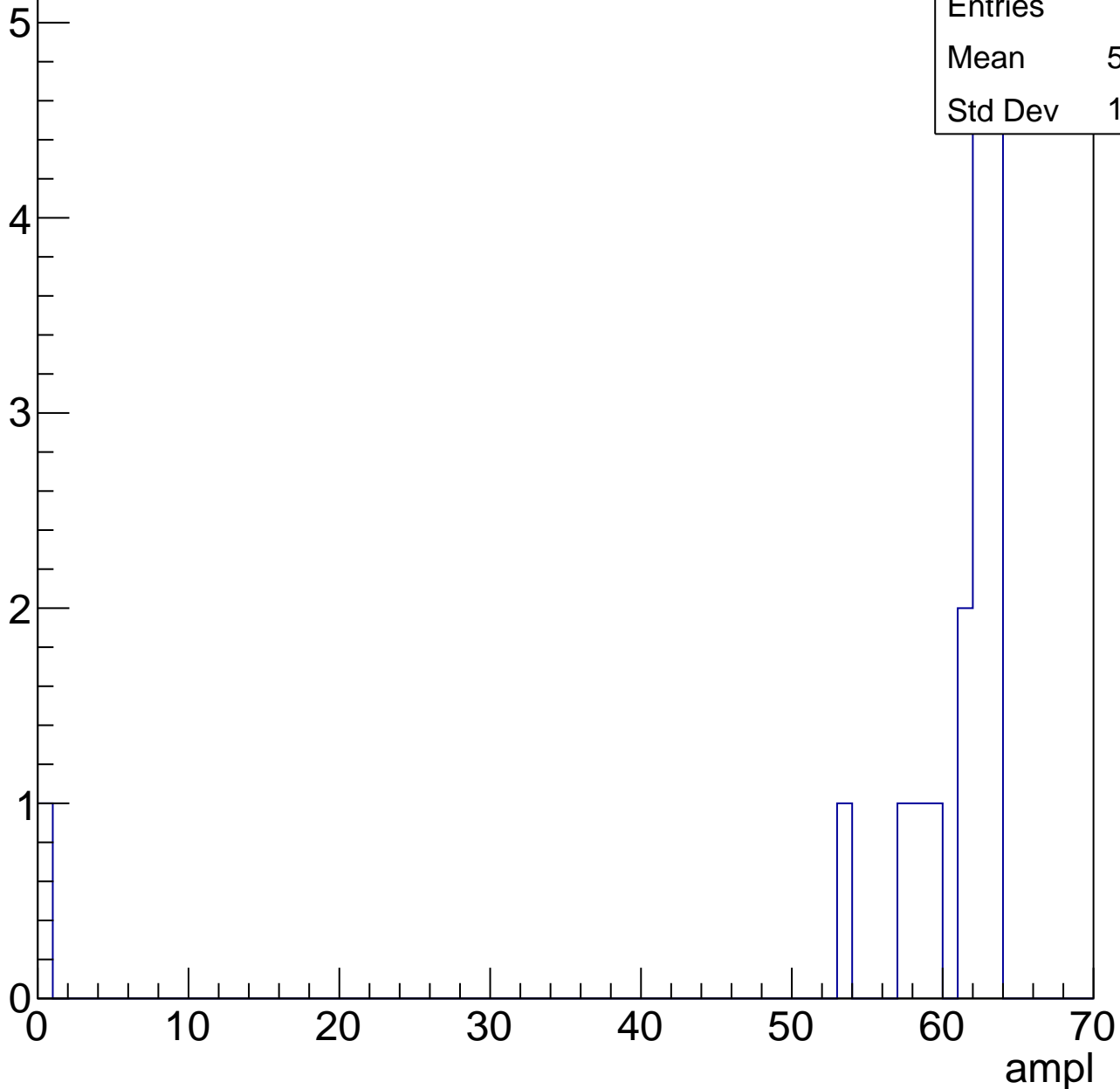


B1L101S, U4-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.29
Std Dev	14.56



B1L101S, U4-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch13, adc0

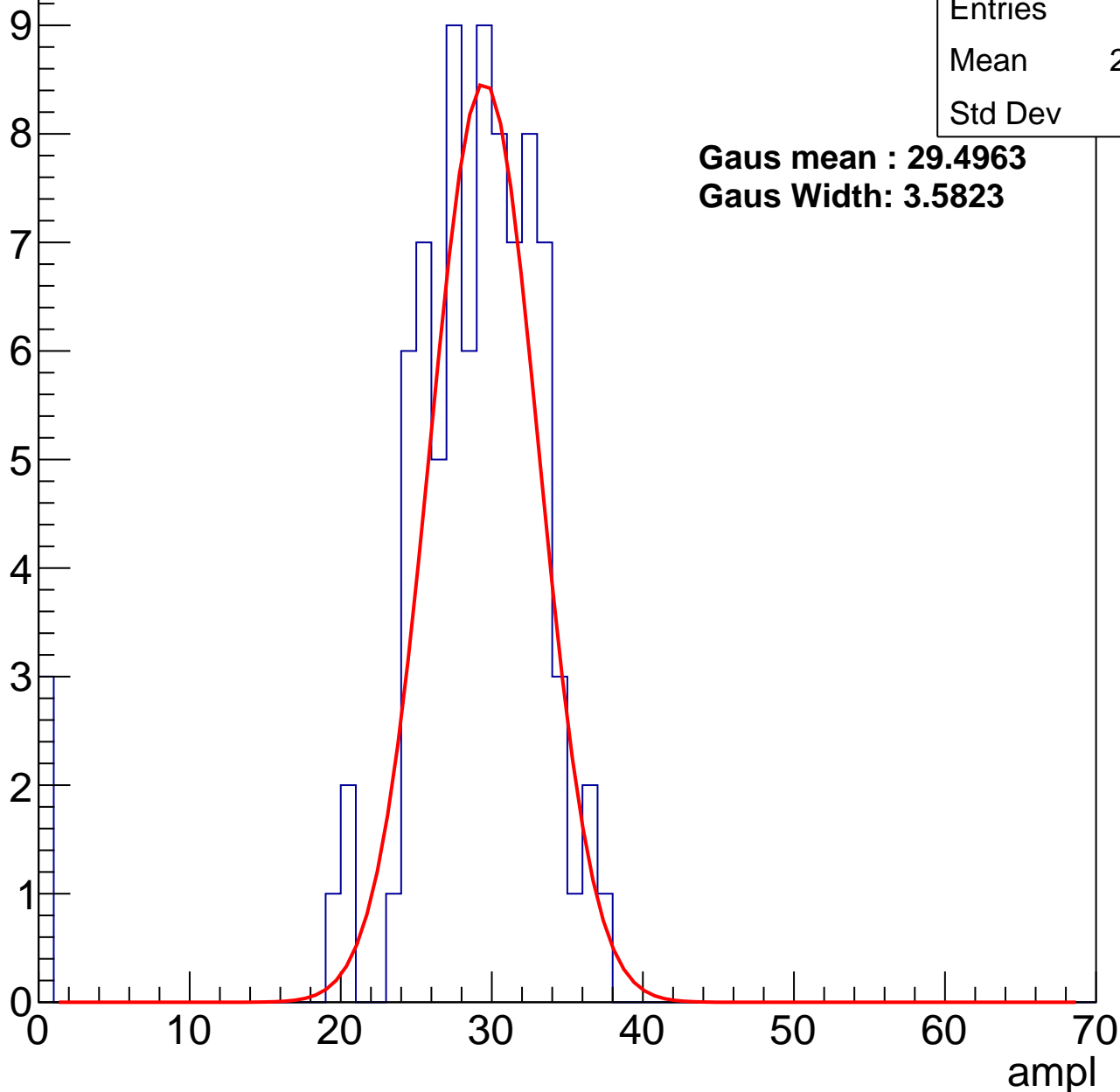
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	27.83
Std Dev	6.42

Gaus mean : 29.4963

Gaus Width: 3.5823



B1L101S, U4-ch13, adc1

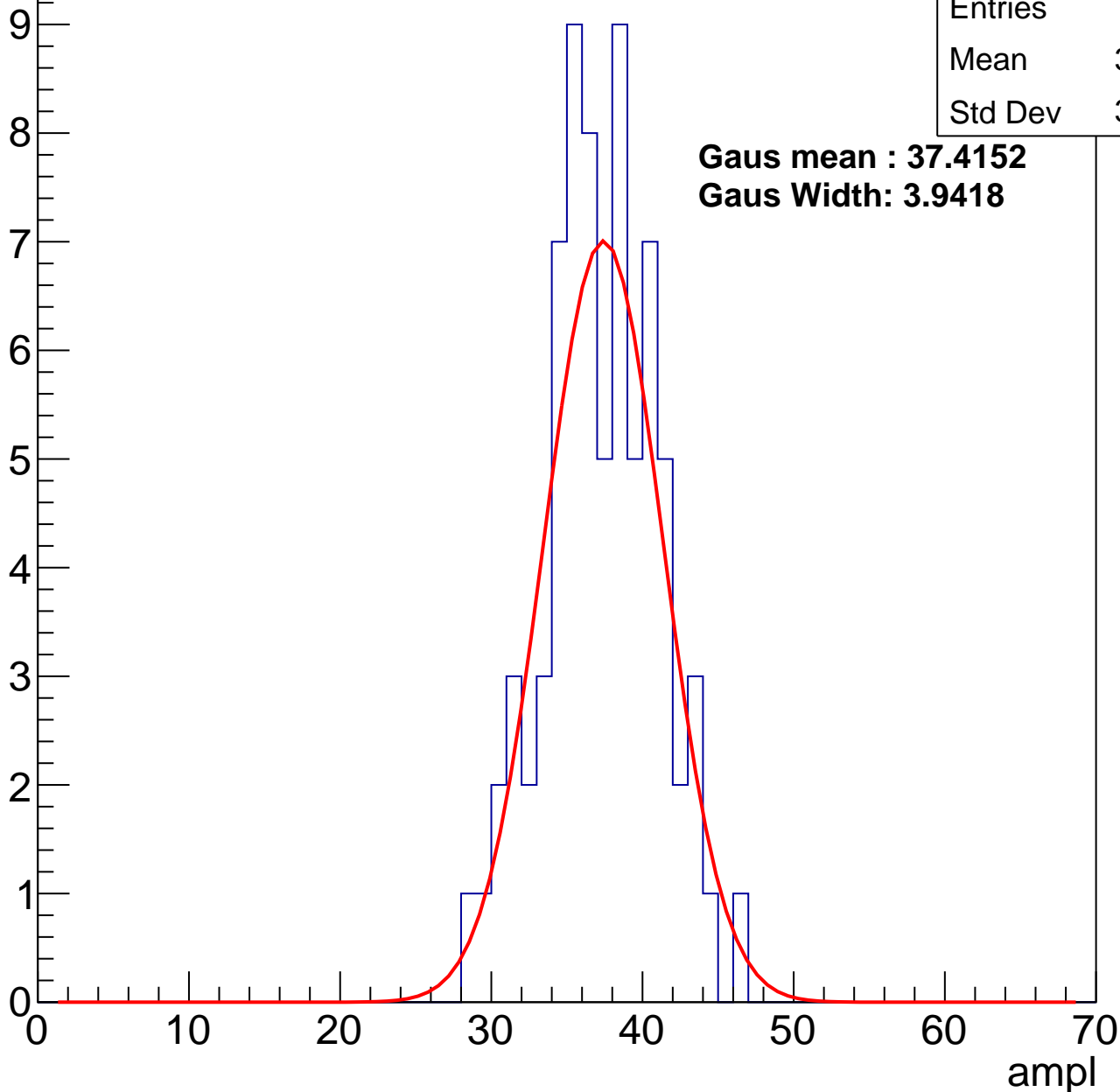
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.81
Std Dev	3.701

Gaus mean : 37.4152

Gaus Width: 3.9418



B1L101S, U4-ch13, adc2

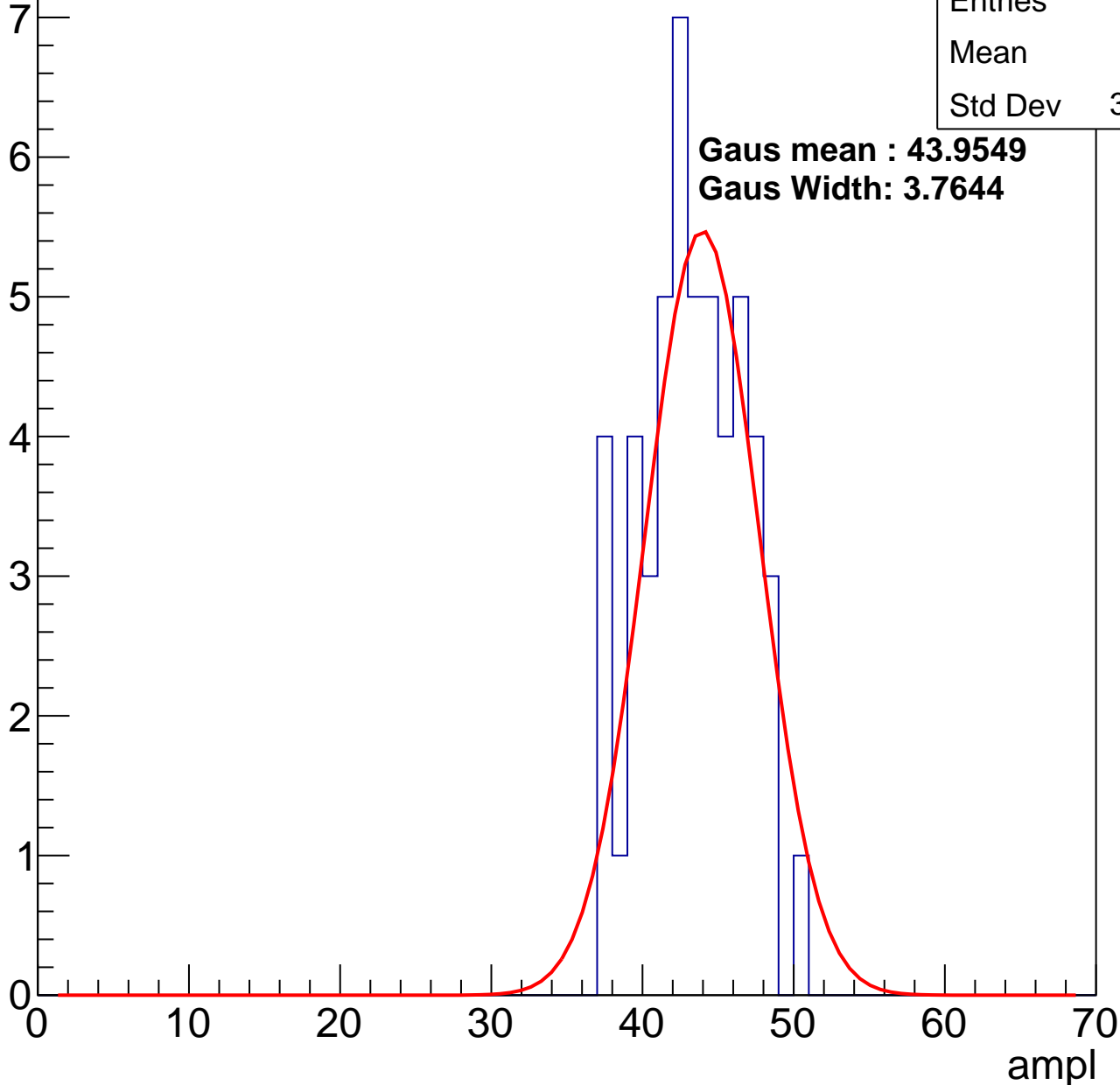
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.9
Std Dev	3.255

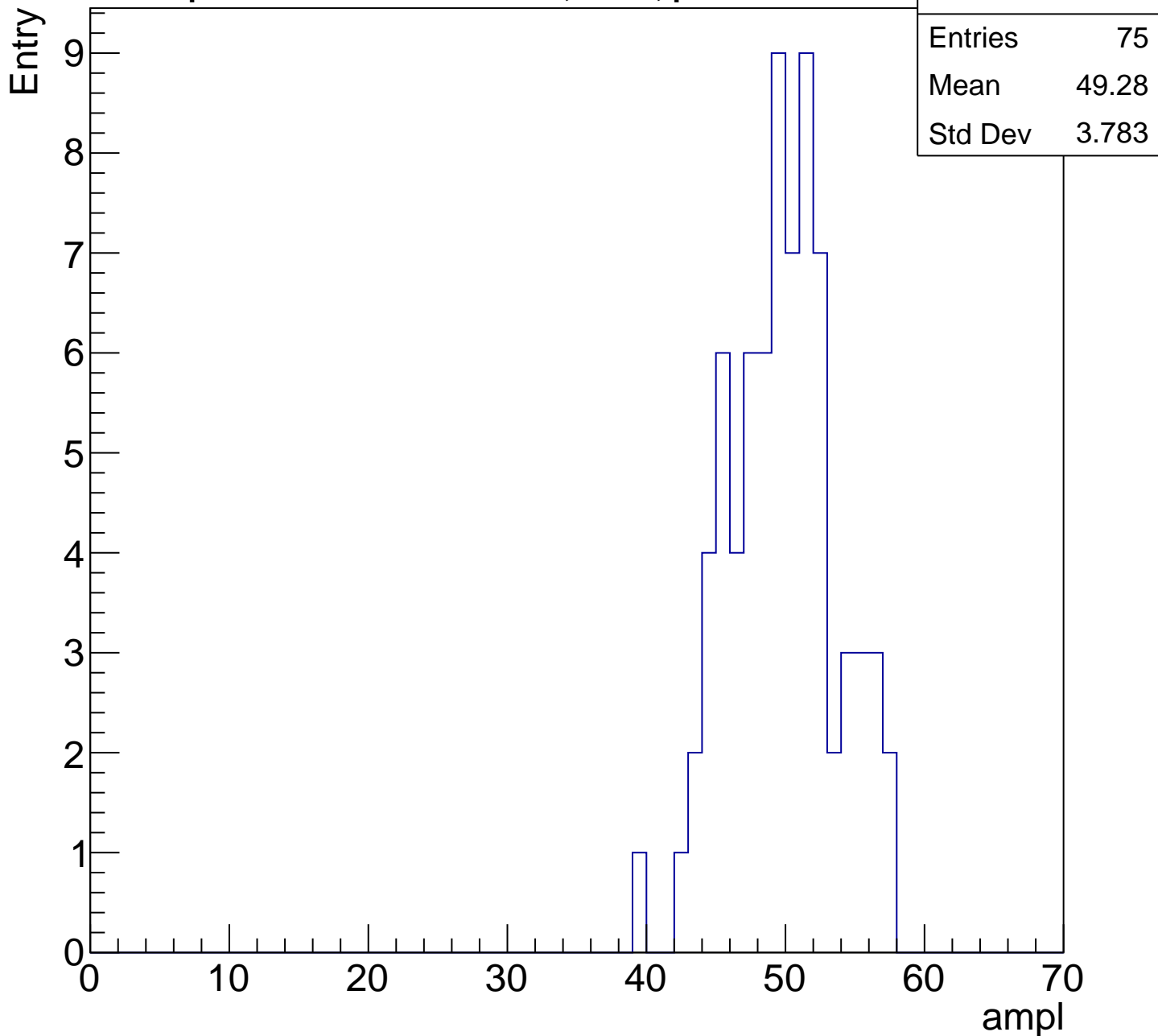
Gaus mean : 43.9549

Gaus Width: 3.7644



B1L101S, U4-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

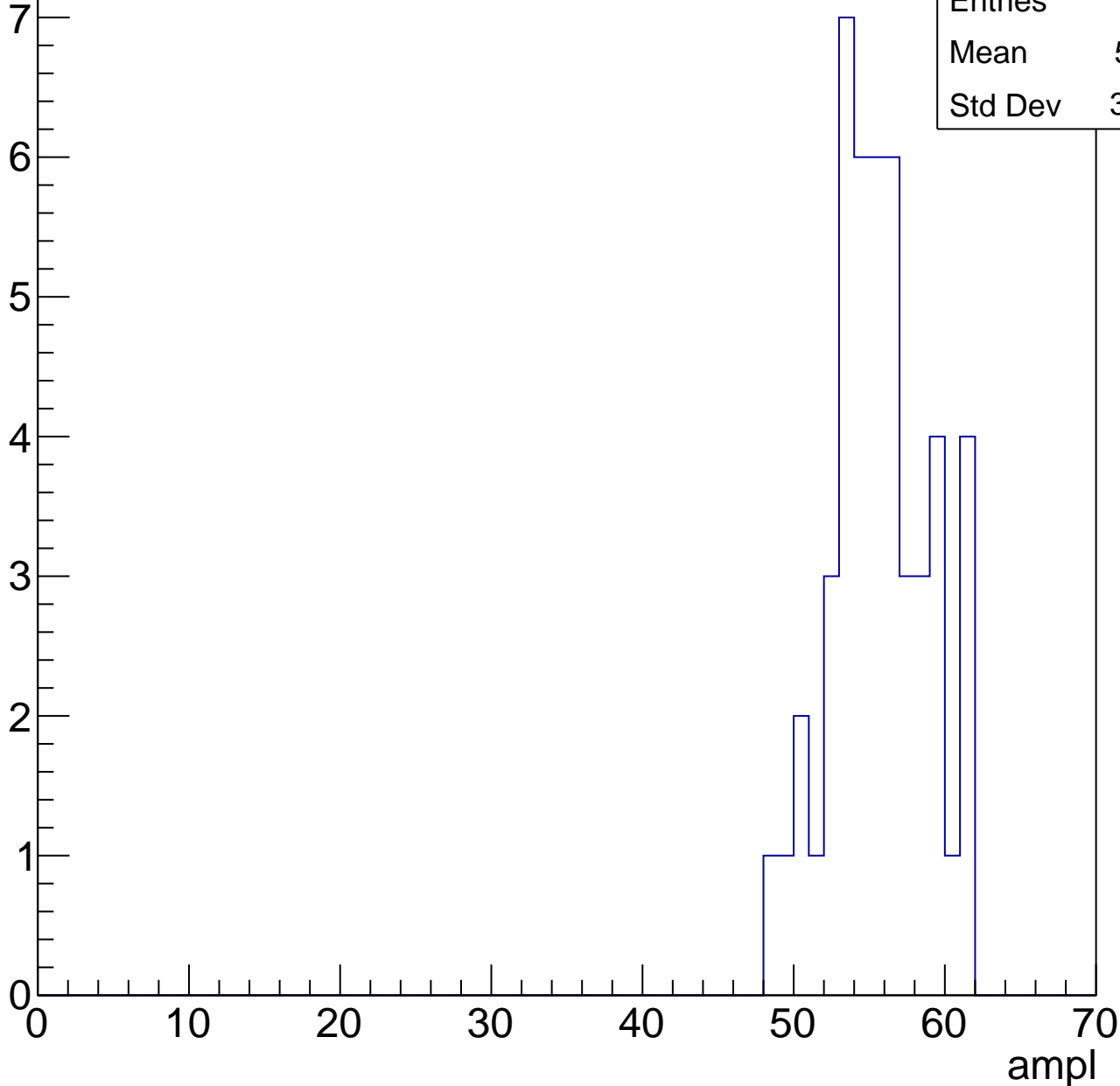


B1L101S, U4-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	55.21
Std Dev	3.188



B1L101S, U4-ch13, adc5

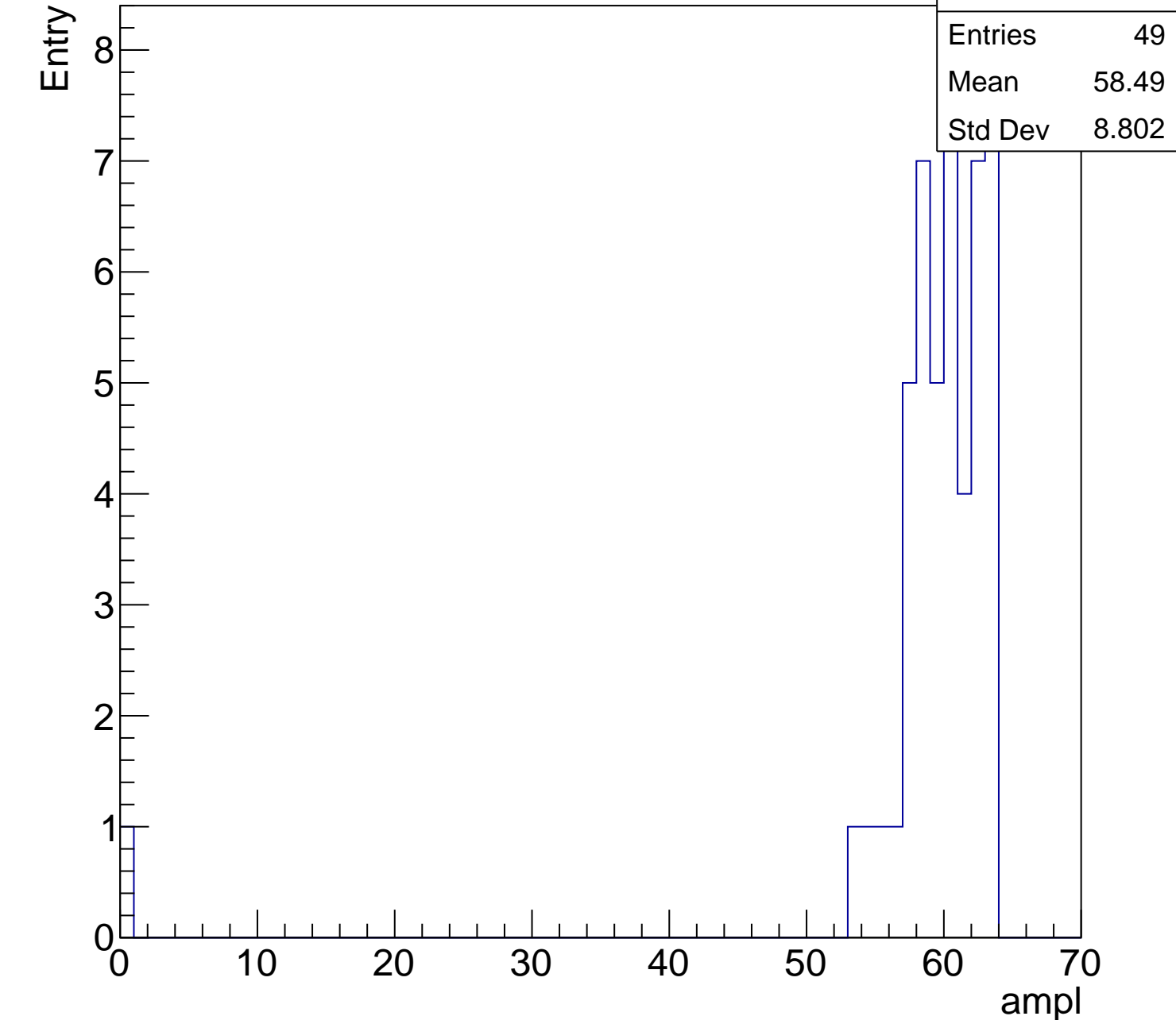
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.49
Std Dev	8.802

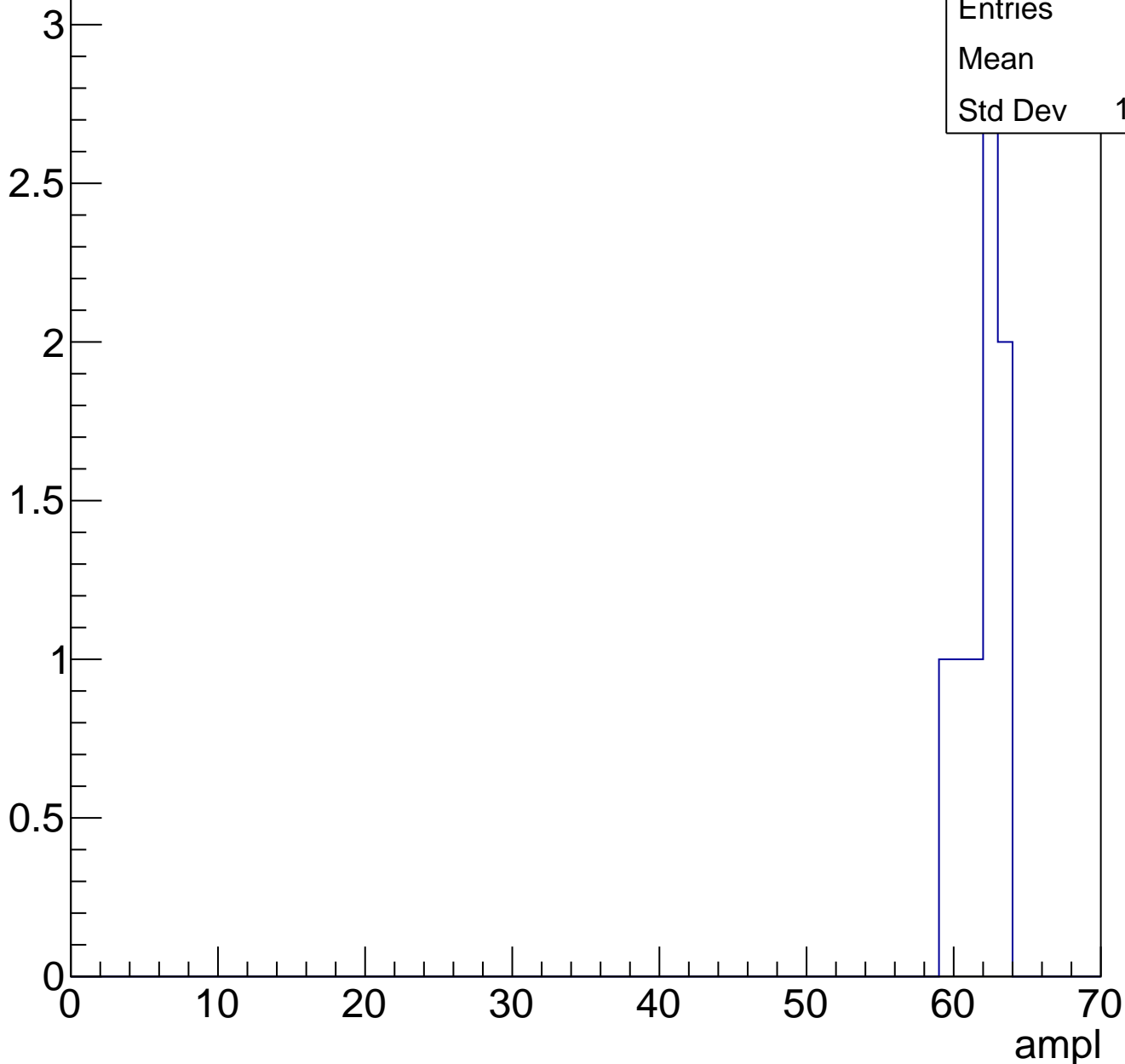
ampl



B1L101S, U4-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

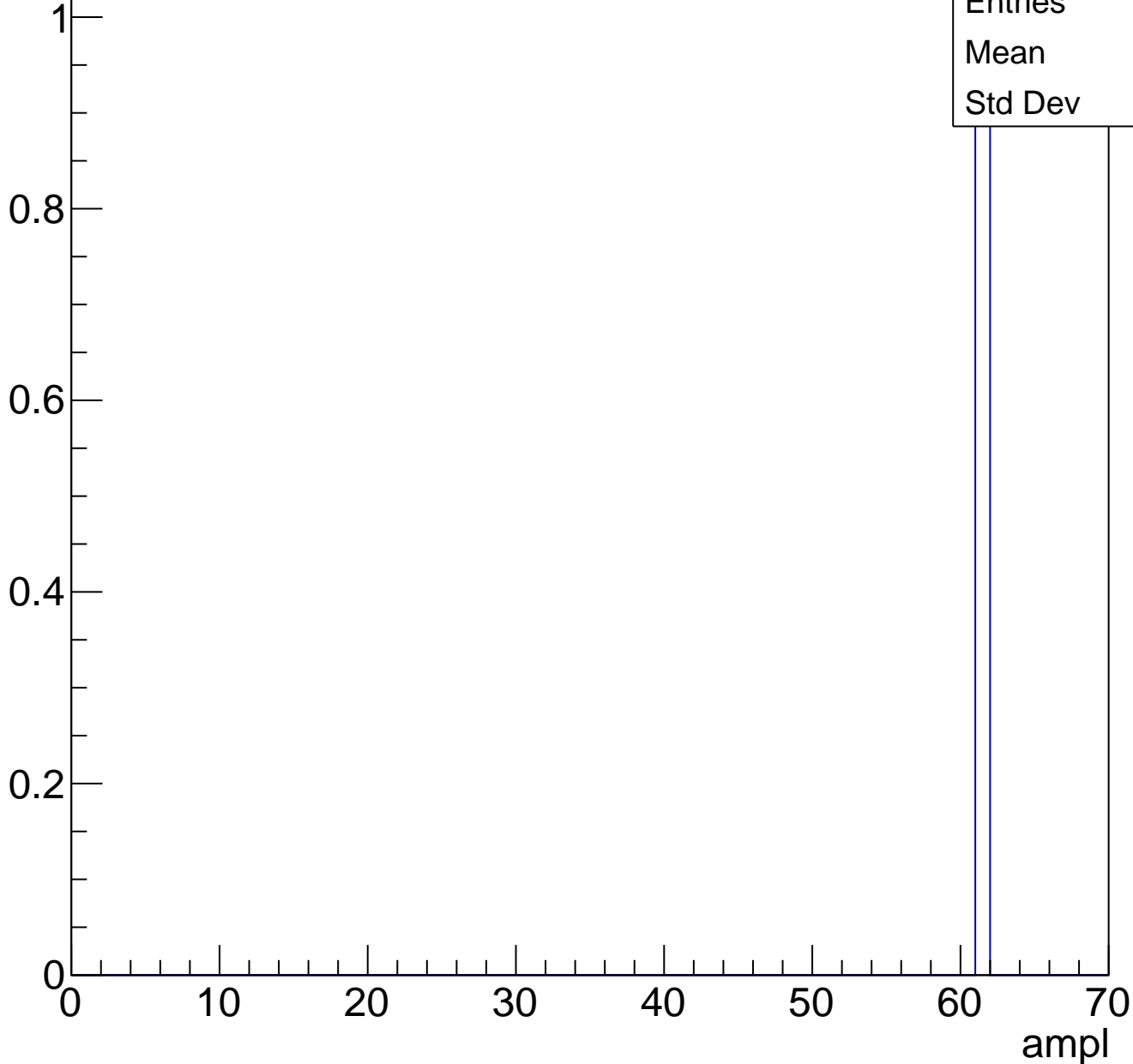
Entry



B1L101S, U4-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch14, adc0

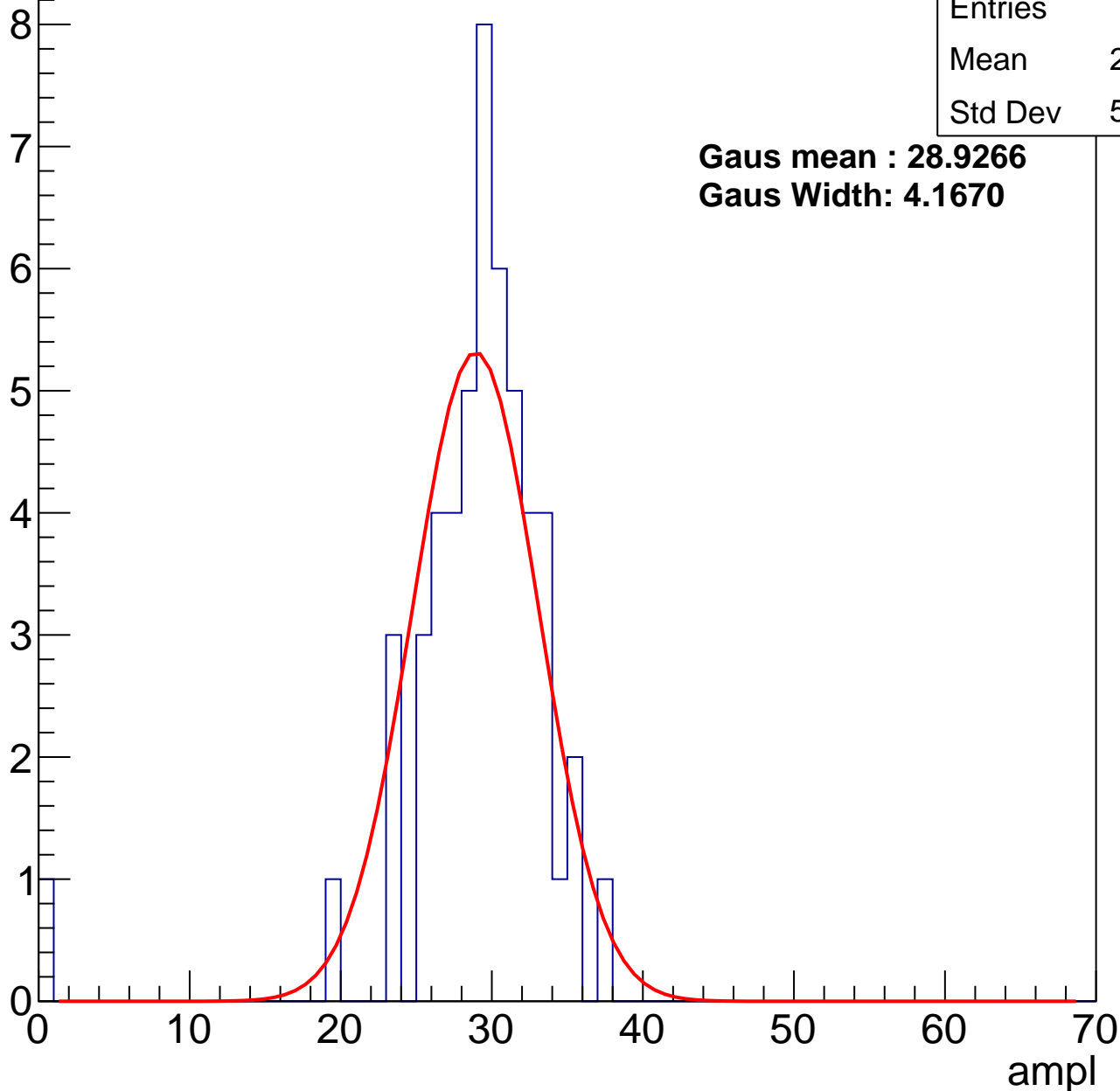
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	28.52
Std Dev	5.242

Gaus mean : 28.9266

Gaus Width: 4.1670



B1L101S, U4-ch14, adc1

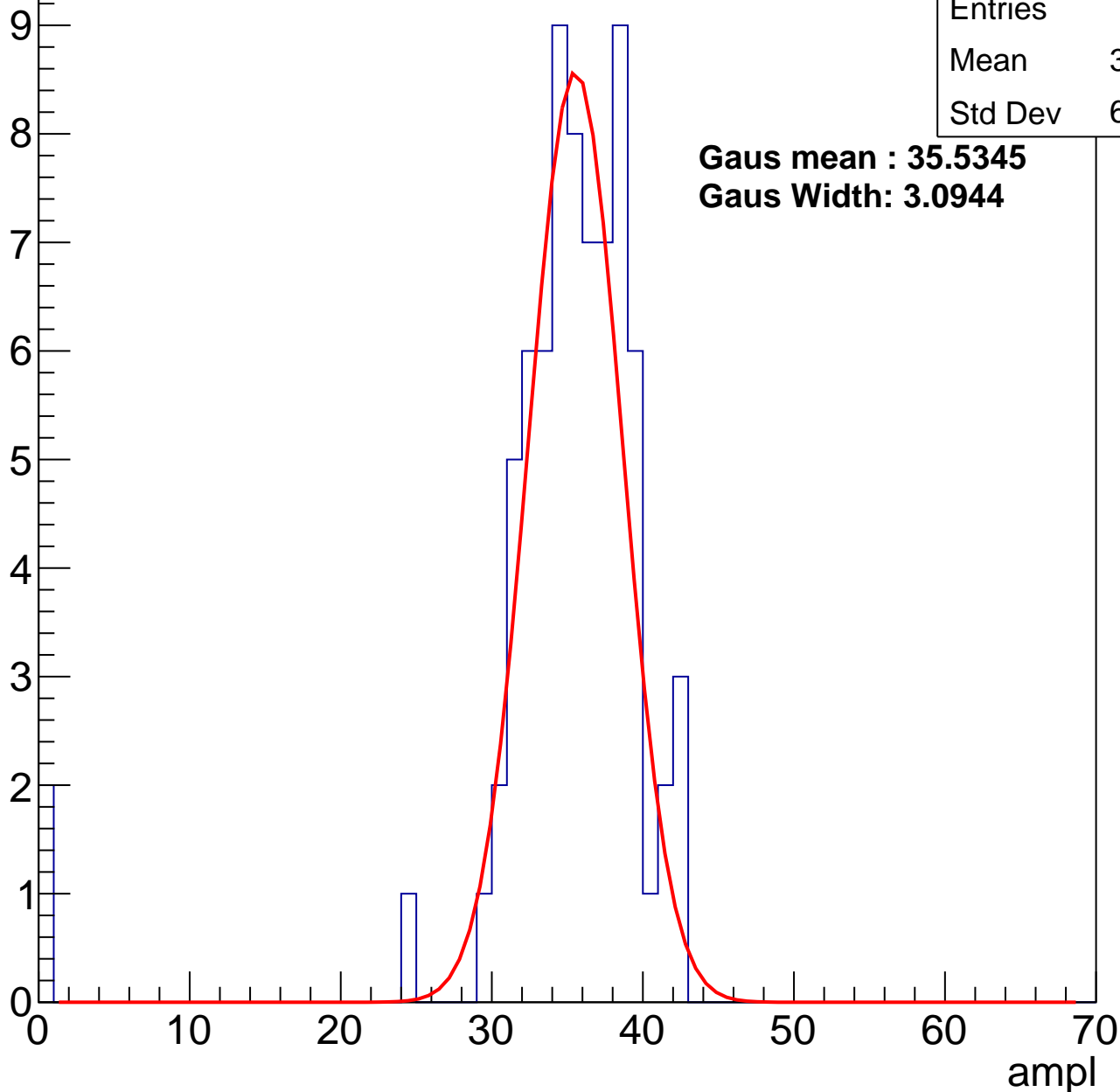
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	34.39
Std Dev	6.585

Gaus mean : 35.5345

Gaus Width: 3.0944



B1L101S, U4-ch14, adc2

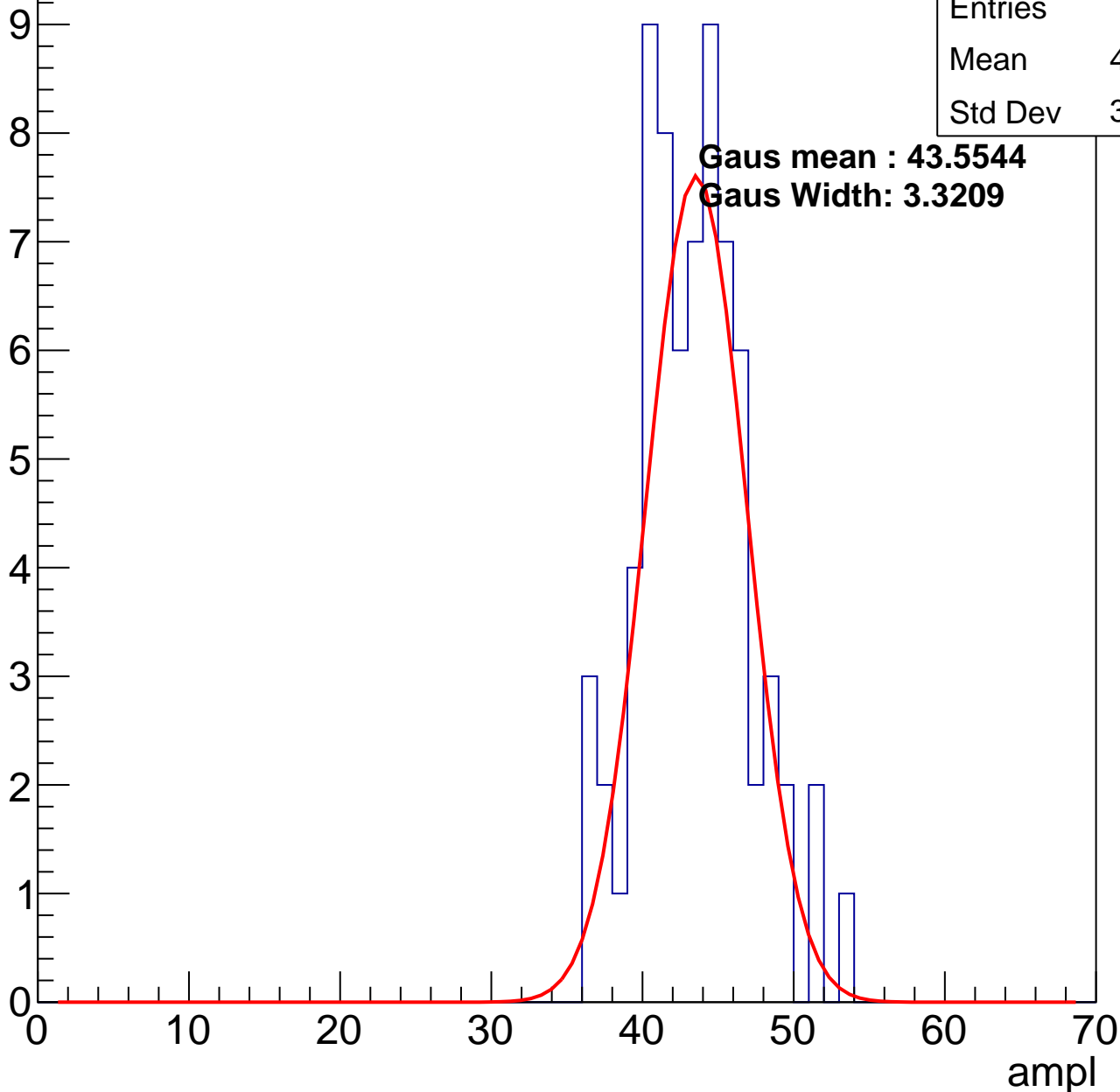
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.99
Std Dev	3.584

Gaus mean : 43.5544

Gaus Width: 3.3209

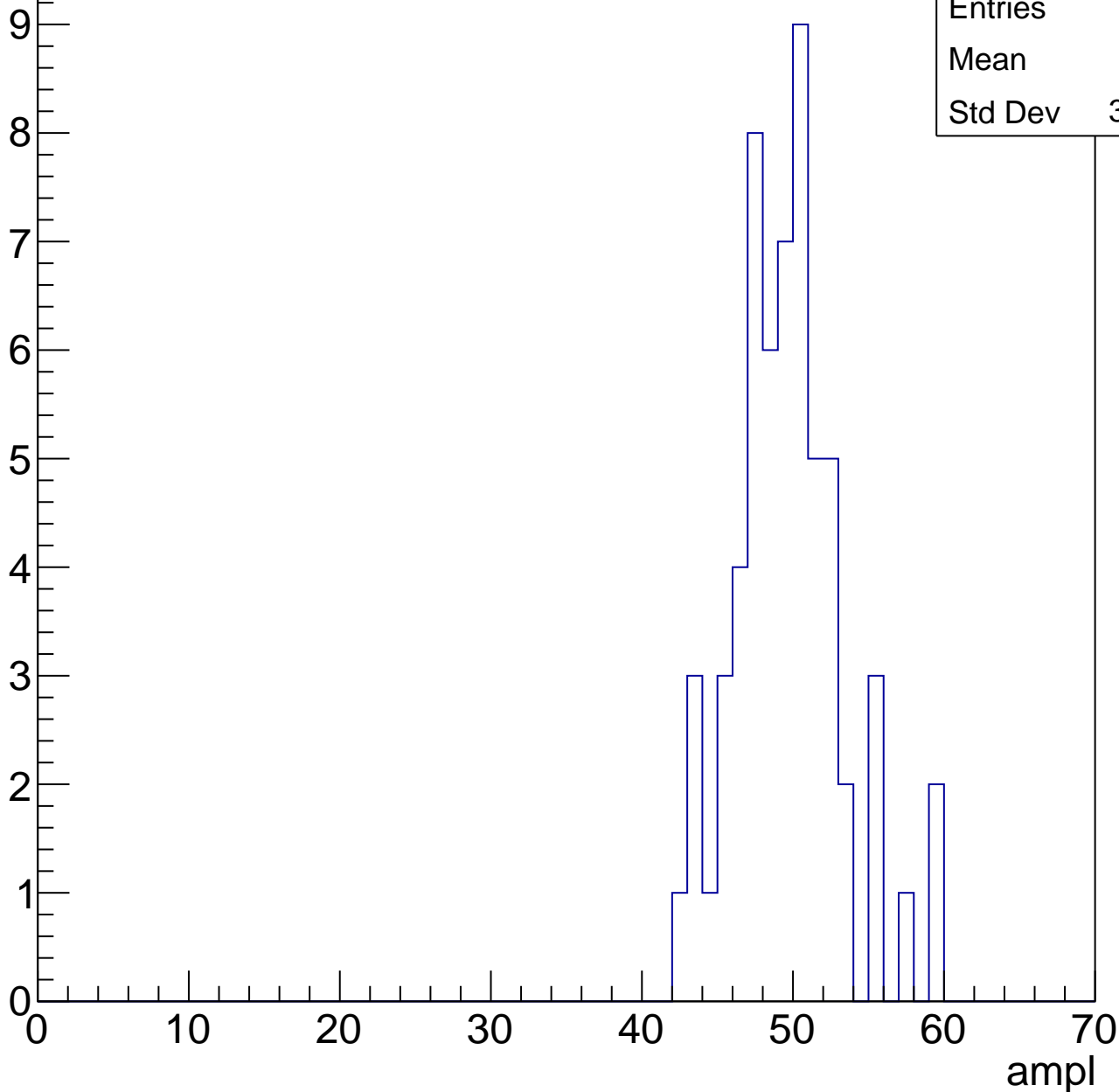


B1L101S, U4-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

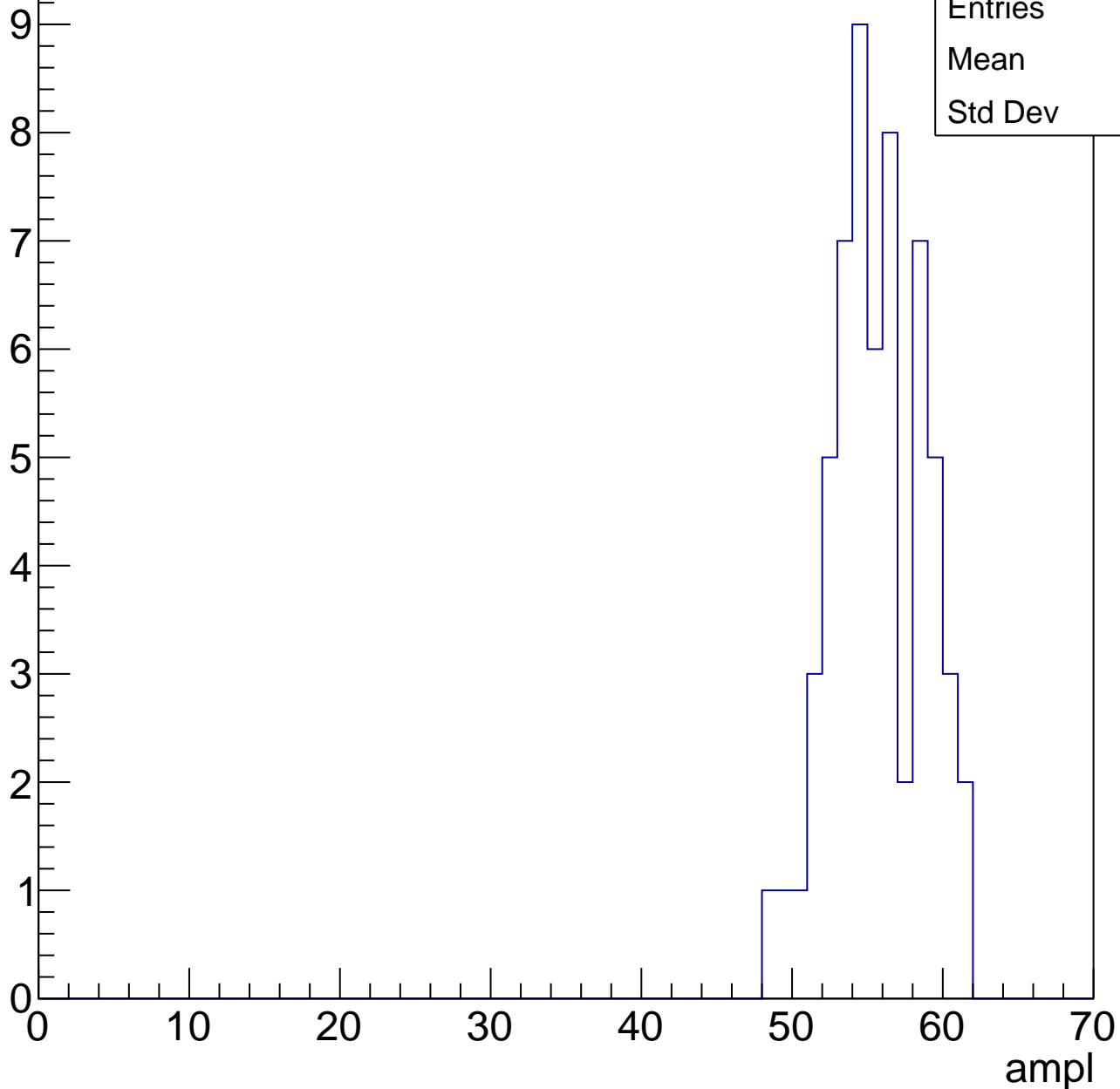
Entries	60
Mean	49.2
Std Dev	3.605



B1L101S, U4-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	60
Mean	55.2
Std Dev	3.01

B1L101S, U4-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	38
Mean	59.32
Std Dev	2.284

ampl

0

10

20

30

40

50

60

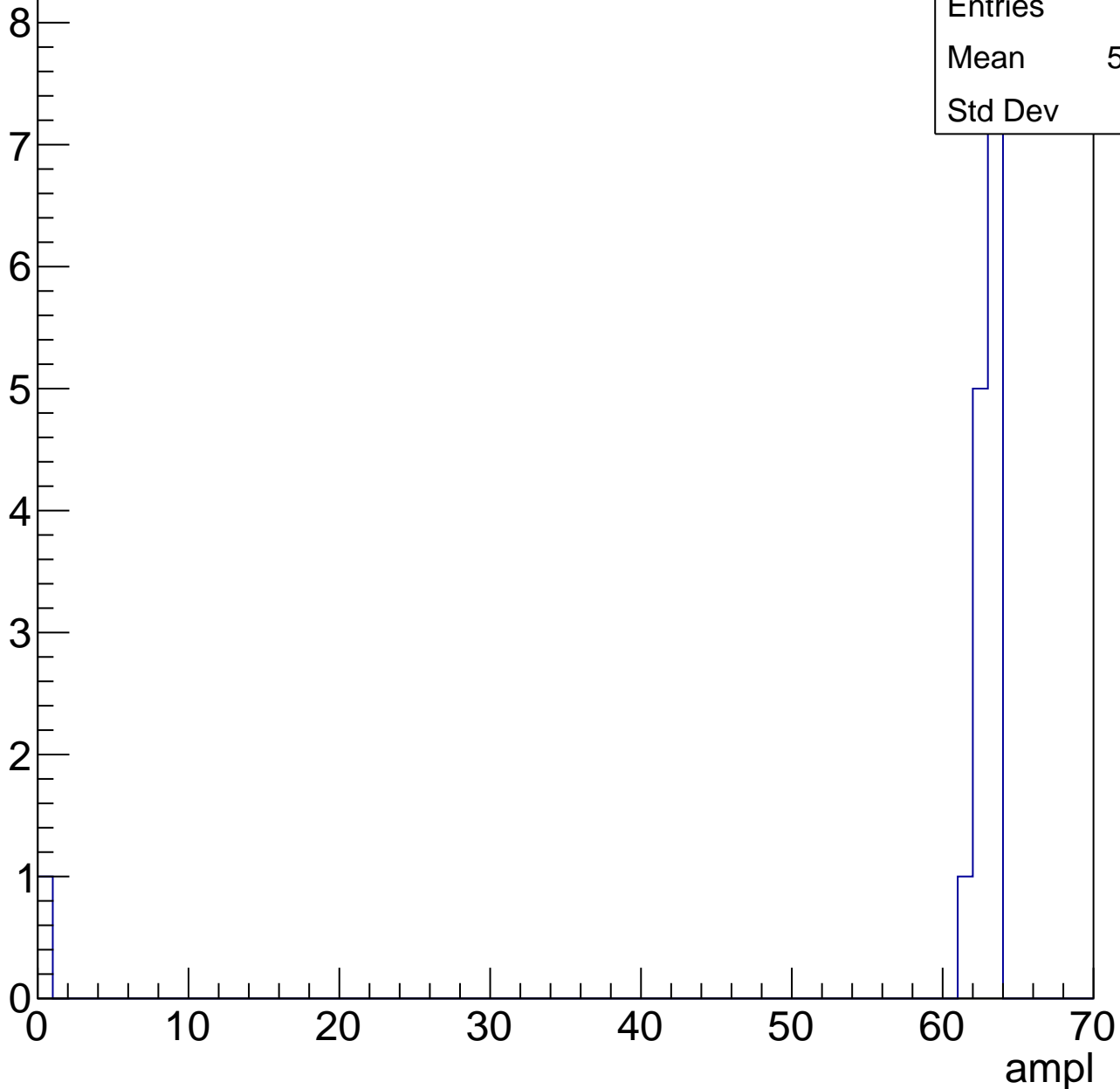
70

B1L101S, U4-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	58.33
Std Dev	15.6



B1L101S, U4-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



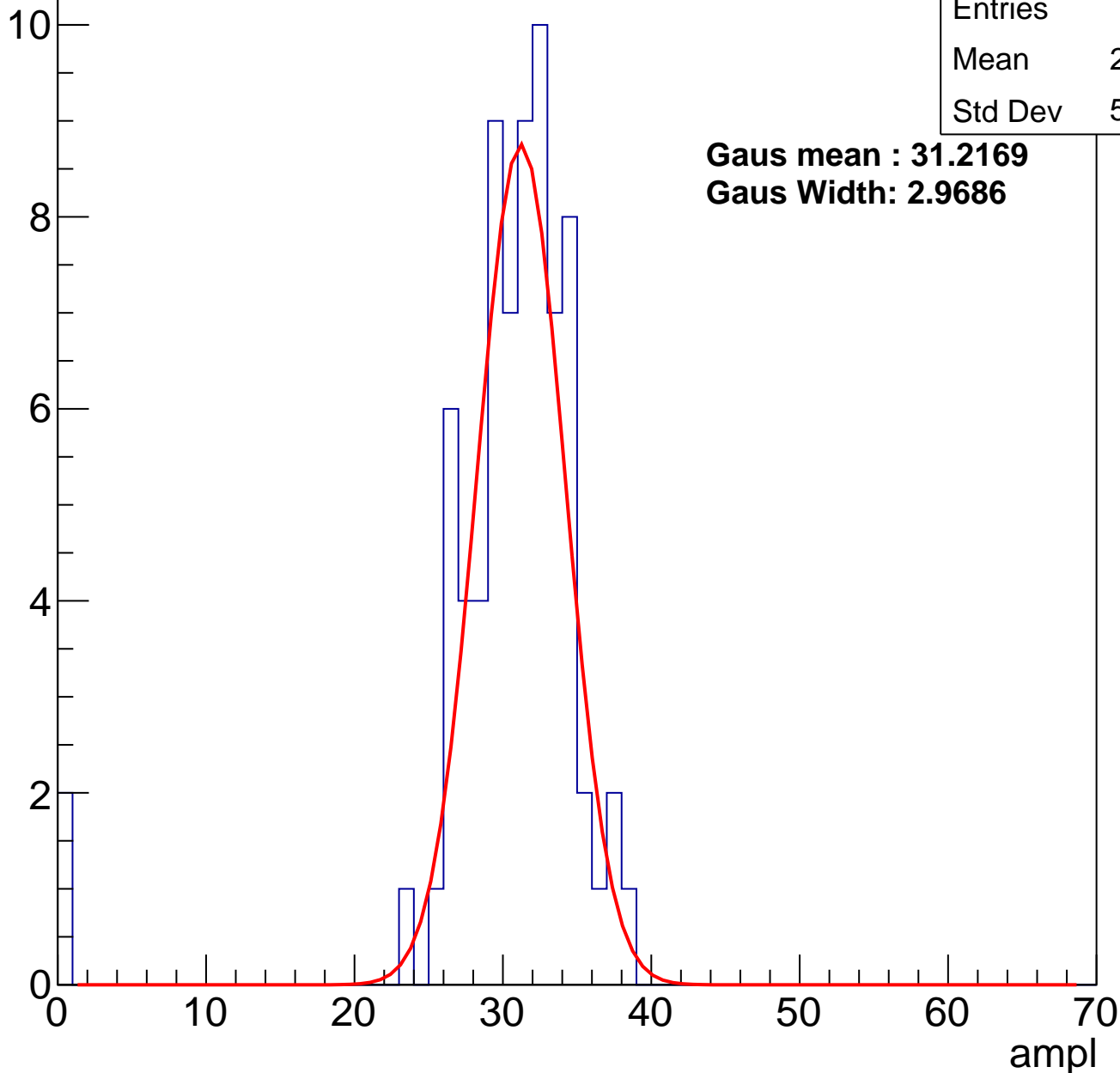
B1L101S, U4-ch15, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.93
Std Dev	5.834

Gaus mean : 31.2169
Gaus Width: 2.9686

Entry



B1L101S, U4-ch15, adc1

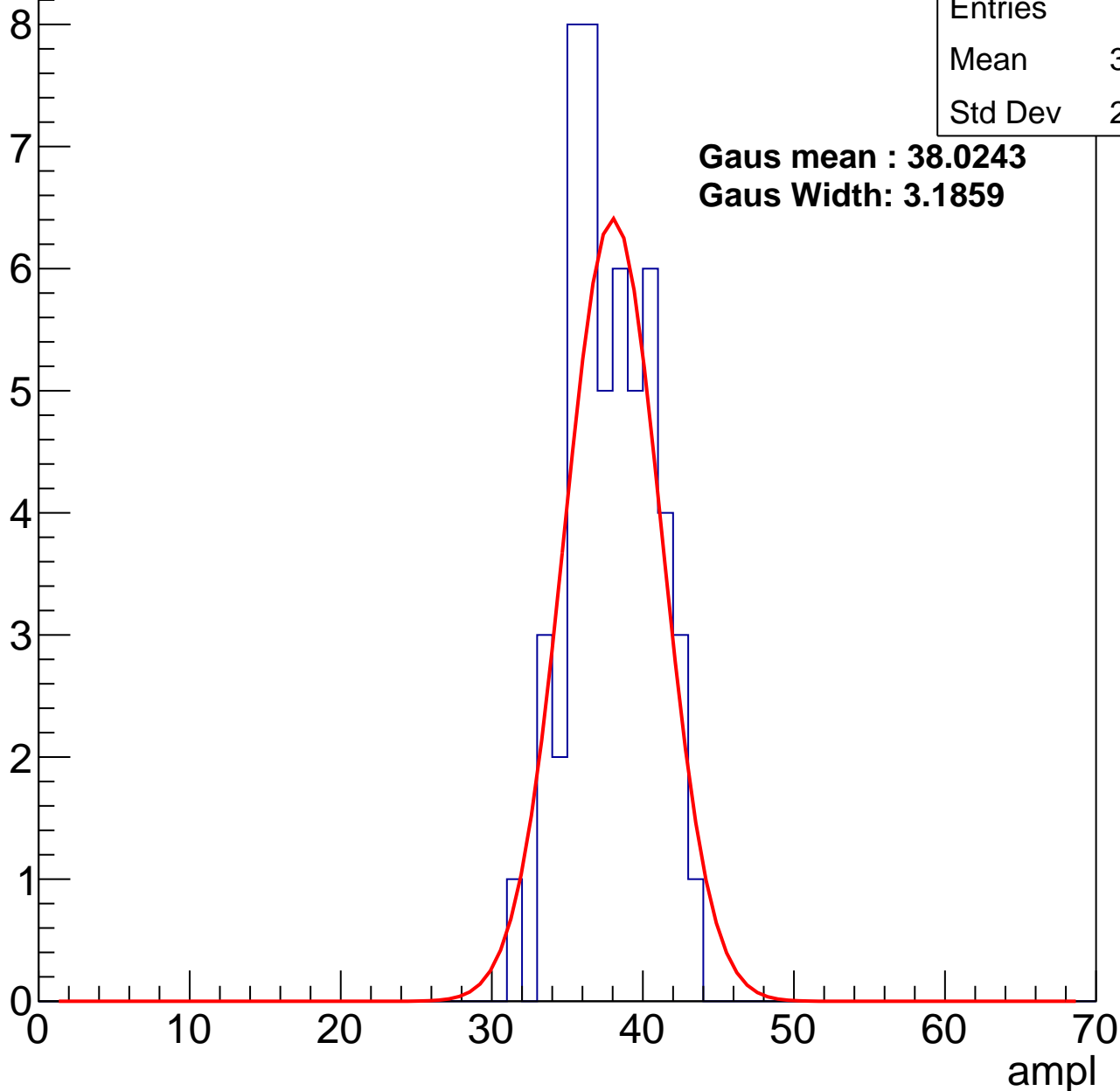
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	37.44
Std Dev	2.727

Gaus mean : 38.0243

Gaus Width: 3.1859



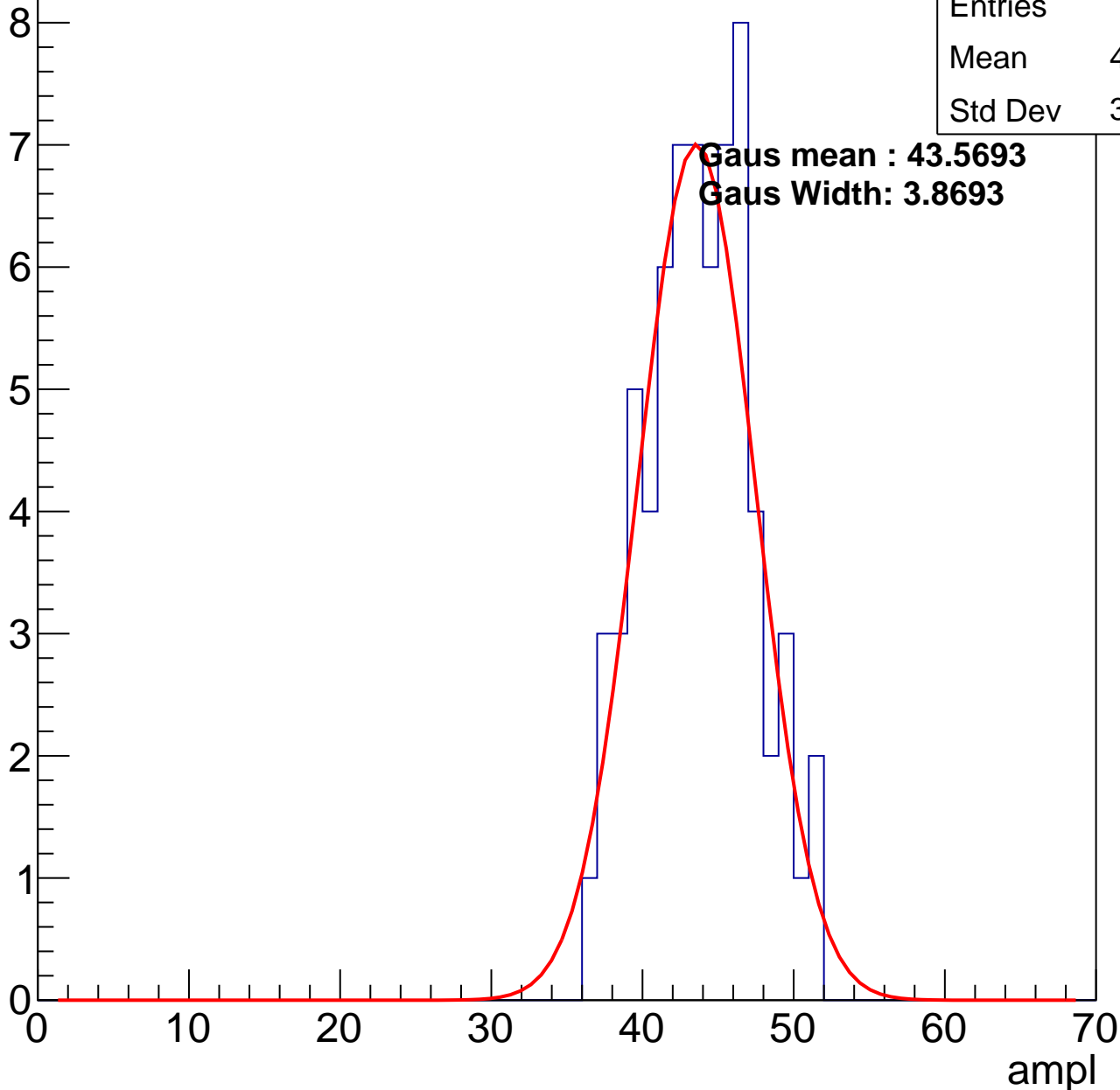
B1L101S, U4-ch15, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

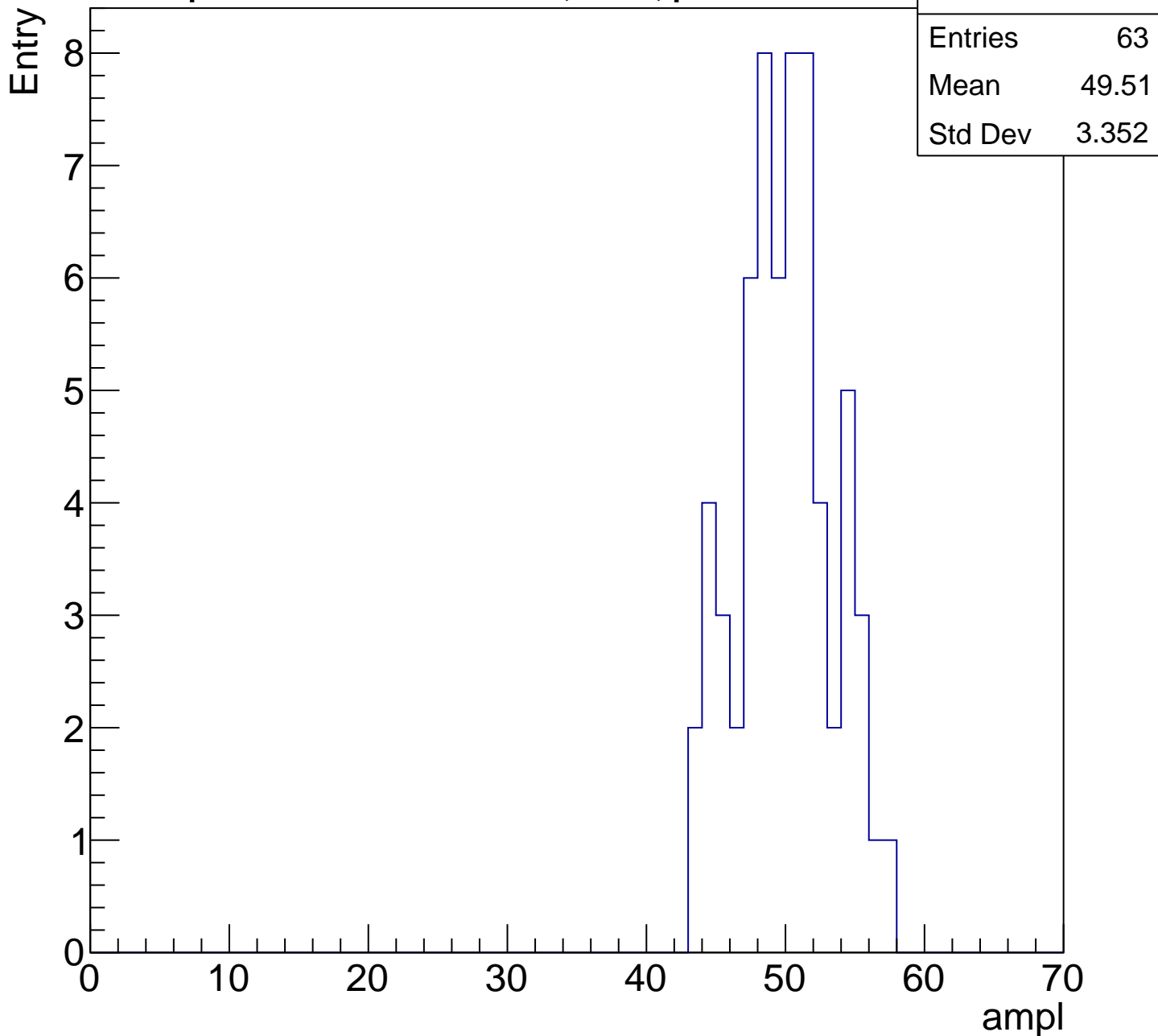
Entries	69
Mean	43.29
Std Dev	3.568

Gaus mean : 43.5693
Gaus Width: 3.8693



B1L101S, U4-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

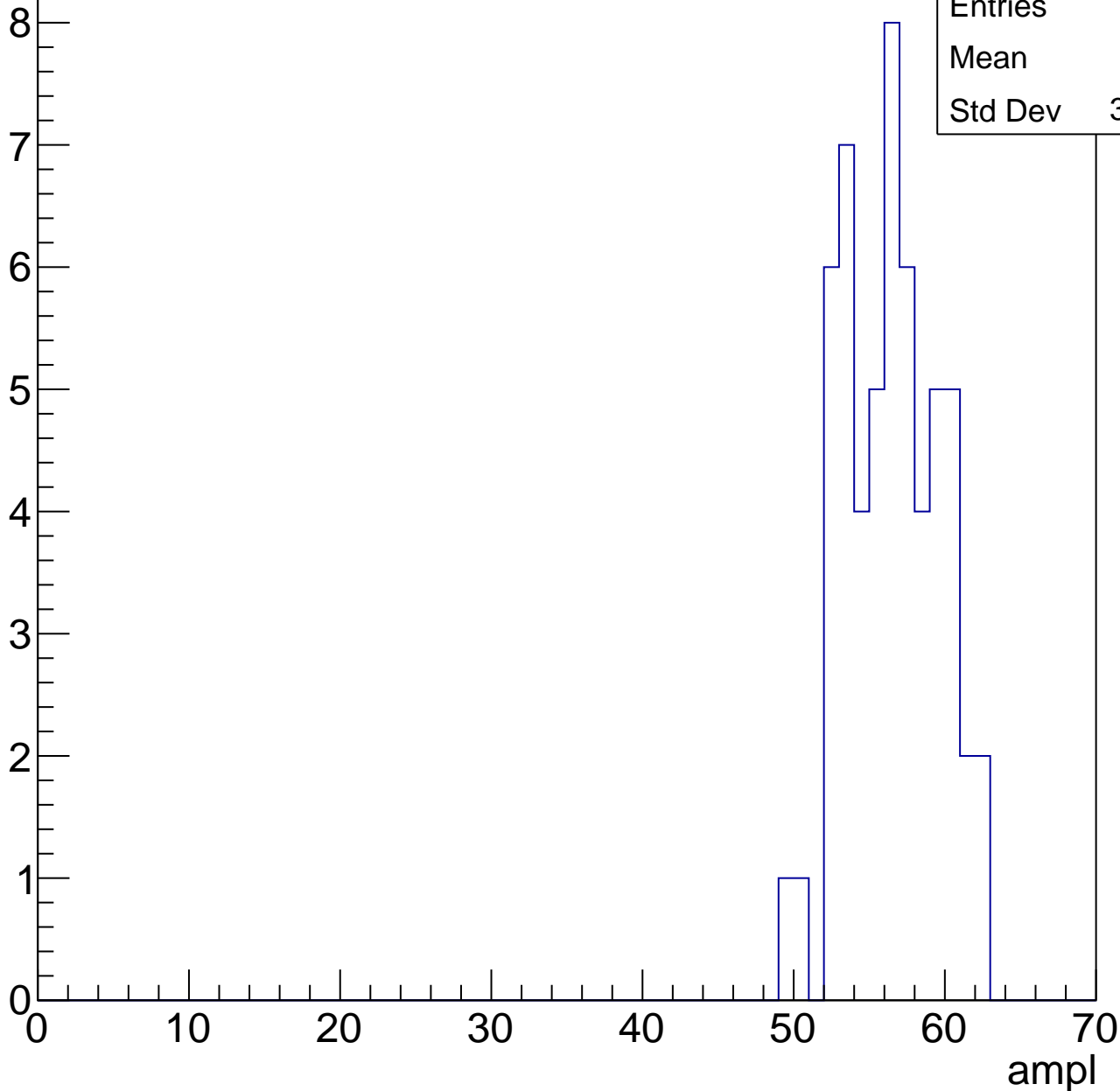


B1L101S, U4-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56
Std Dev	3.088

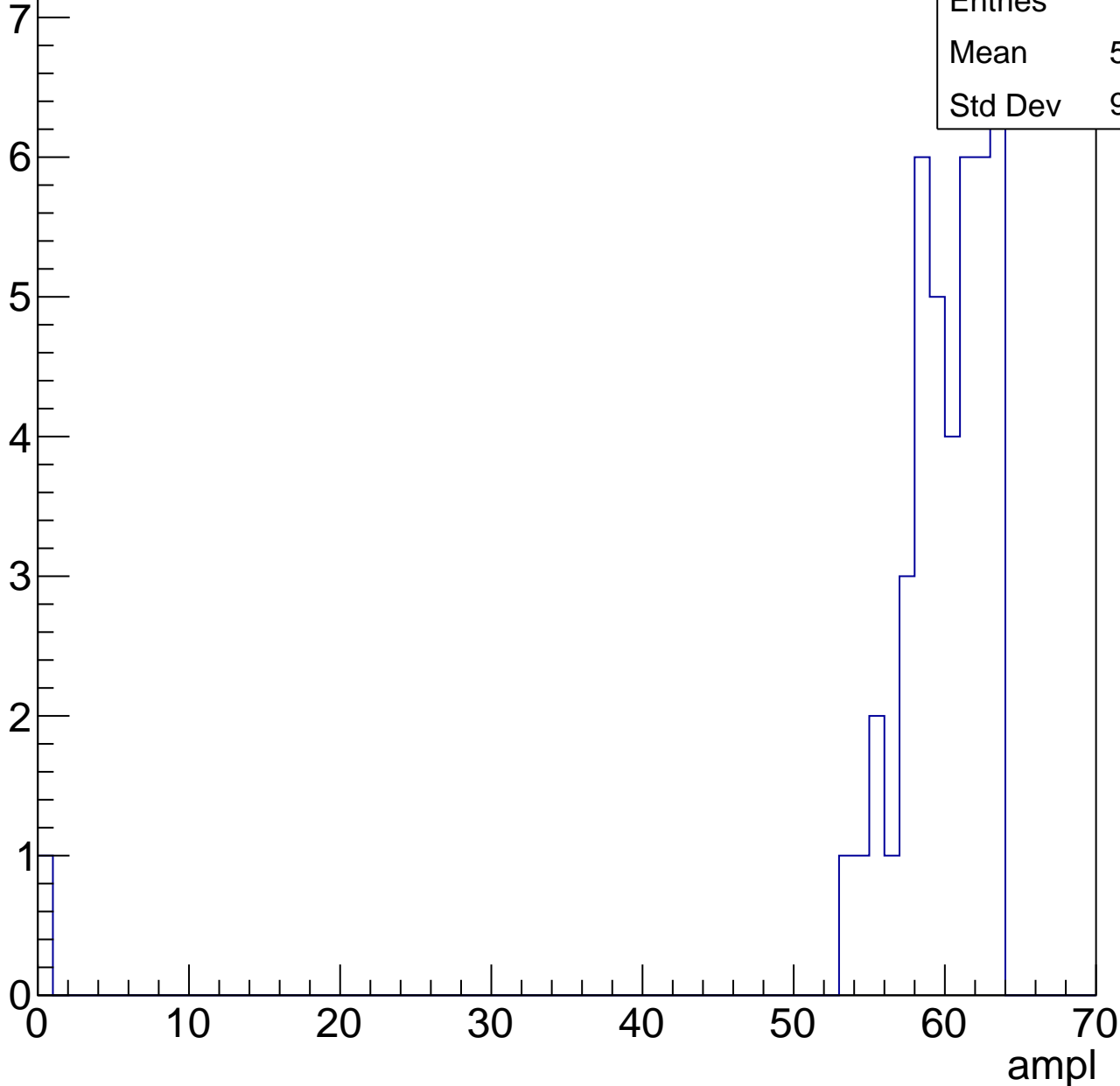


B1L101S, U4-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.28
Std Dev	9.367



B1L101S, U4-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch16, adc0

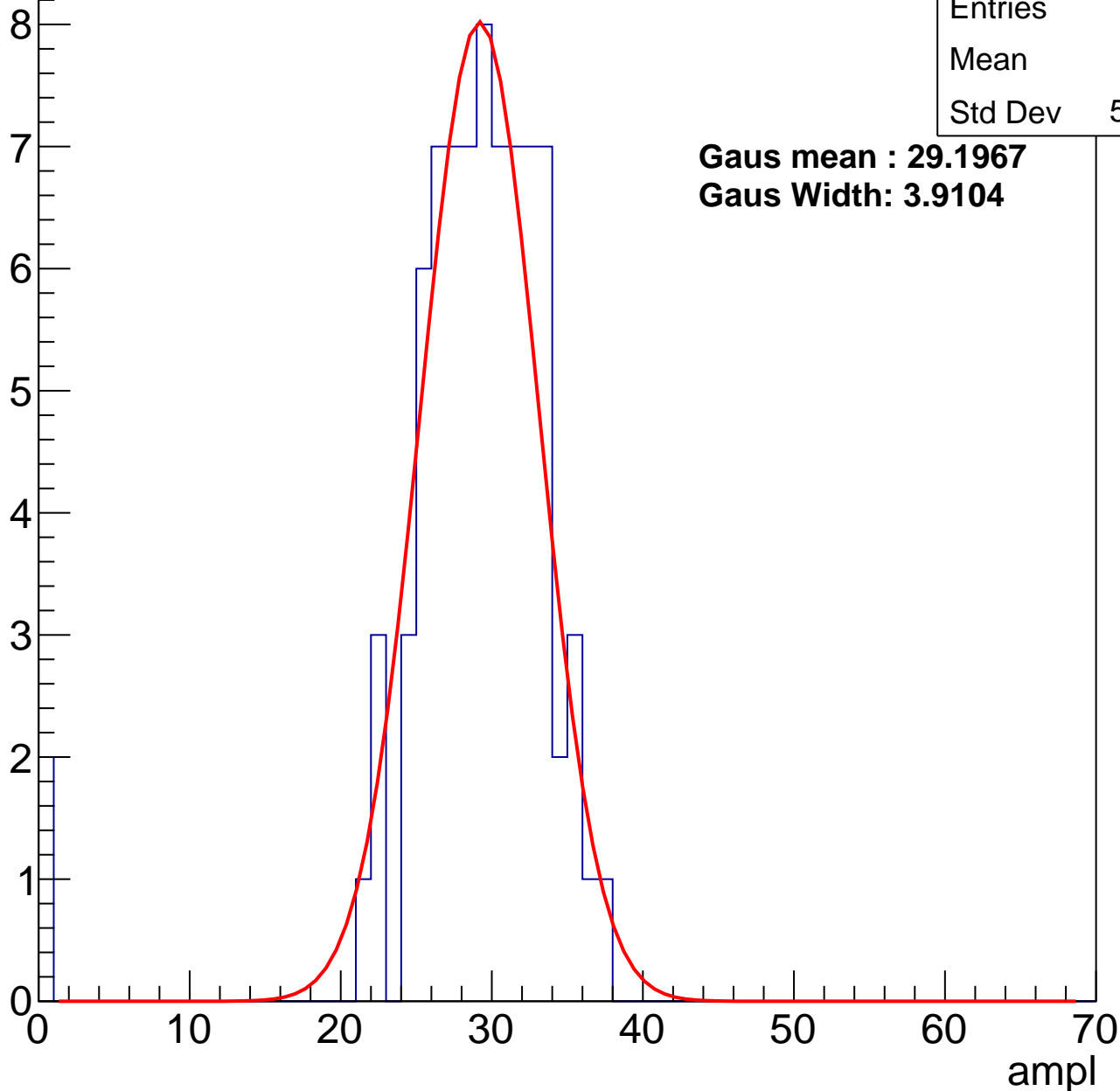
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.3
Std Dev	5.742

Gaus mean : 29.1667

Gaus Width: 3.9104



B1L101S, U4-ch16, adc1

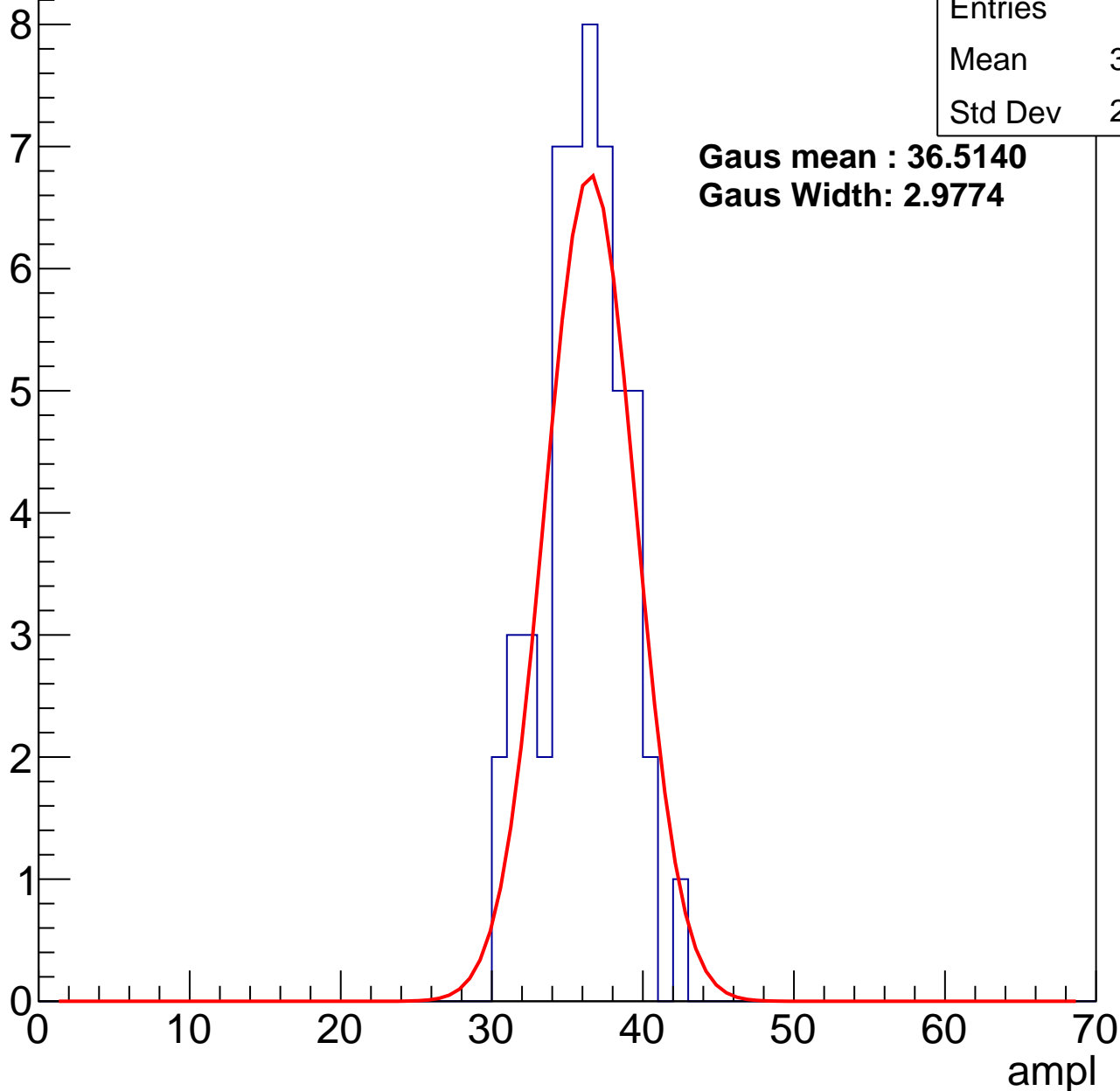
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	35.62
Std Dev	2.704

Gaus mean : 36.5140

Gaus Width: 2.9774



B1L101S, U4-ch16, adc2

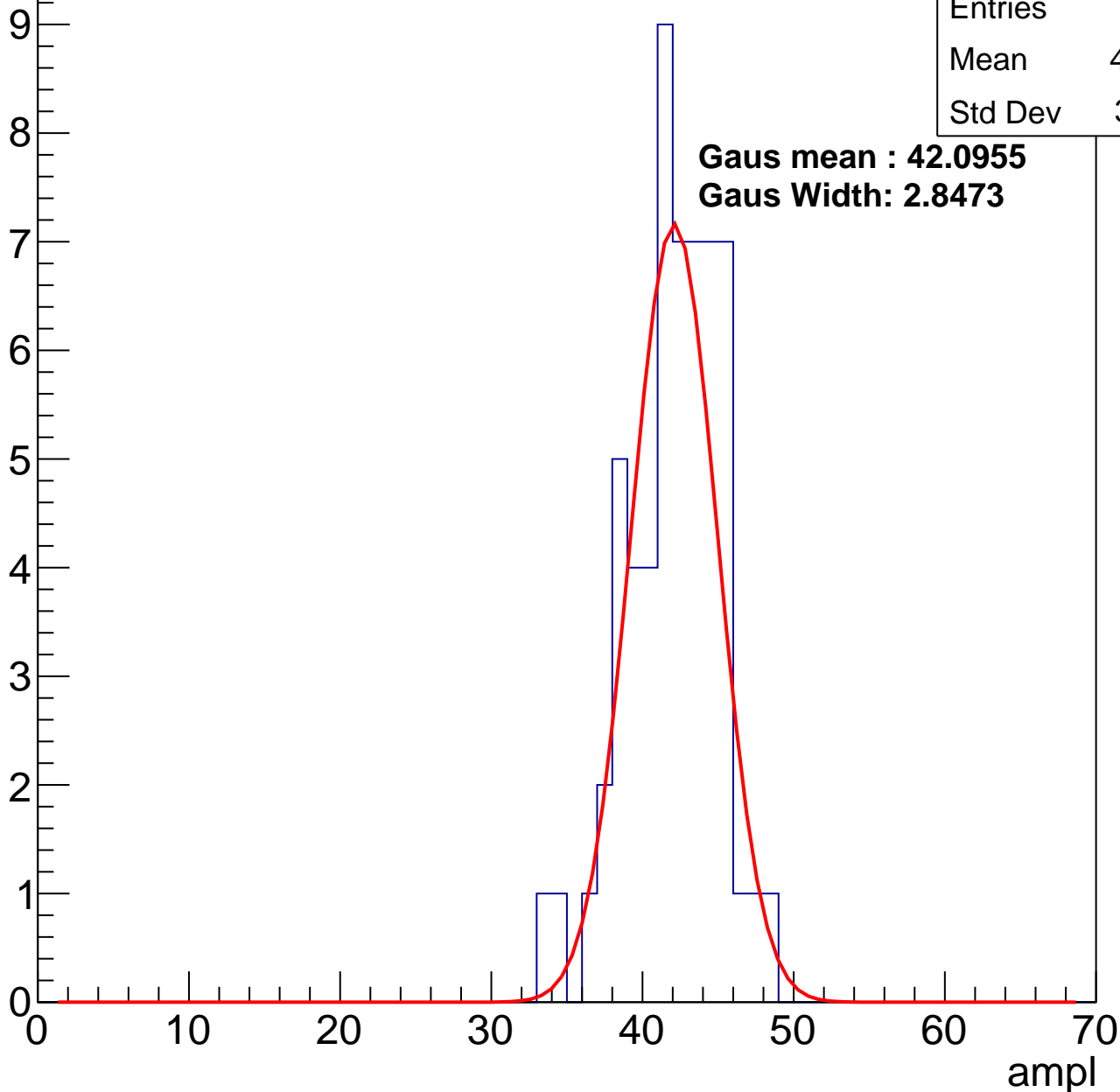
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.57
Std Dev	3.041

Gaus mean : 42.0955

Gaus Width: 2.8473

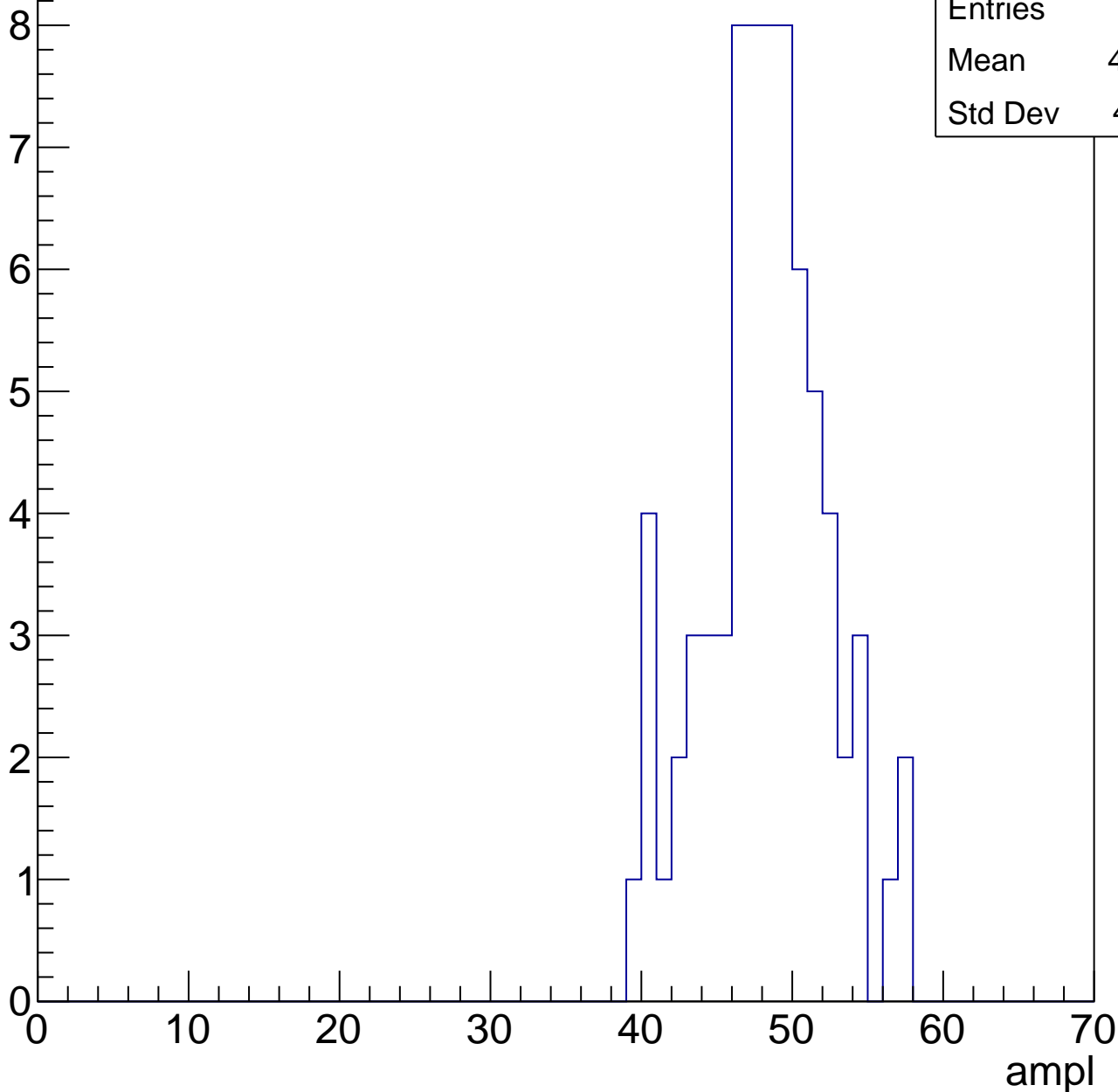


B1L101S, U4-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

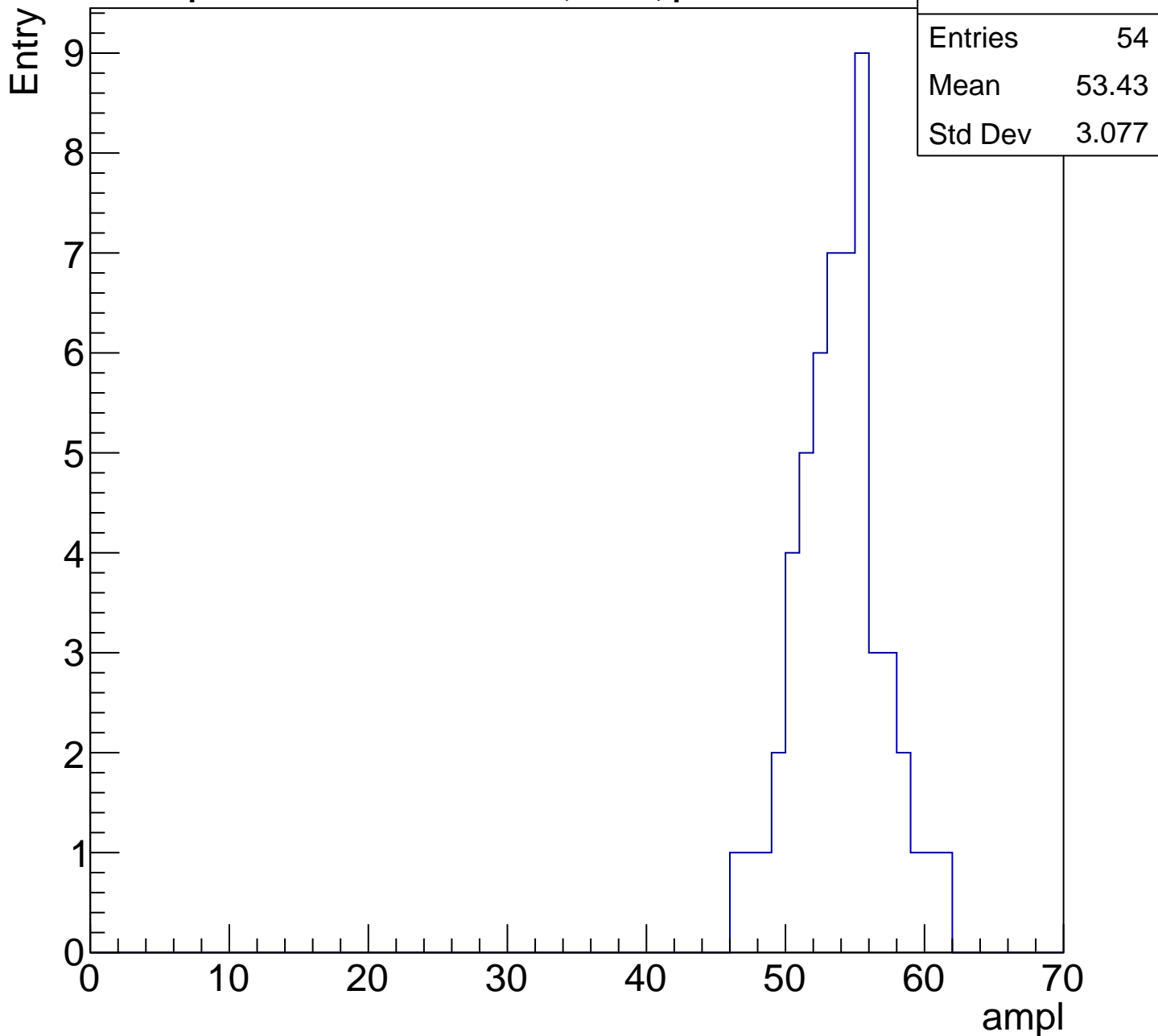
Entry

Entries	72
Mean	47.79
Std Dev	4.031



B1L101S, U4-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

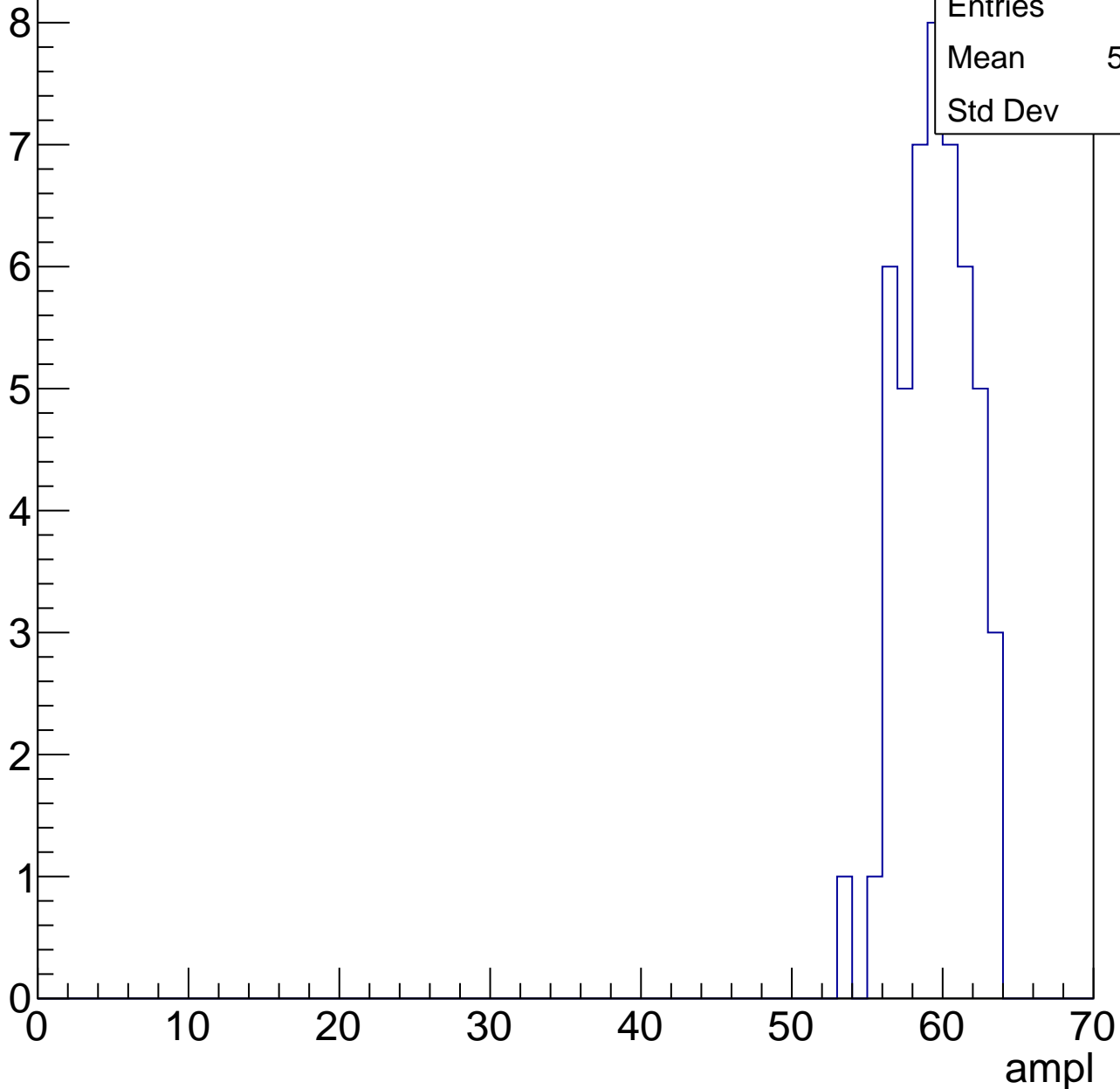


B1L101S, U4-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.02
Std Dev	2.29

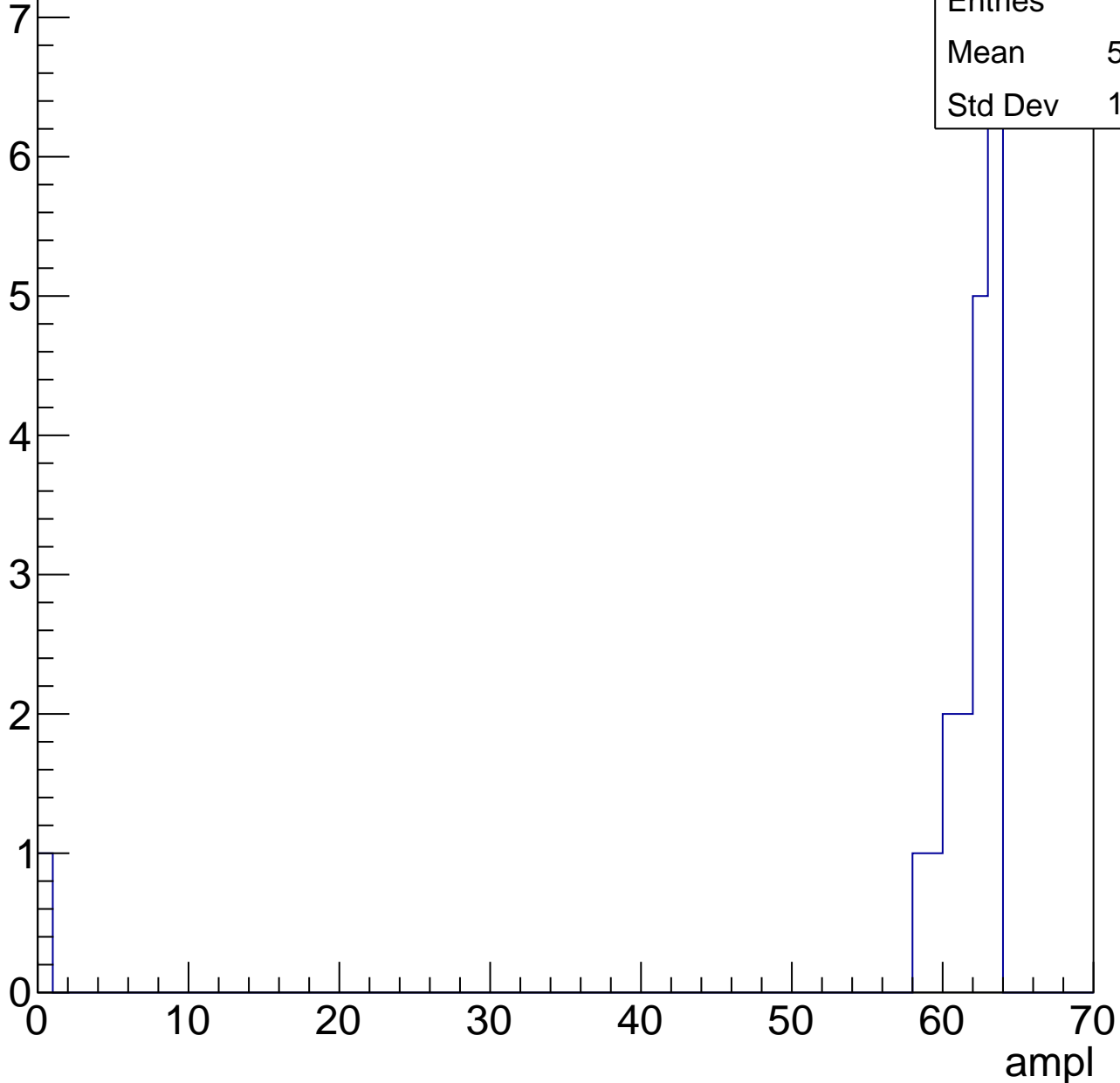


B1L101S, U4-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

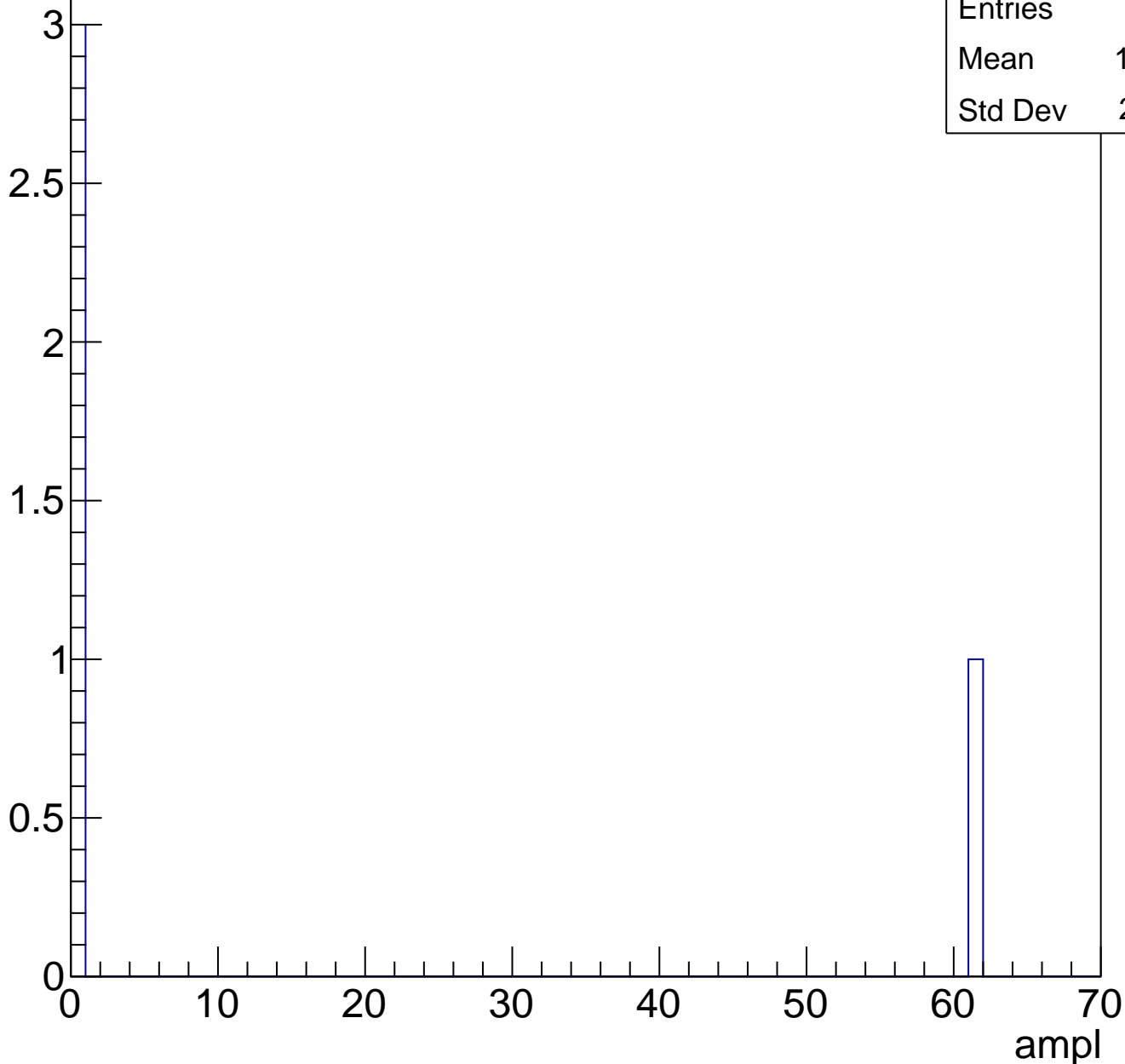
Entries	19
Mean	58.42
Std Dev	13.85



B1L101S, U4-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch17, adc0

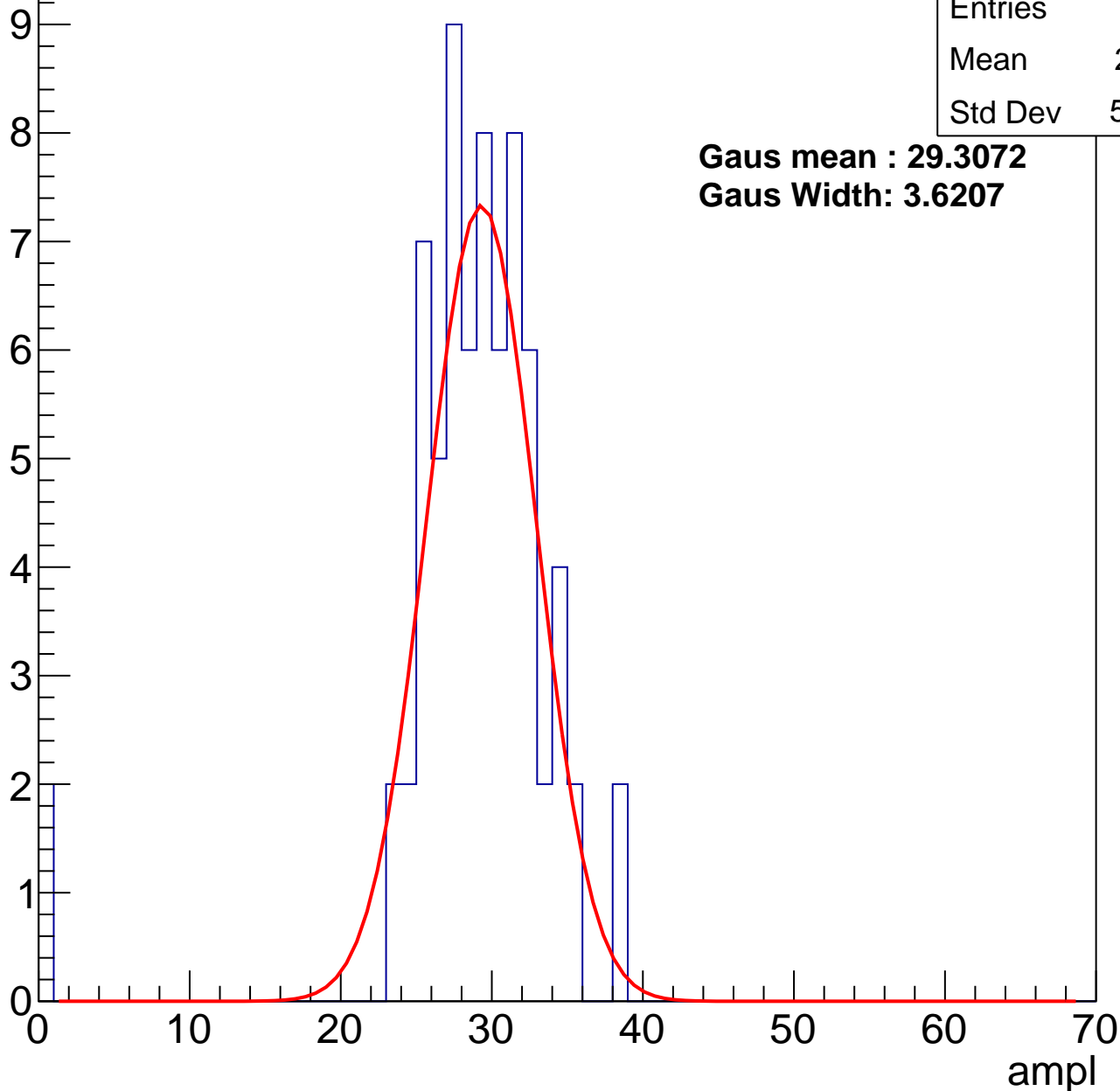
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.31
Std Dev	5.847

Gaus mean : 29.3072

Gaus Width: 3.6207



B1L101S, U4-ch17, adc1

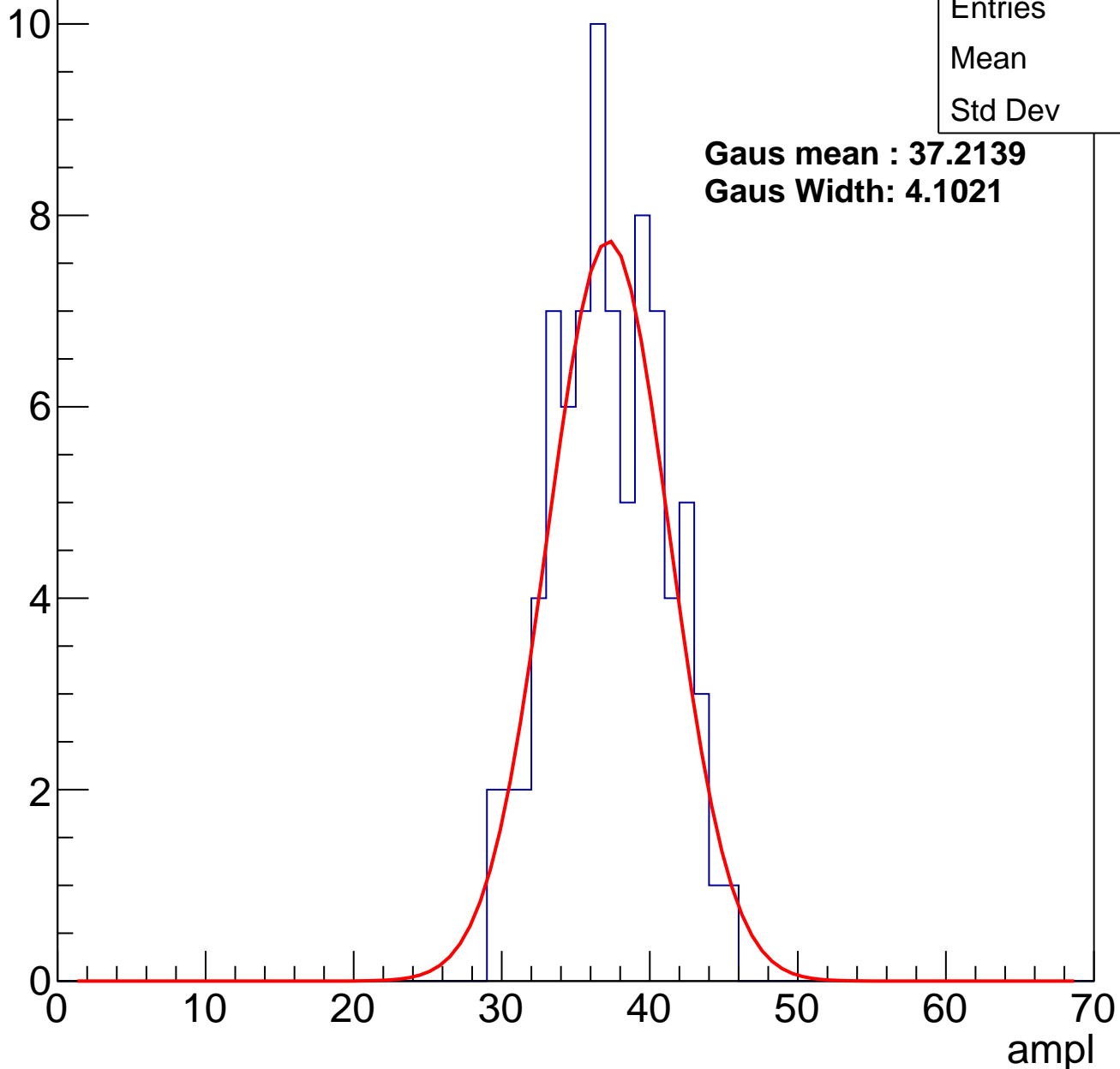
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	36.8
Std Dev	3.7

Gaus mean : 37.2139

Gaus Width: 4.1021

Entry



B1L101S, U4-ch17, adc2

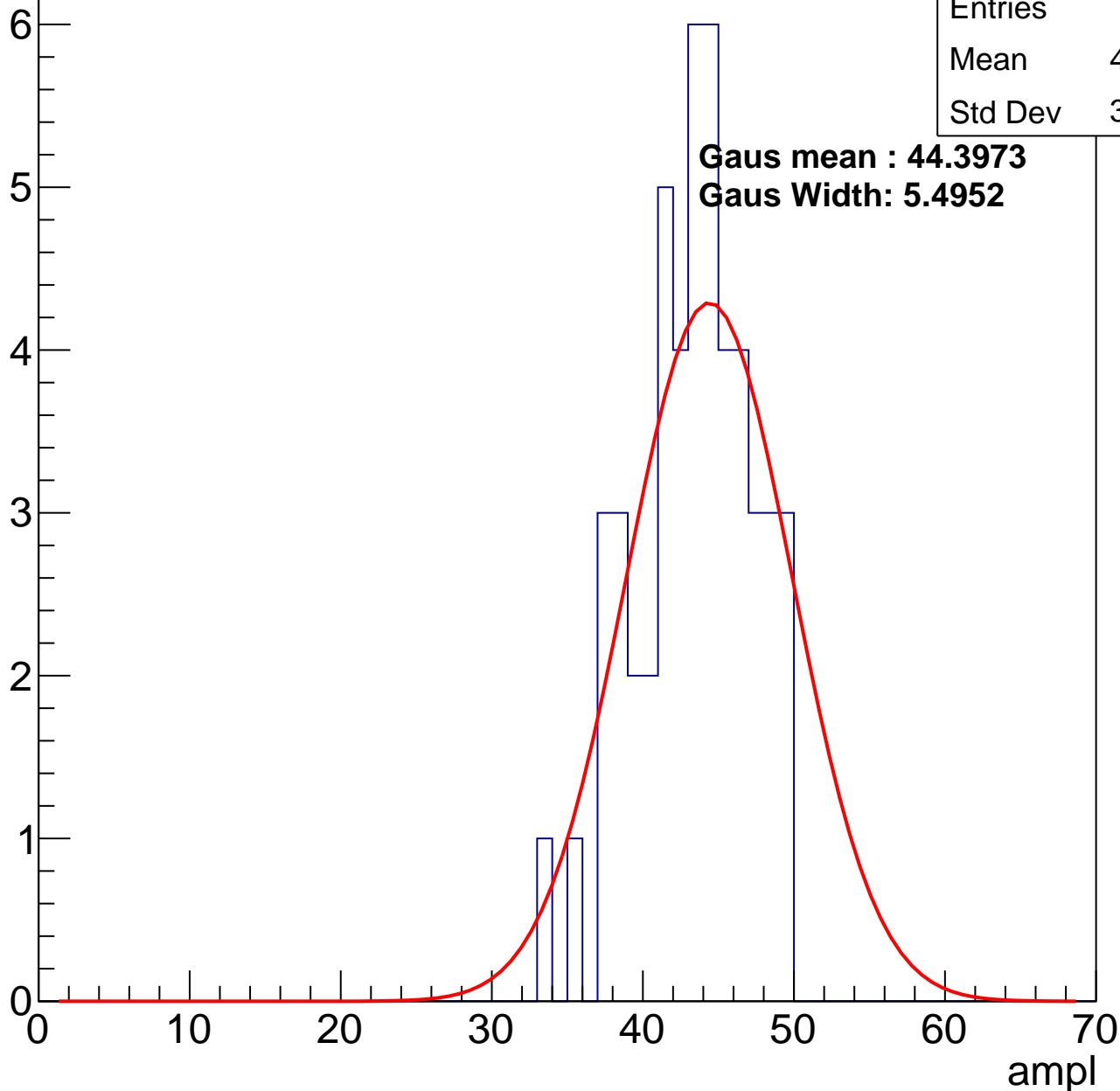
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.84
Std Dev	3.765

Gaus mean : 44.3973

Gaus Width: 5.4952

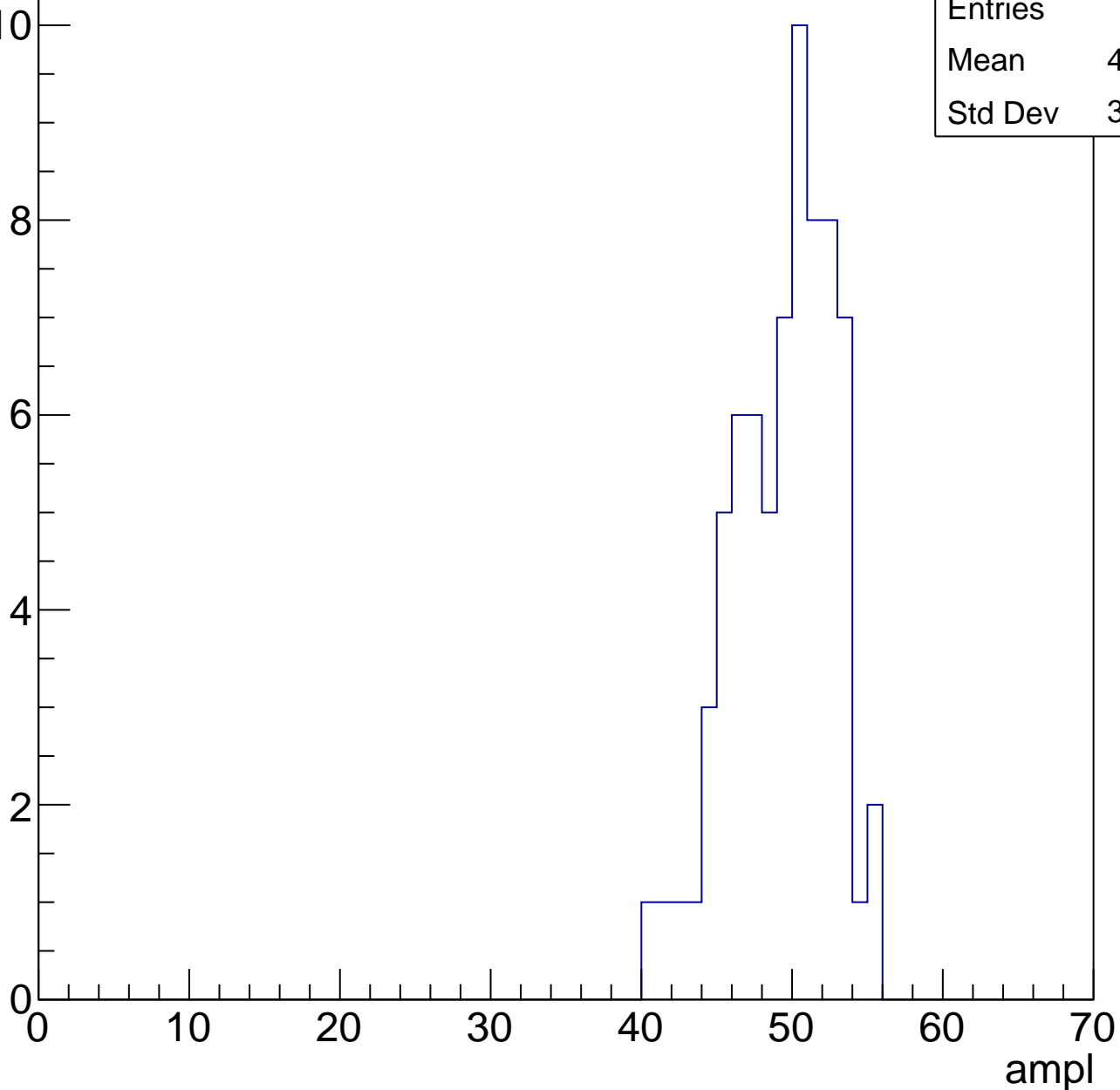


B1L101S, U4-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

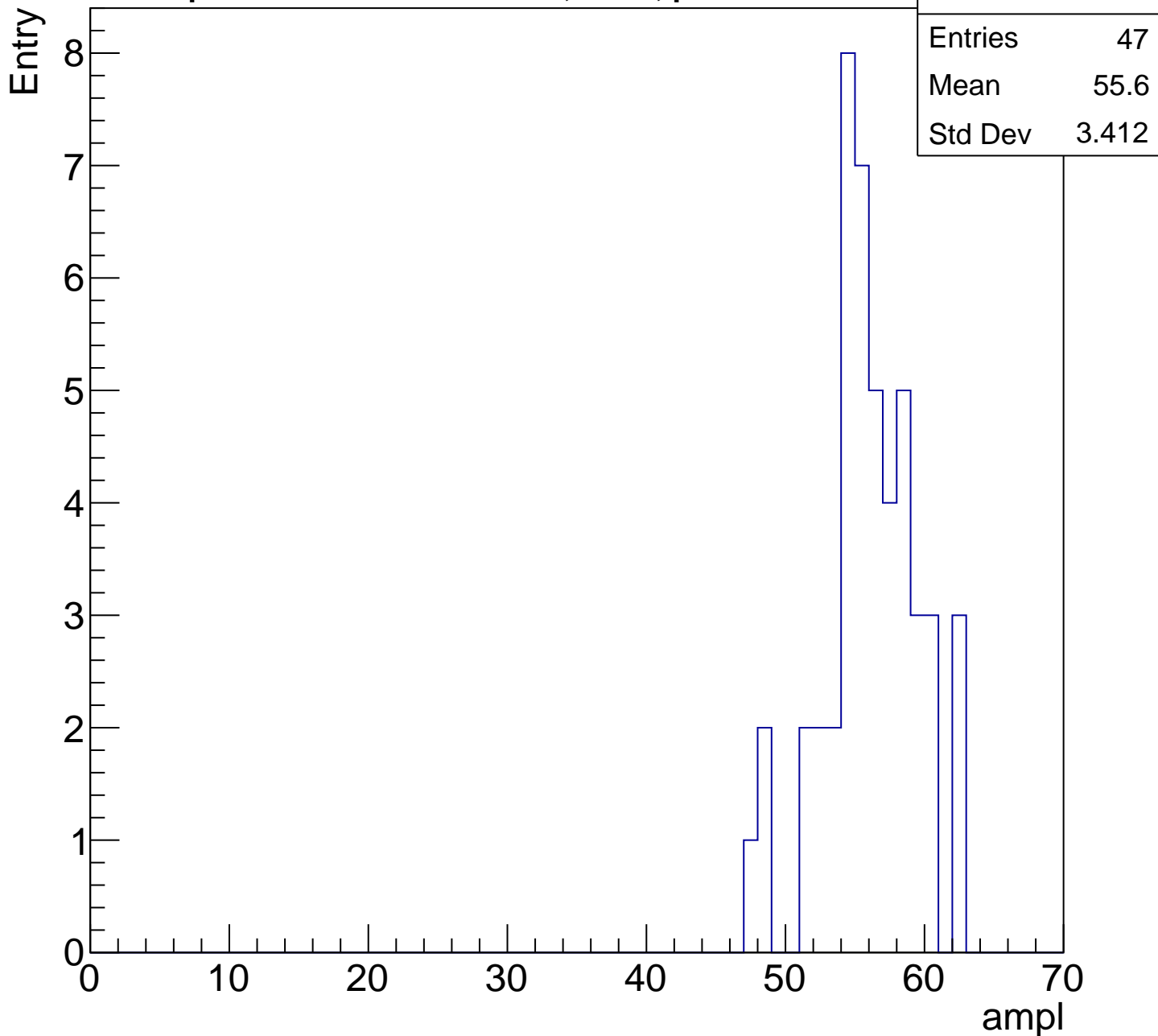
Entry

Entries	72
Mean	48.93
Std Dev	3.314



B1L101S, U4-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

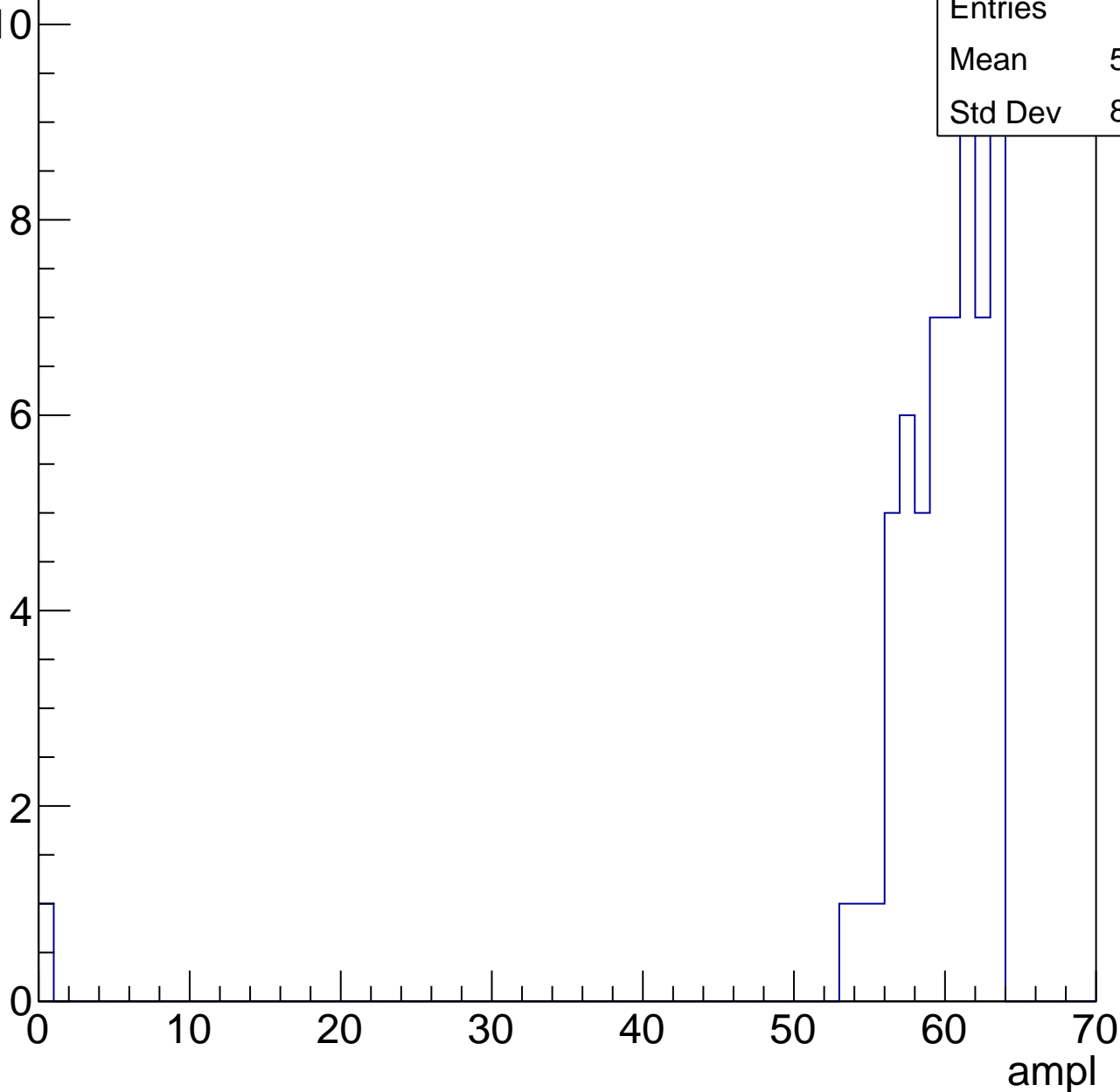


B1L101S, U4-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	58.67
Std Dev	8.053



B1L101S, U4-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch18, adc0

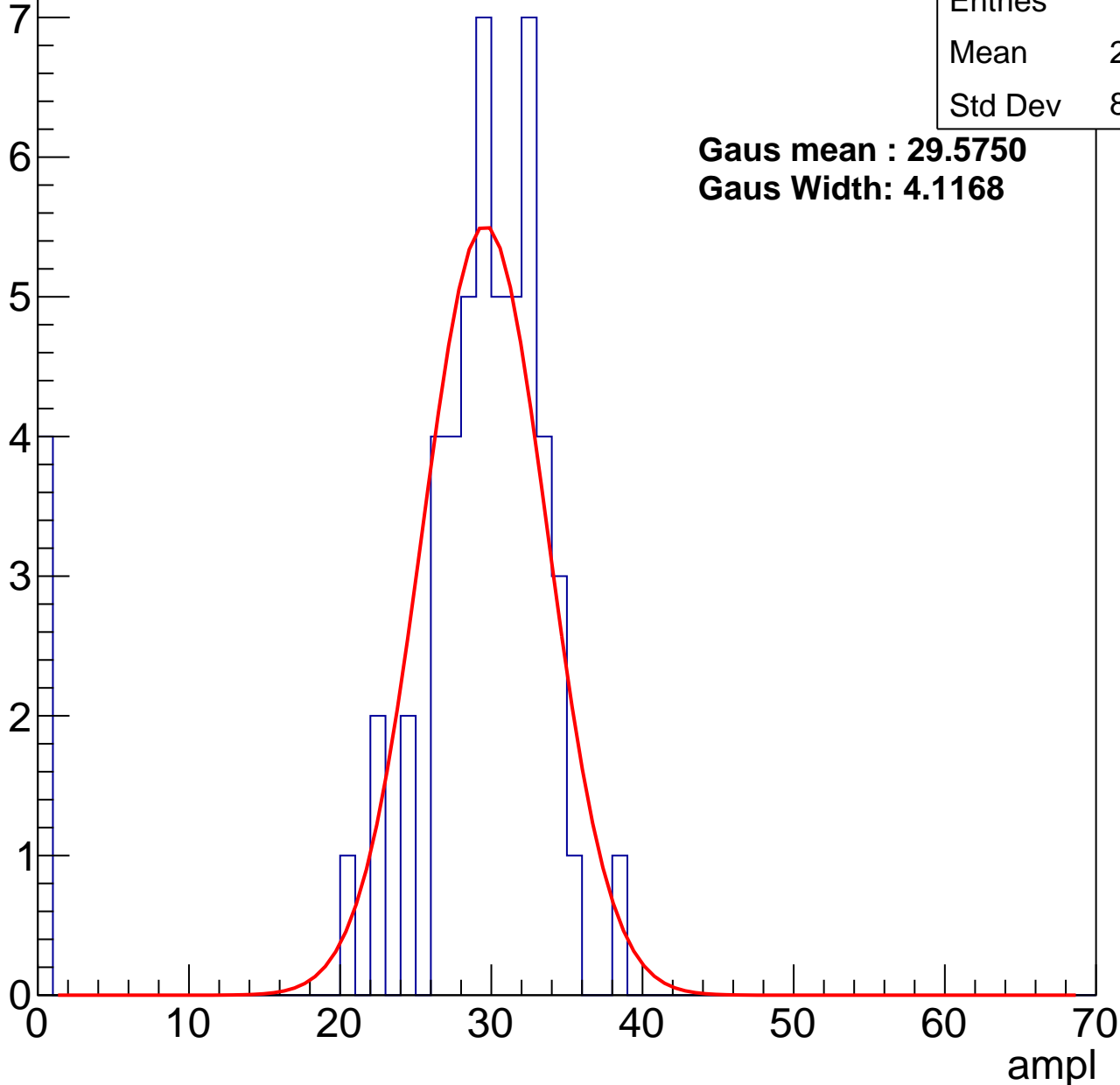
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	27.33
Std Dev	8.356

Gaus mean : 29.5750

Gaus Width: 4.1168



B1L101S, U4-ch18, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

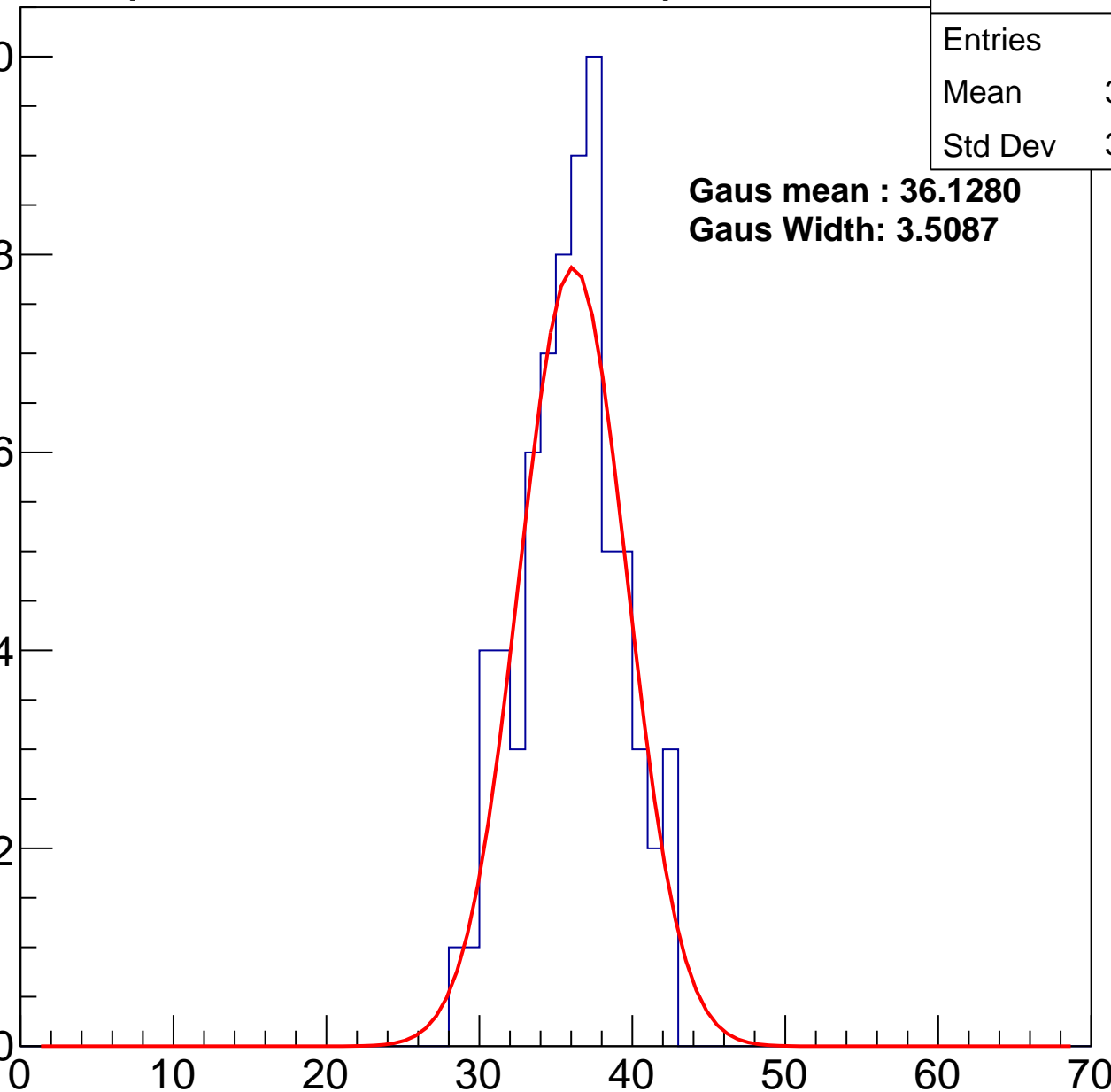
Entries	71
Mean	35.49
Std Dev	3.267

Gaus mean : 36.1280

Gaus Width: 3.5087

10
8
6
4
2
0

ampl



B1L101S, U4-ch18, adc2

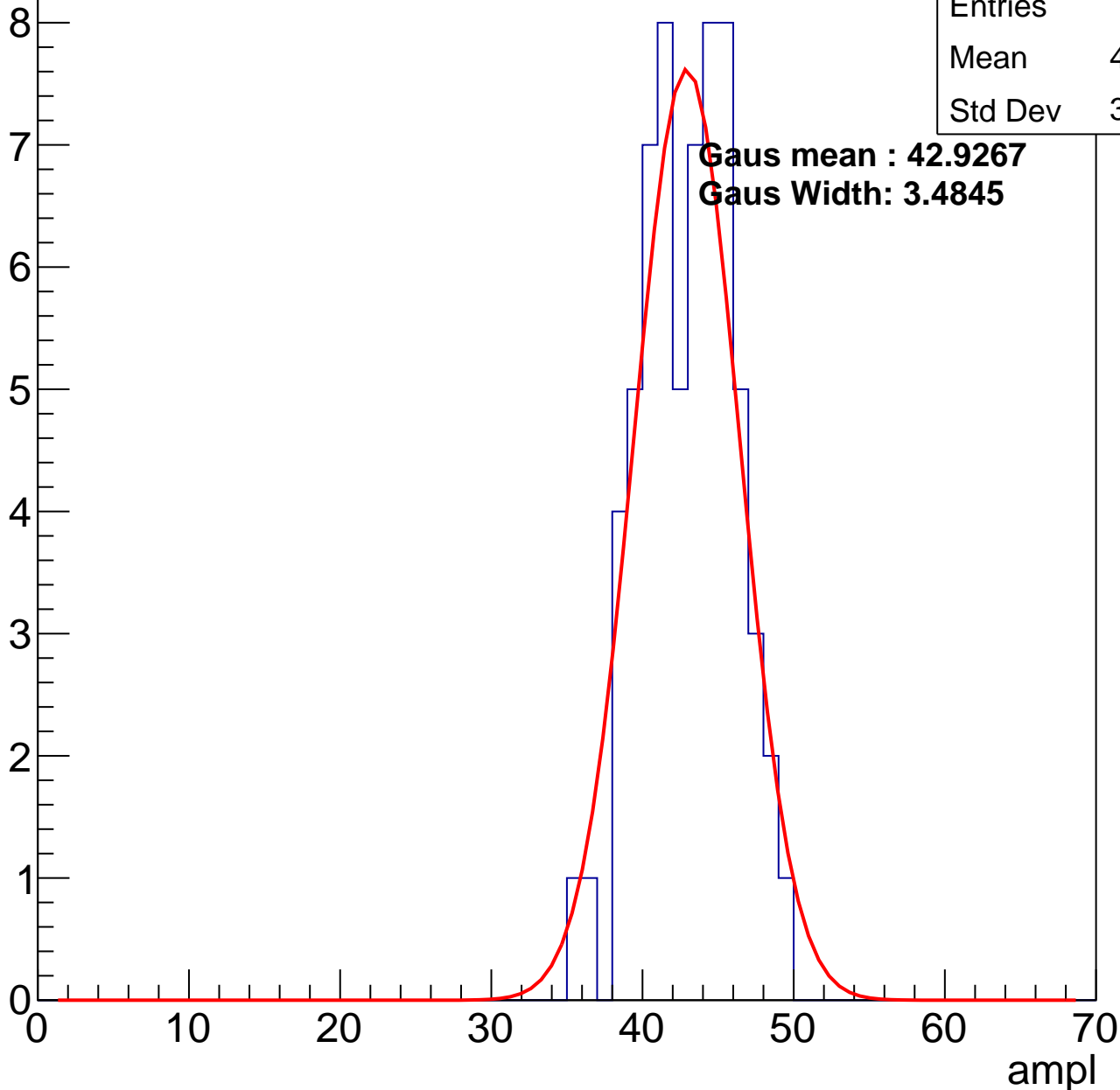
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.54
Std Dev	3.034

Gaus mean : 42.9267

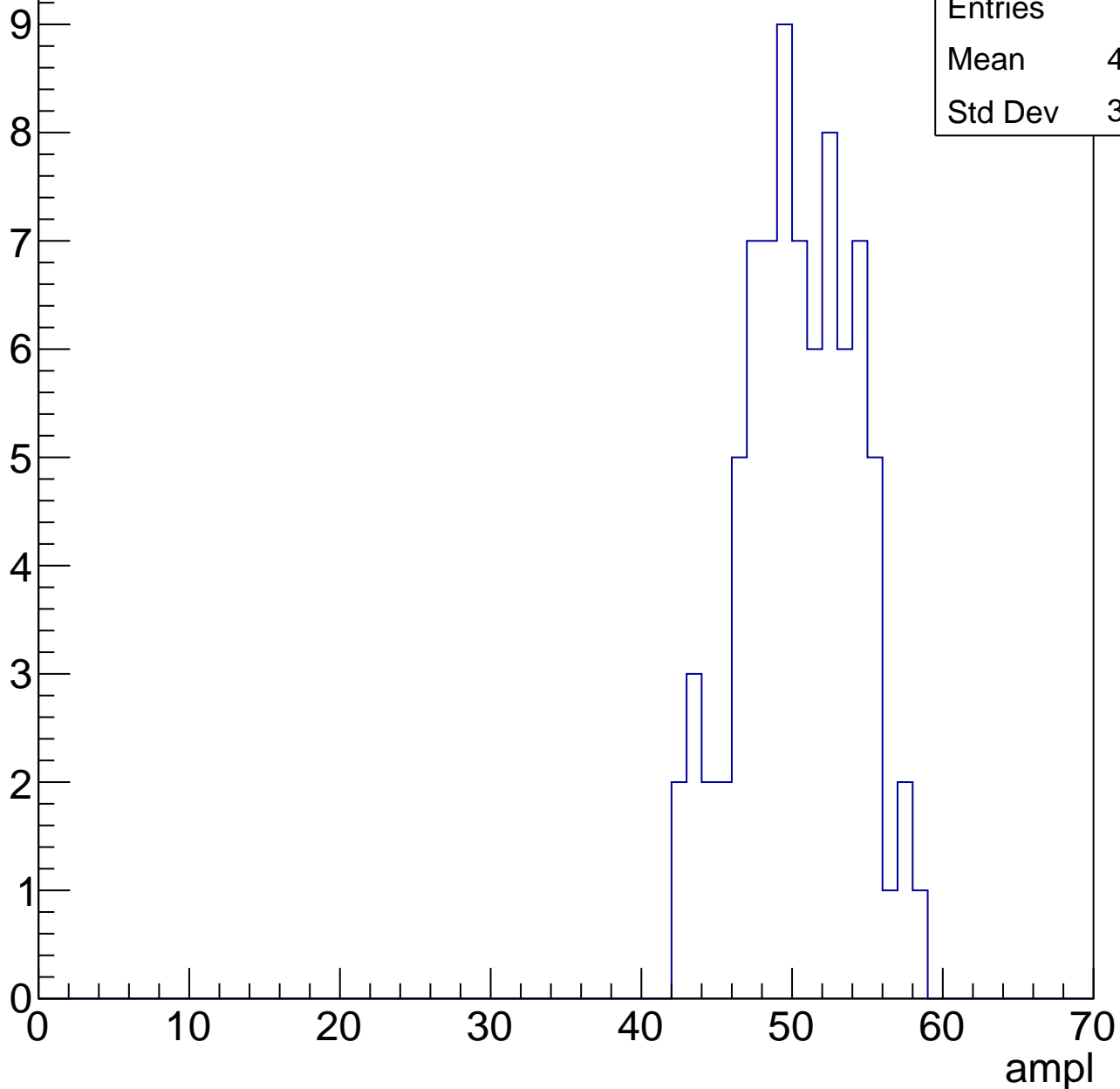
Gaus Width: 3.4845



B1L101S, U4-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

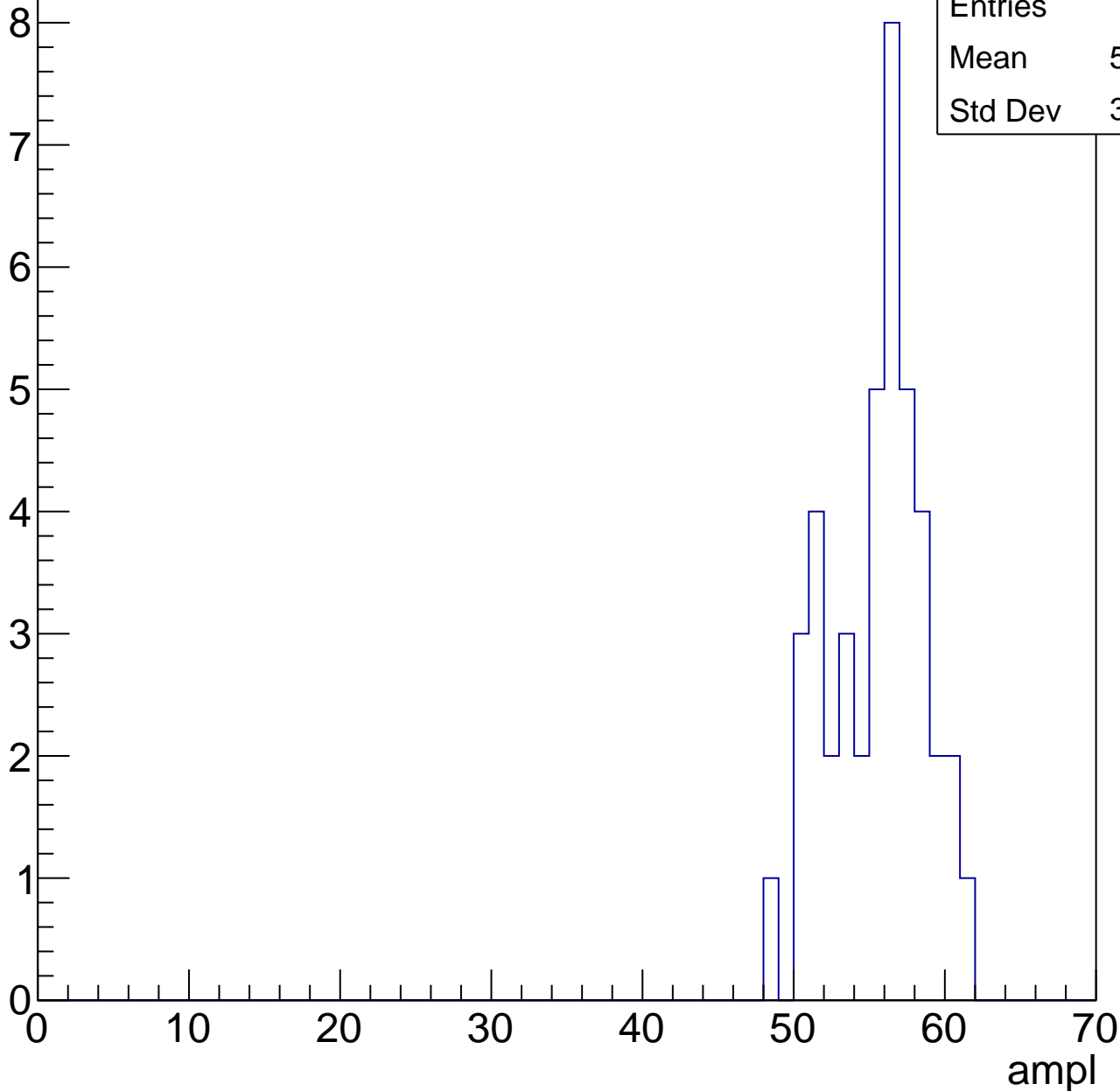


B1L101S, U4-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	55.05
Std Dev	3.093

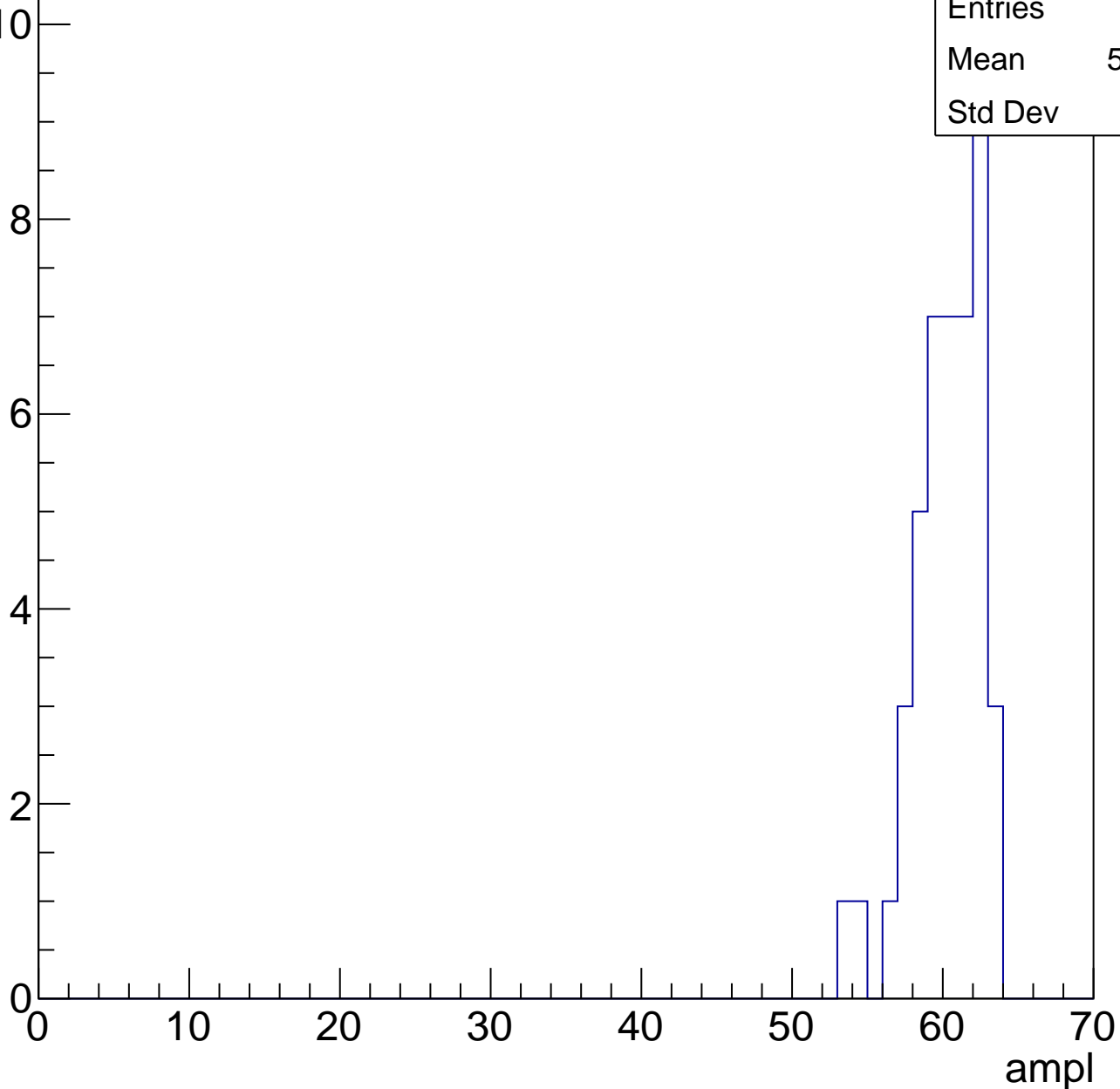


B1L101S, U4-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.84
Std Dev	2.25

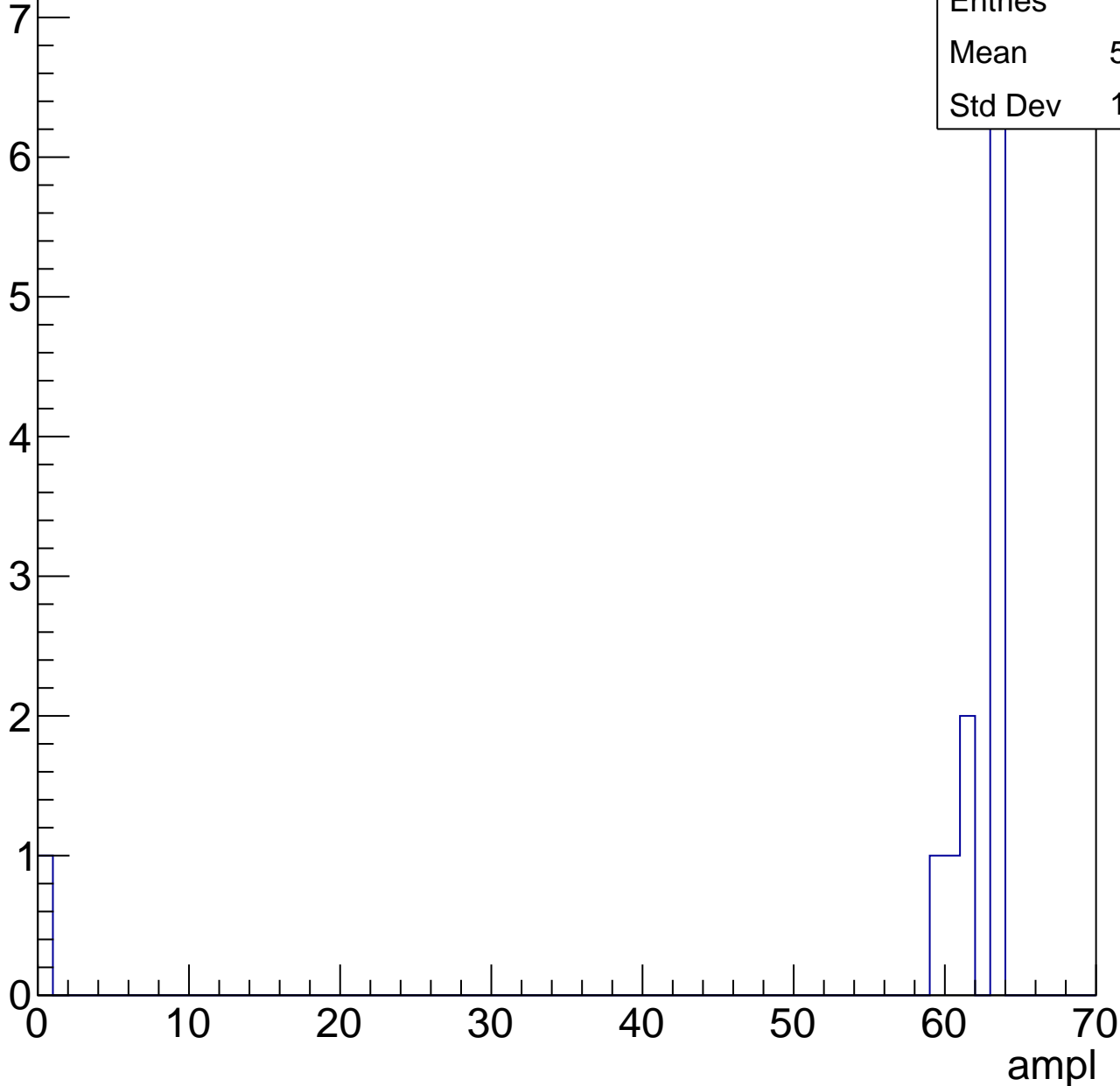


B1L101S, U4-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	12
Mean	56.83
Std Dev	17.19



B1L101S, U4-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch19, adc0

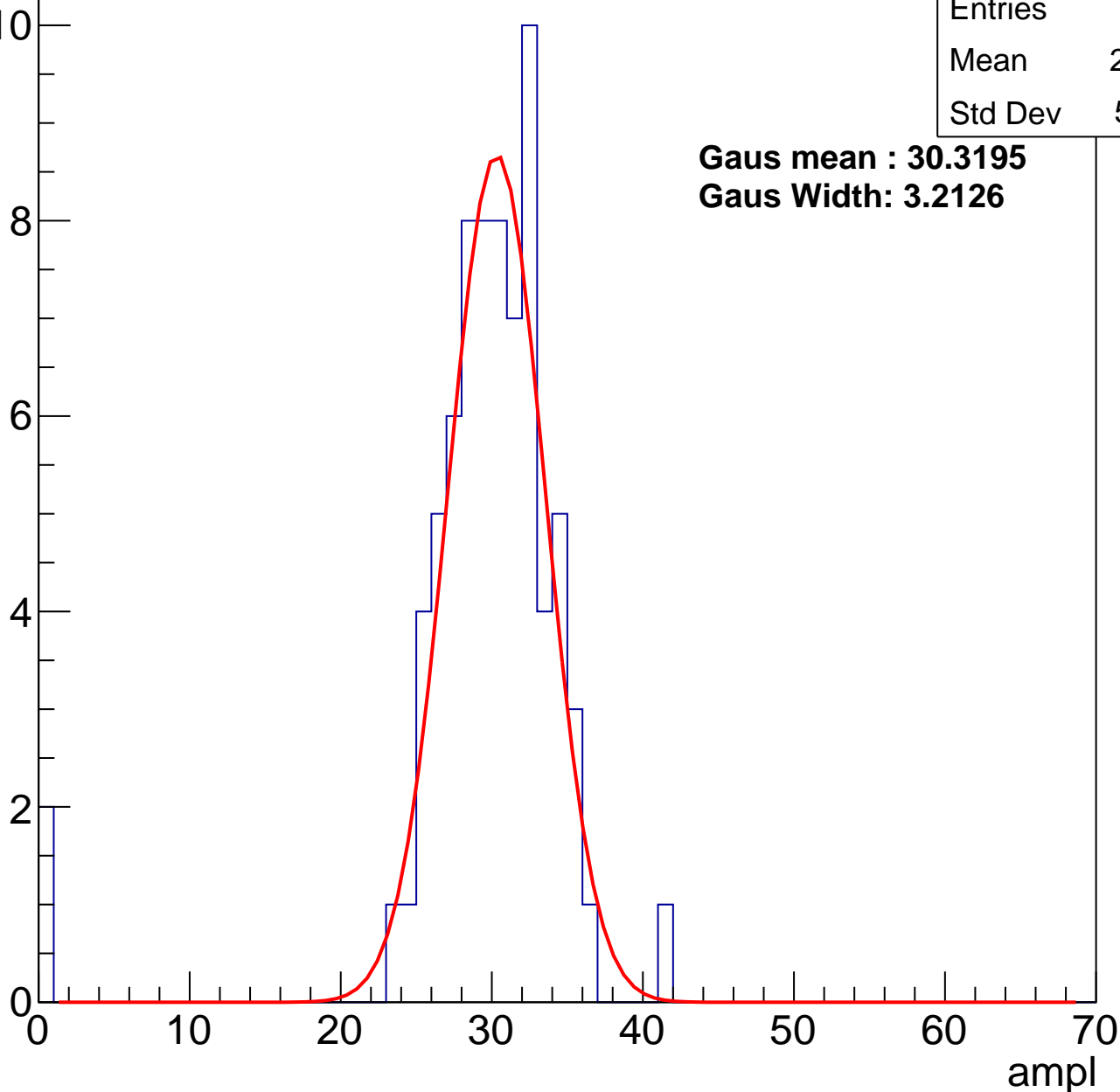
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.14
Std Dev	5.811

Gaus mean : 30.3195

Gaus Width: 3.2126



B1L101S, U4-ch19, adc1

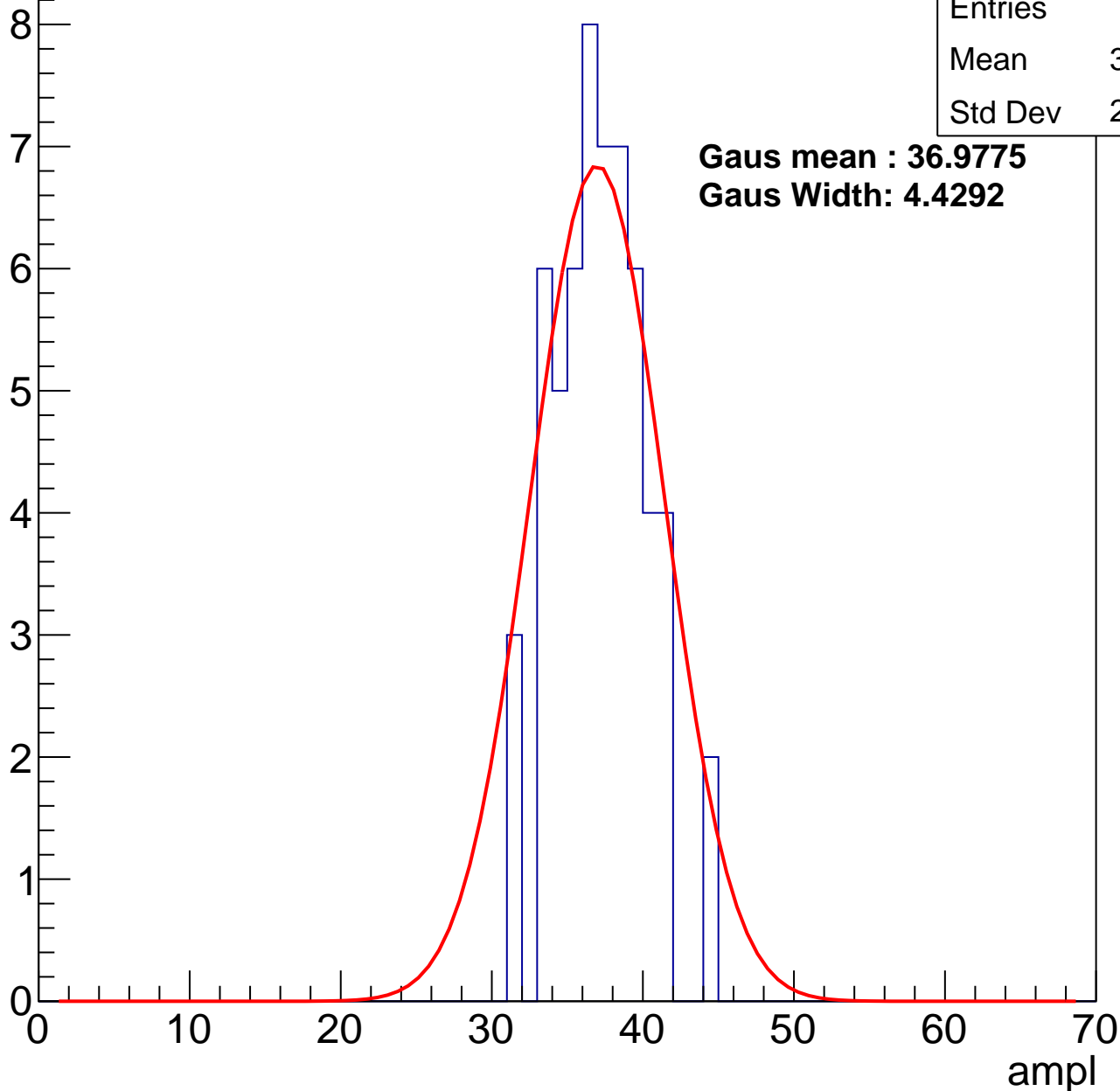
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.72
Std Dev	2.952

Gaus mean : 36.9775

Gaus Width: 4.4292



B1L101S, U4-ch19, adc2

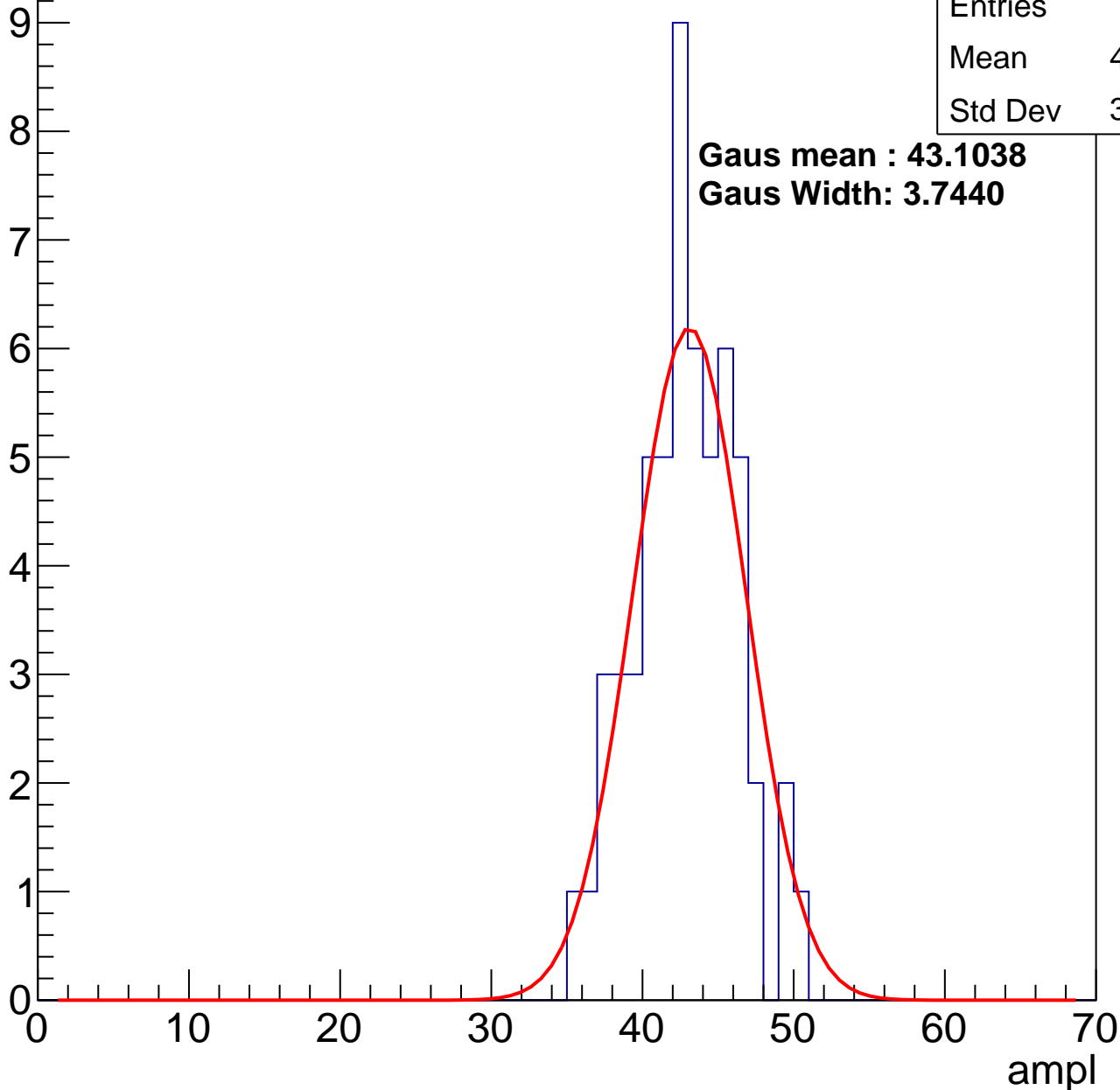
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.39
Std Dev	3.307

Gaus mean : 43.1038

Gaus Width: 3.7440

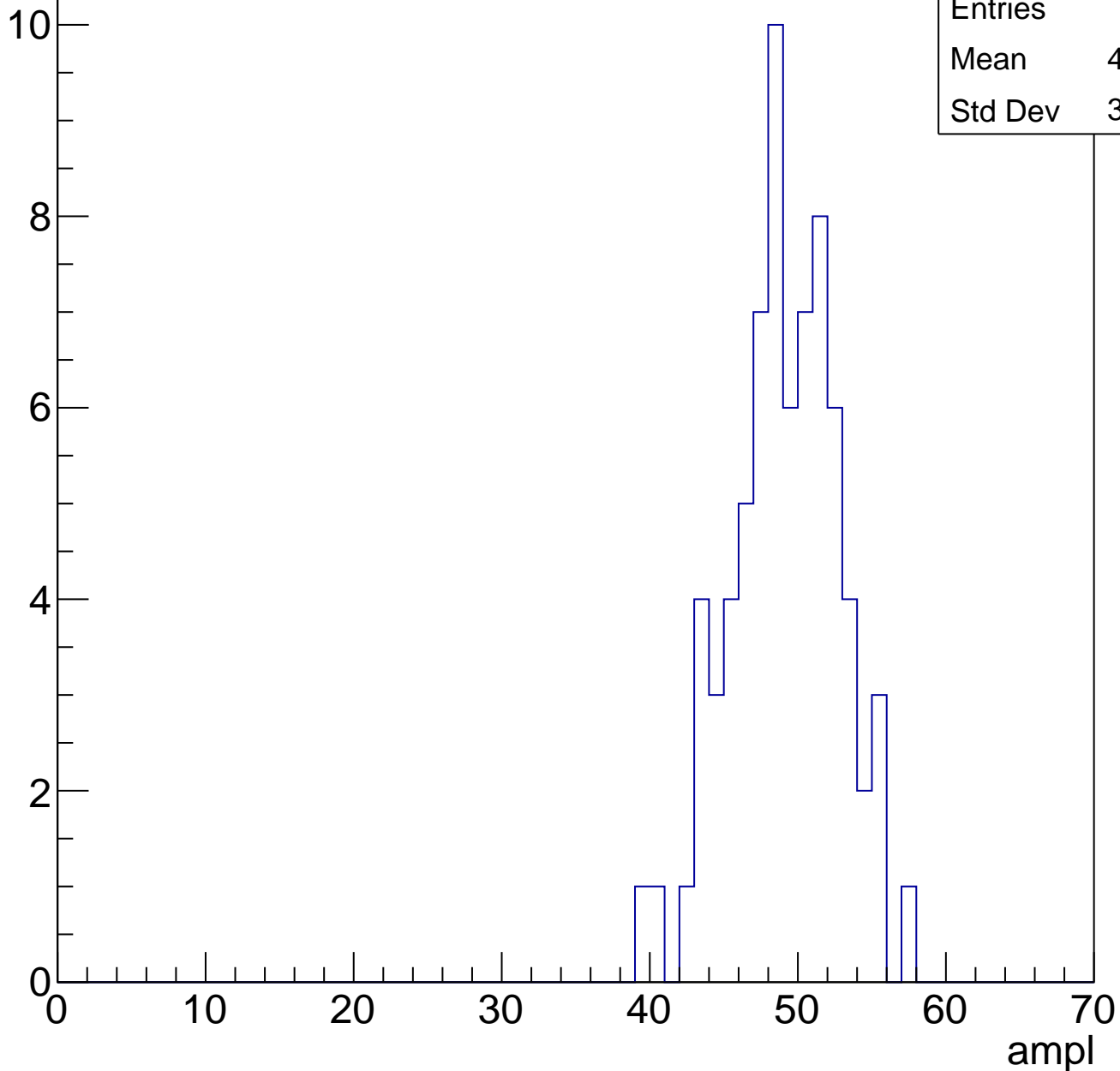


B1L101S, U4-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	48.63
Std Dev	3.636

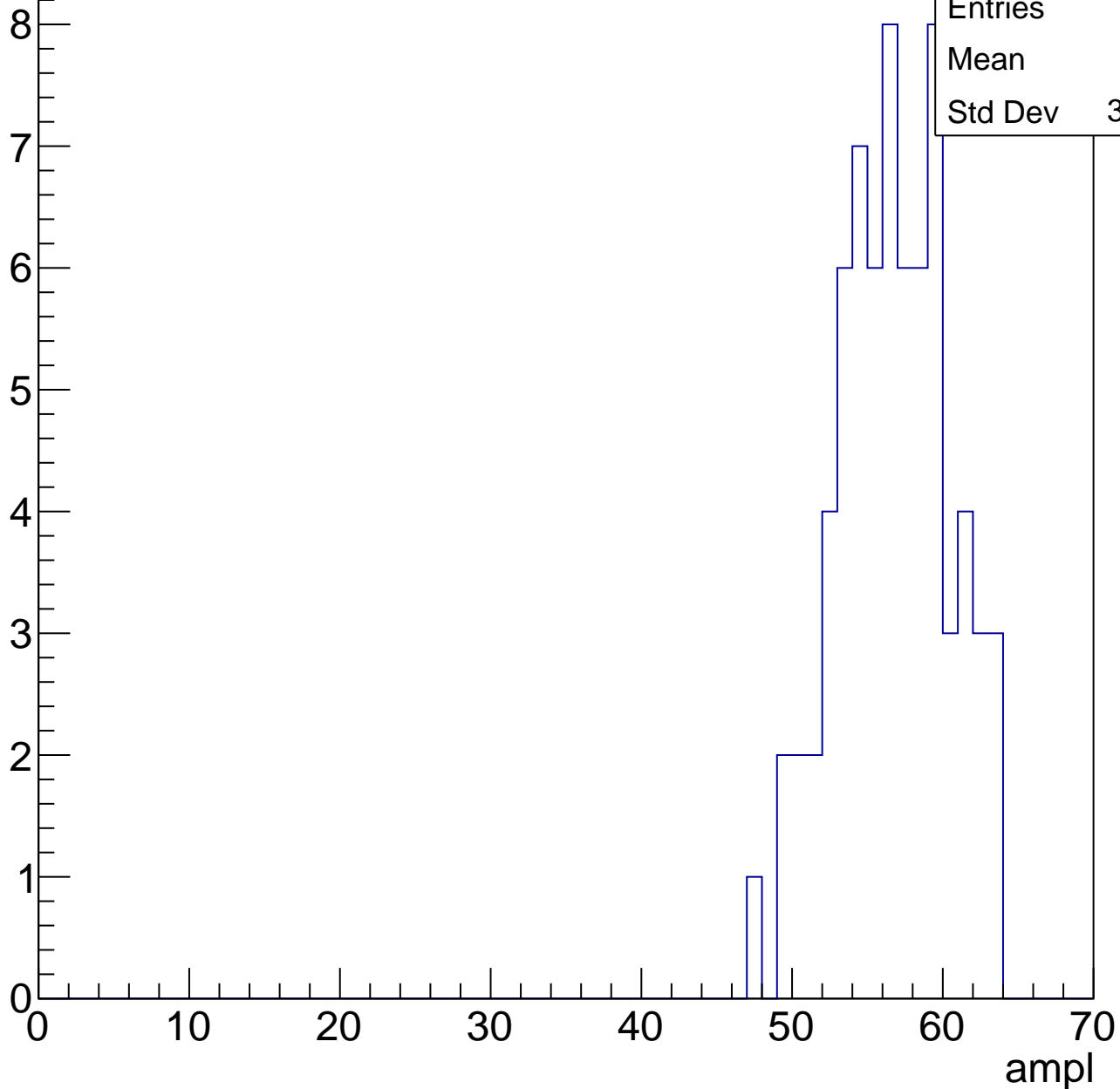
Entry



B1L101S, U4-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

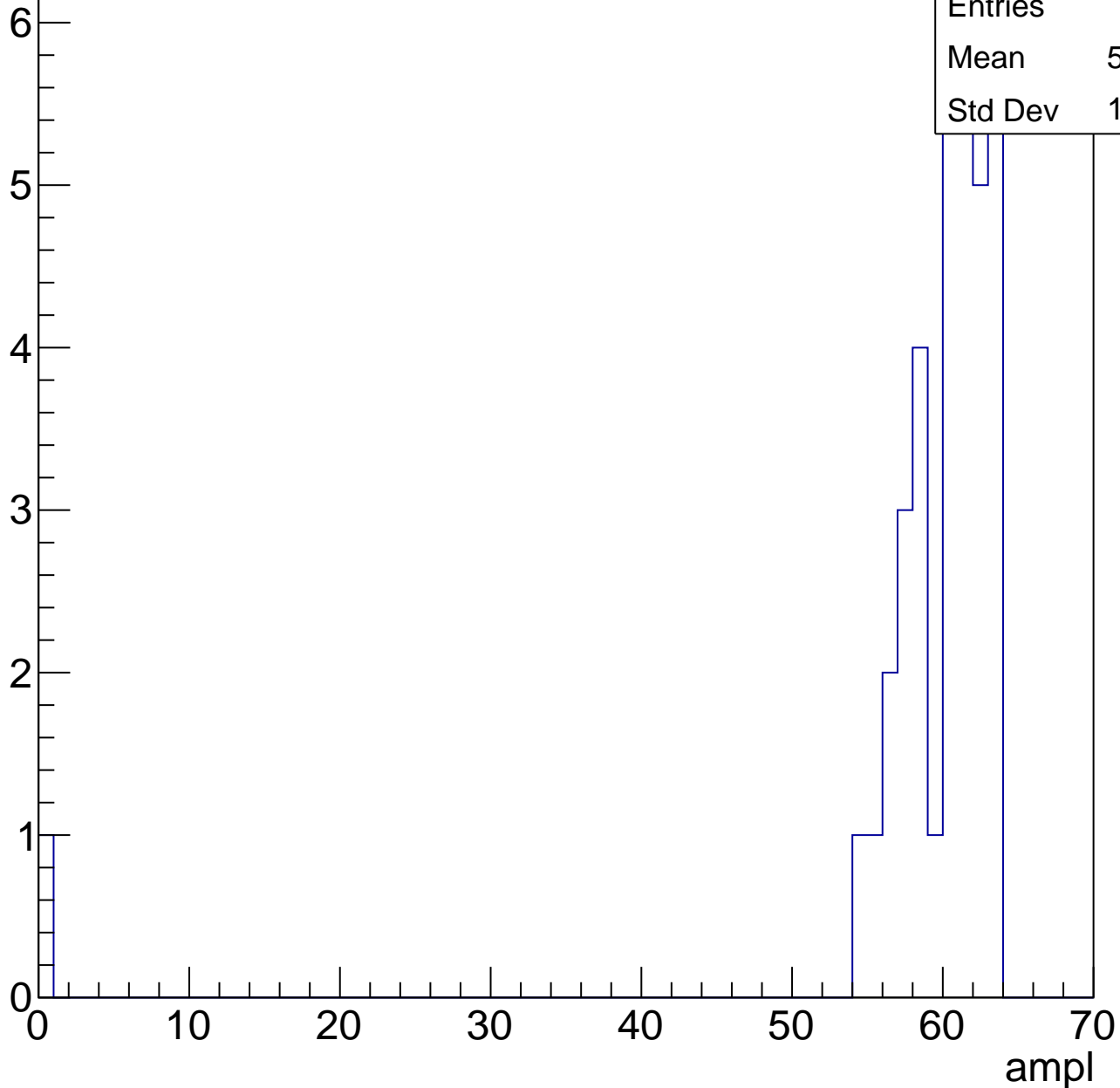
Entry



B1L101S, U4-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.47

ampl

0 10 20 30 40 50 60 70

B1L101S, U4-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch20, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	98
Mean	26.08
Std Dev	4.888

Gaus mean : 27.2976

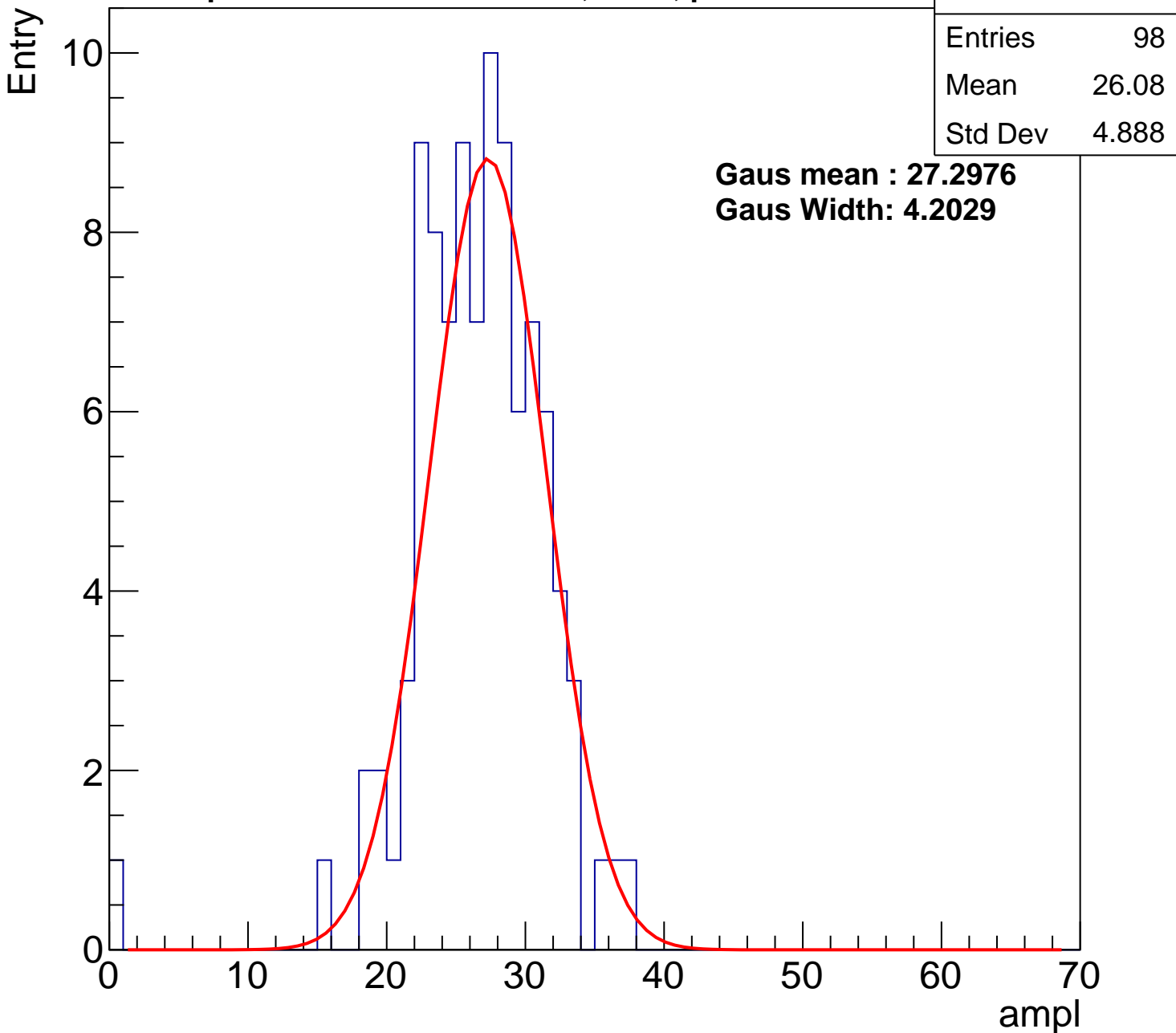
Gaus Width: 4.2029

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch20, adc1

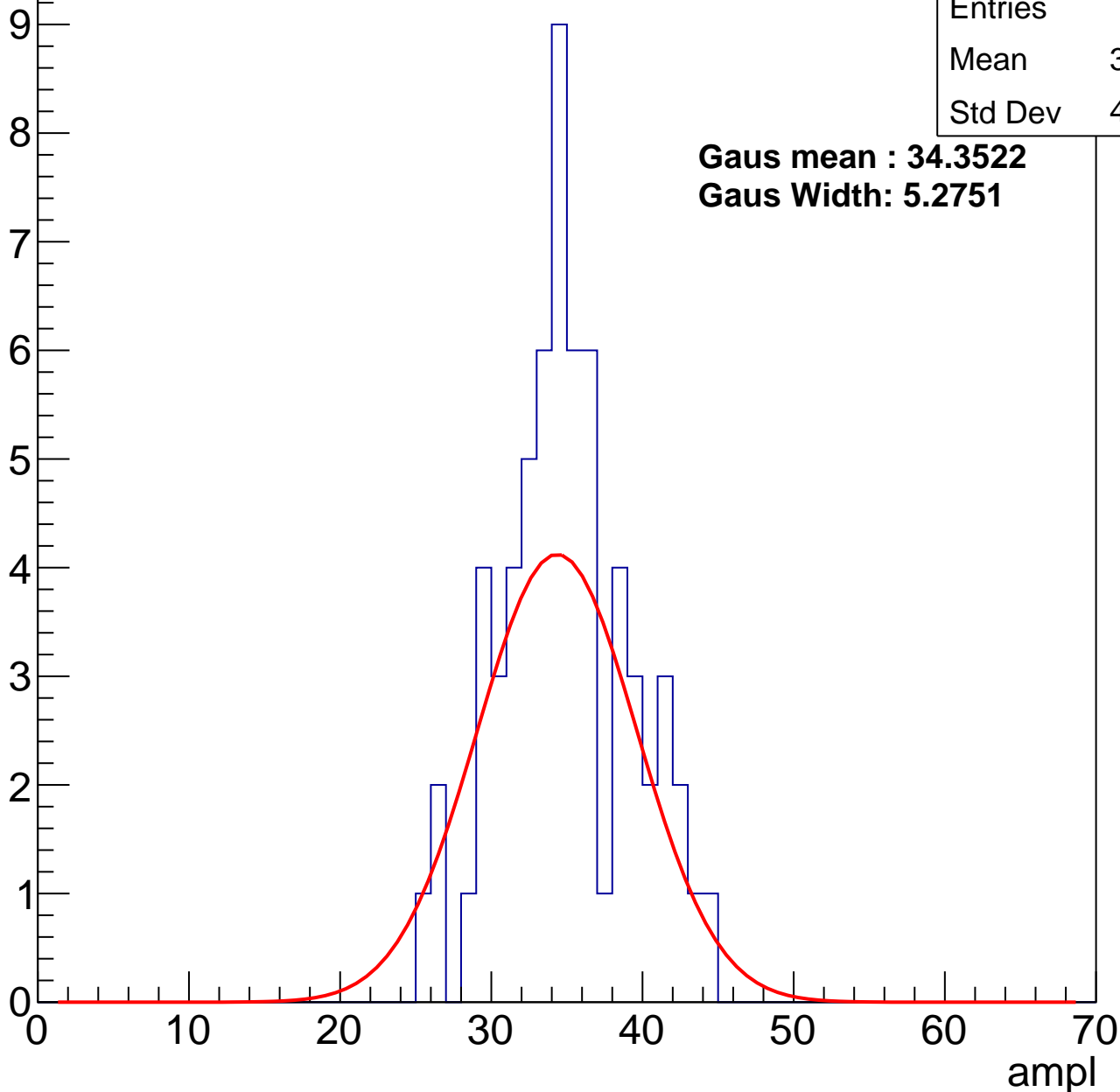
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	34.45
Std Dev	4.235

Gaus mean : 34.3522

Gaus Width: 5.2751

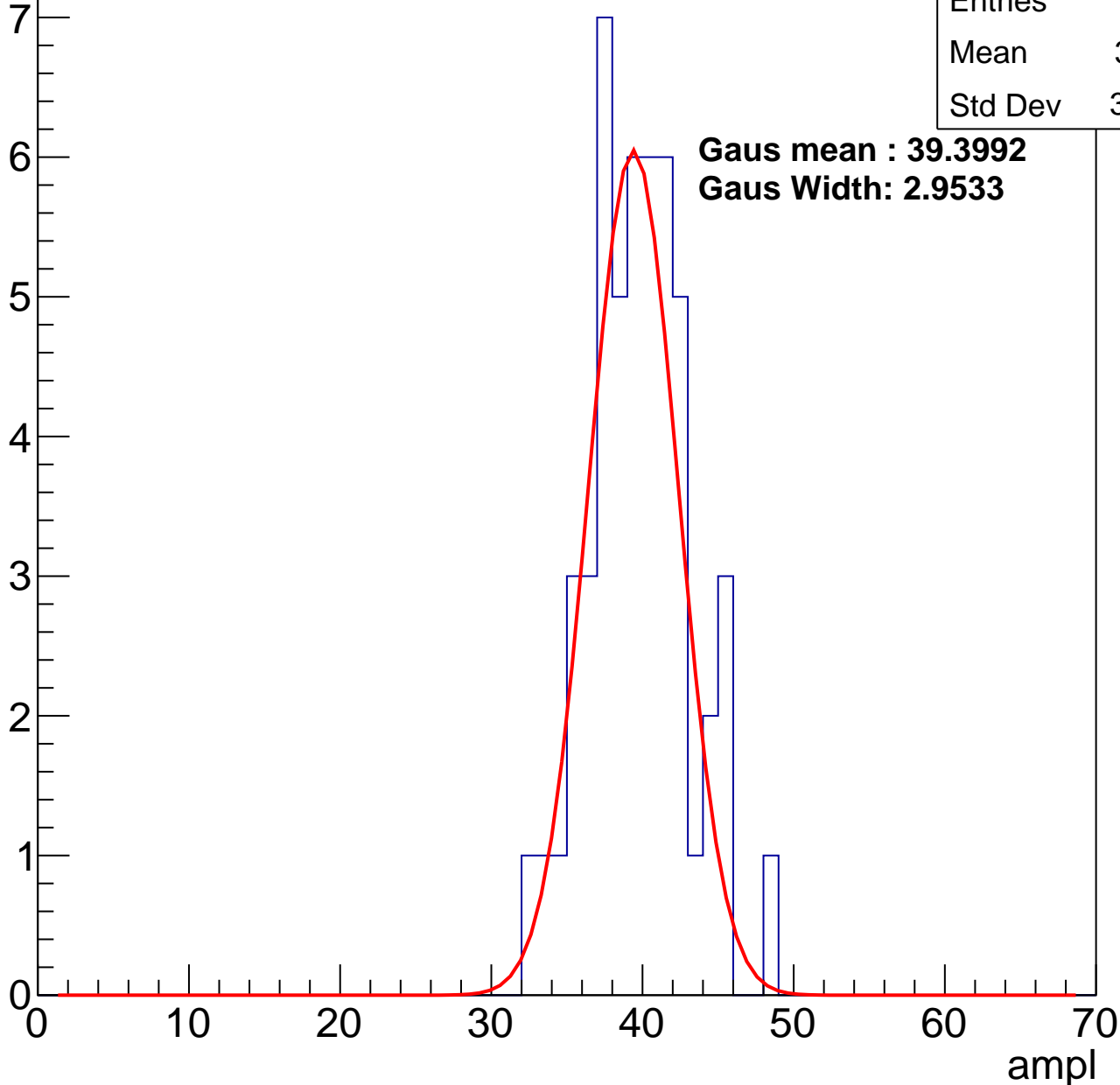


B1L101S, U4-ch20, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	39.31
Std Dev	3.263

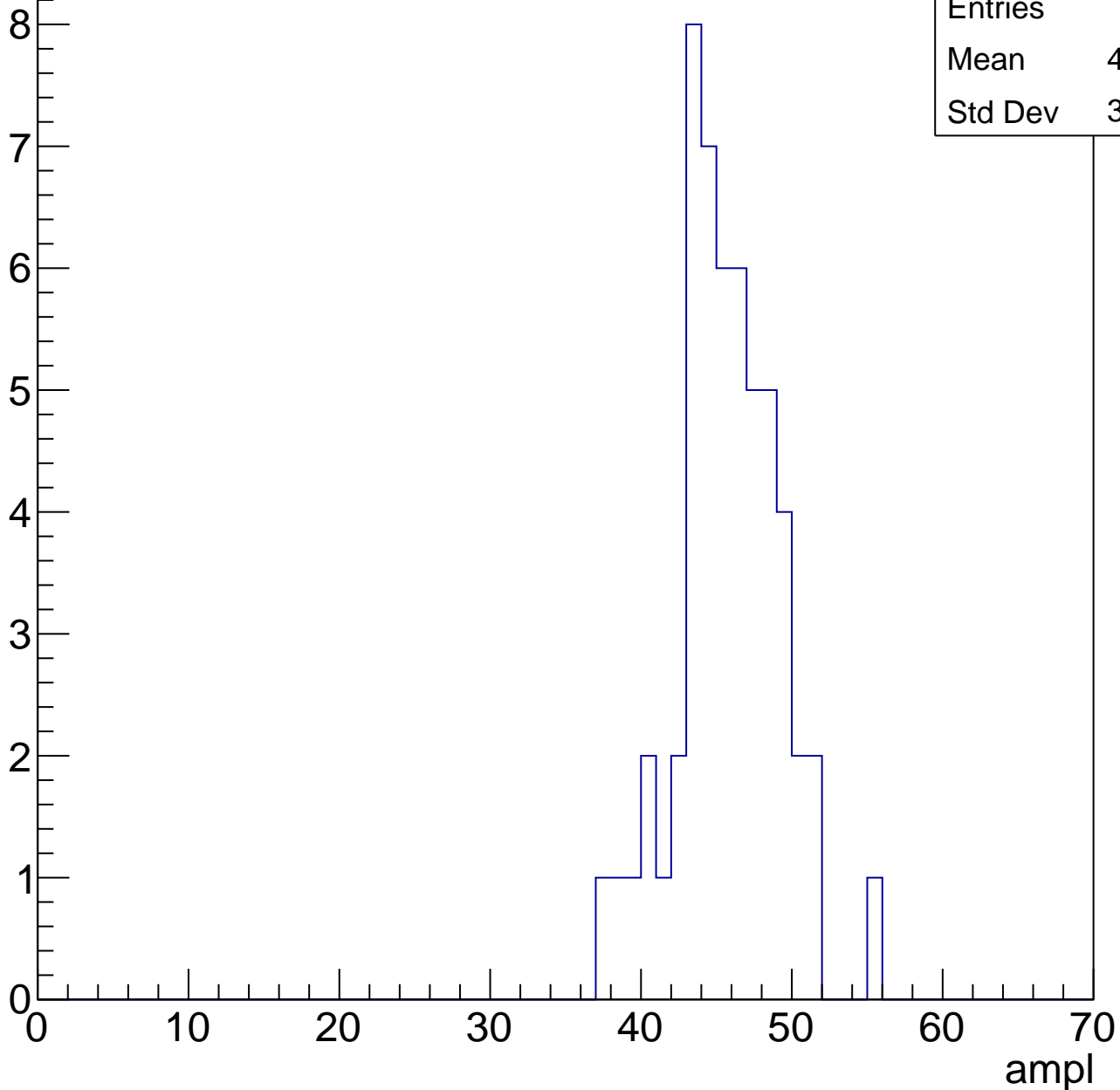


B1L101S, U4-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	45.28
Std Dev	3.402

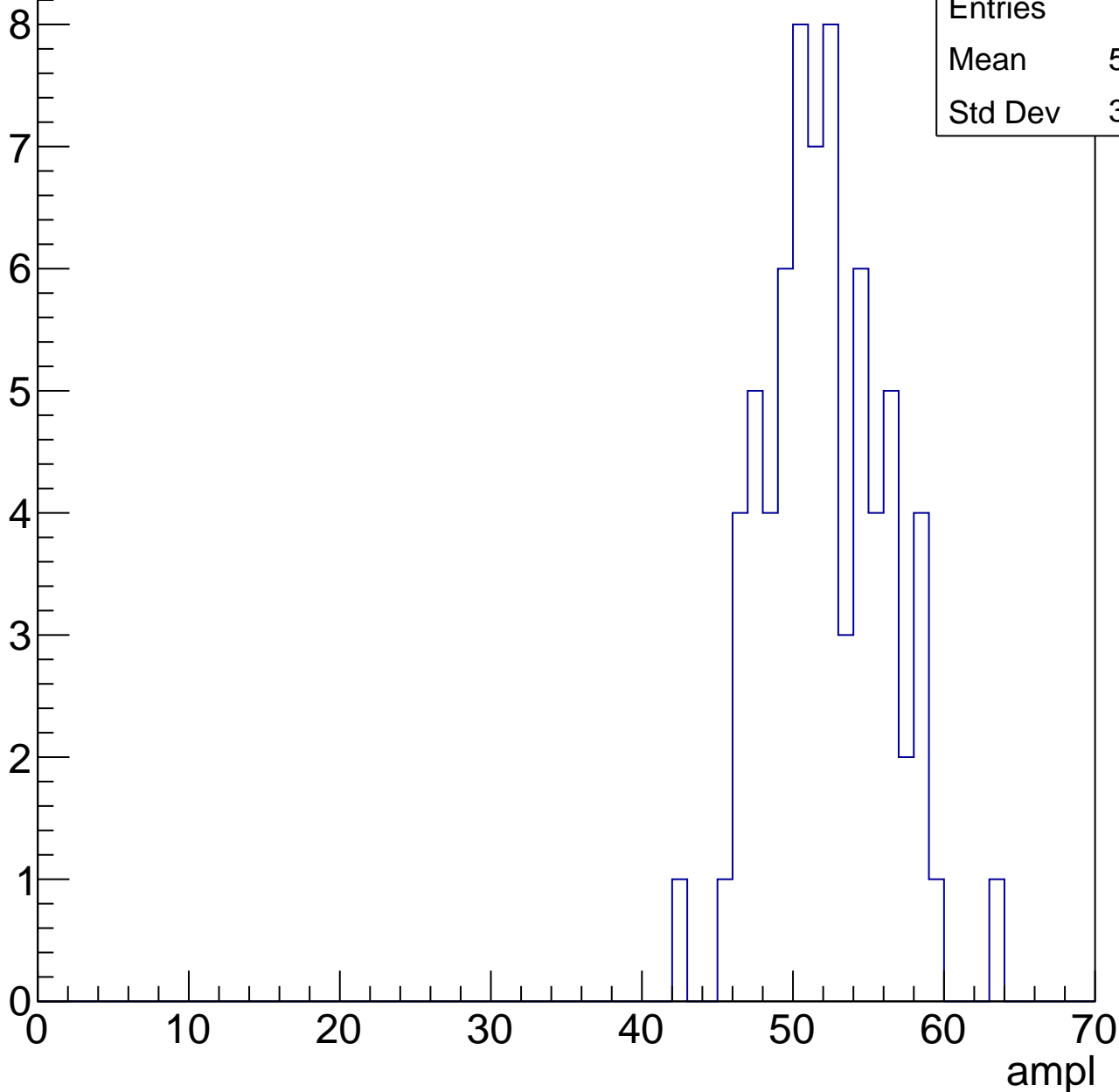


B1L101S, U4-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

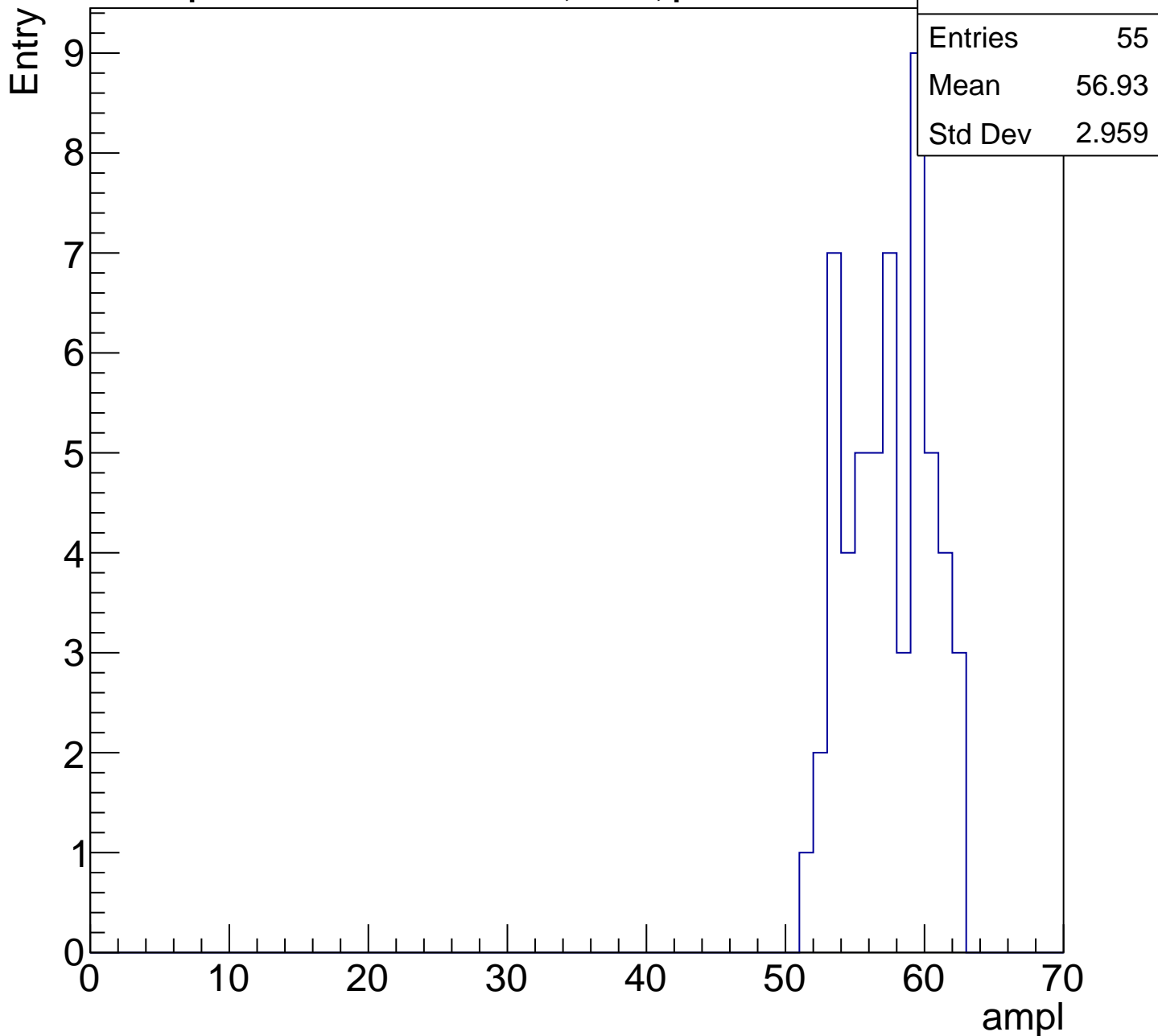
Entry

Entries	70
Mean	51.66
Std Dev	3.917



B1L101S, U4-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

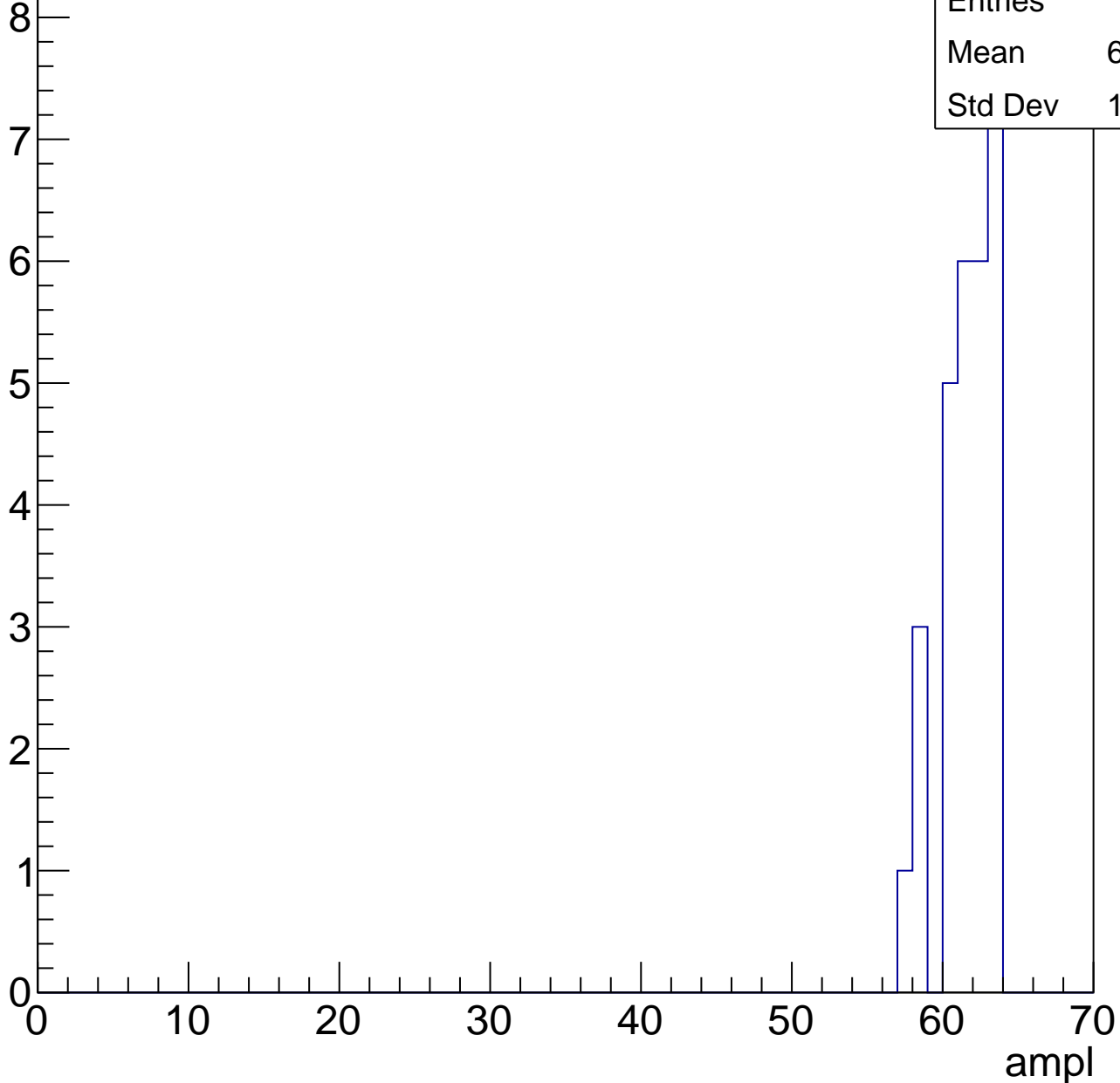


B1L101S, U4-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	61.14
Std Dev	1.717



B1L101S, U4-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch21, adc0

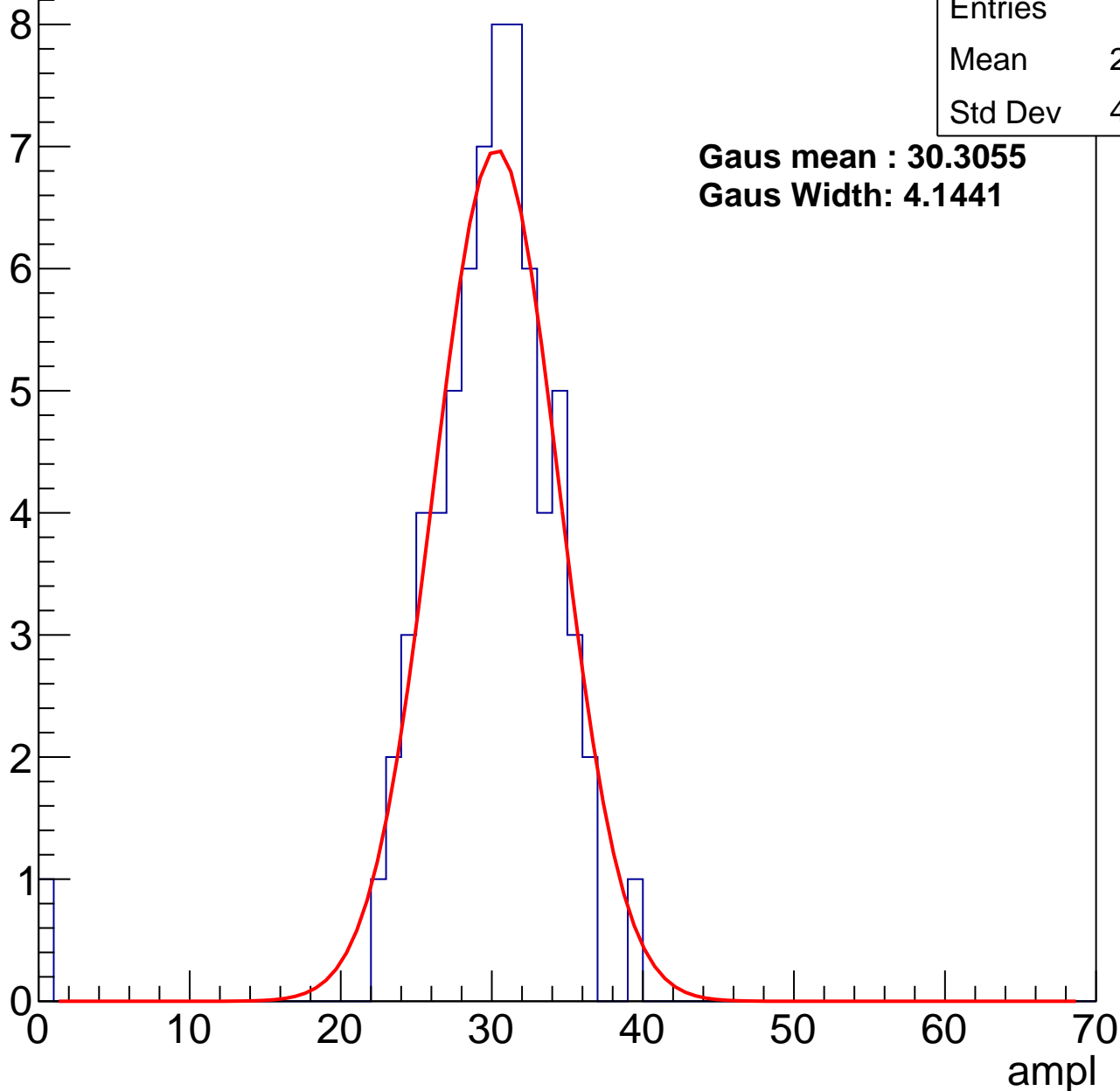
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.26
Std Dev	4.996

Gaus mean : 30.3055

Gaus Width: 4.1441



B1L101S, U4-ch21, adc1

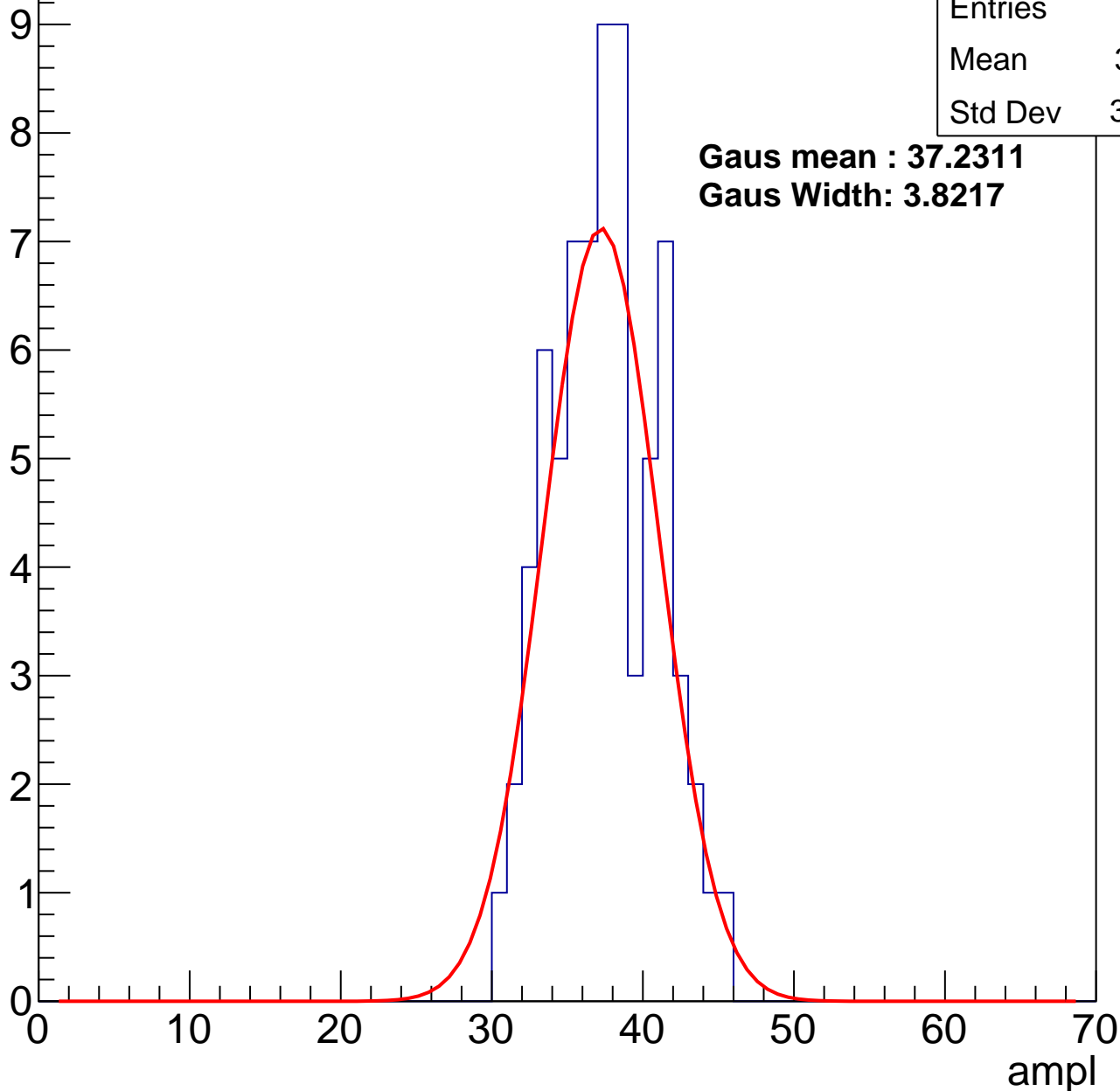
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.01
Std Dev	3.405

Gaus mean : 37.2311

Gaus Width: 3.8217



B1L101S, U4-ch21, adc2

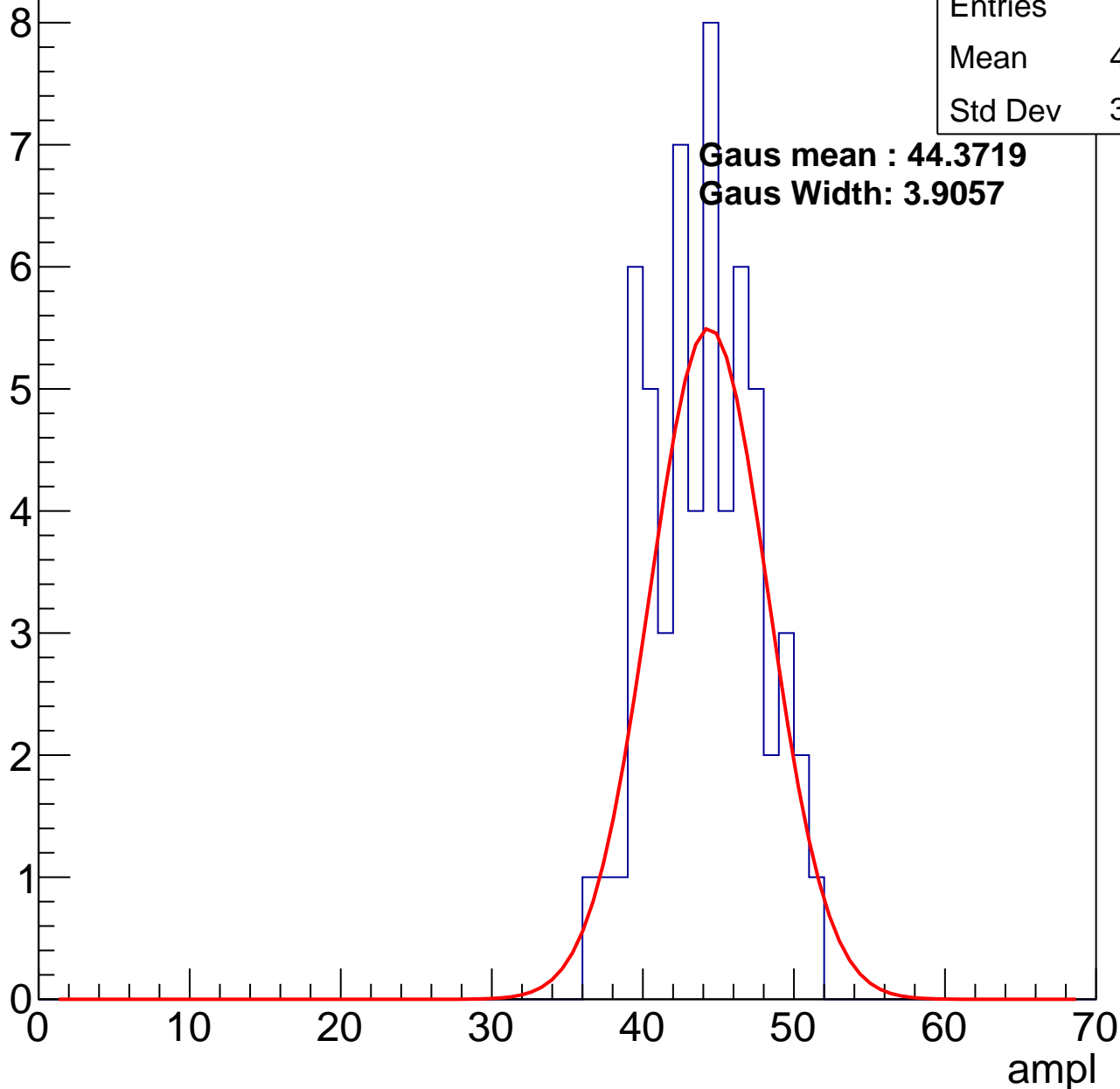
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.58
Std Dev	3.514

Gaus mean : 44.3719

Gaus Width: 3.9057

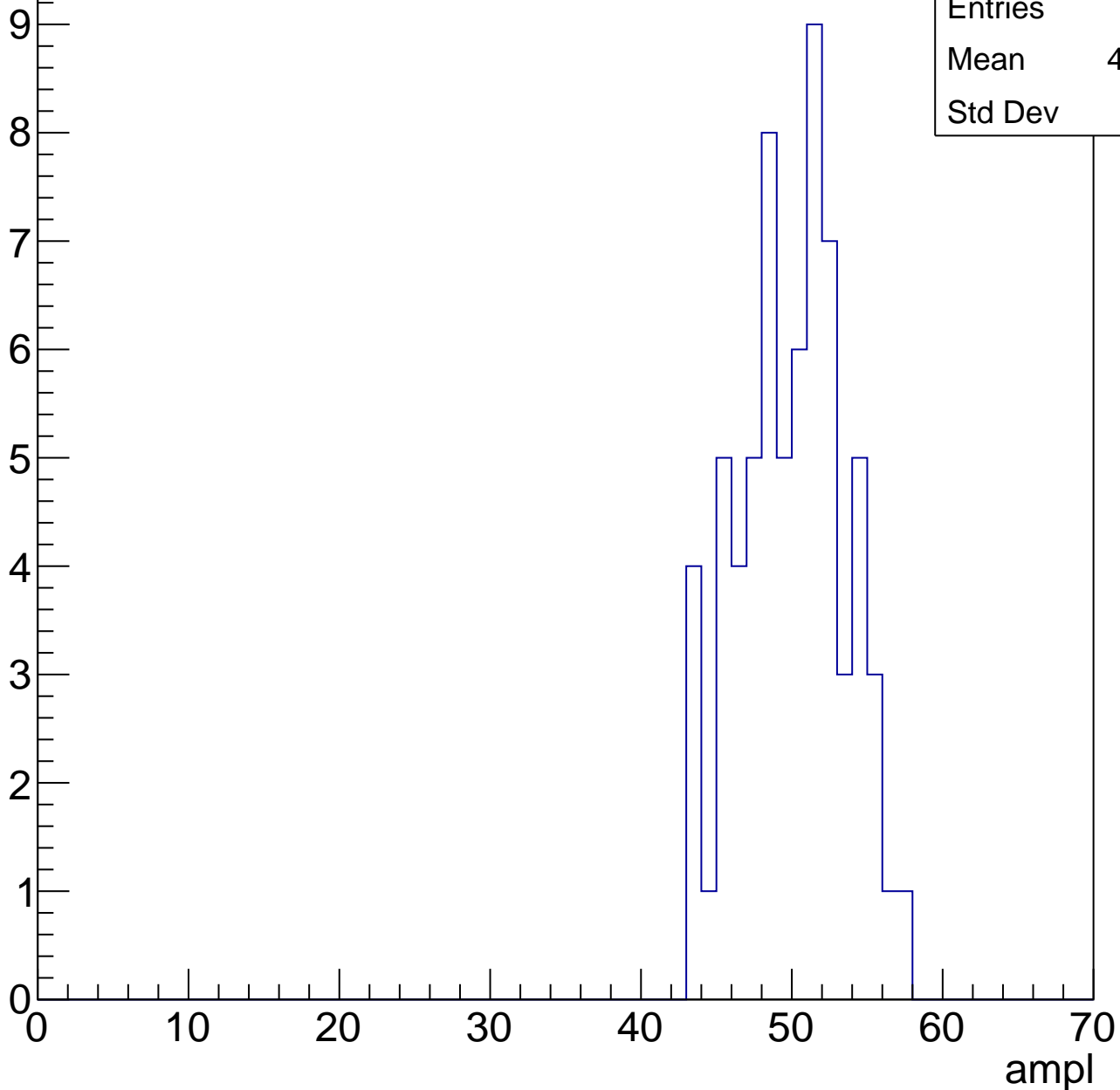


B1L101S, U4-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

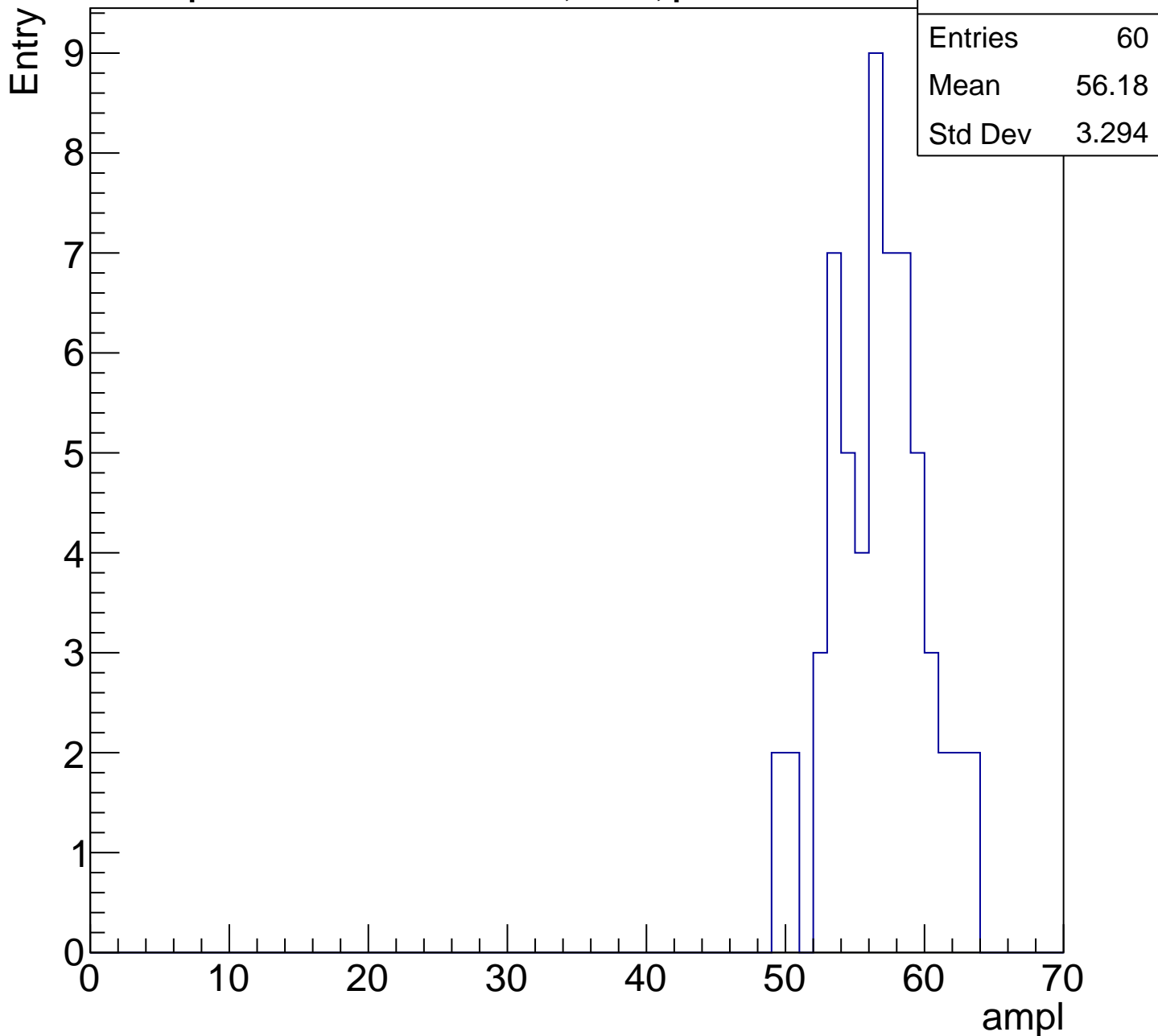
Entry

Entries	67
Mean	49.54
Std Dev	3.44



B1L101S, U4-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

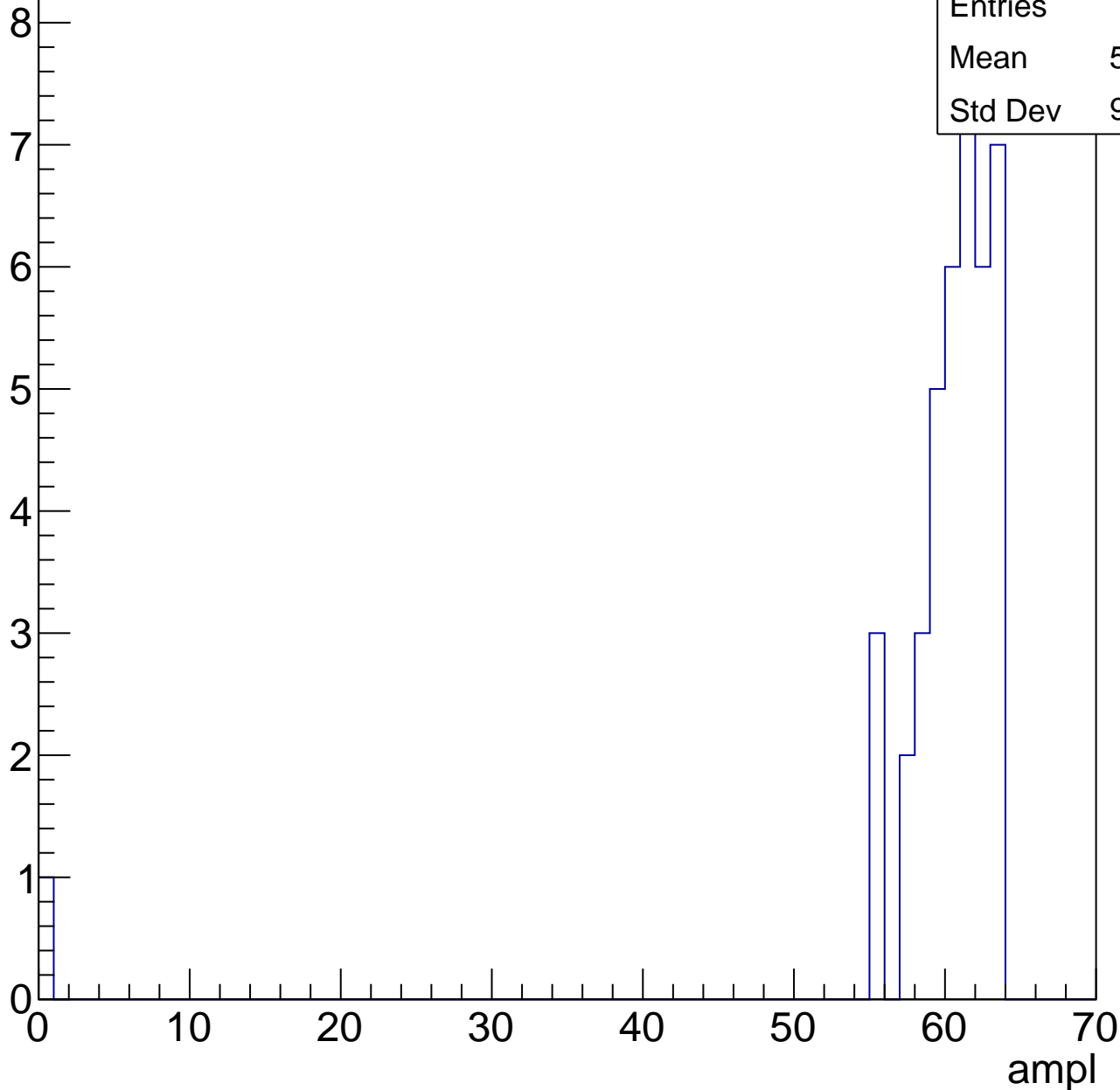


B1L101S, U4-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.76
Std Dev	9.553



B1L101S, U4-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

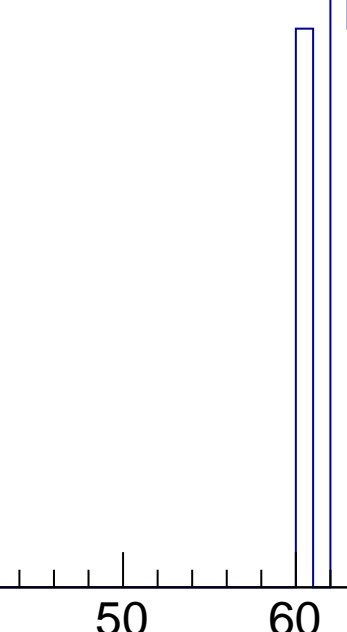
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch22, adc0

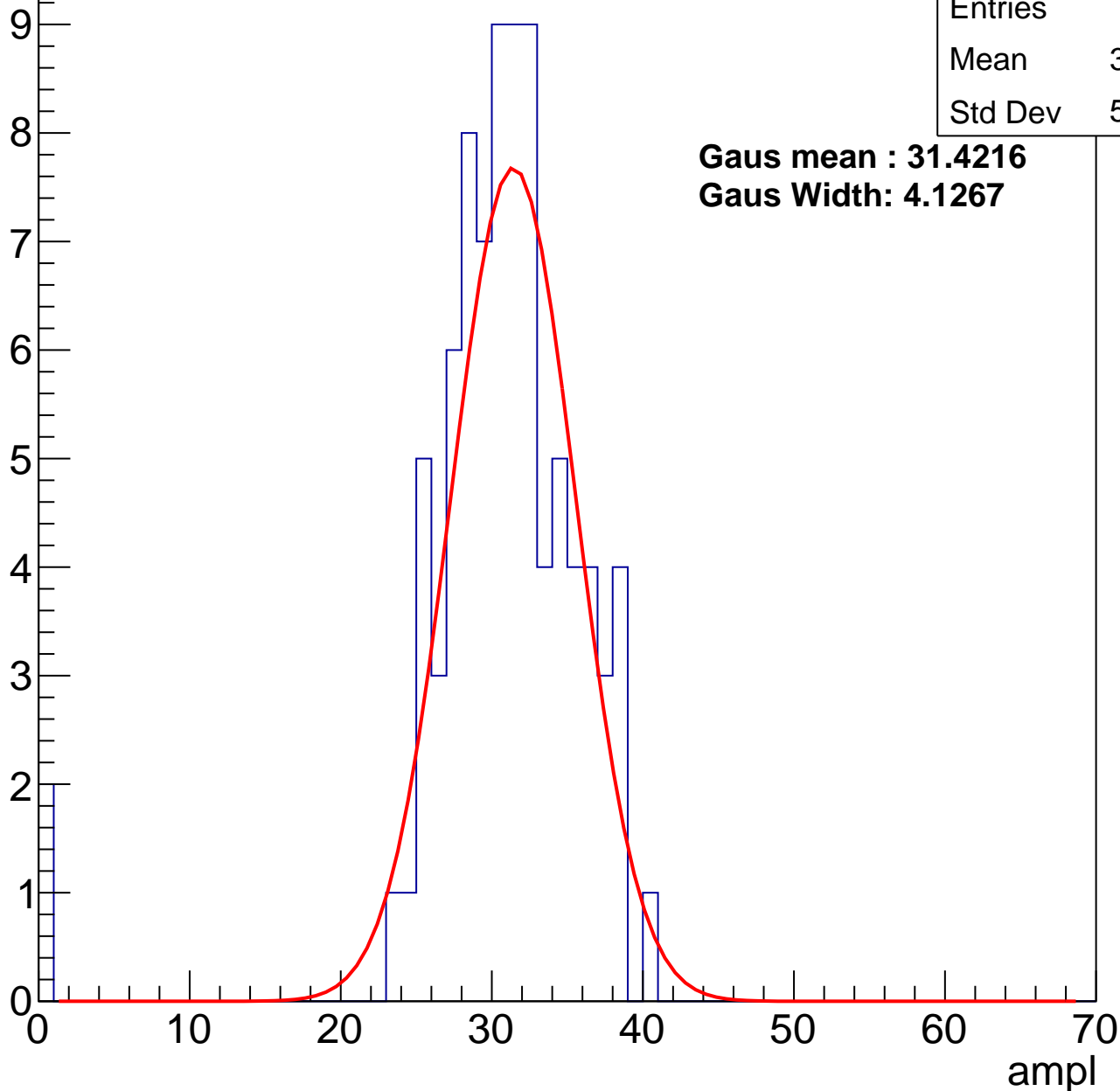
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	30.18
Std Dev	5.996

Gaus mean : 31.4216

Gaus Width: 4.1267



B1L101S, U4-ch22, adc1

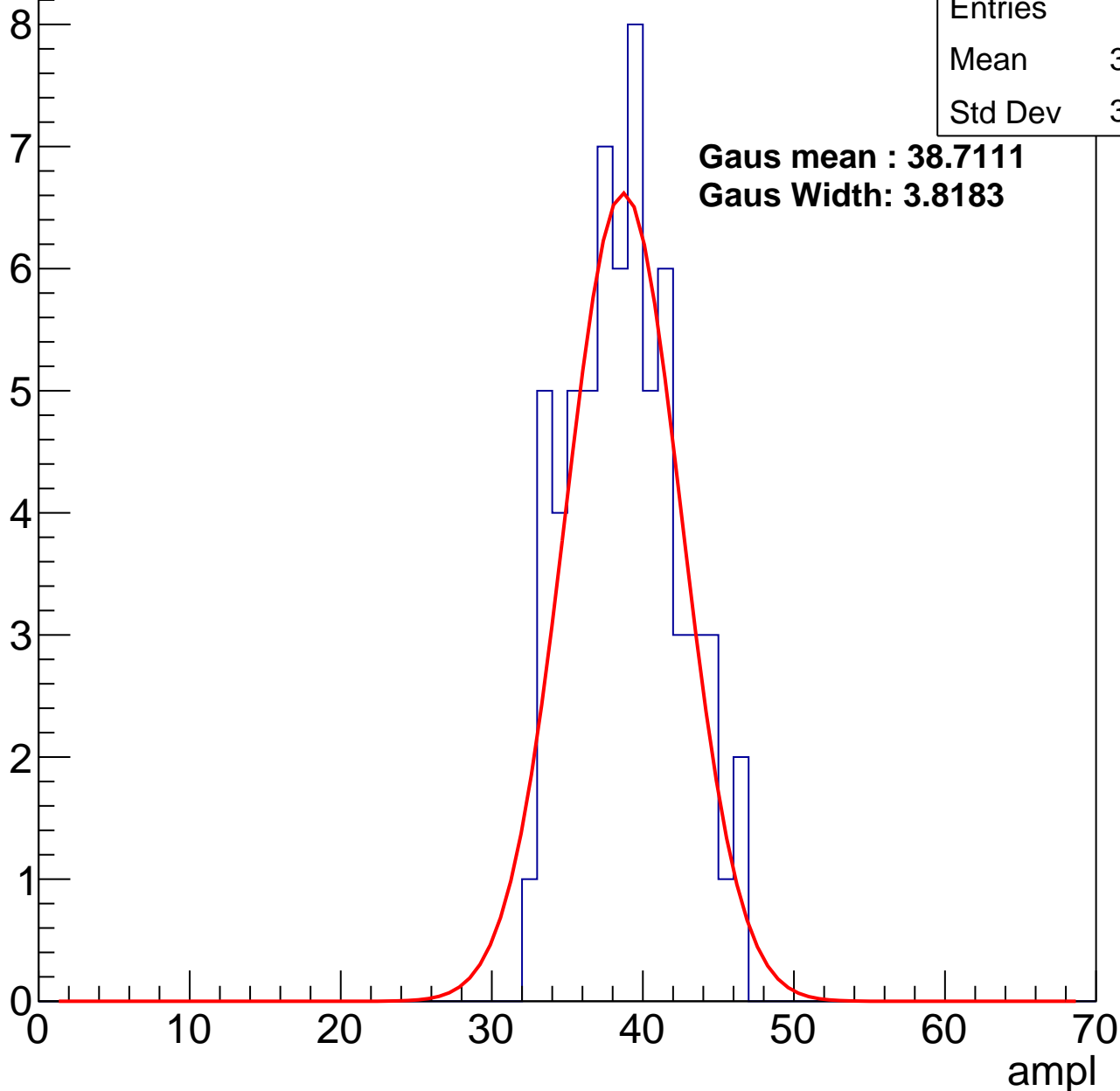
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	38.39
Std Dev	3.485

Gaus mean : 38.7111

Gaus Width: 3.8183

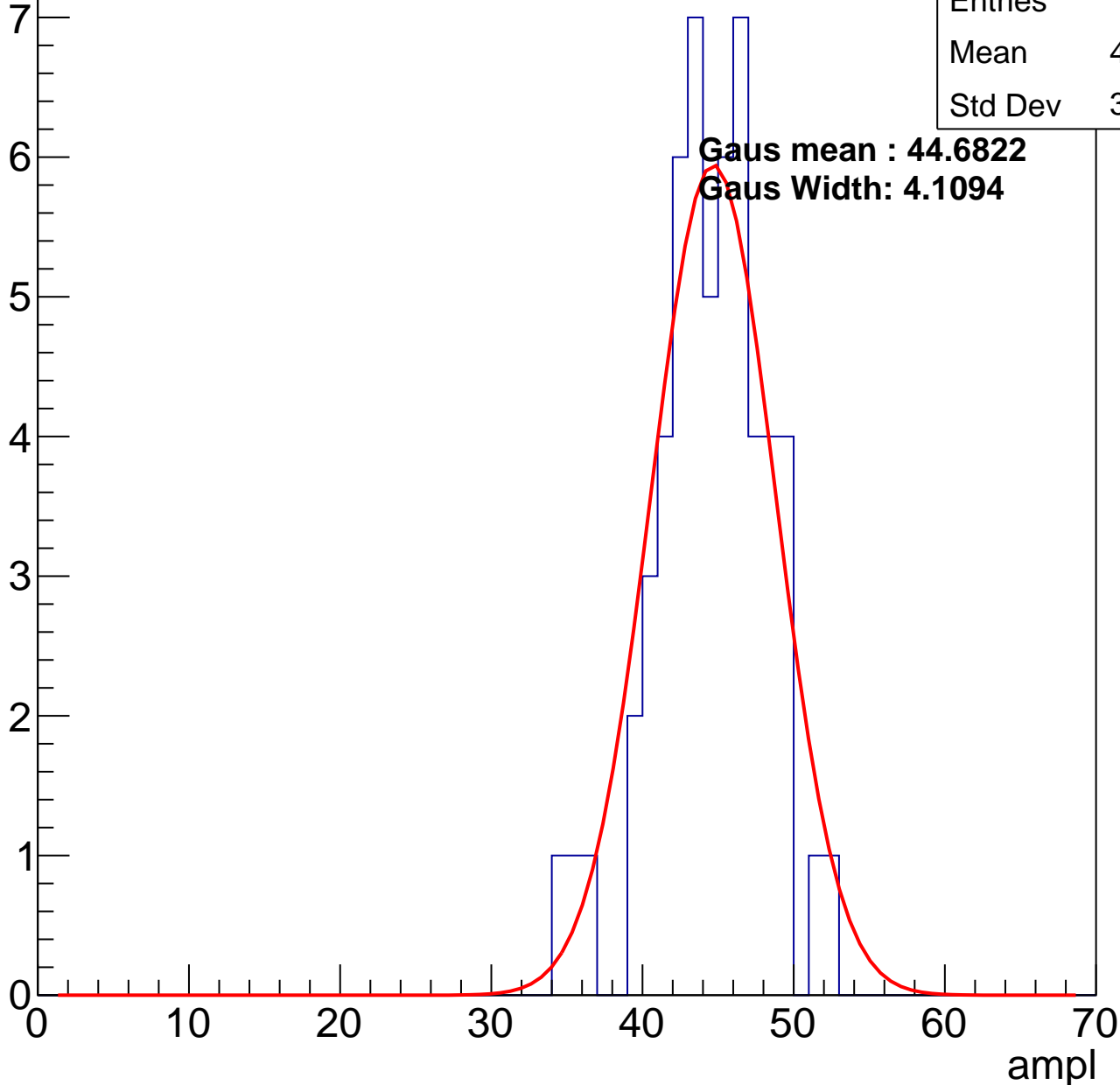


B1L101S, U4-ch22, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.05
Std Dev	3.644

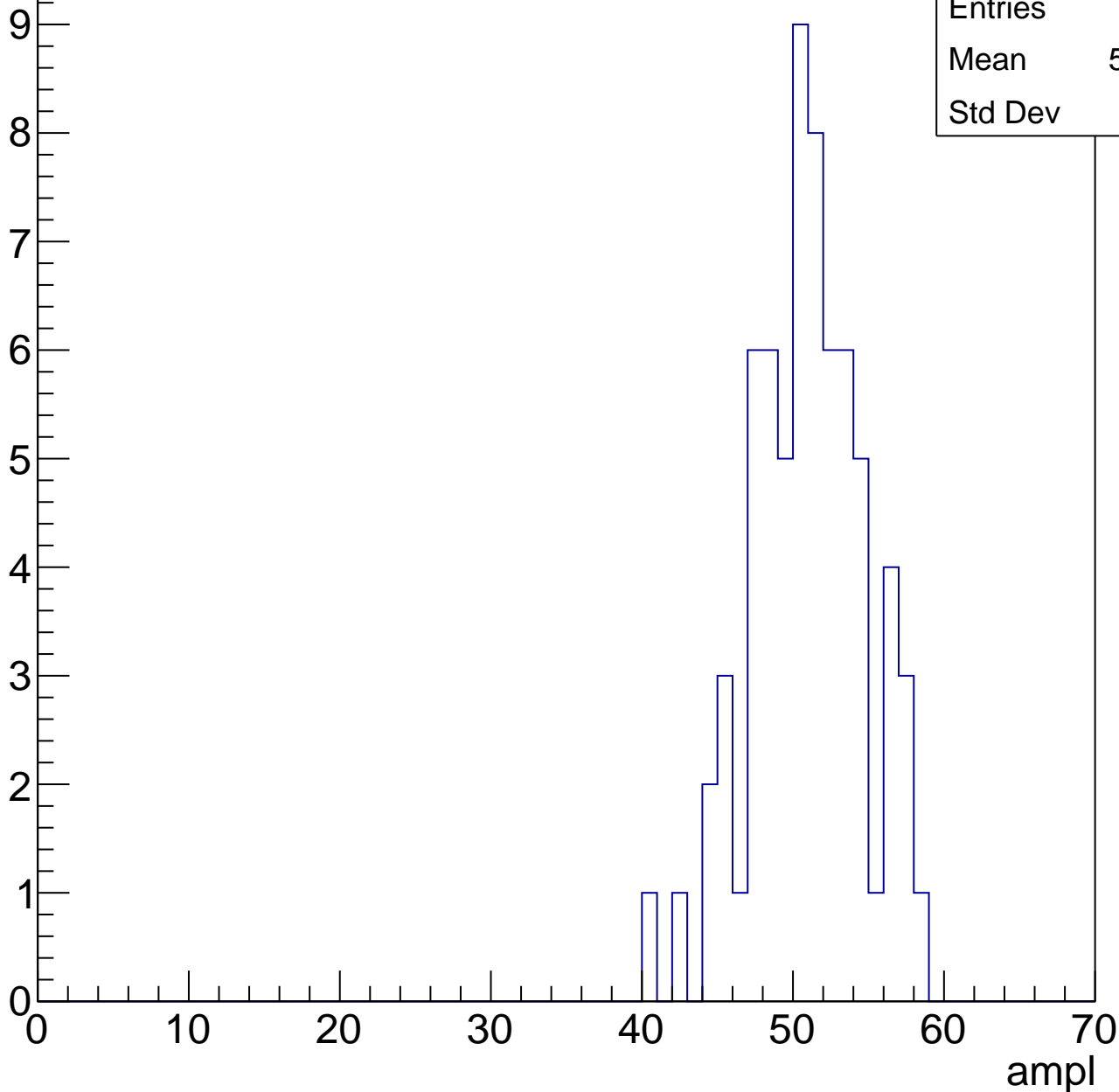


B1L101S, U4-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

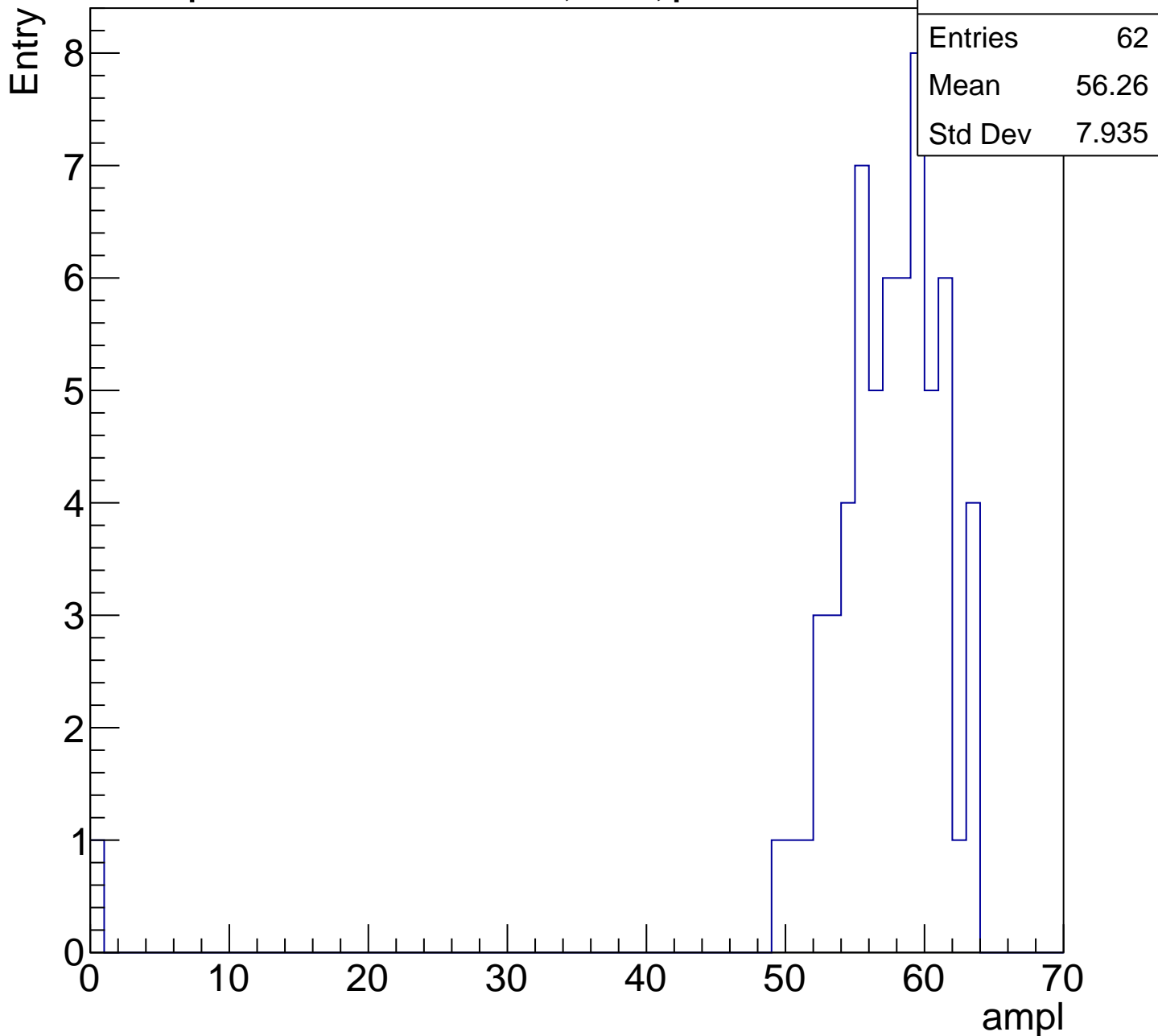
Entry

Entries	68
Mean	50.47
Std Dev	3.72



B1L101S, U4-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

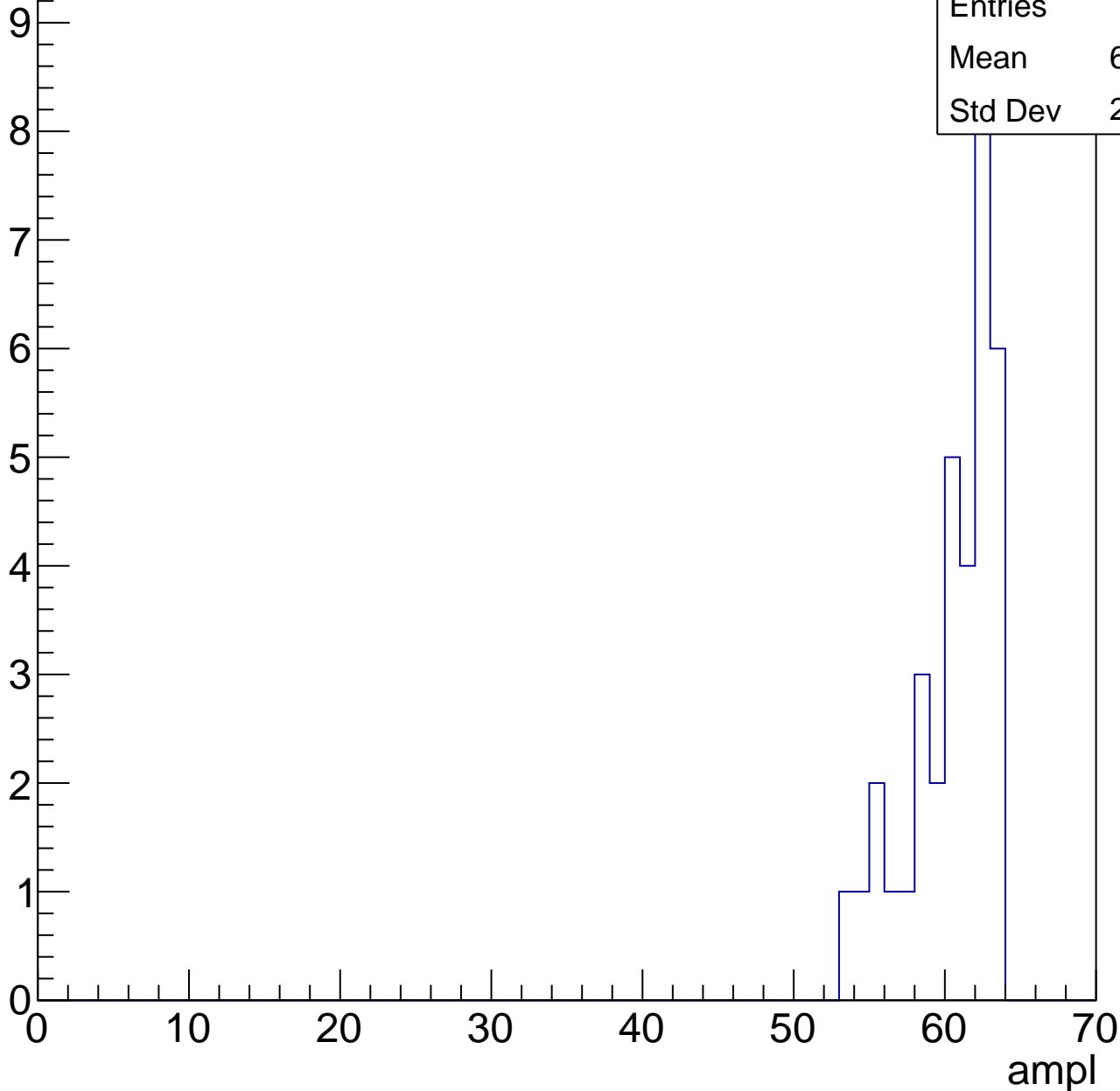


B1L101S, U4-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.06
Std Dev	2.767



B1L101S, U4-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch23, adc0

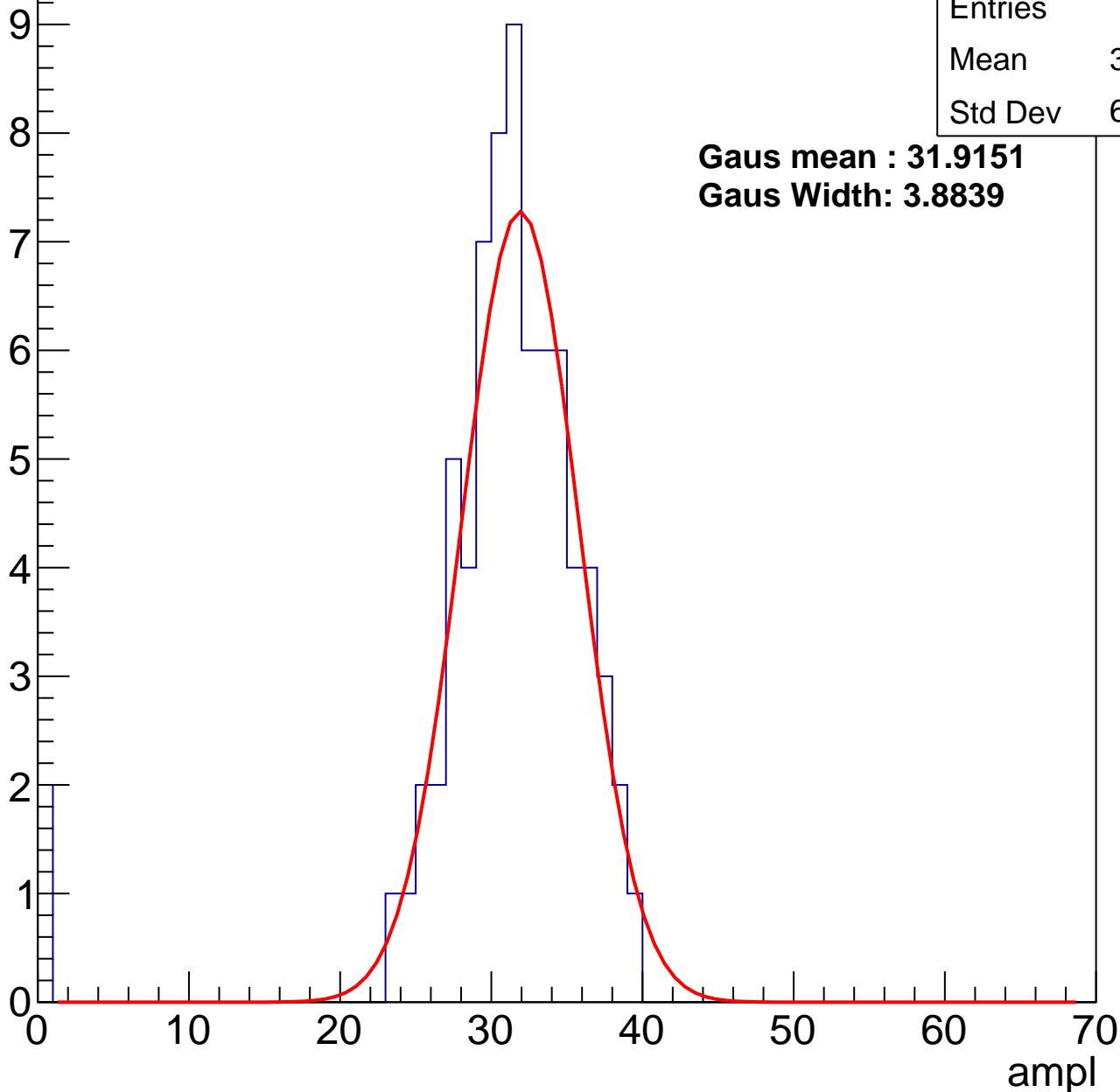
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.44
Std Dev	6.198

Gaus mean : 31.9151

Gaus Width: 3.8839



B1L101S, U4-ch23, adc1

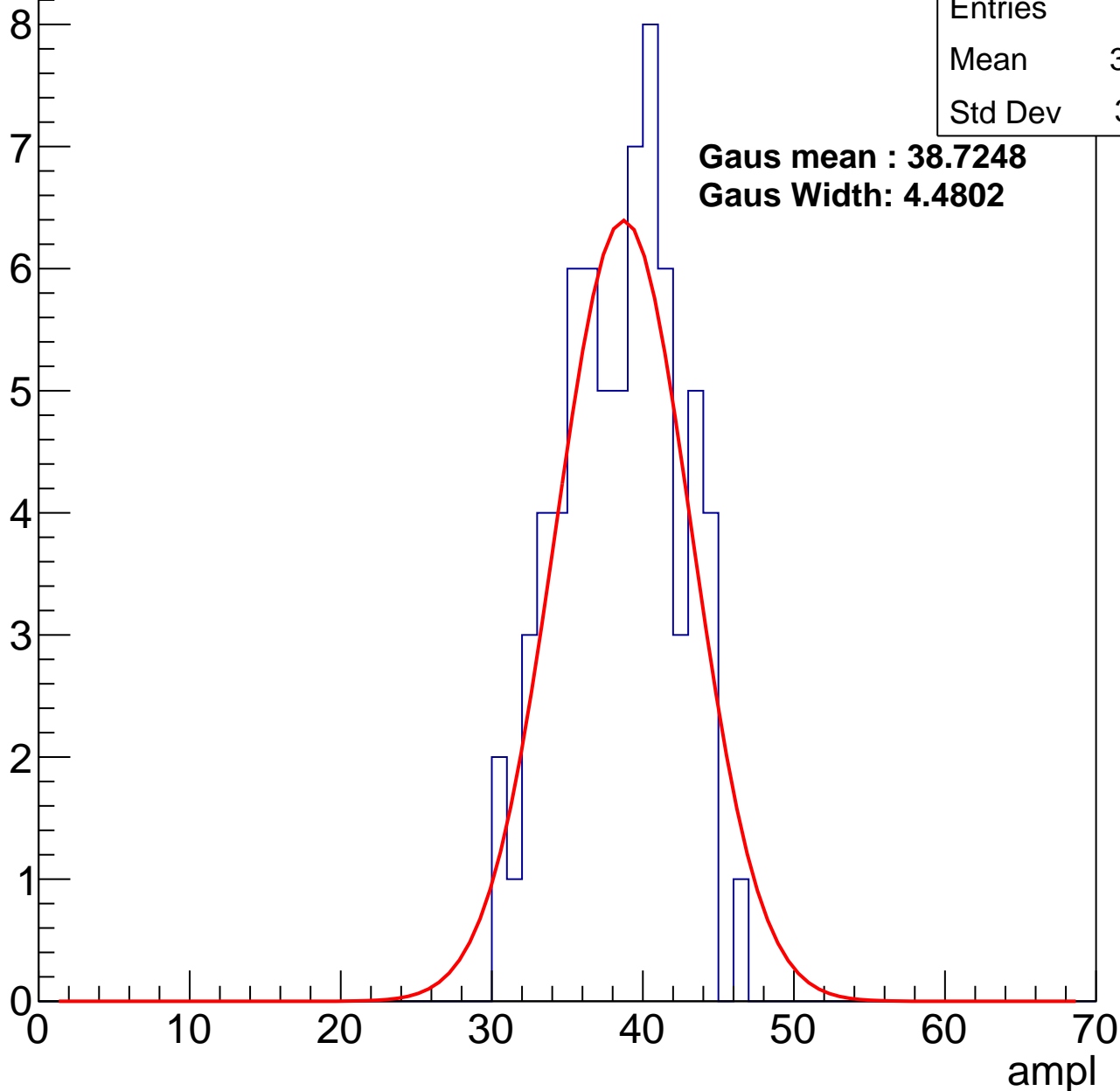
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.97
Std Dev	3.791

Gaus mean : 38.7248

Gaus Width: 4.4802



B1L101S, U4-ch23, adc2

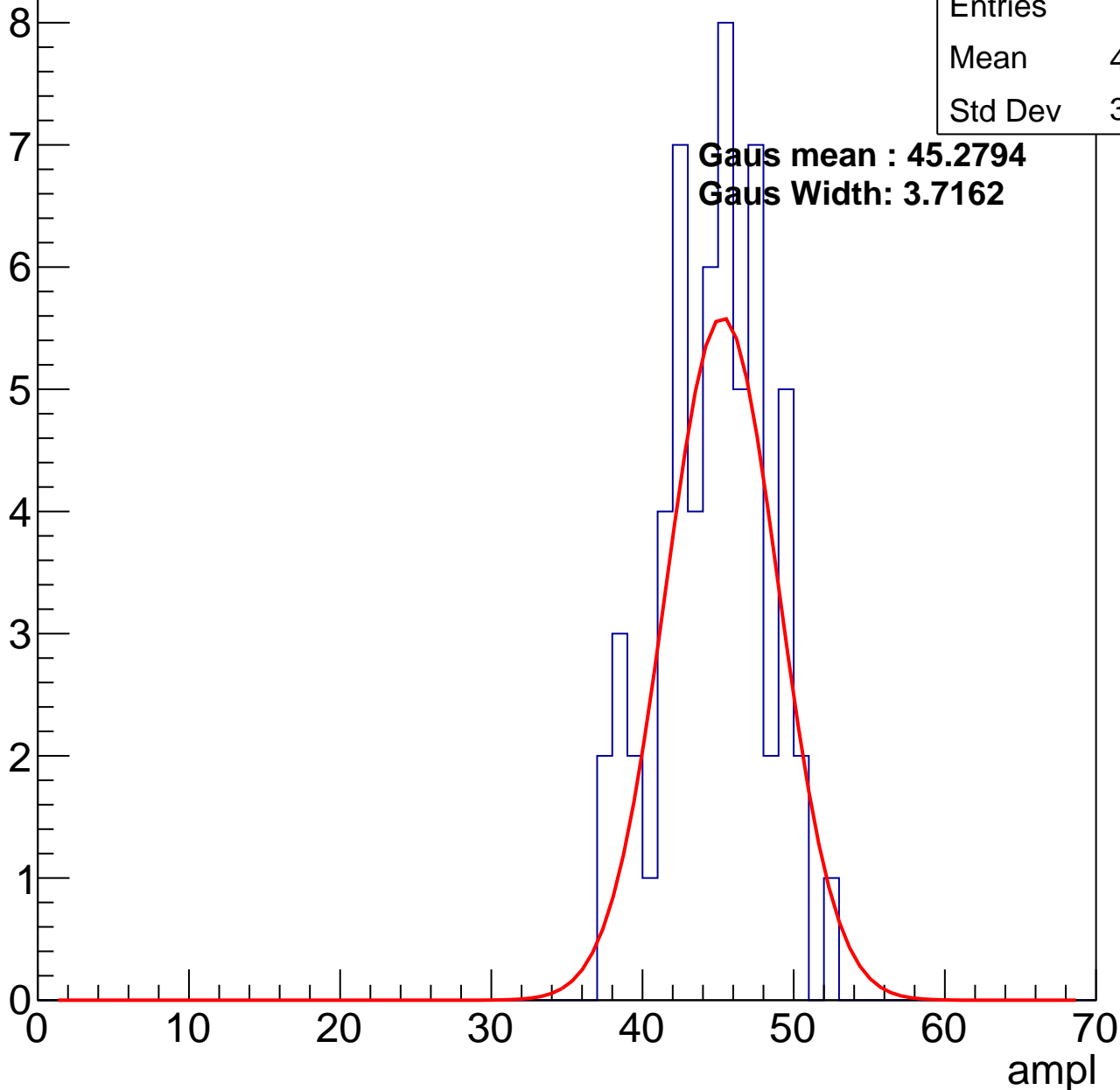
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.27
Std Dev	3.493

Gaus mean : 45.2794

Gaus Width: 3.7162

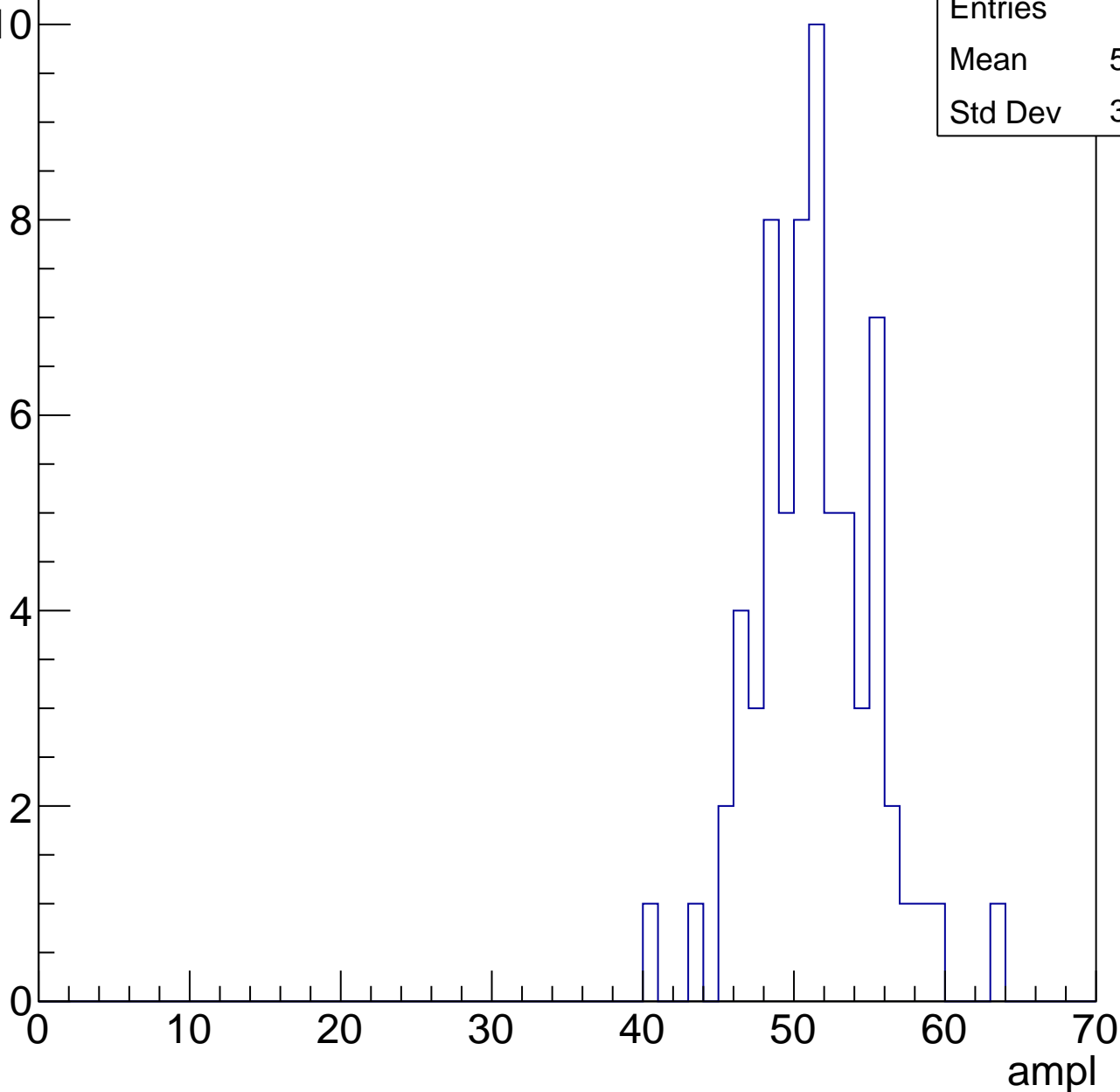


B1L101S, U4-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

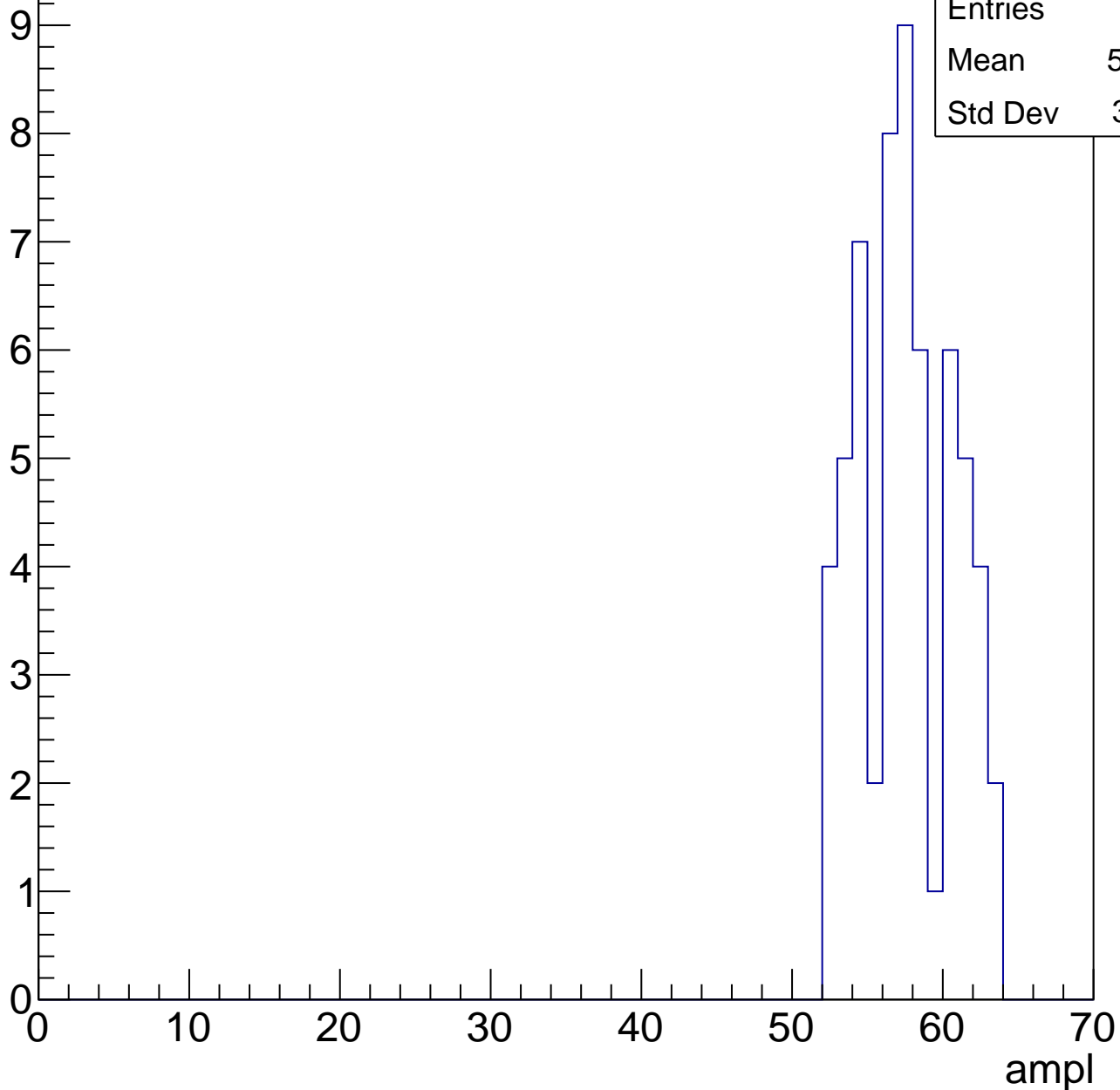
Entries	68
Mean	50.85
Std Dev	3.855



B1L101S, U4-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	59
Mean	57.08
Std Dev	3.121

B1L101S, U4-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	58.71
Std Dev	10.39

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

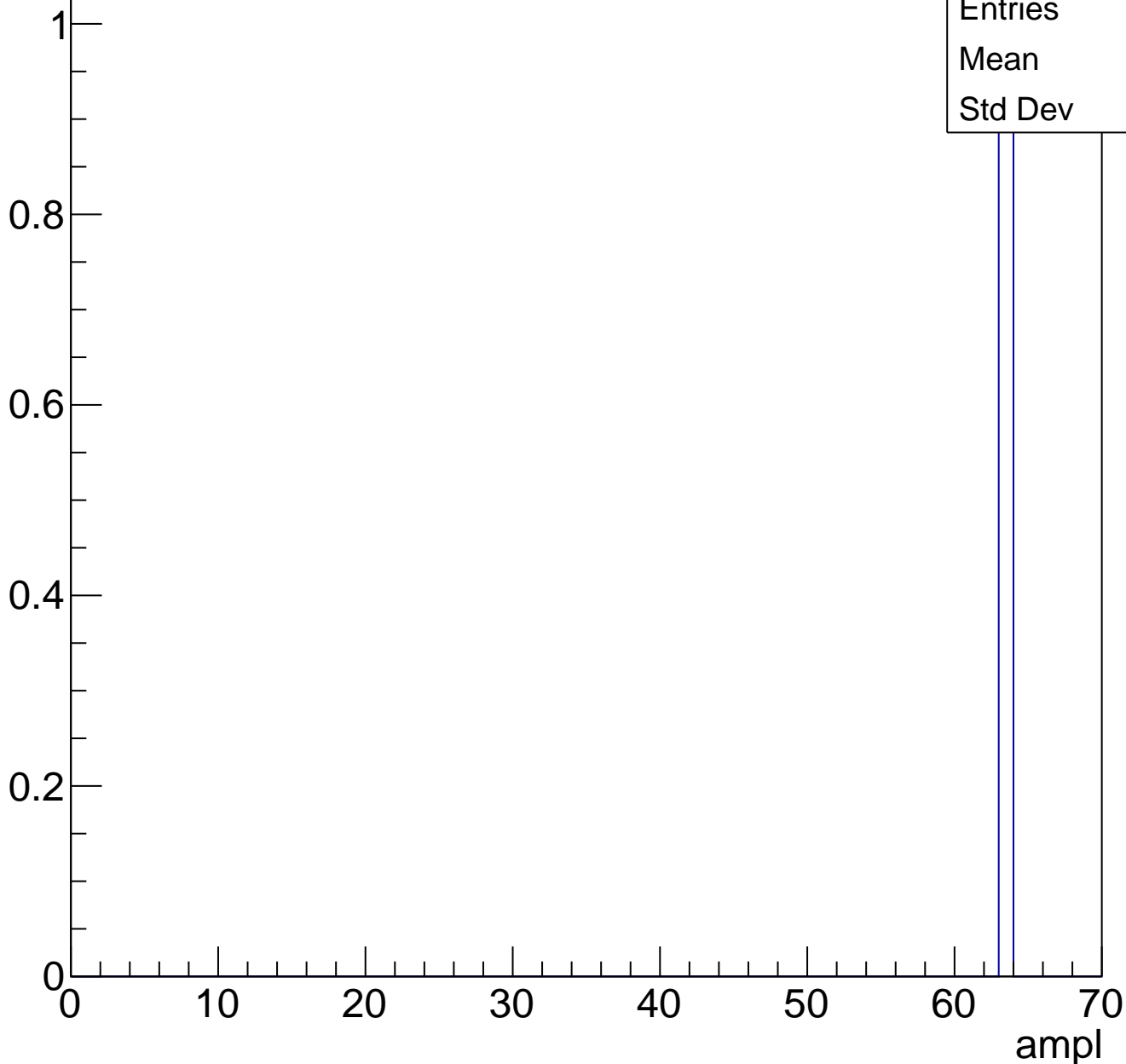
7

8

B1L101S, U4-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch24, adc0

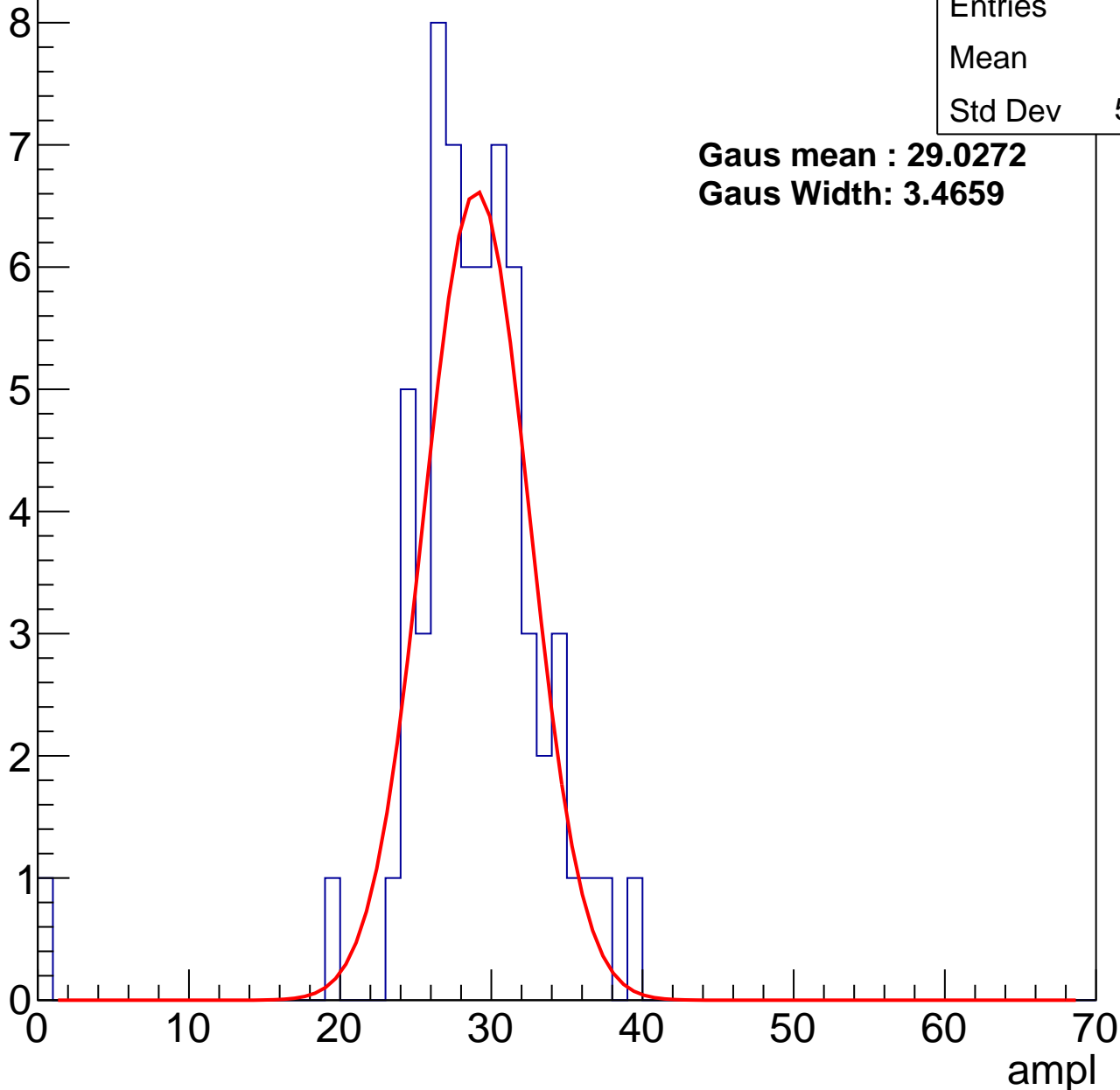
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.3
Std Dev	5.101

Gaus mean : 29.0272

Gaus Width: 3.4659



B1L101S, U4-ch24, adc1

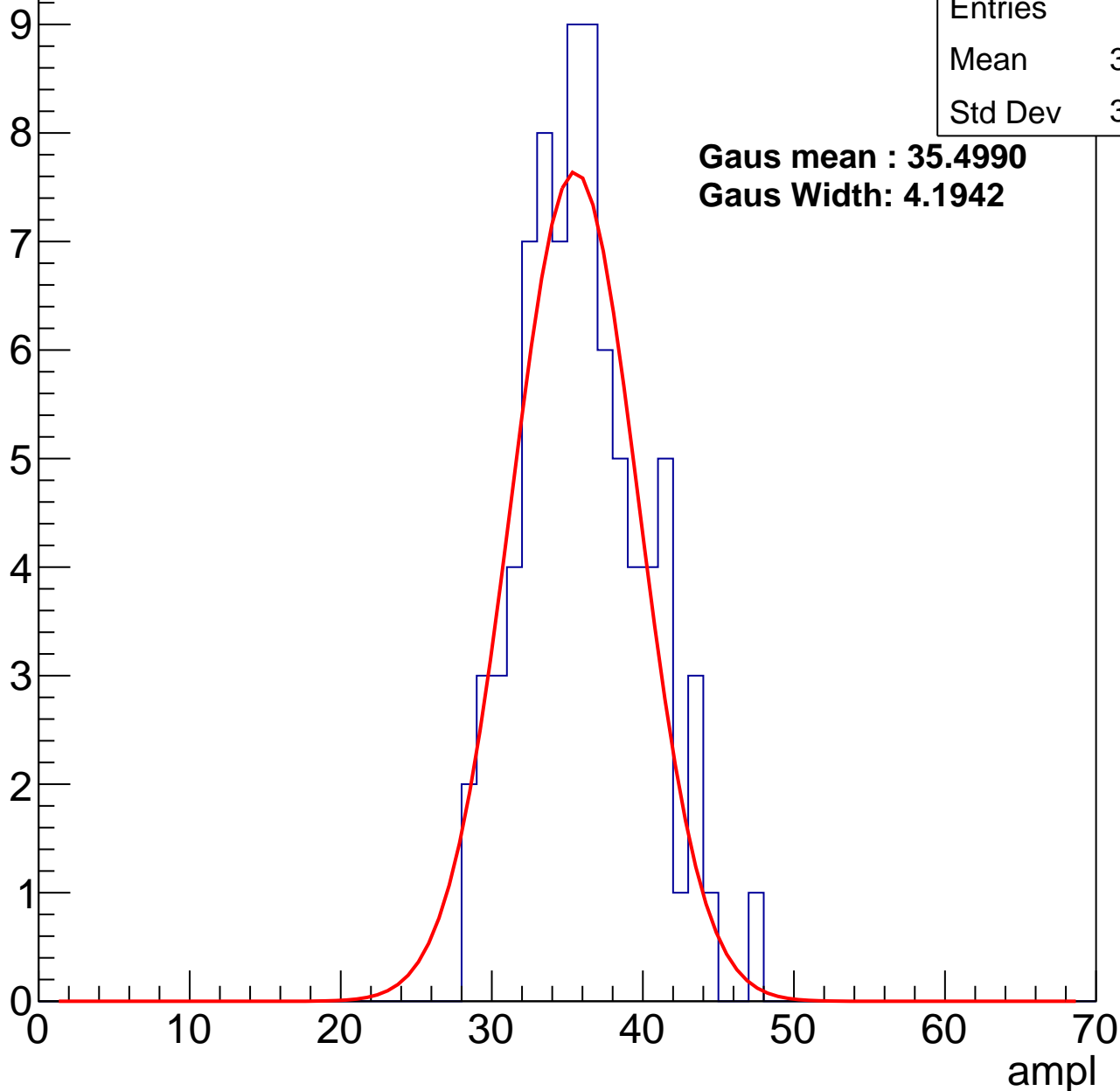
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	35.57
Std Dev	3.994

Gaus mean : 35.4990

Gaus Width: 4.1942



B1L101S, U4-ch24, adc2

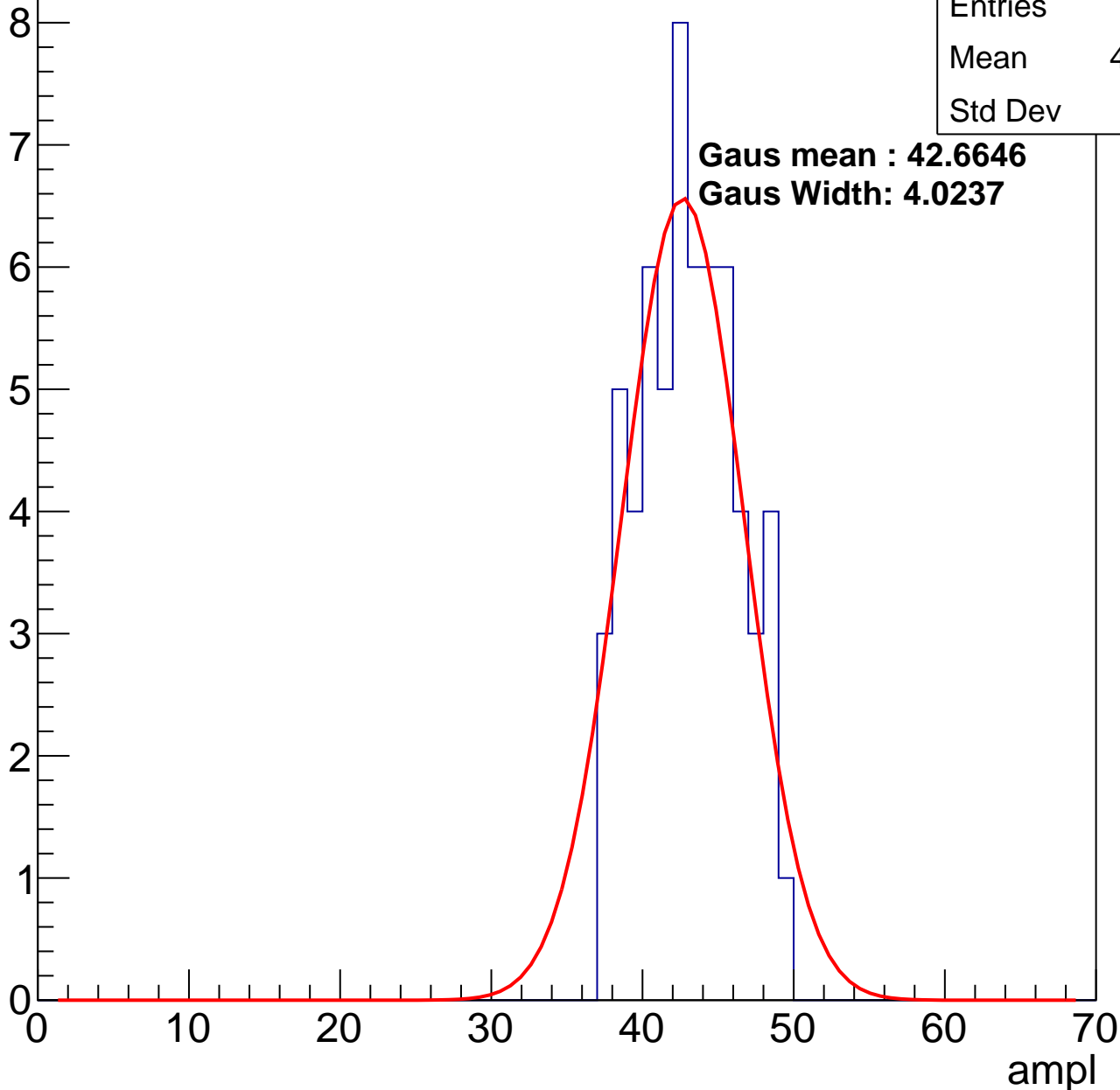
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.56
Std Dev	3.18

Gaus mean : 42.6646

Gaus Width: 4.0237

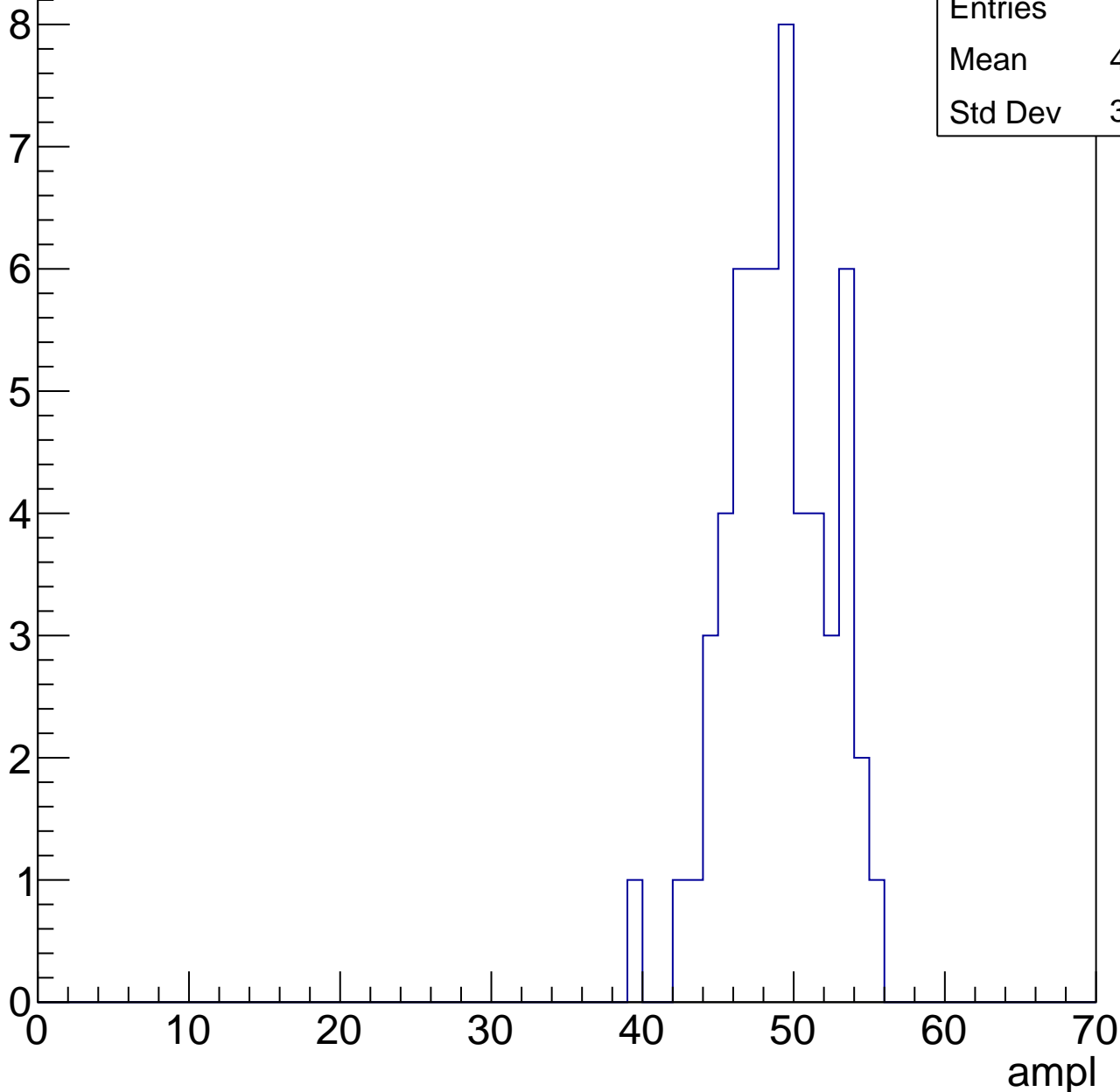


B1L101S, U4-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	48.48
Std Dev	3.333

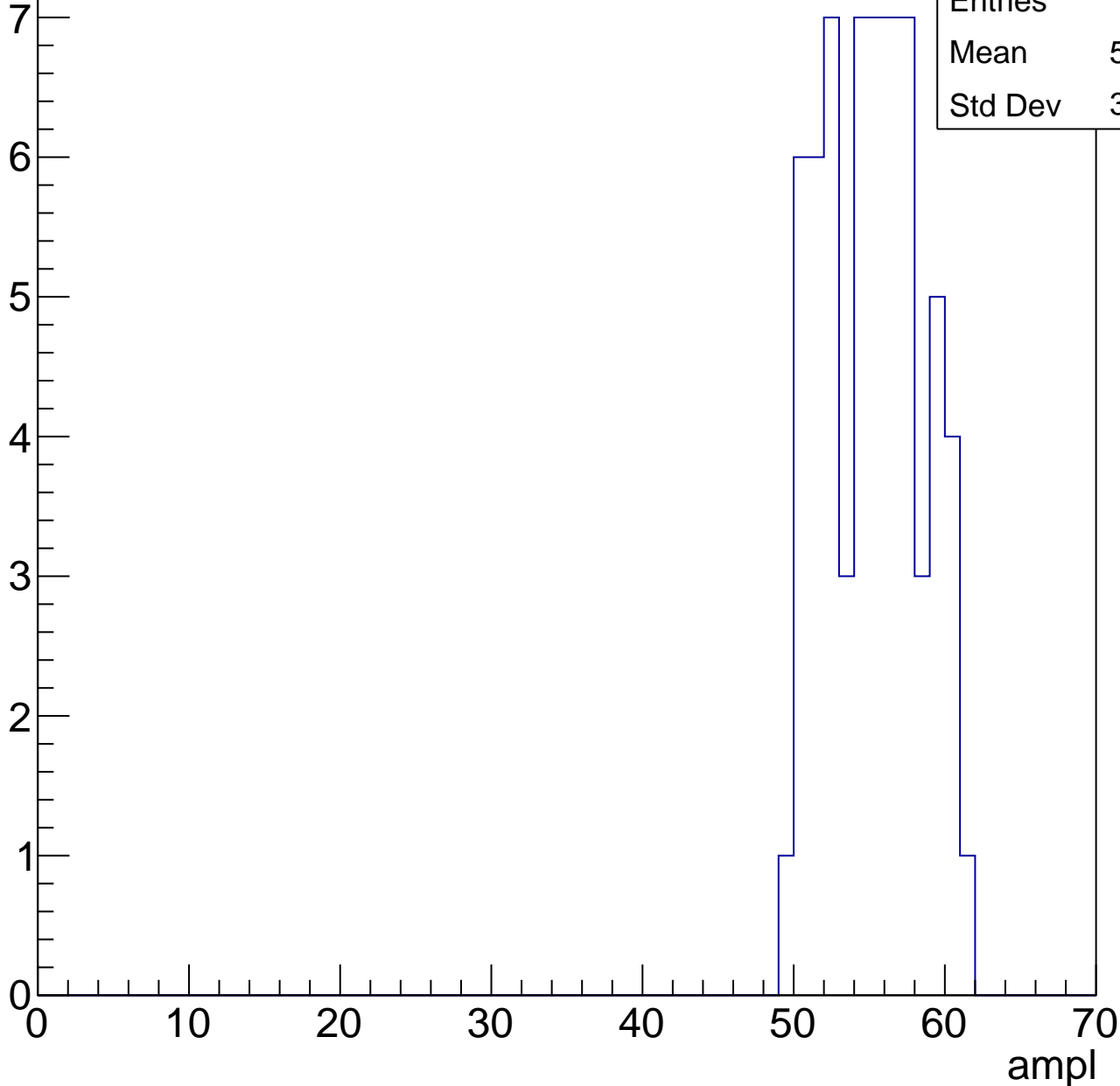


B1L101S, U4-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	54.72
Std Dev	3.155

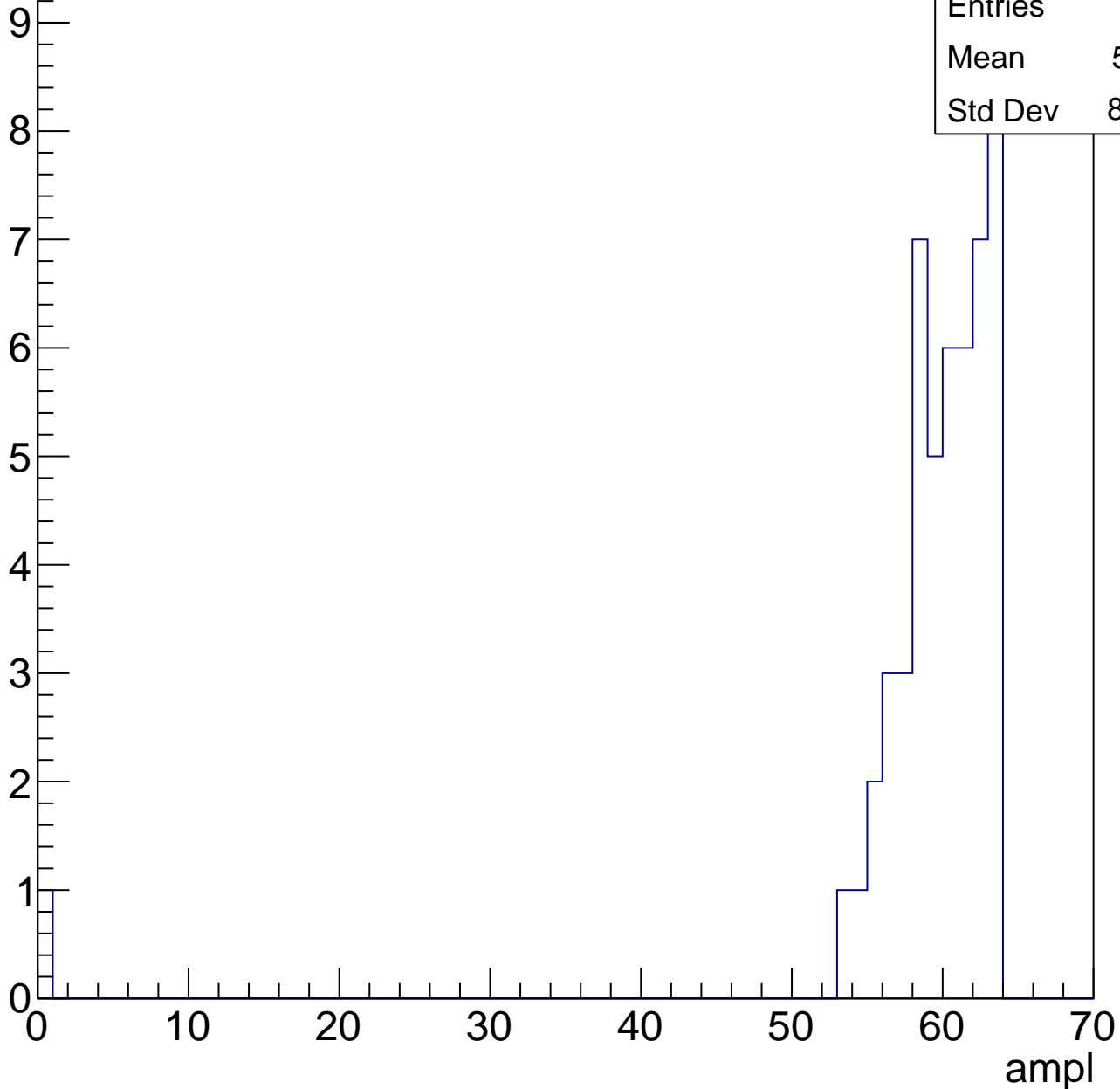


B1L101S, U4-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

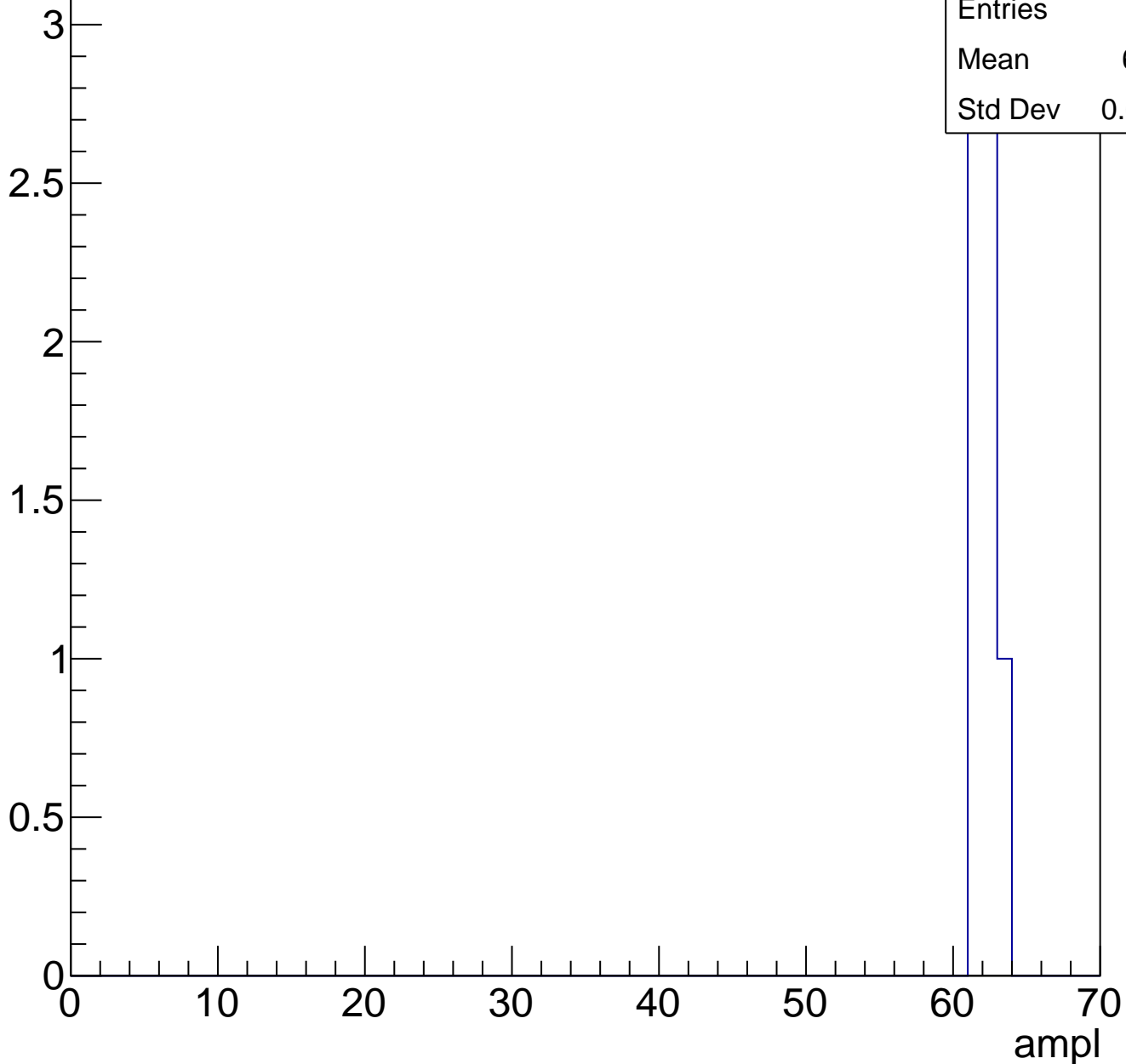
Entries	51
Mean	58.51
Std Dev	8.683



B1L101S, U4-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch25, adc0

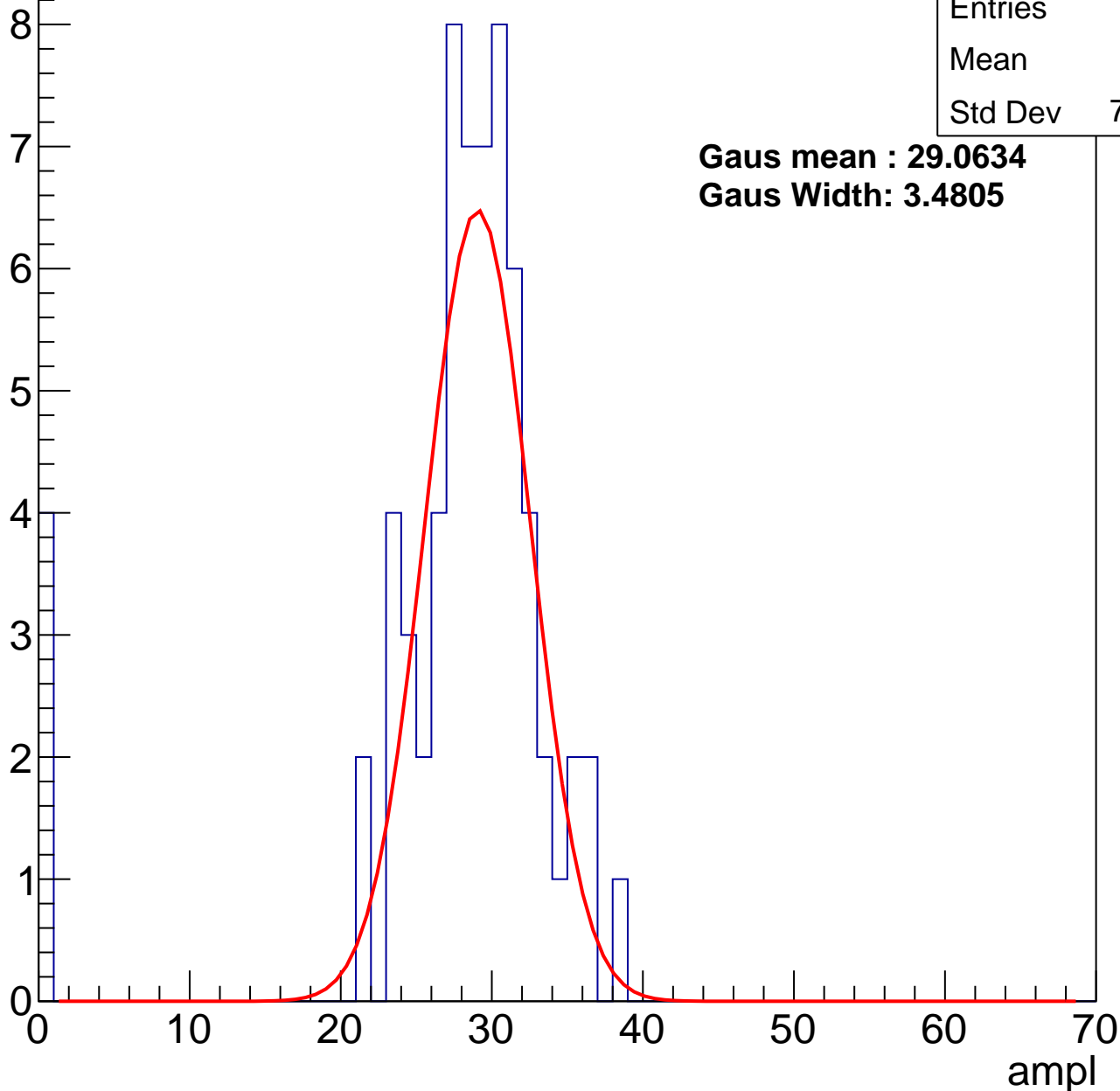
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	27
Std Dev	7.655

Gaus mean : 29.0634

Gaus Width: 3.4805



B1L101S, U4-ch25, adc1

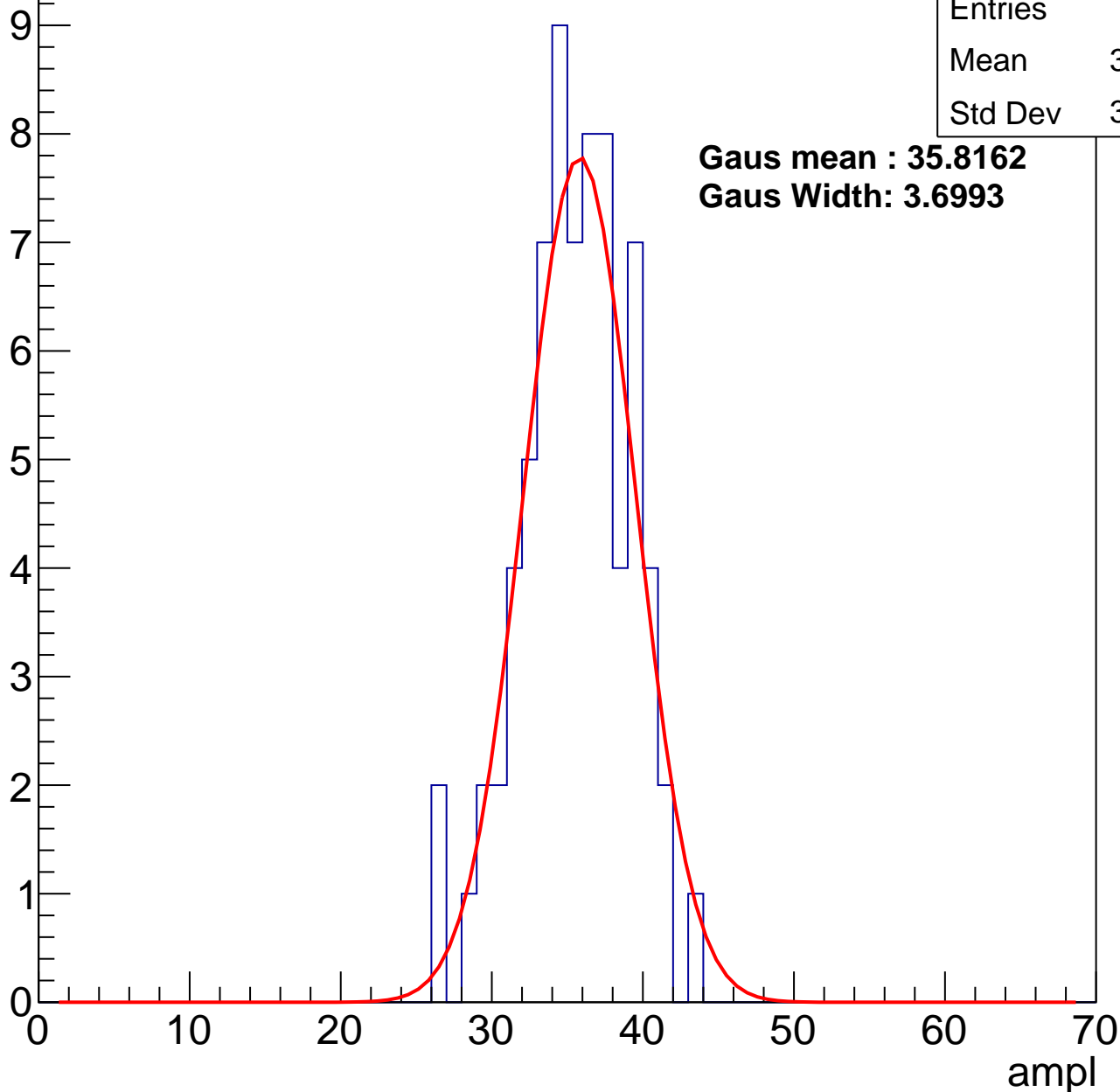
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.04
Std Dev	3.517

Gaus mean : 35.8162

Gaus Width: 3.6993



B1L101S, U4-ch25, adc2

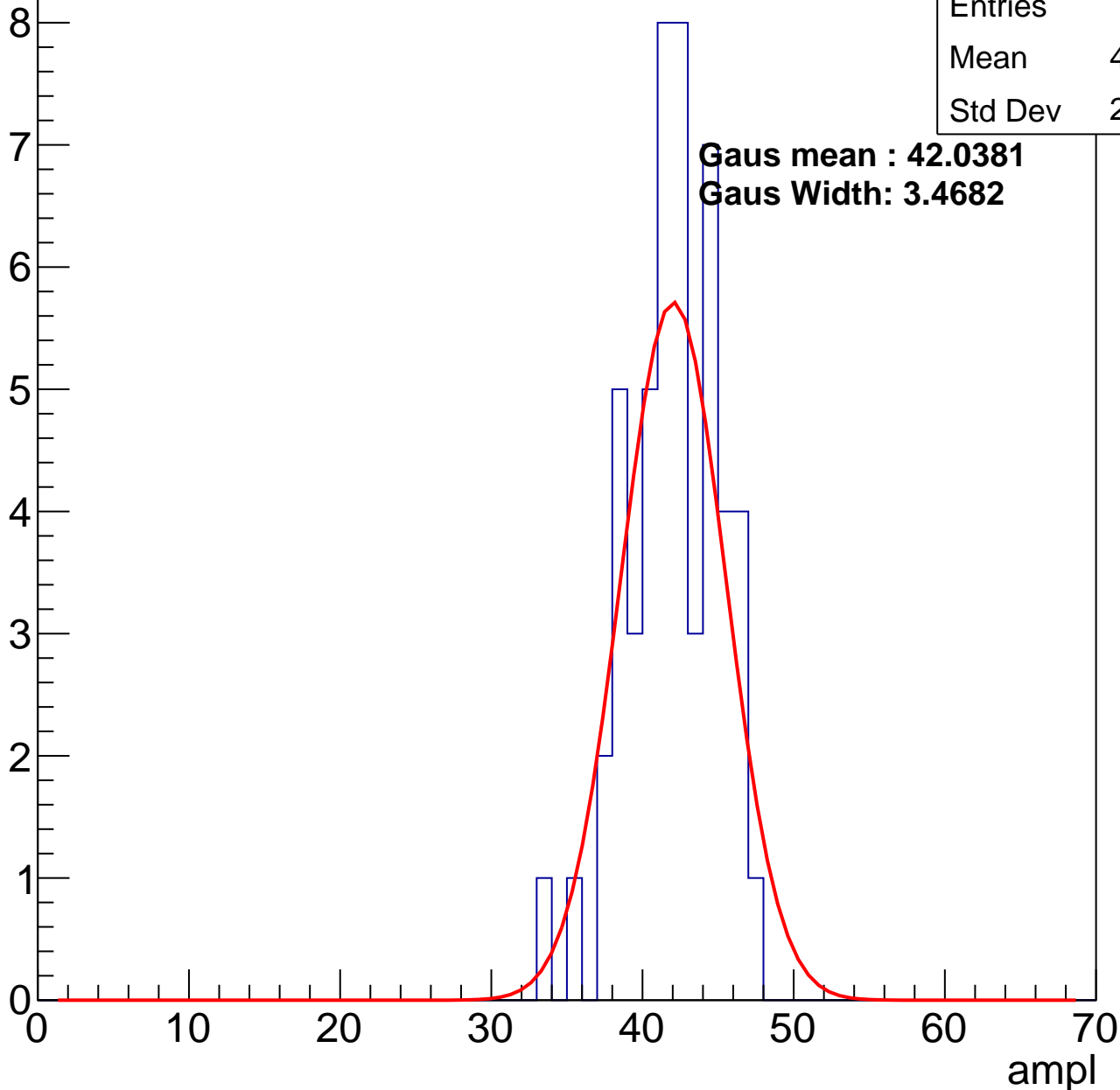
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.56
Std Dev	2.977

Gaus mean : 42.0381

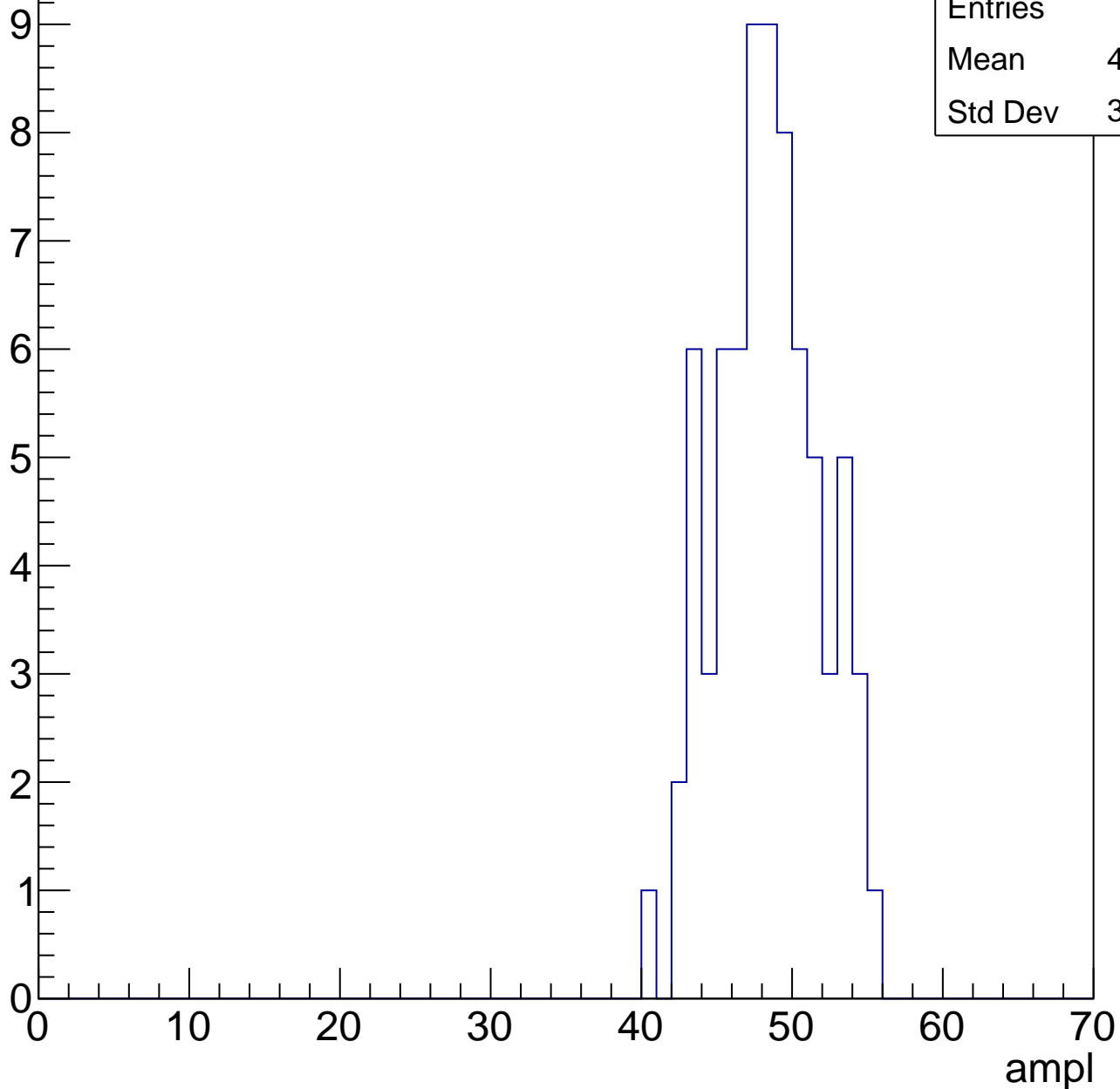
Gaus Width: 3.4682



B1L101S, U4-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

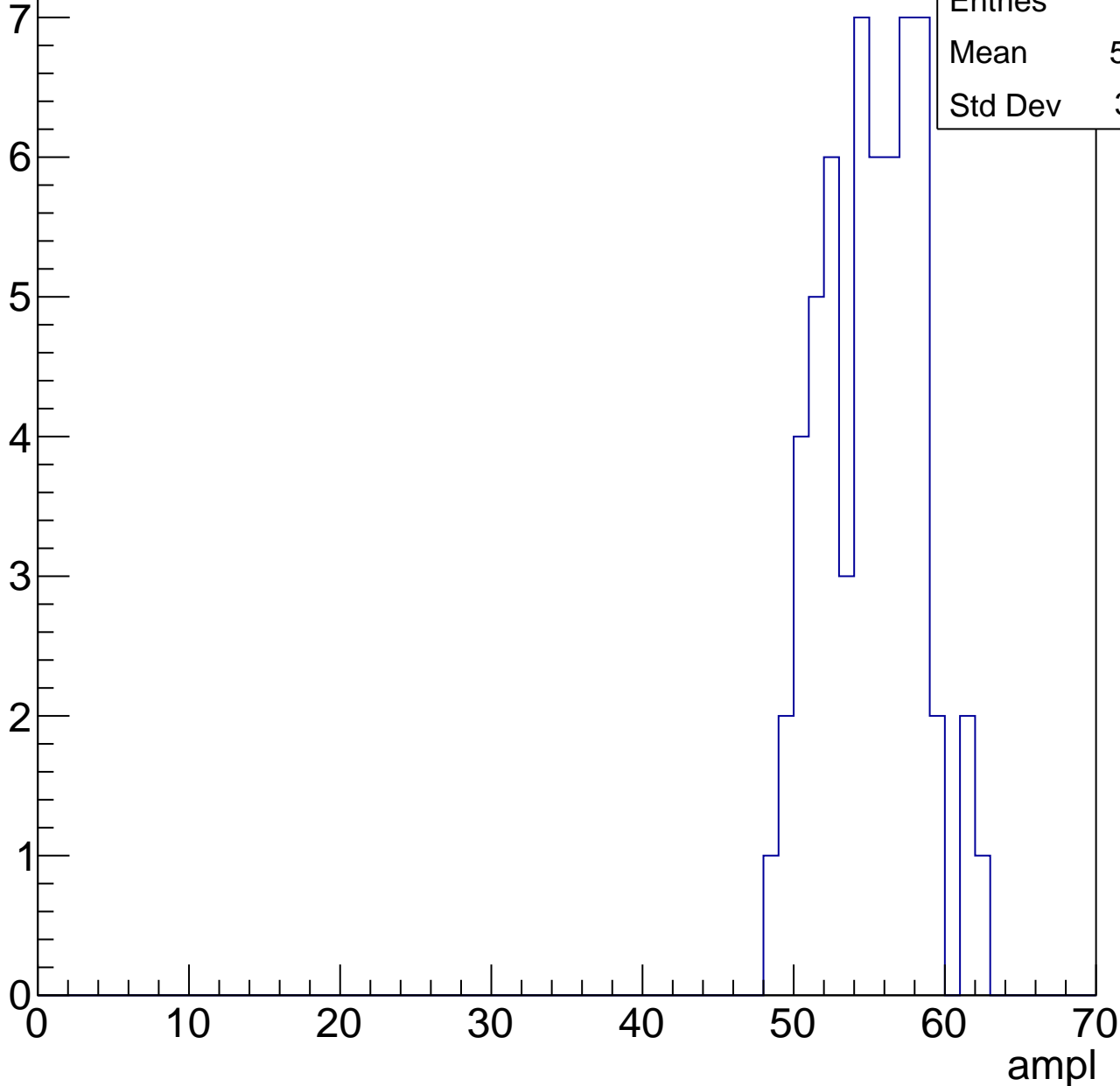


B1L101S, U4-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.63
Std Dev	3.231

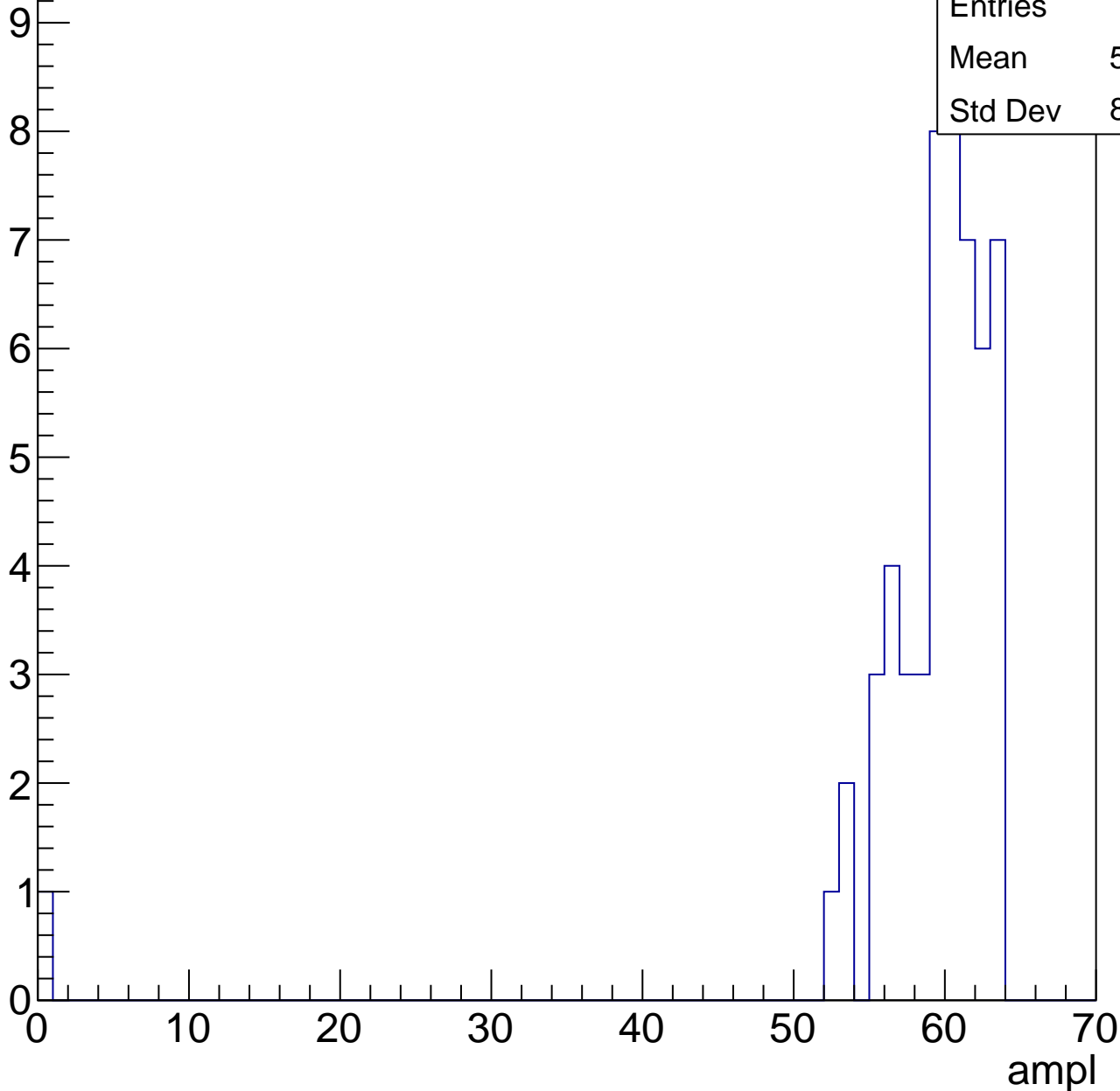


B1L101S, U4-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

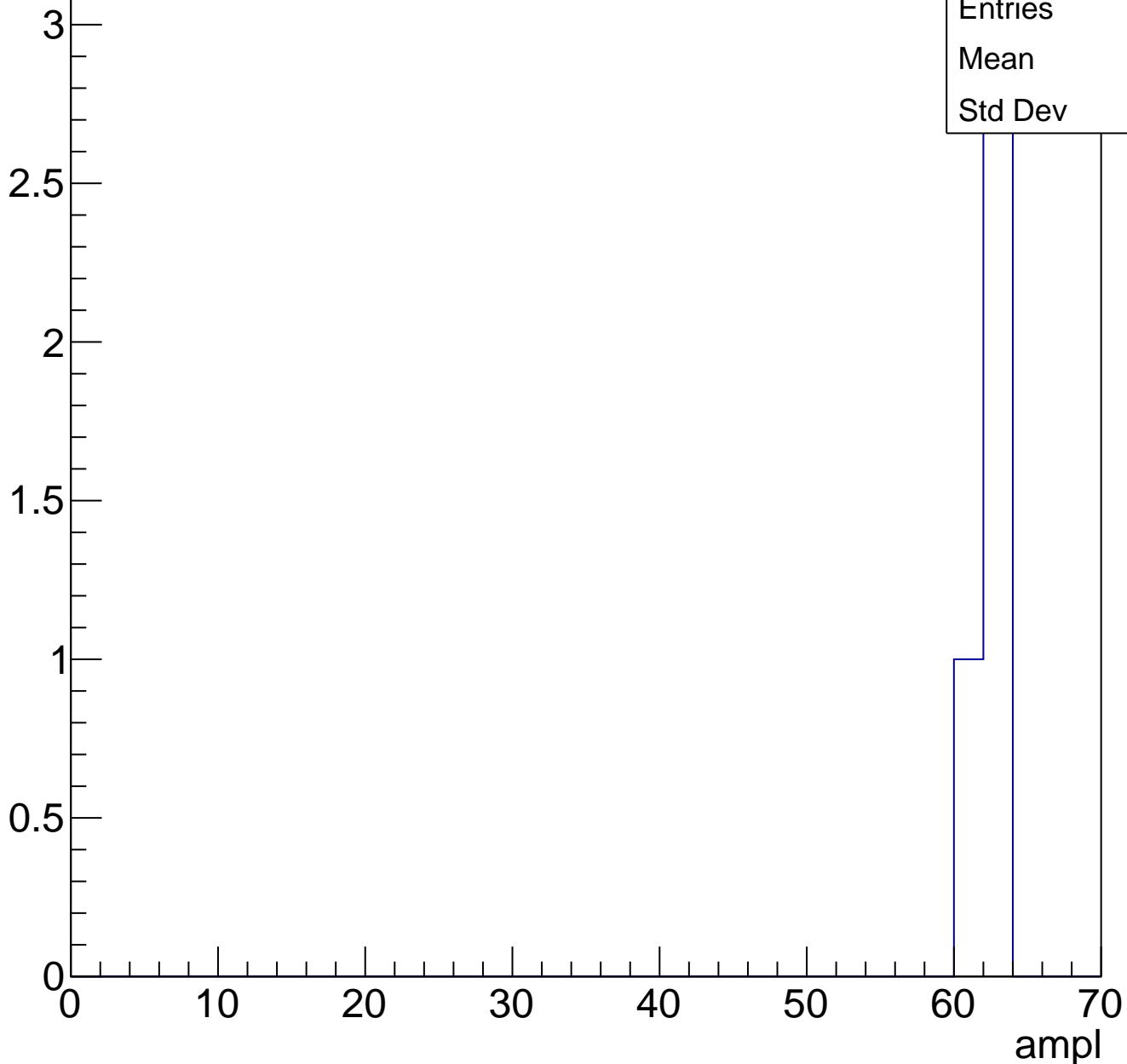
Entries	54
Mean	58.22
Std Dev	8.467



B1L101S, U4-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch26, adc0

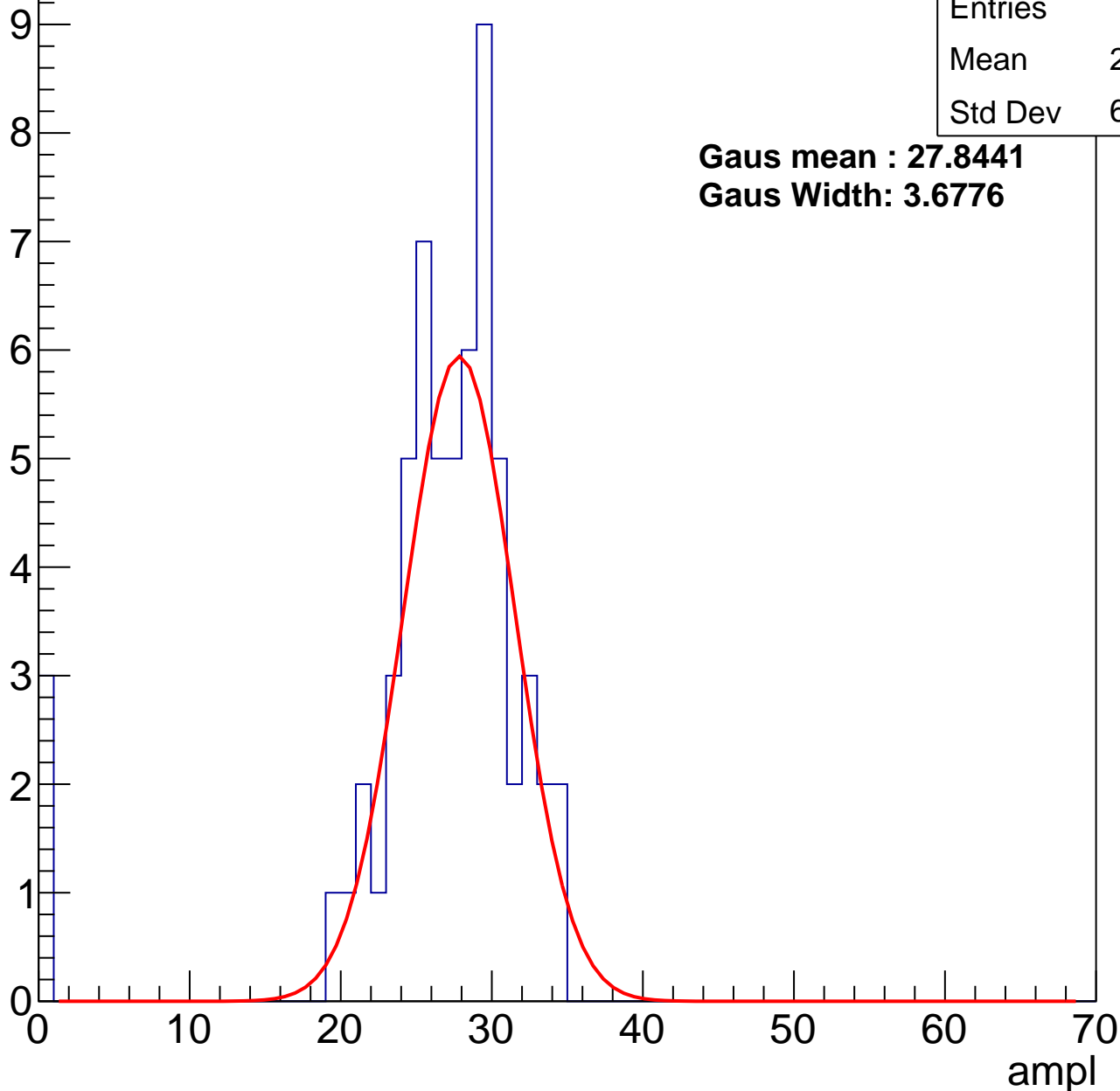
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	25.85
Std Dev	6.727

Gaus mean : 27.8441

Gaus Width: 3.6776



B1L101S, U4-ch26, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

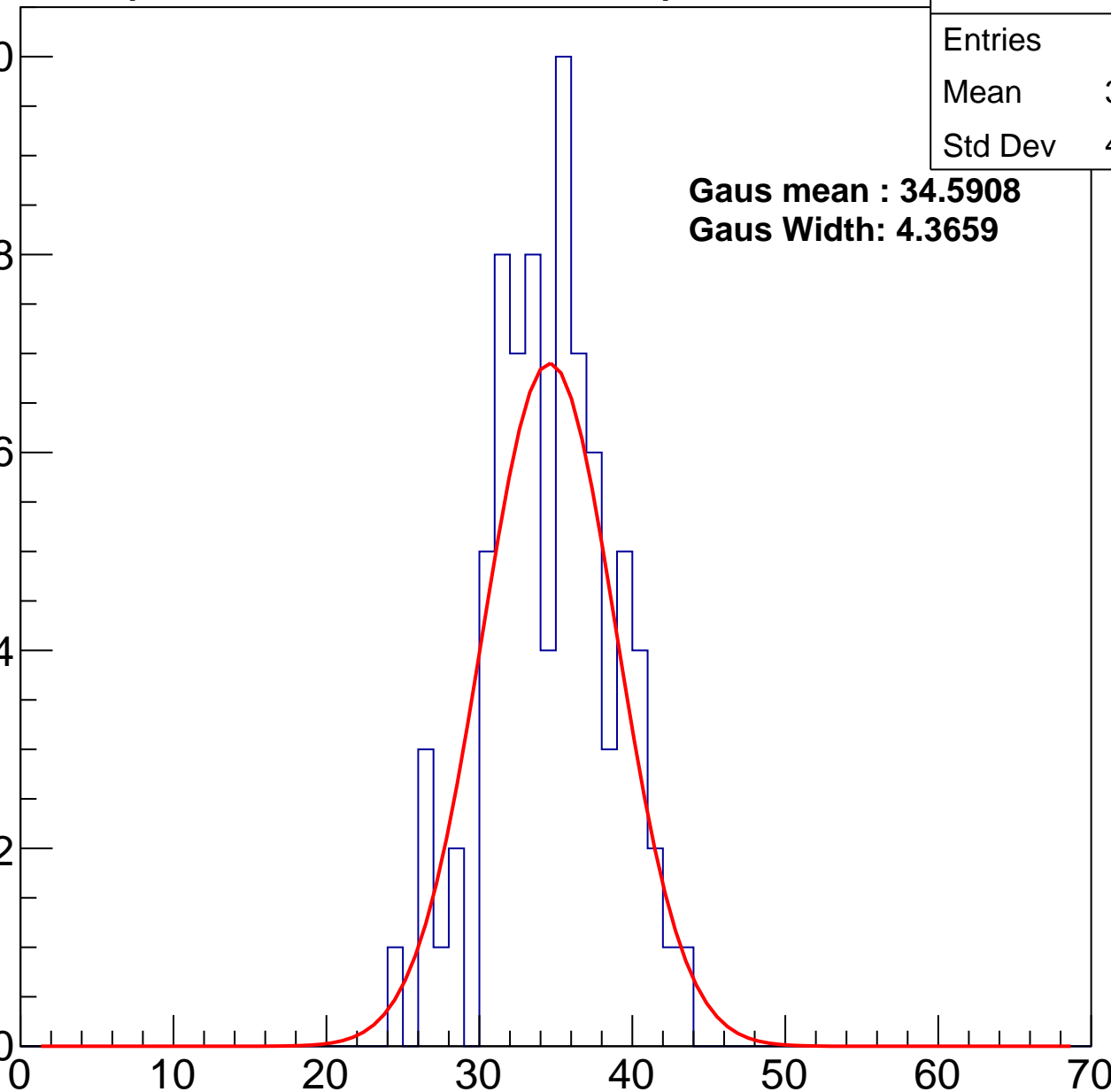
Entries	78
Mean	34.19
Std Dev	4.003

Gaus mean : 34.5908

Gaus Width: 4.3659

10
8
6
4
2
0

ampl



B1L101S, U4-ch26, adc2

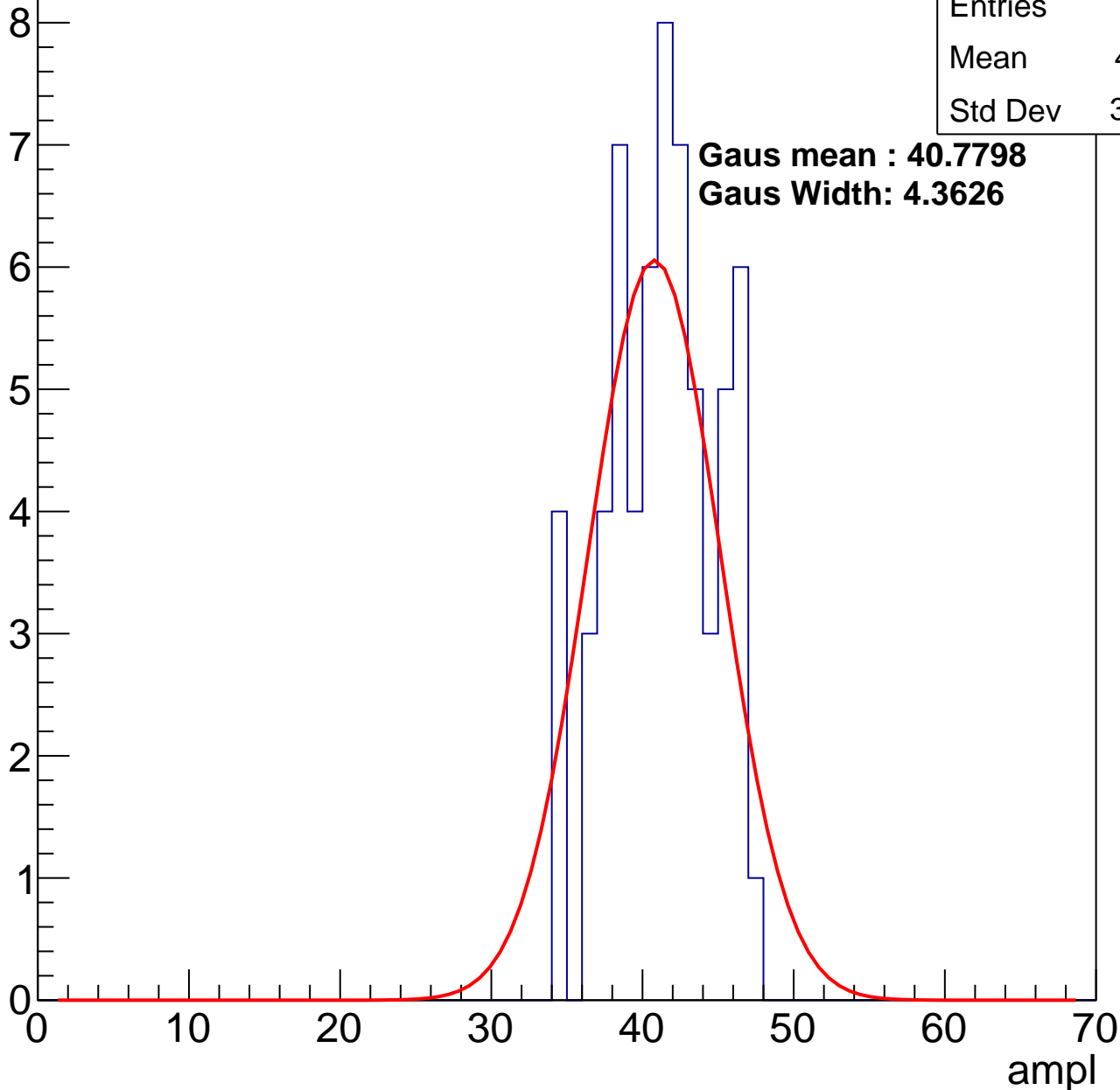
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	40.81
Std Dev	3.422

Gaus mean : 40.7798

Gaus Width: 4.3626

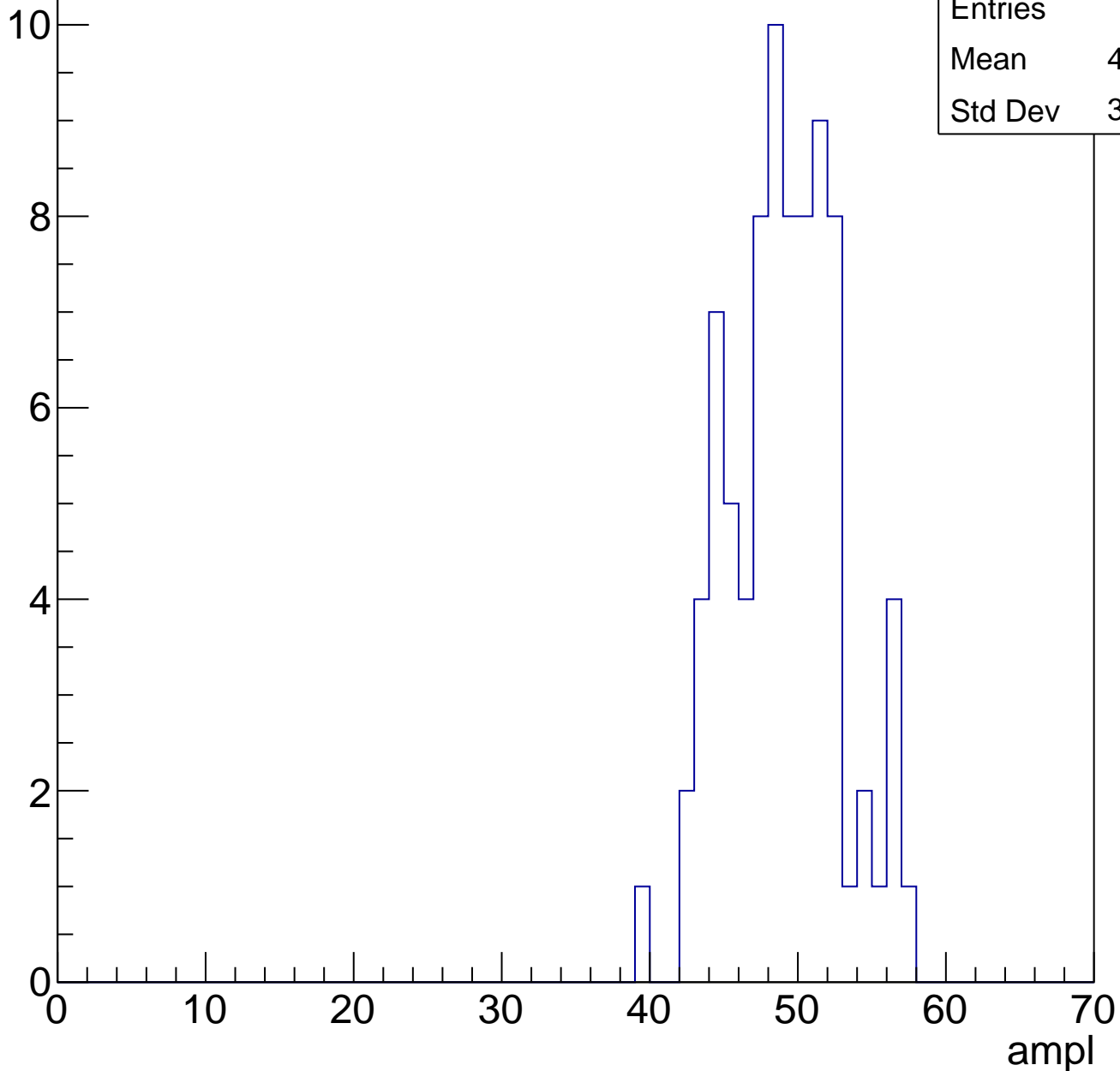


B1L101S, U4-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	48.58
Std Dev	3.703

Entry

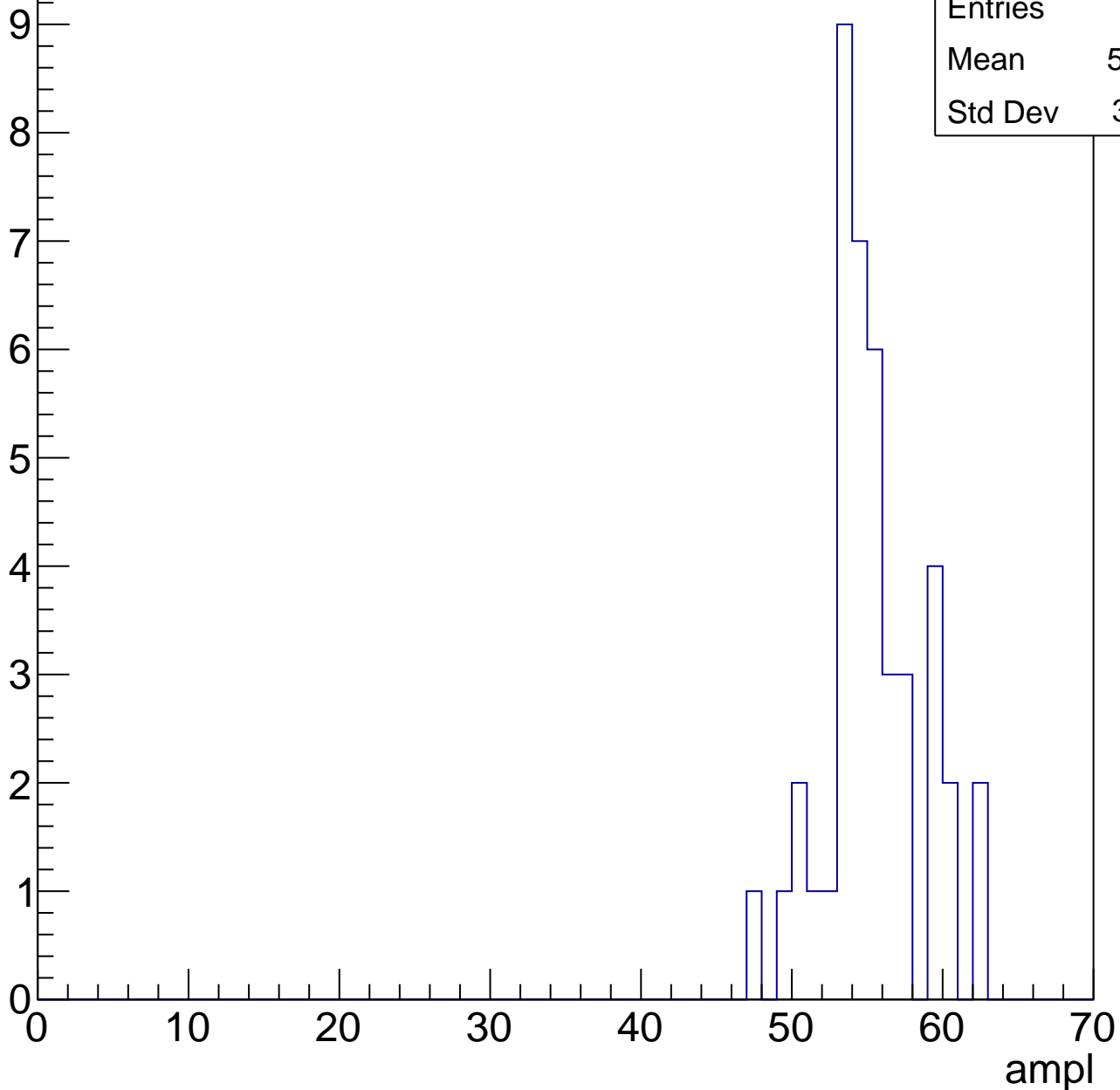


B1L101S, U4-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	54.83
Std Dev	3.251



B1L101S, U4-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

Entries 57

Mean 58.98

Std Dev 2.844

8

6

4

2

0

0

10

20

30

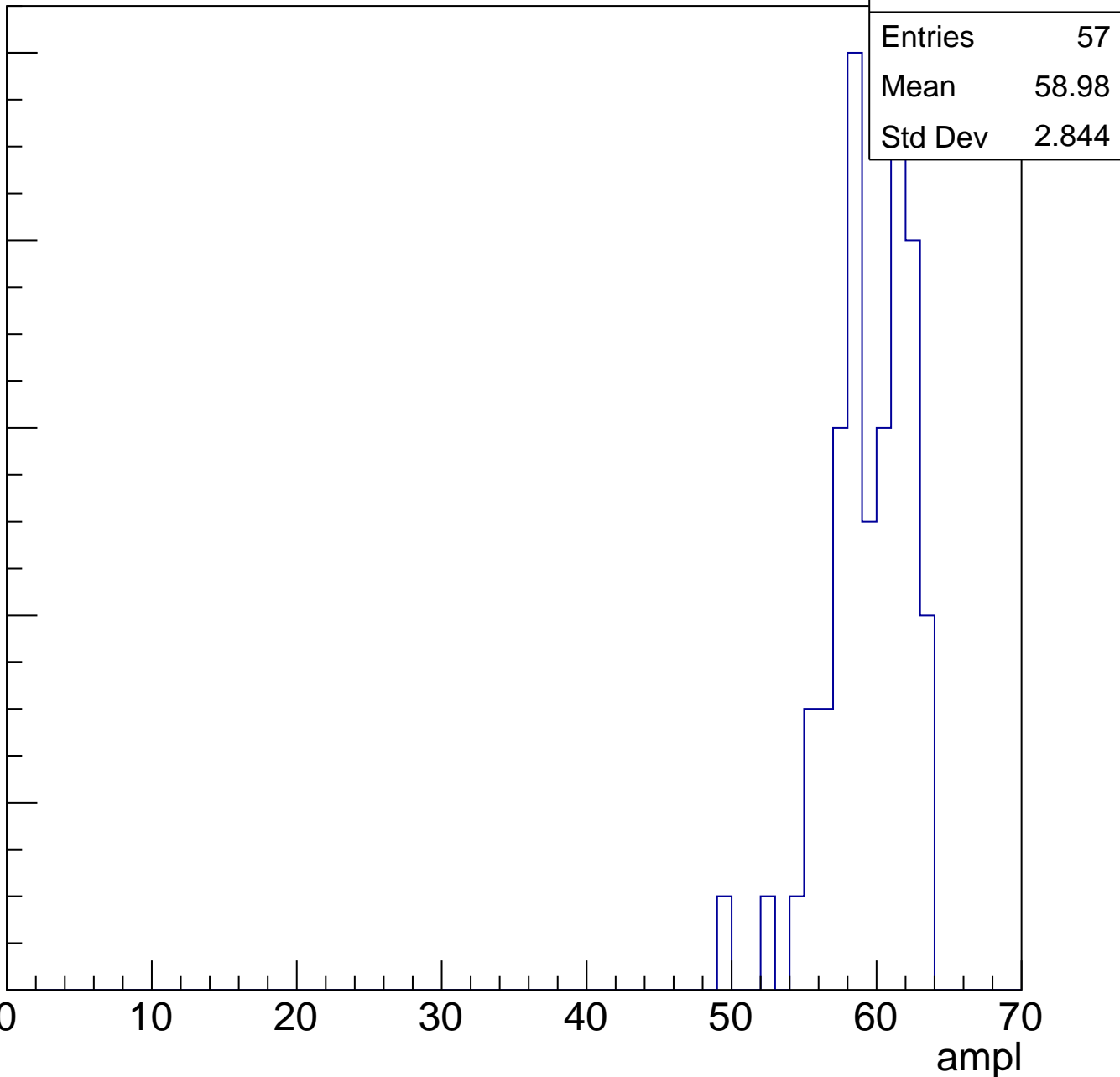
40

50

60

70

ampl

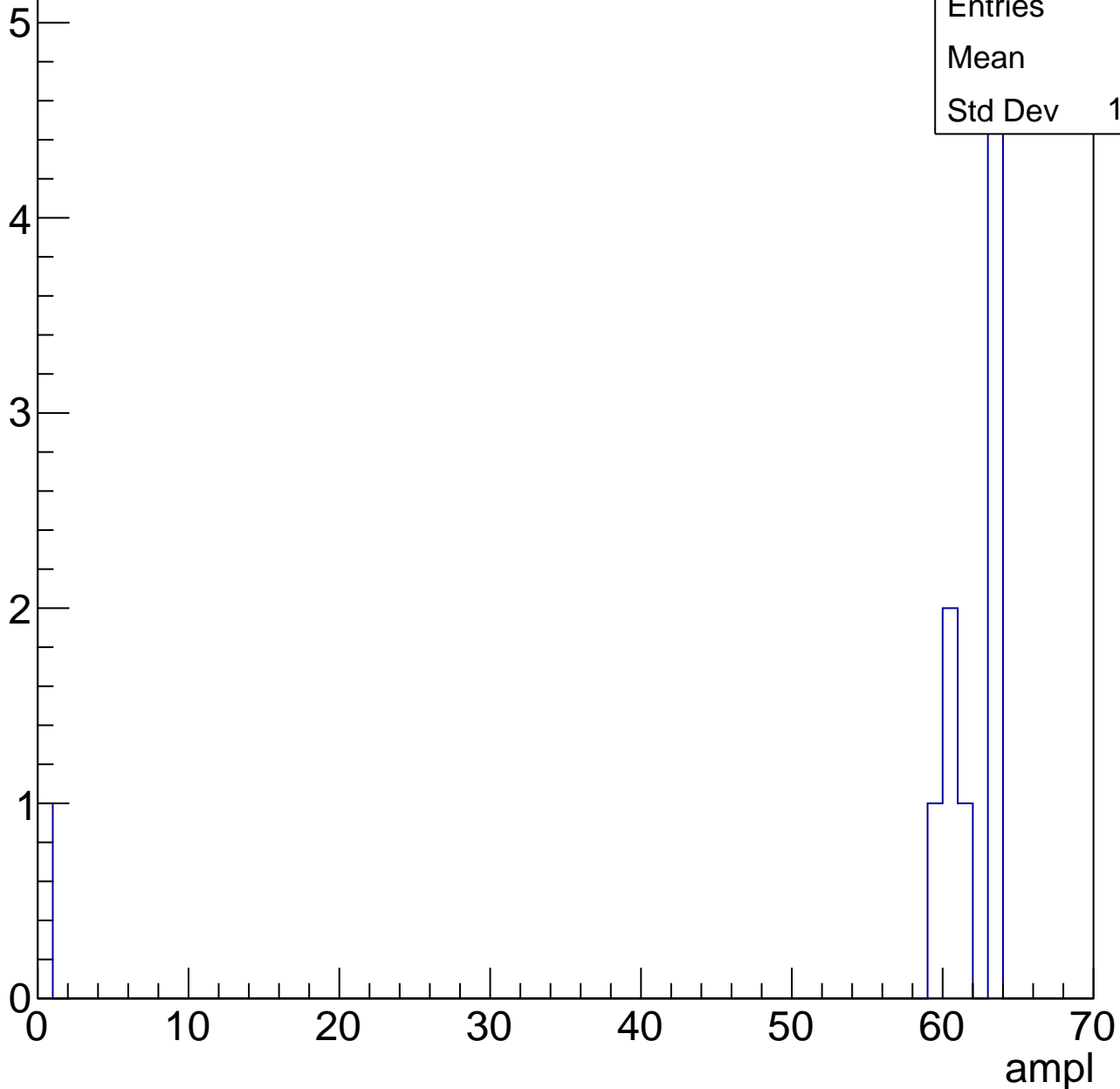


B1L101S, U4-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	55.5
Std Dev	18.56



B1L101S, U4-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U4-ch27, adc0

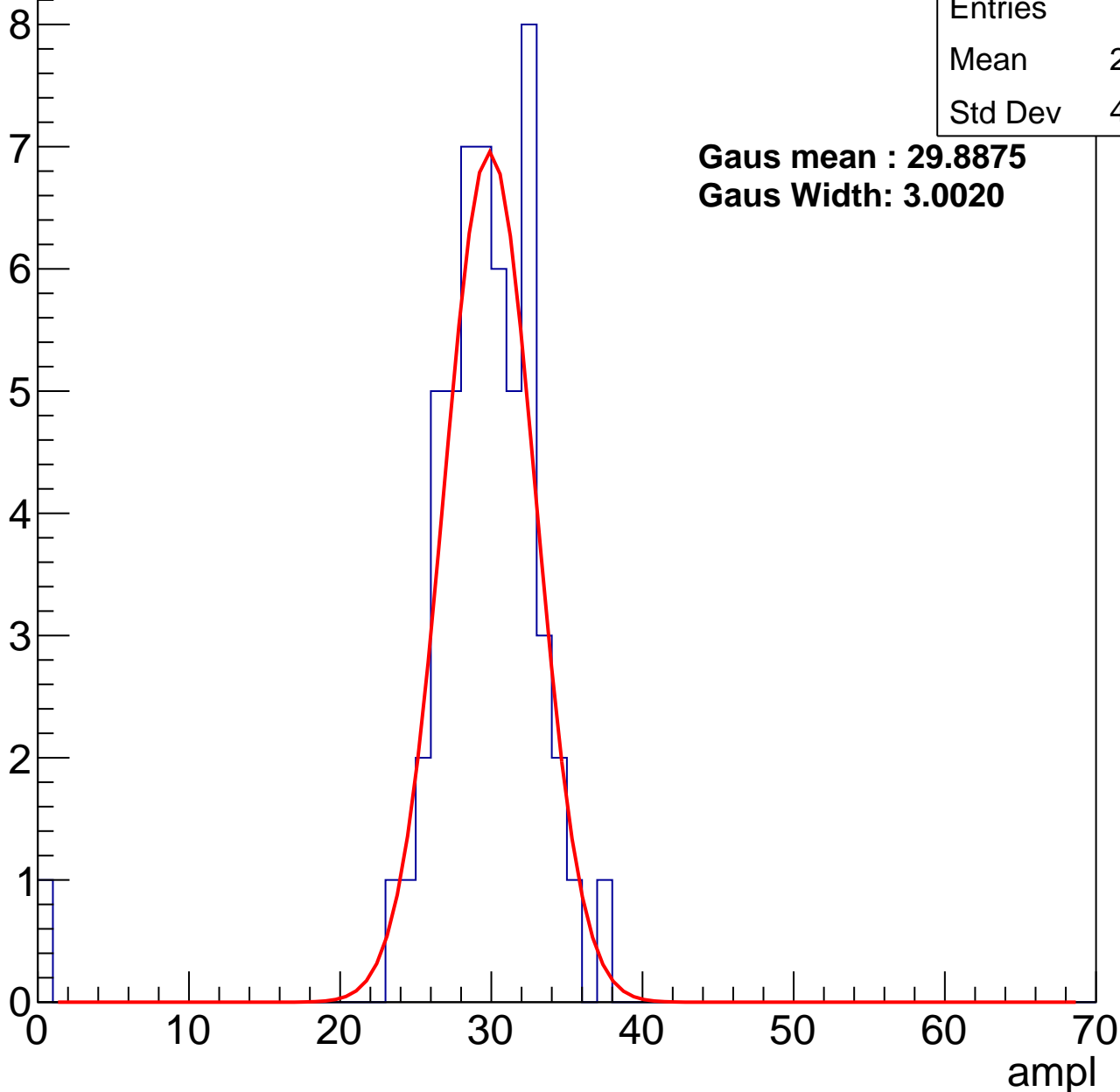
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.93
Std Dev	4.865

Gaus mean : 29.8875

Gaus Width: 3.0020



B1L101S, U4-ch27, adc1

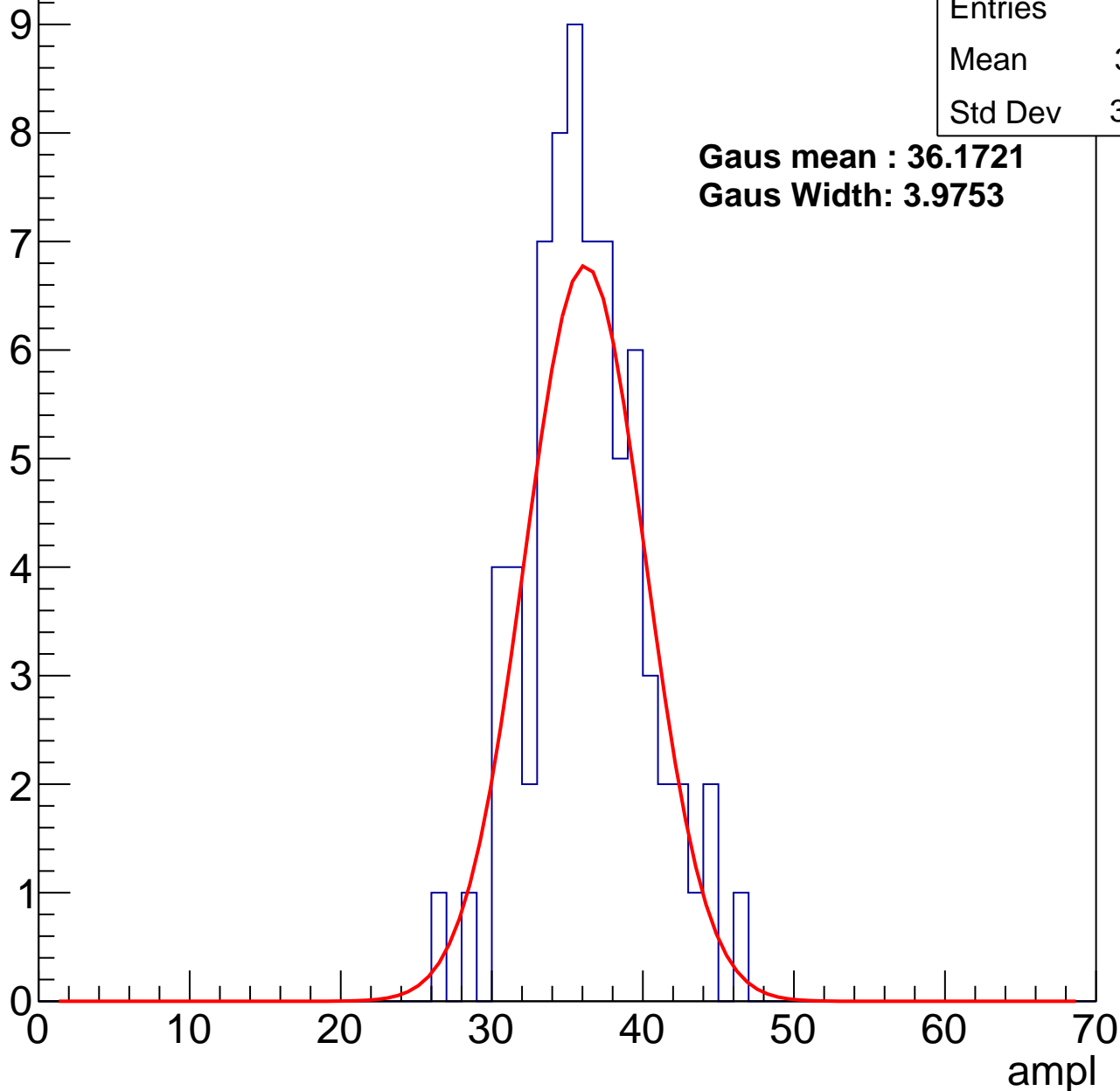
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	35.81
Std Dev	3.854

Gaus mean : 36.1721

Gaus Width: 3.9753



B1L101S, U4-ch27, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

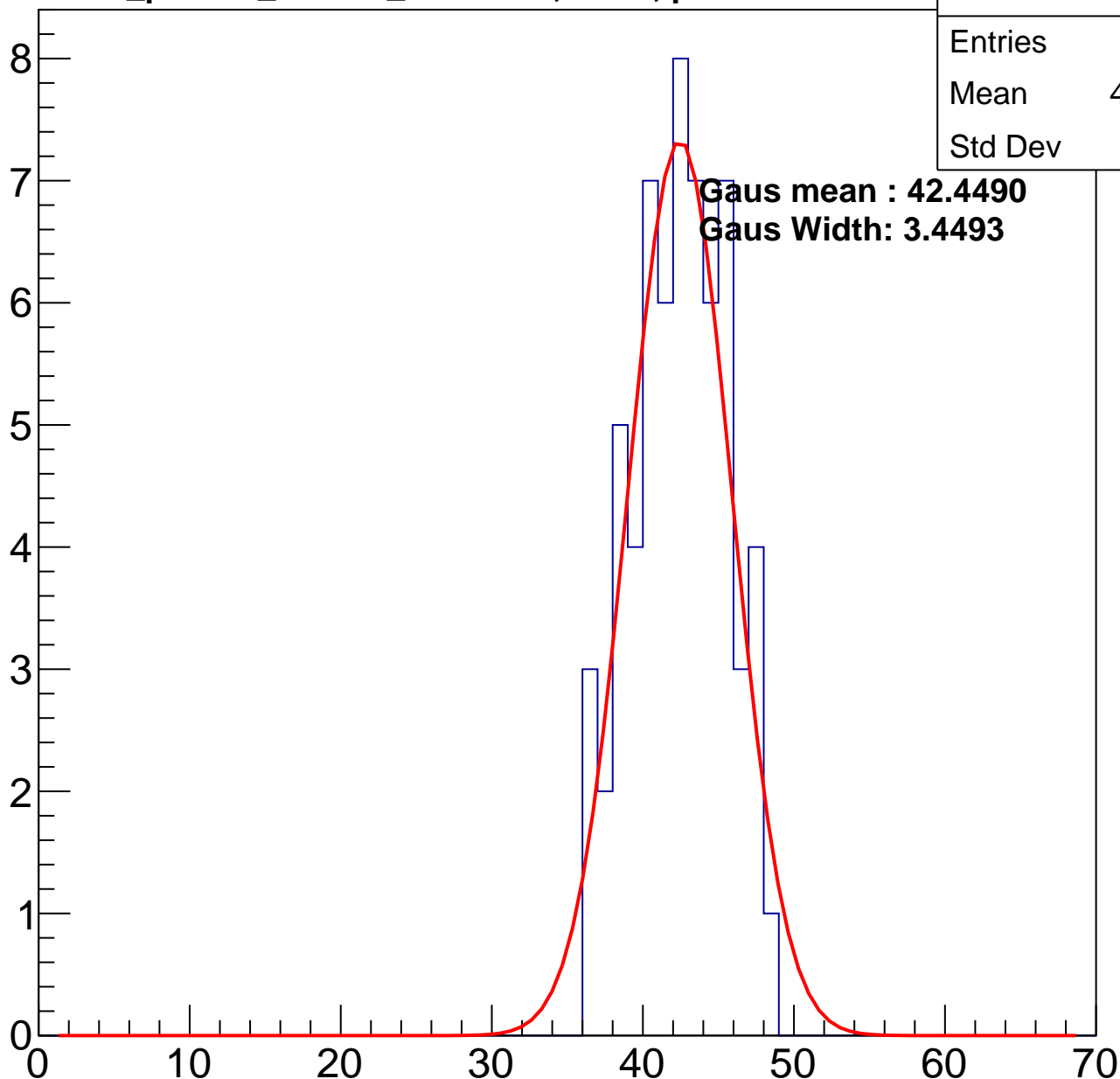
8
7
6
5
4
3
2
1
0

Entries	63
Mean	41.97
Std Dev	3.05

Gaus mean : 42.4490

Gaus Width: 3.4493

ampl

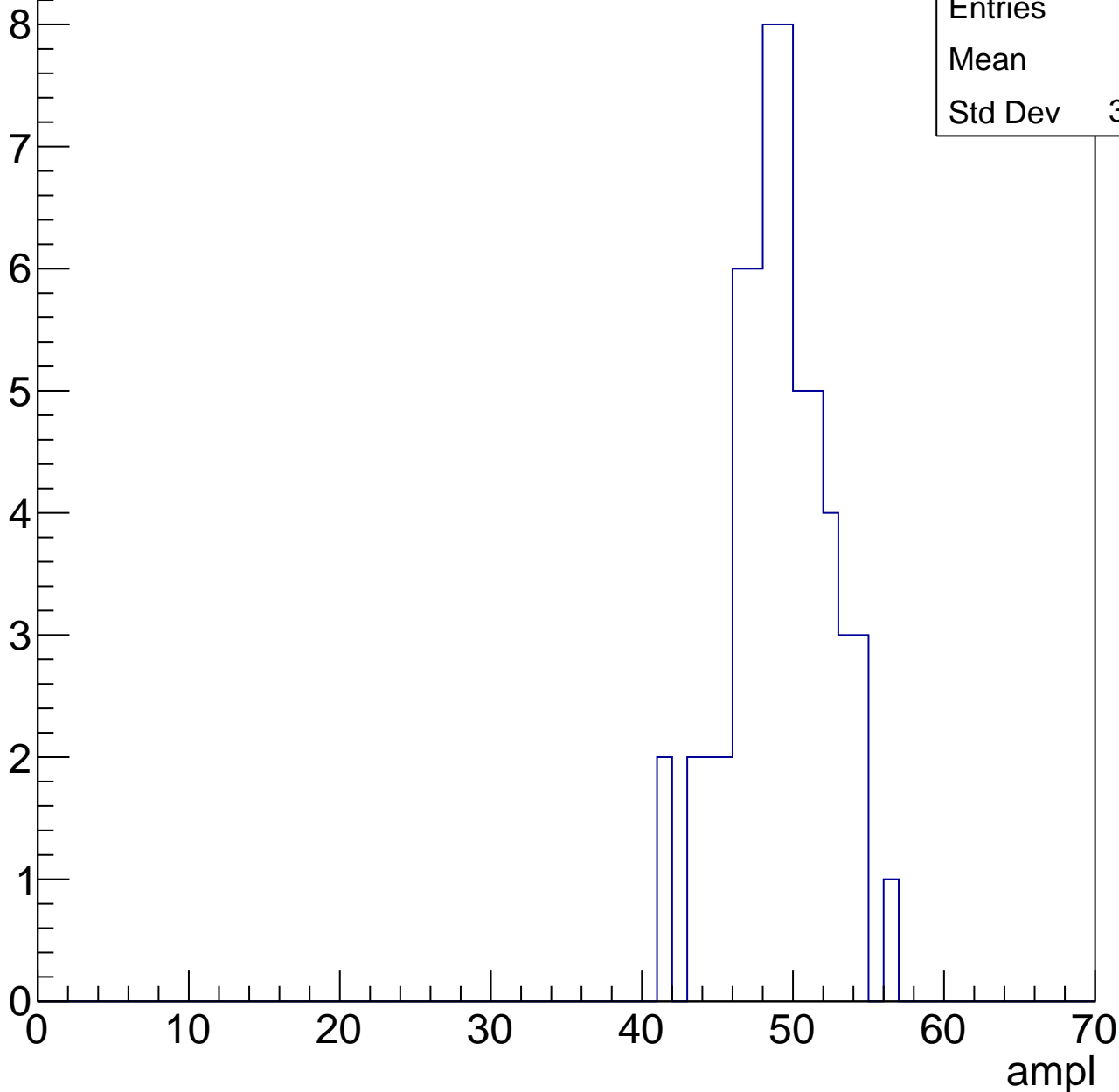


B1L101S, U4-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

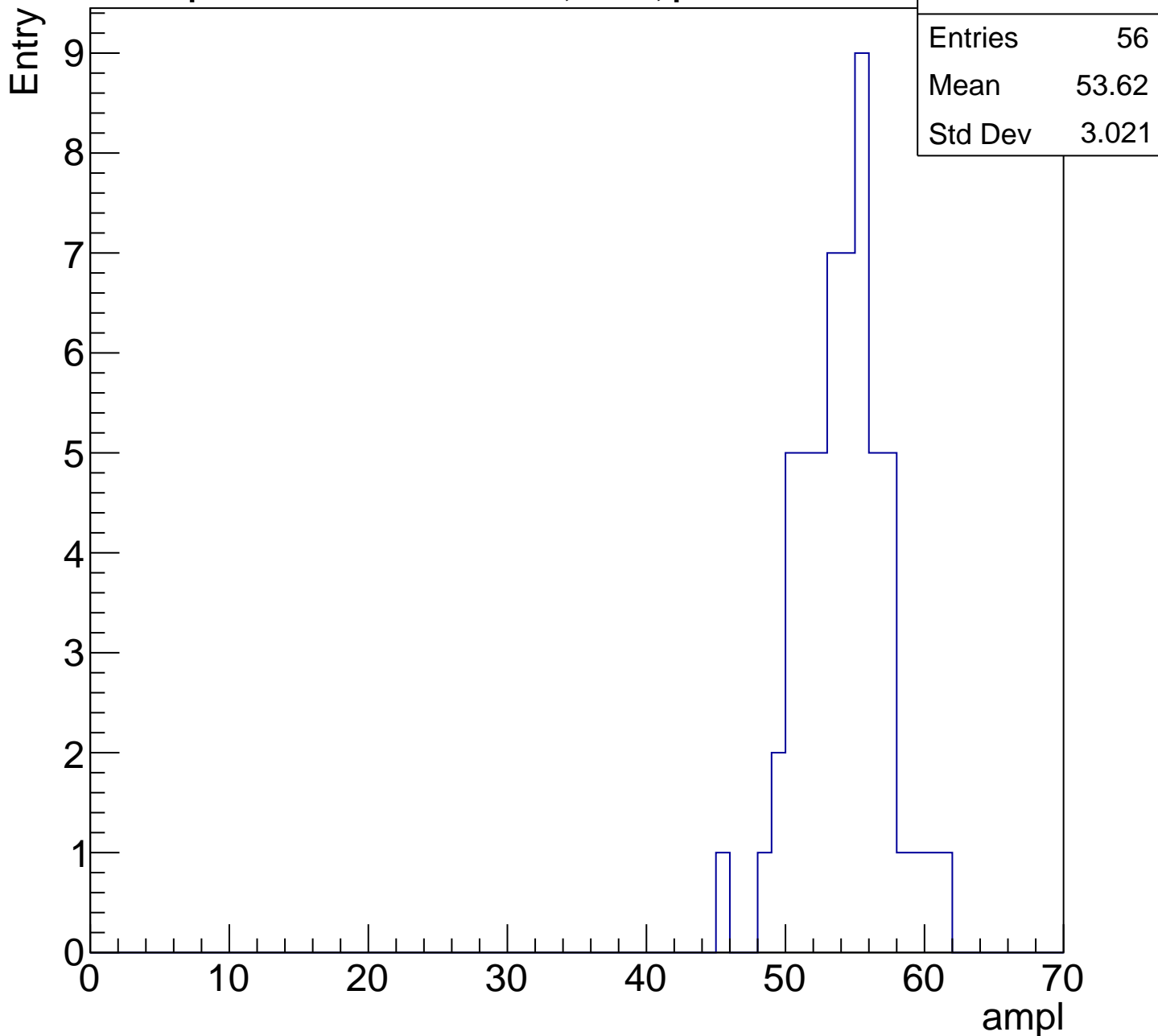
Entry

Entries	57
Mean	48.6
Std Dev	3.222



B1L101S, U4-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch27, adc5

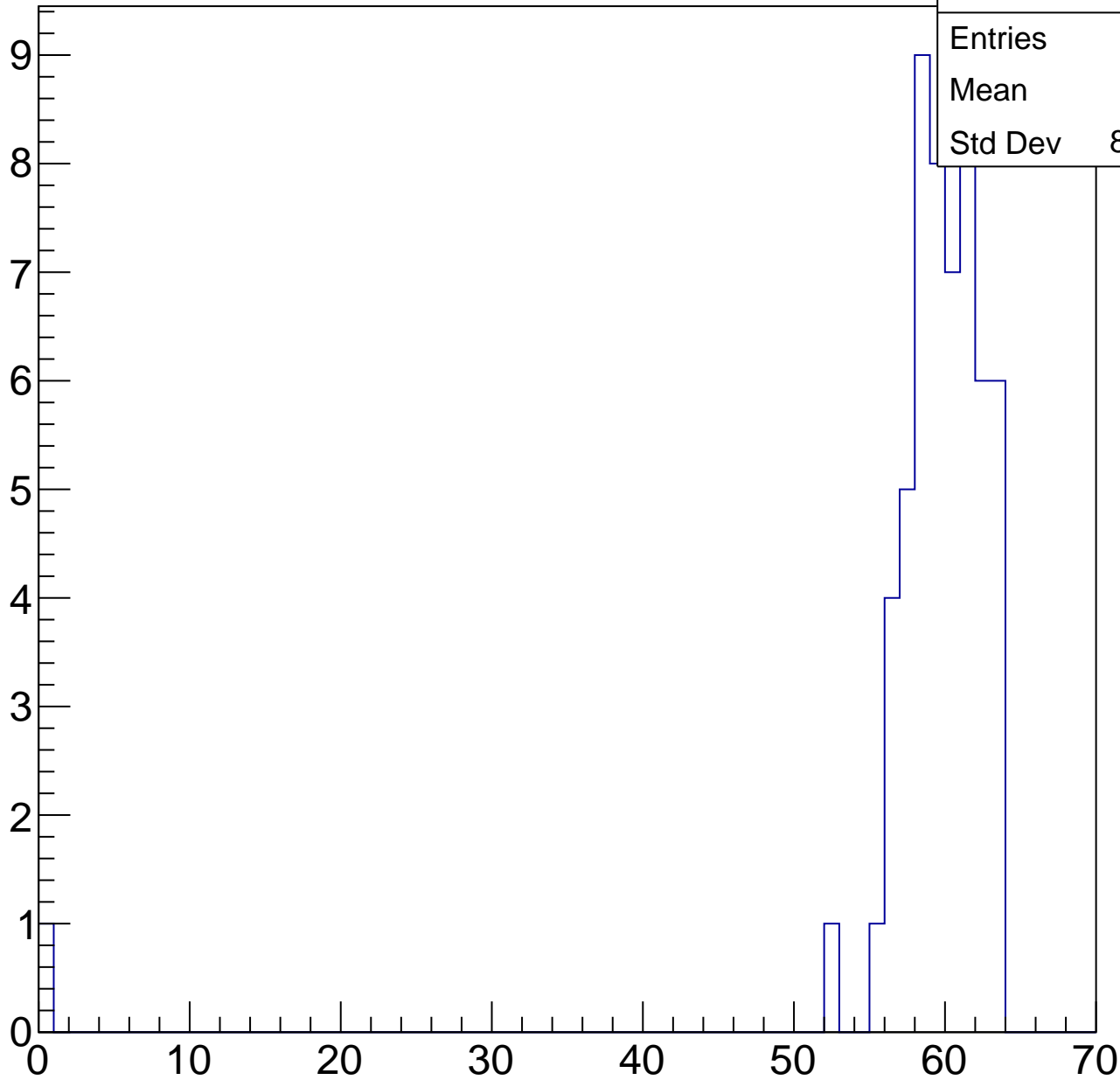
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.4
Std Dev	8.147

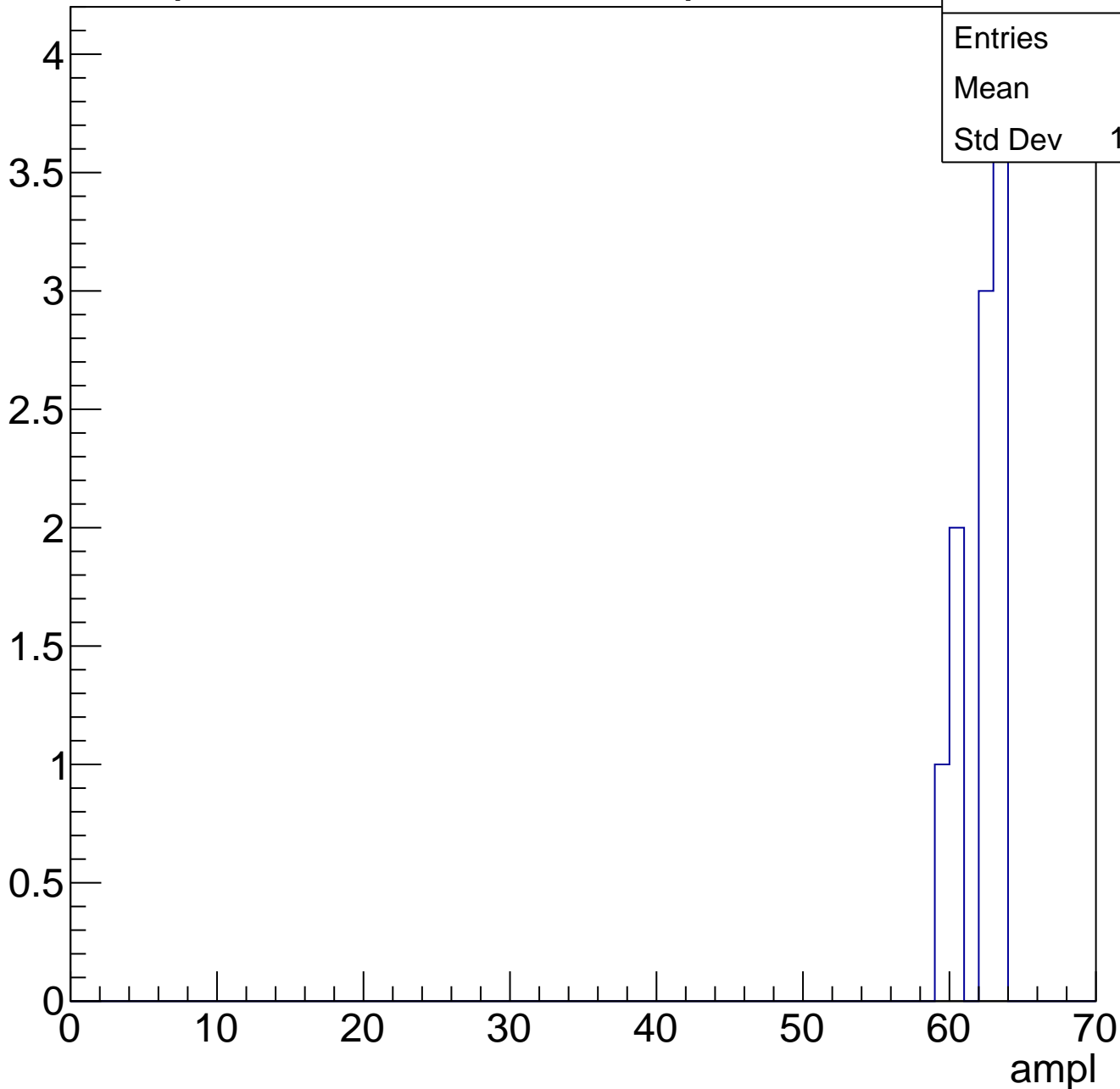
ampl



B1L101S, U4-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch28, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	27.46
Std Dev	3.308

Gaus mean : 28.1163

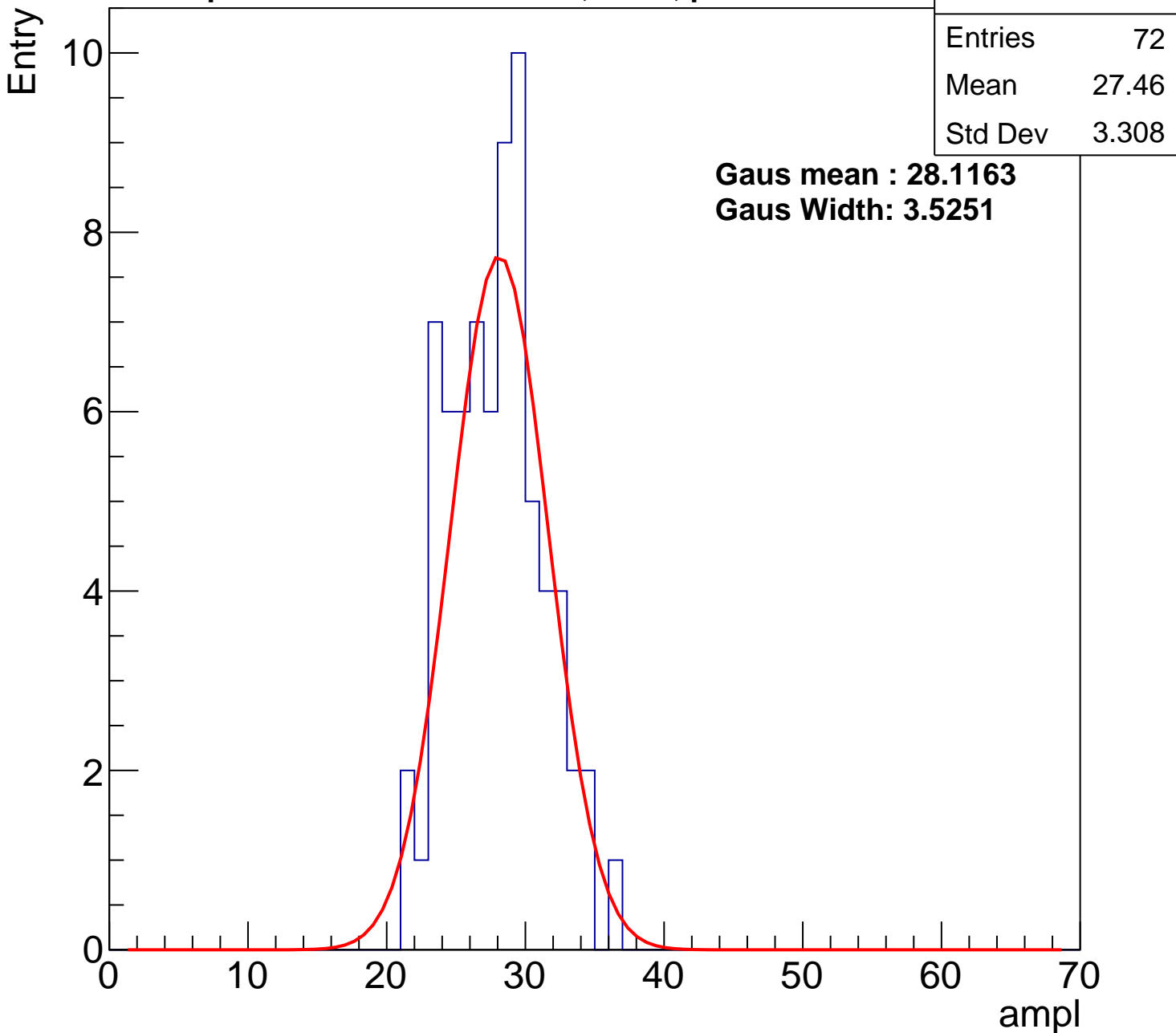
Gaus Width: 3.5251

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch28, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	97
Mean	35.72
Std Dev	4.119

Gaus mean : 35.7498

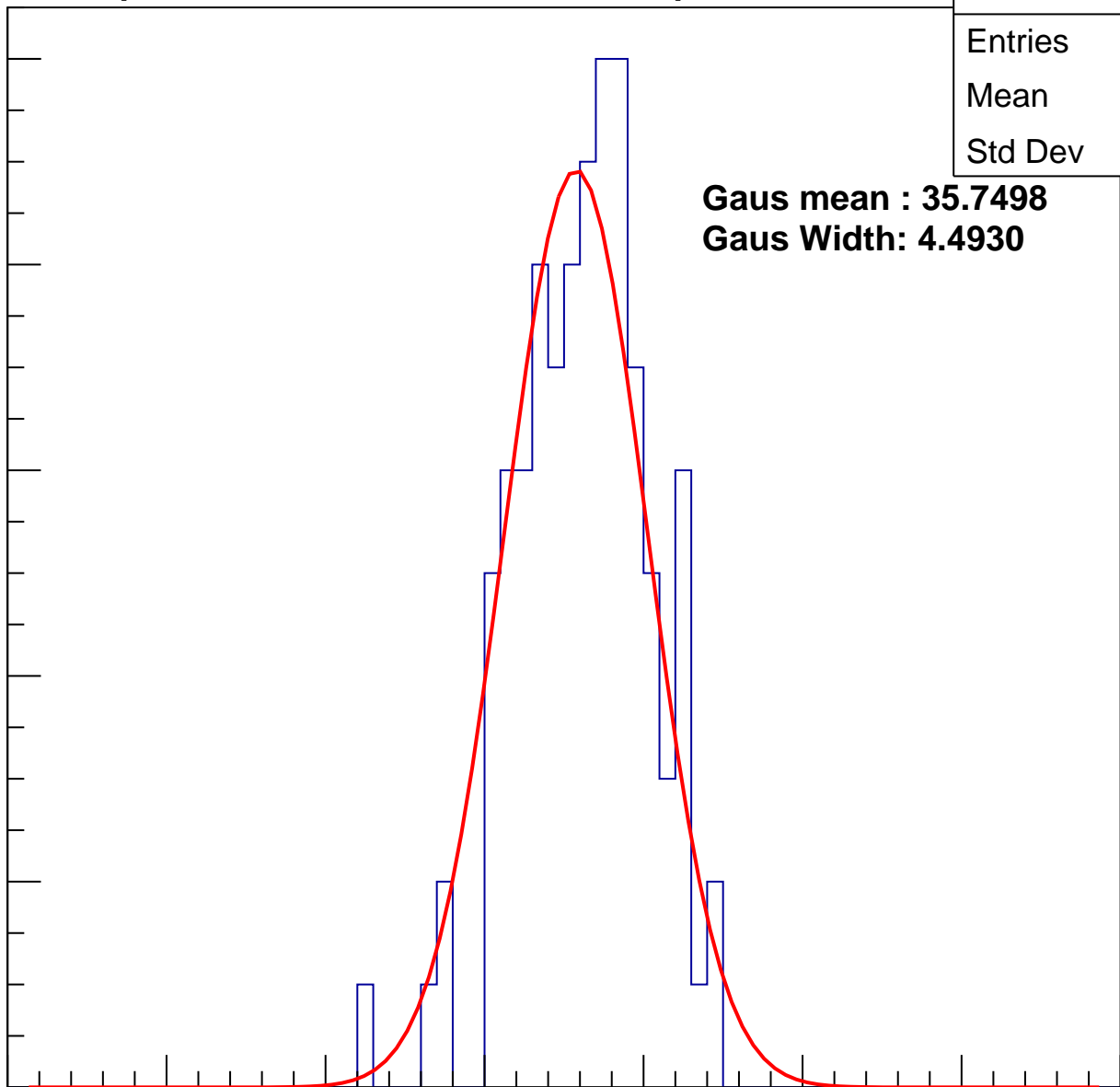
Gaus Width: 4.4930

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch28, adc2

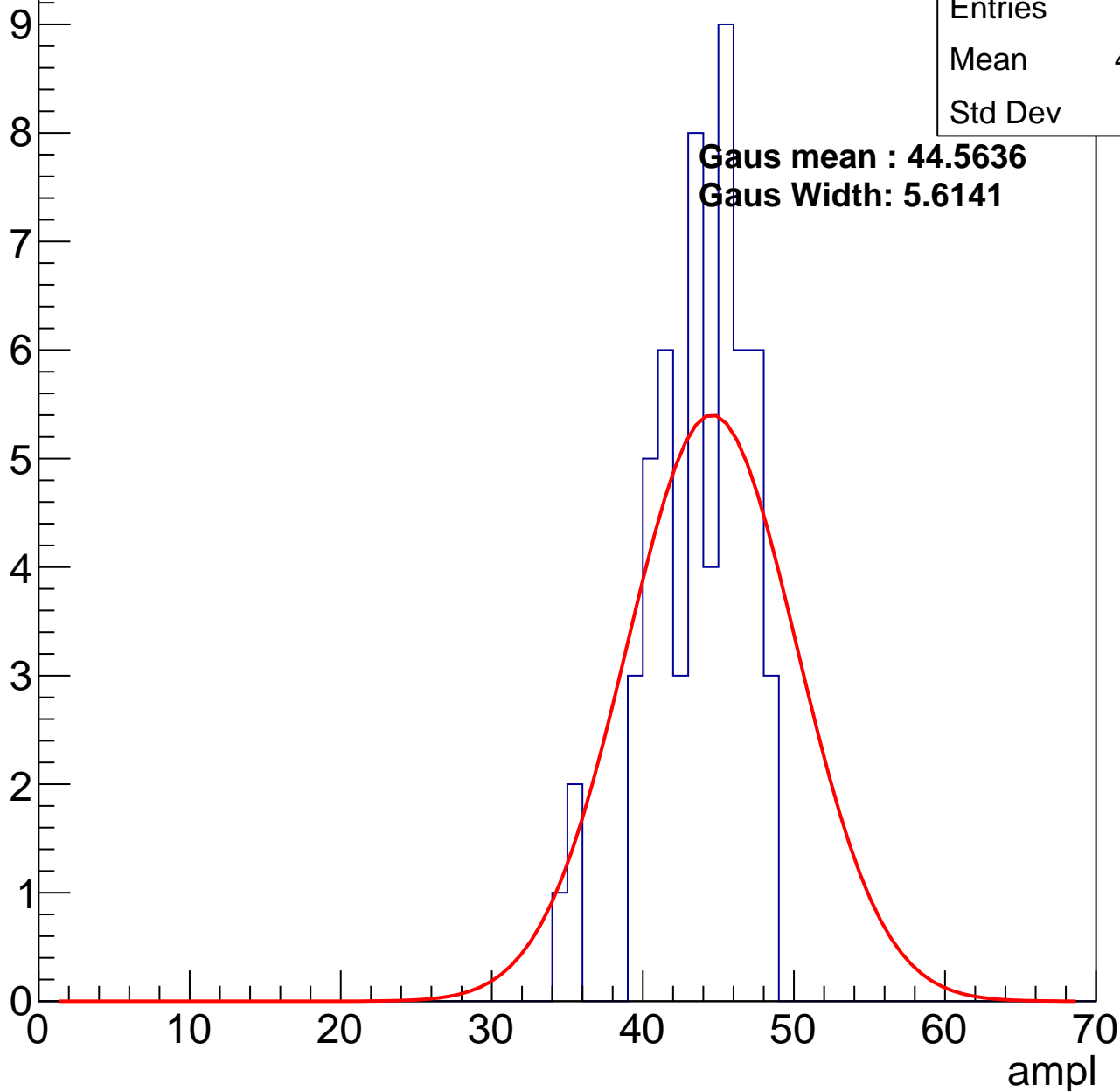
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.21
Std Dev	3.25

Gaus mean : 44.5636

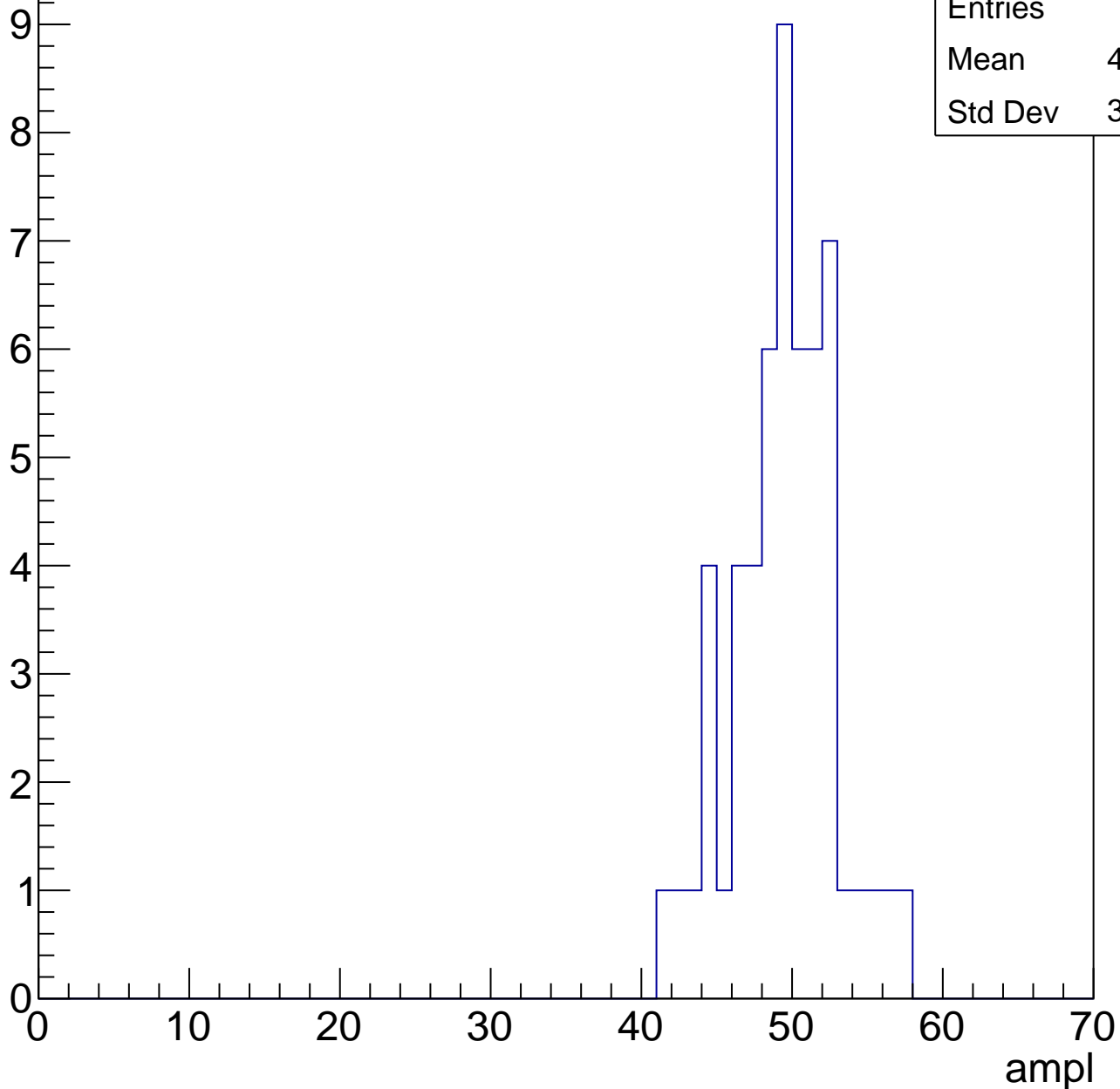
Gaus Width: 5.6141



B1L101S, U4-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



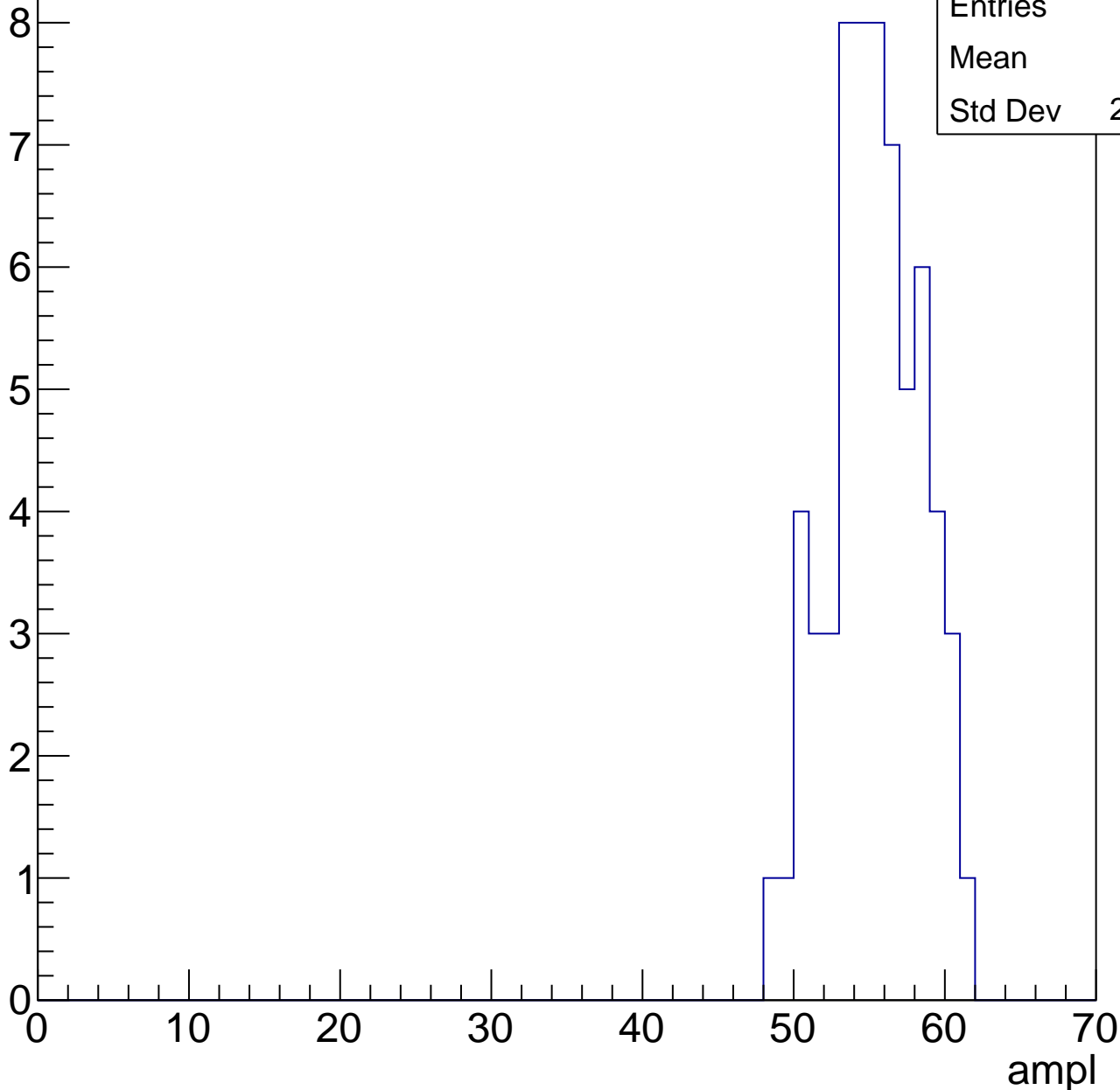
Entries	55
Mean	48.96
Std Dev	3.319

B1L101S, U4-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.9
Std Dev	2.993

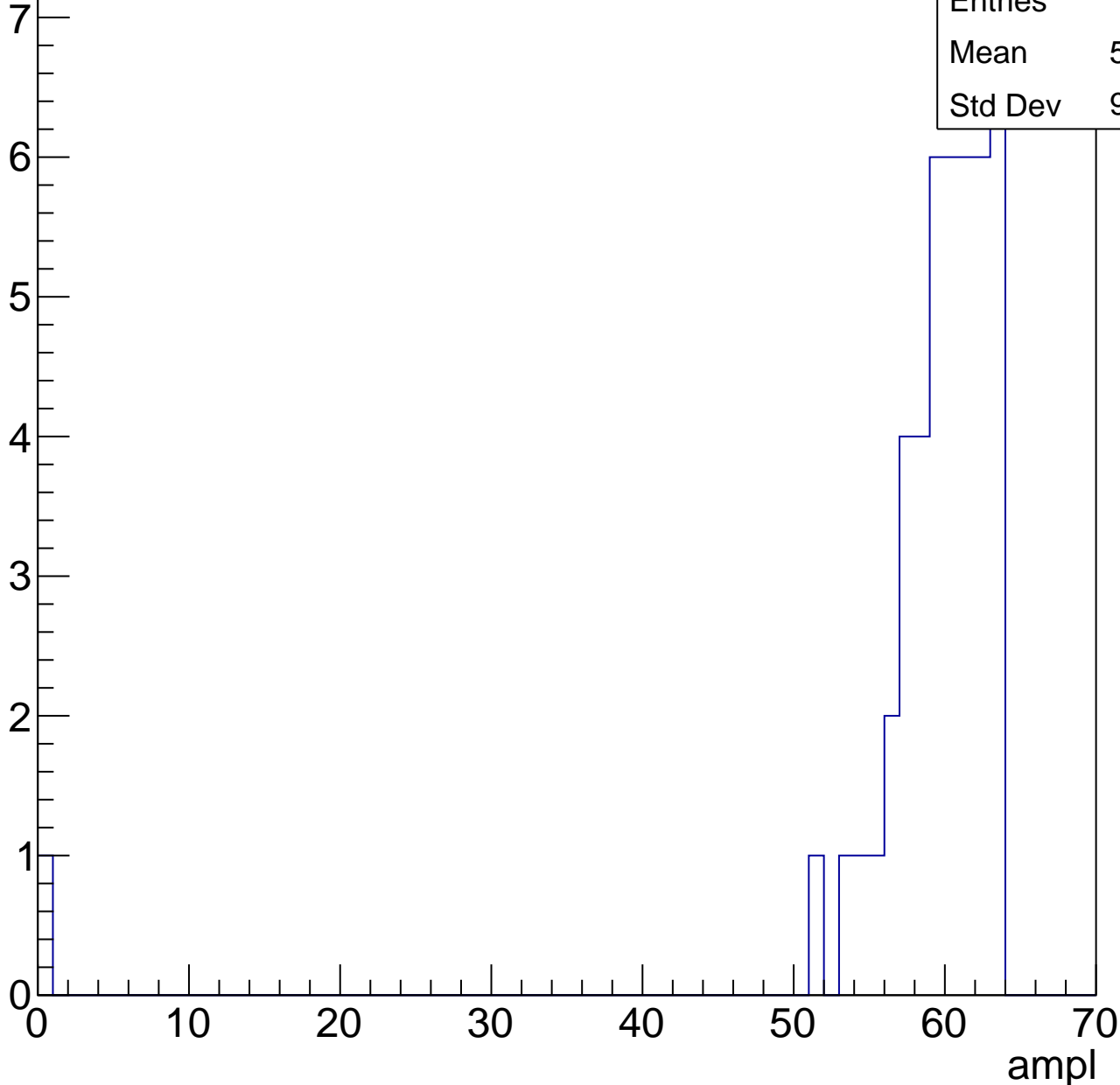


B1L101S, U4-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

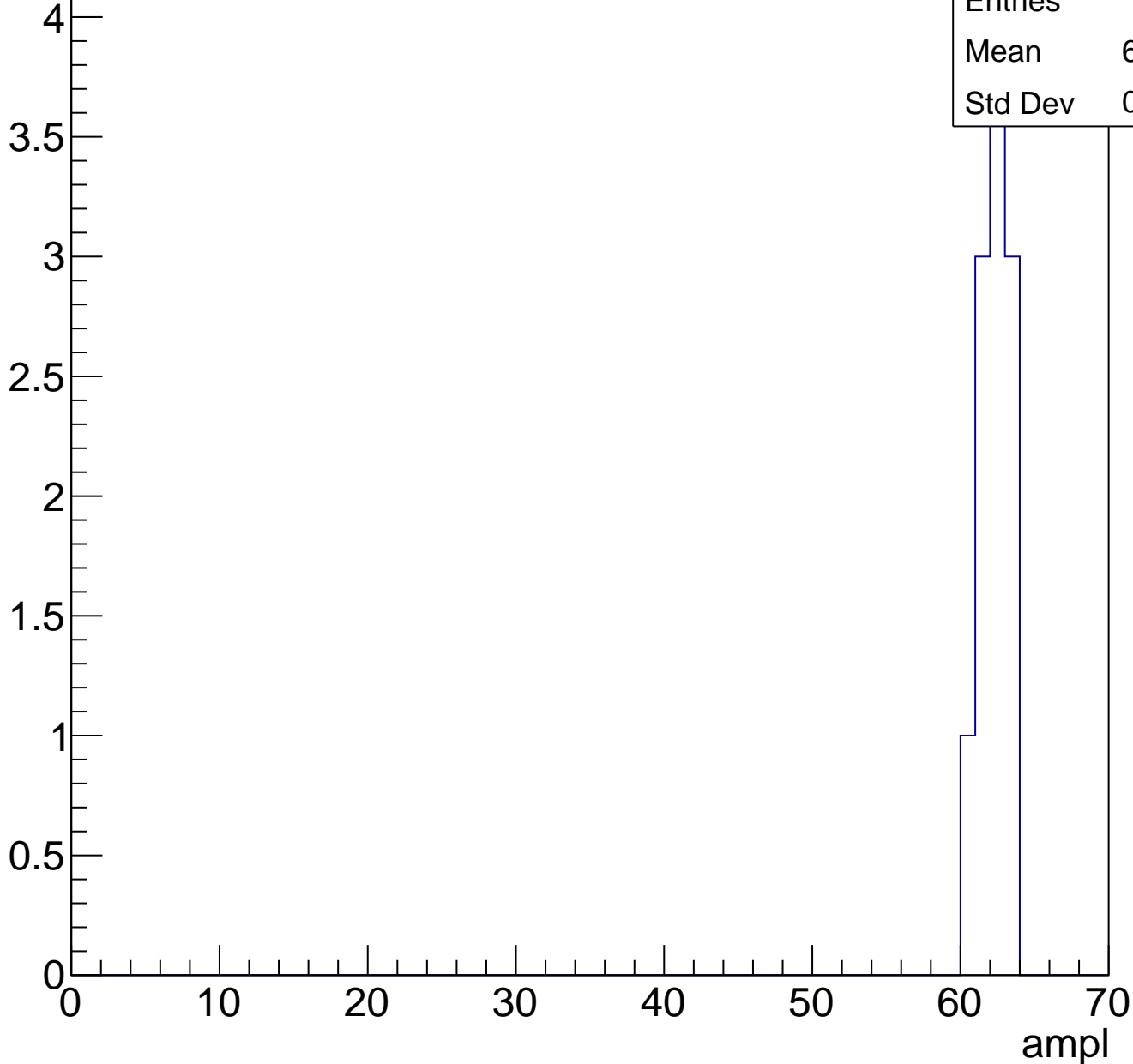
Entries	46
Mean	58.22
Std Dev	9.122



B1L101S, U4-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L101S, U4-ch29, adc0

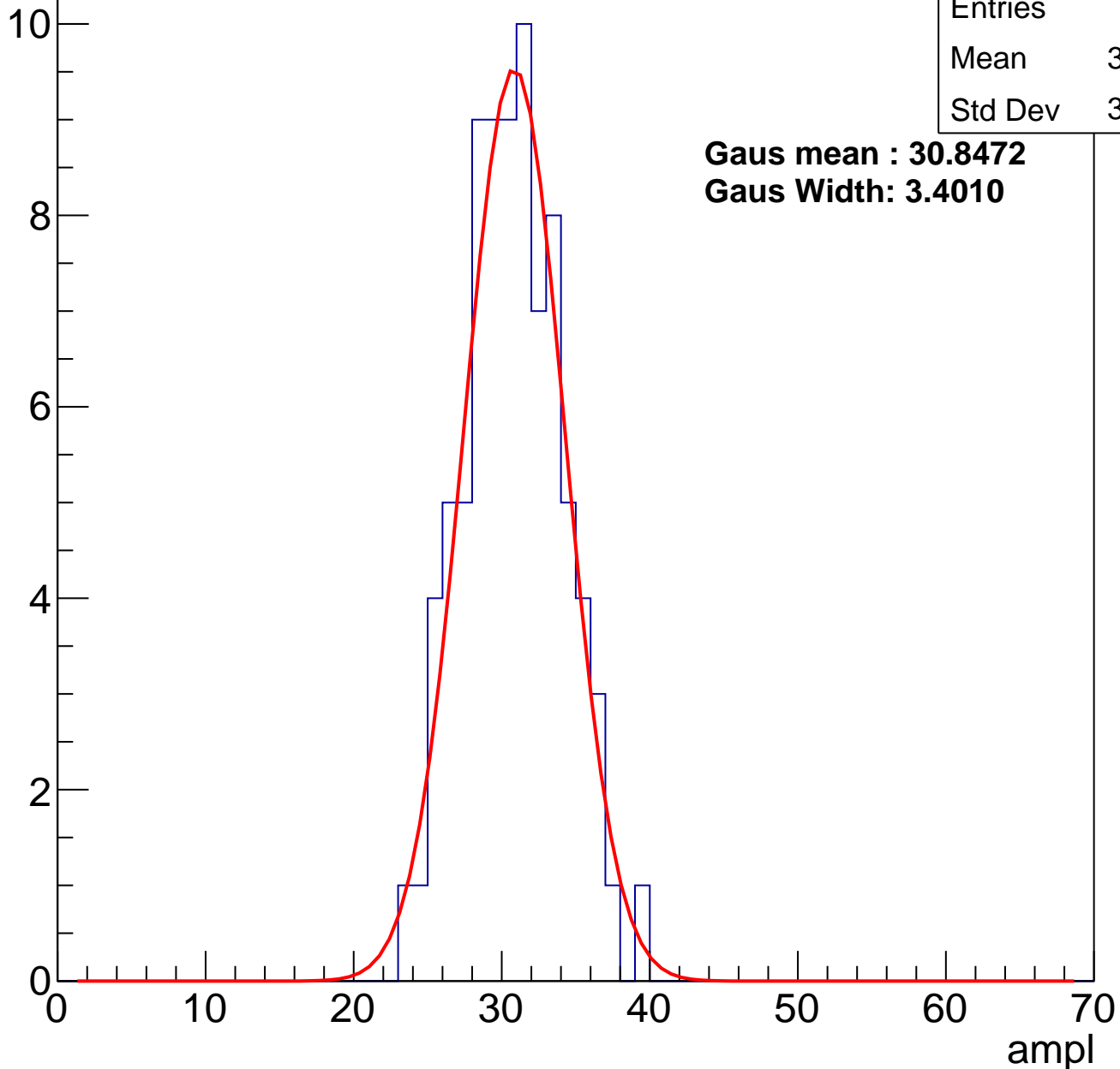
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	30.33
Std Dev	3.265

Gaus mean : 30.8472

Gaus Width: 3.4010

Entry



B1L101S, U4-ch29, adc1

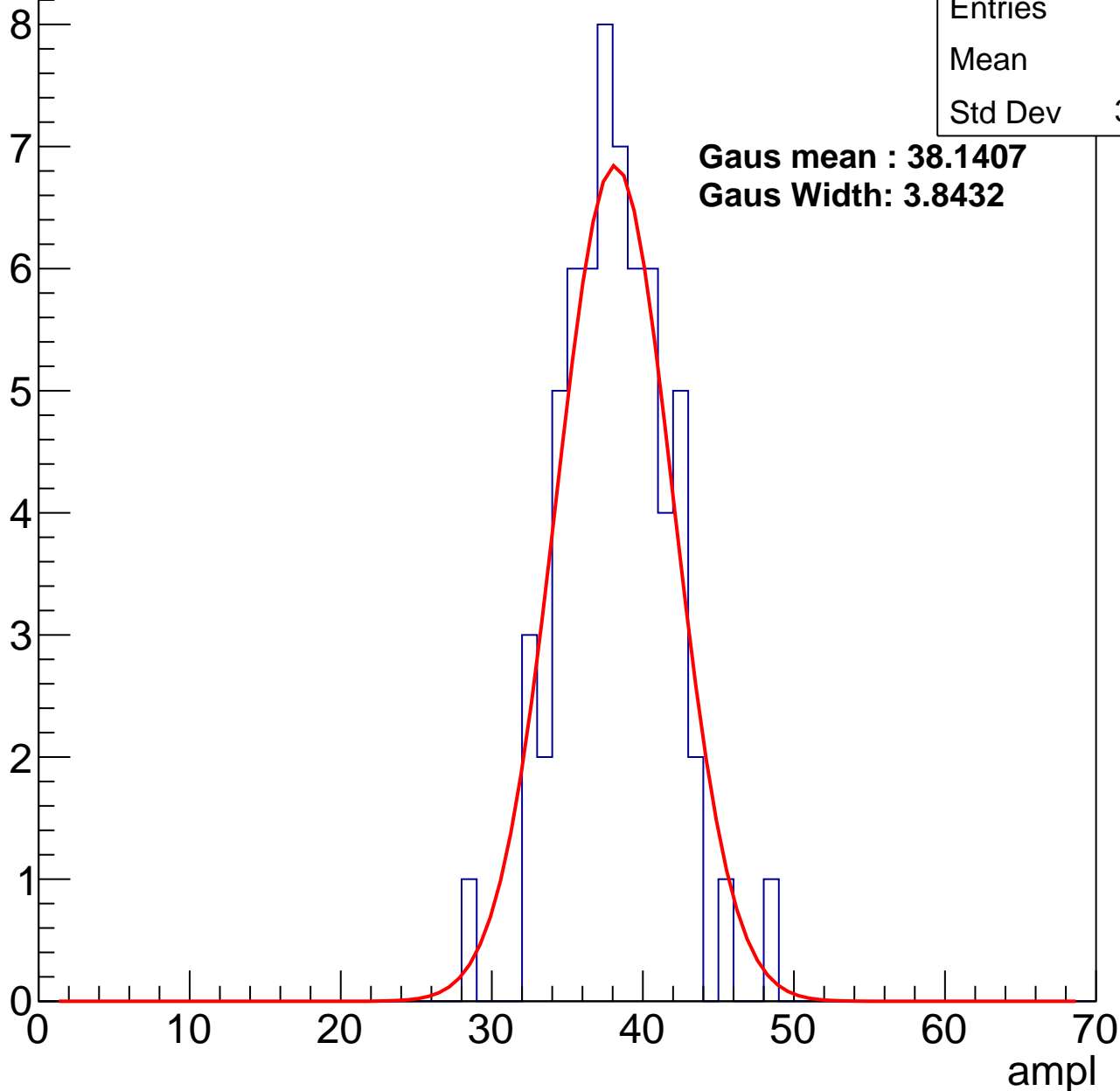
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.7
Std Dev	3.481

Gaus mean : 38.1407

Gaus Width: 3.8432



B1L101S, U4-ch29, adc2

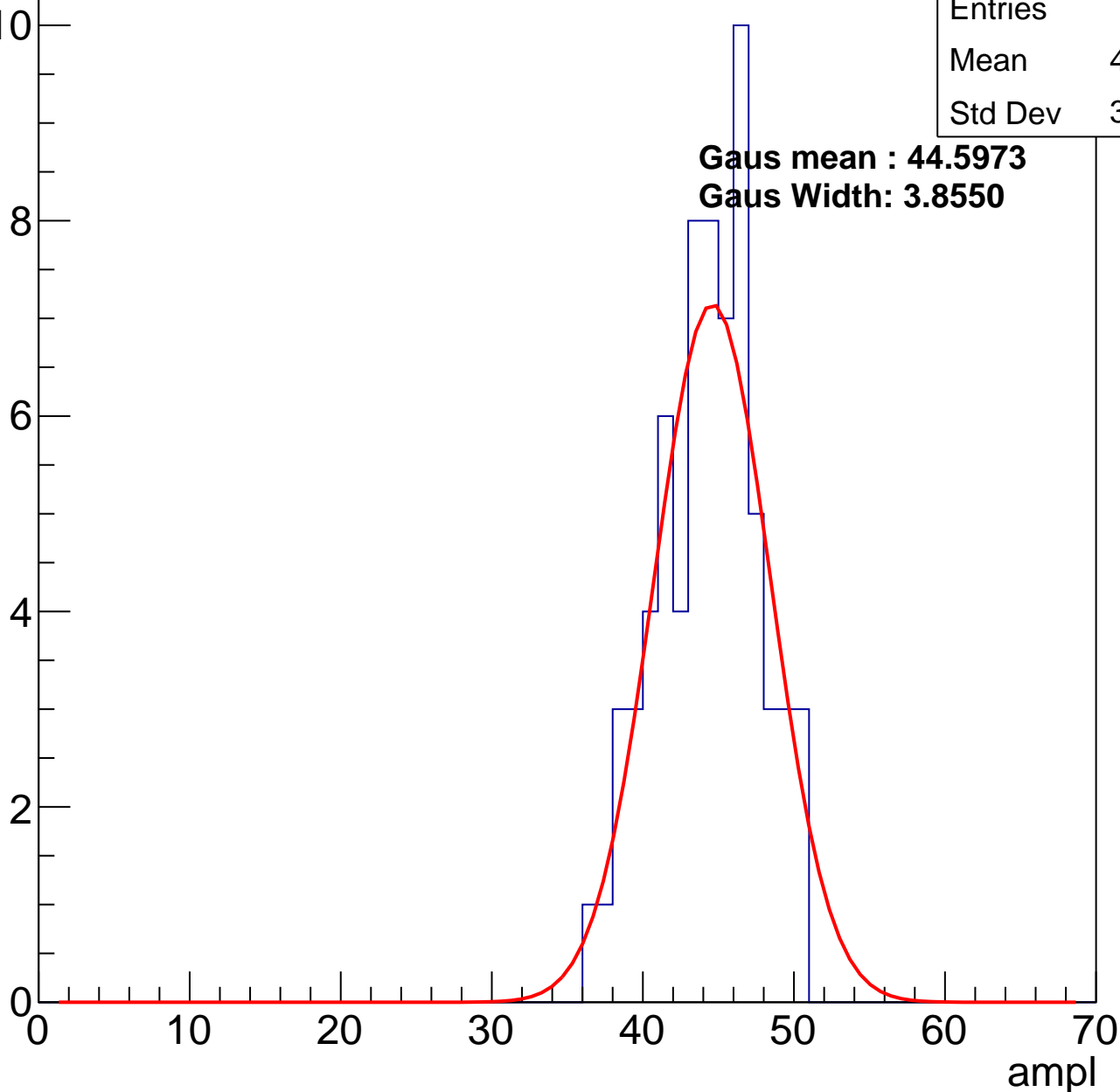
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.84
Std Dev	3.317

Gaus mean : 44.5973

Gaus Width: 3.8550

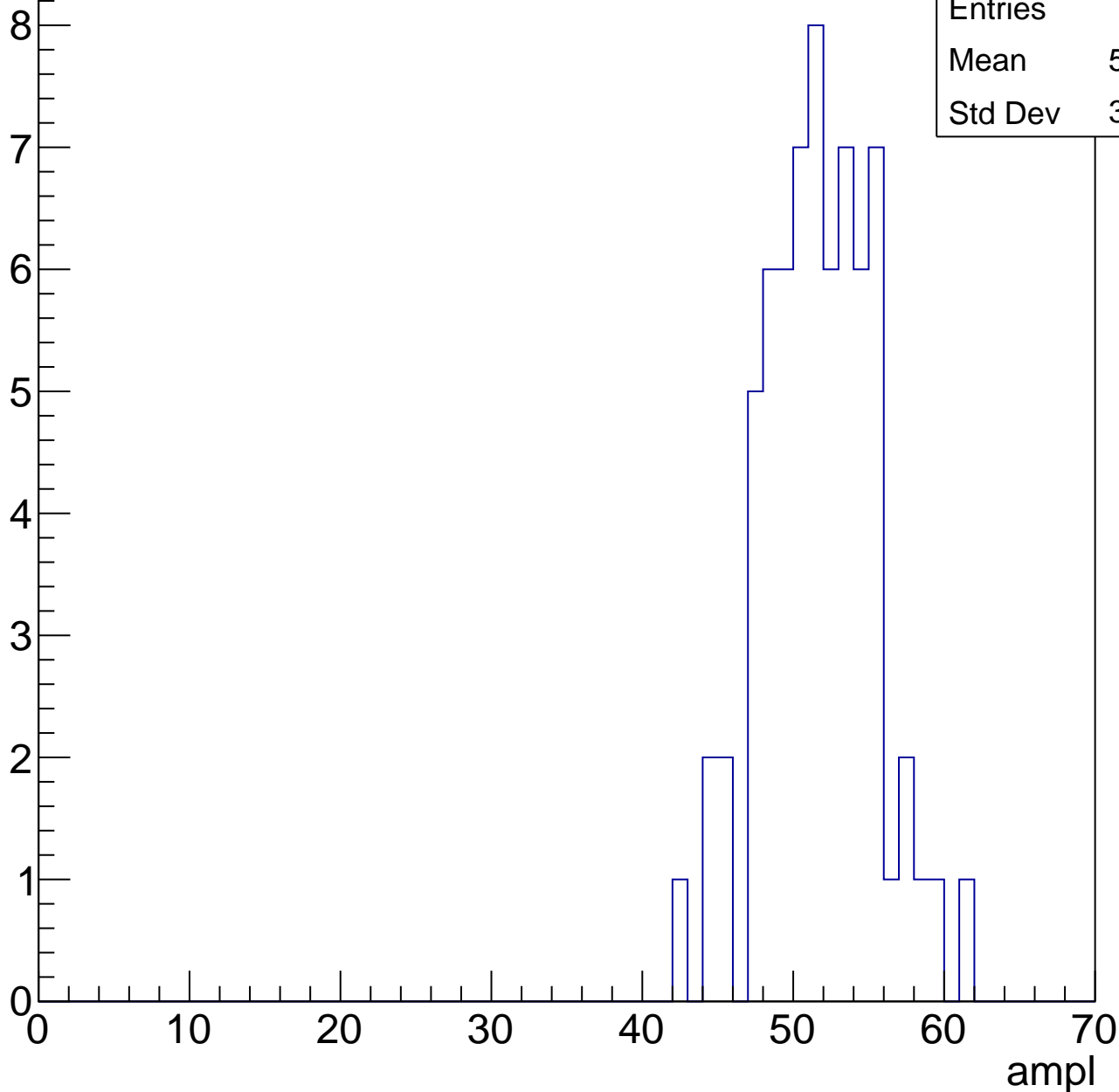


B1L101S, U4-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

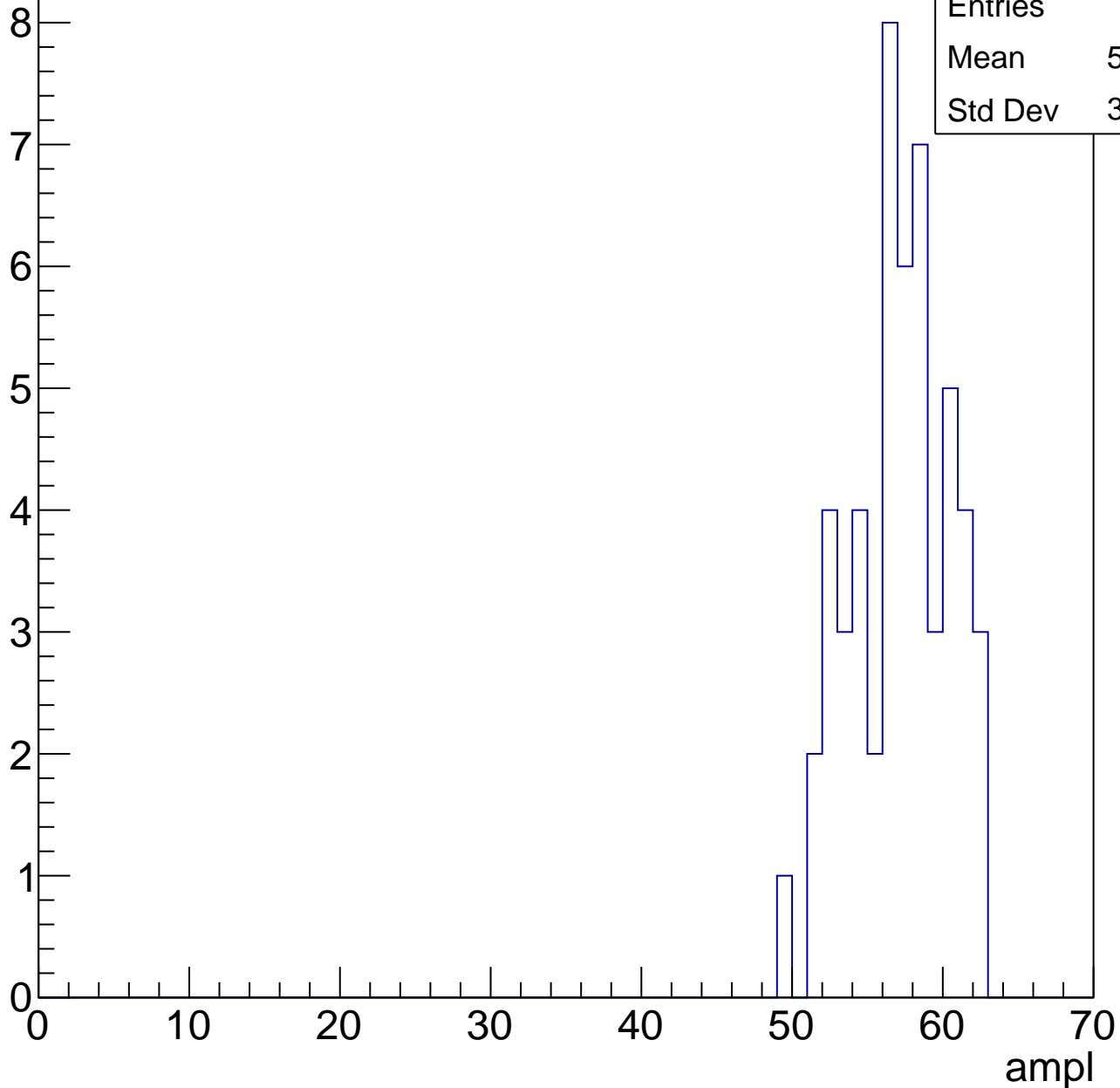
Entries	69
Mean	51.23
Std Dev	3.656



B1L101S, U4-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

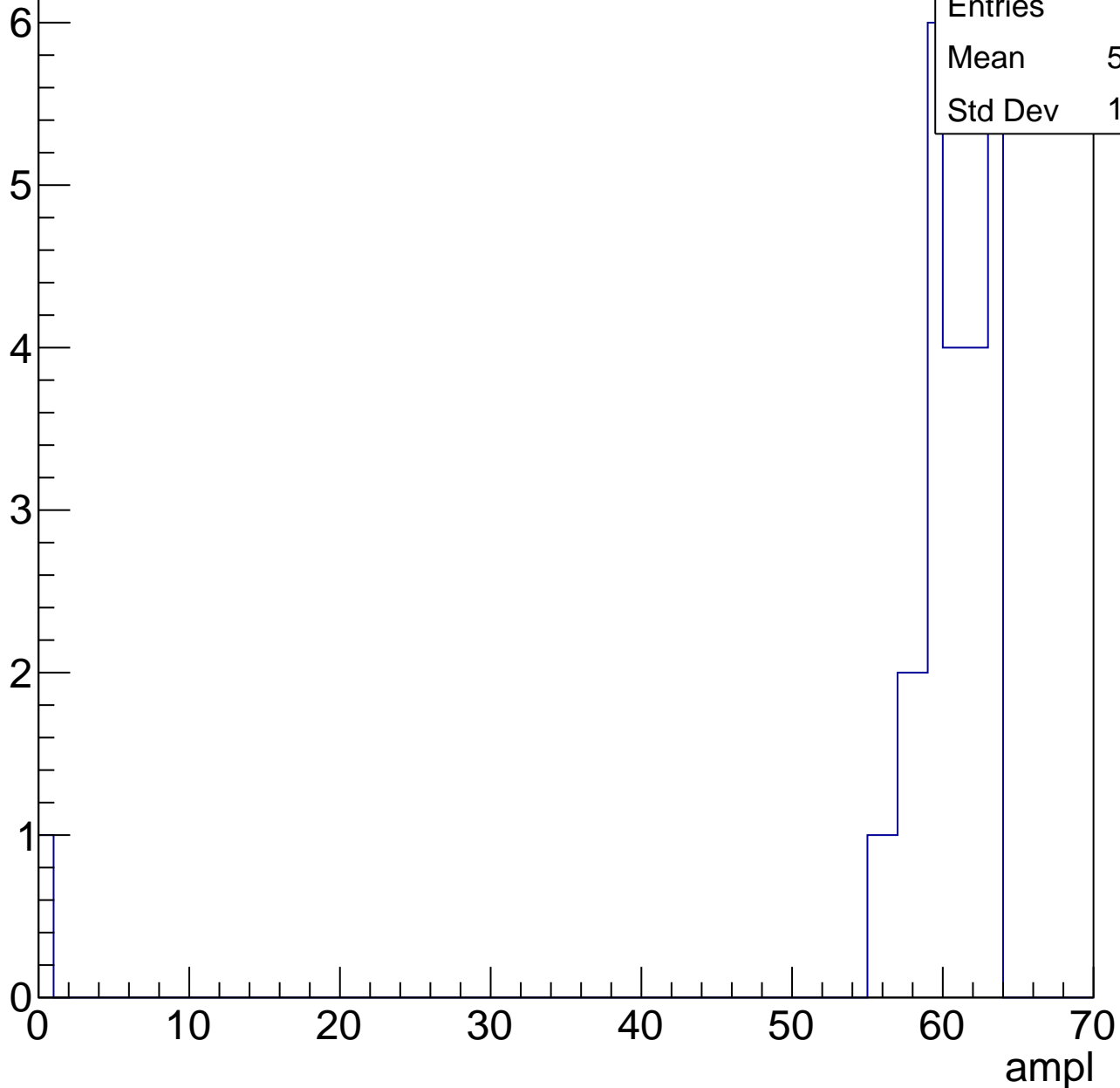
Entry



B1L101S, U4-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

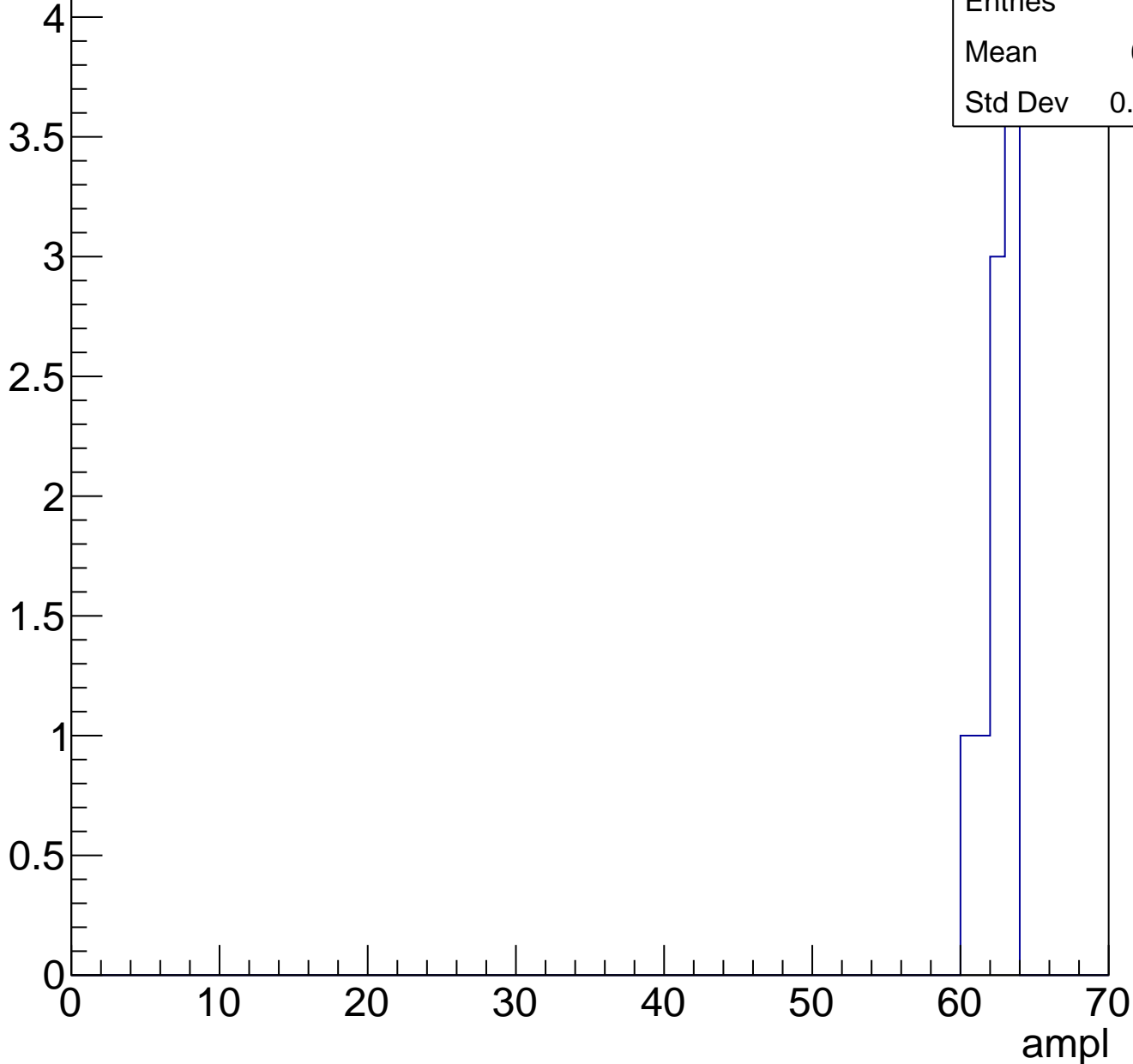
Entry



B1L101S, U4-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch30, adc0

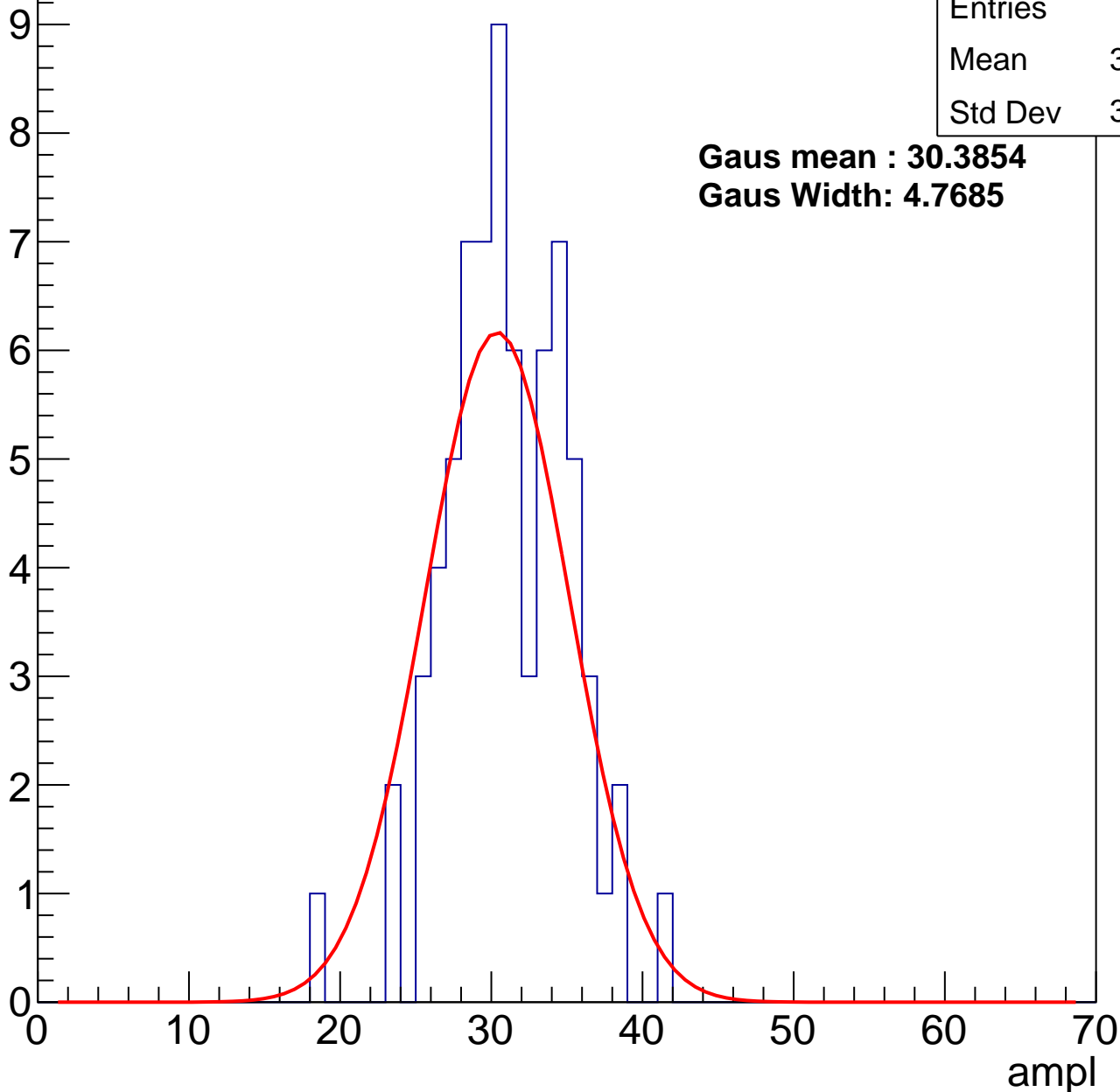
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.58
Std Dev	3.999

Gaus mean : 30.3854

Gaus Width: 4.7685



B1L101S, U4-ch30, adc1

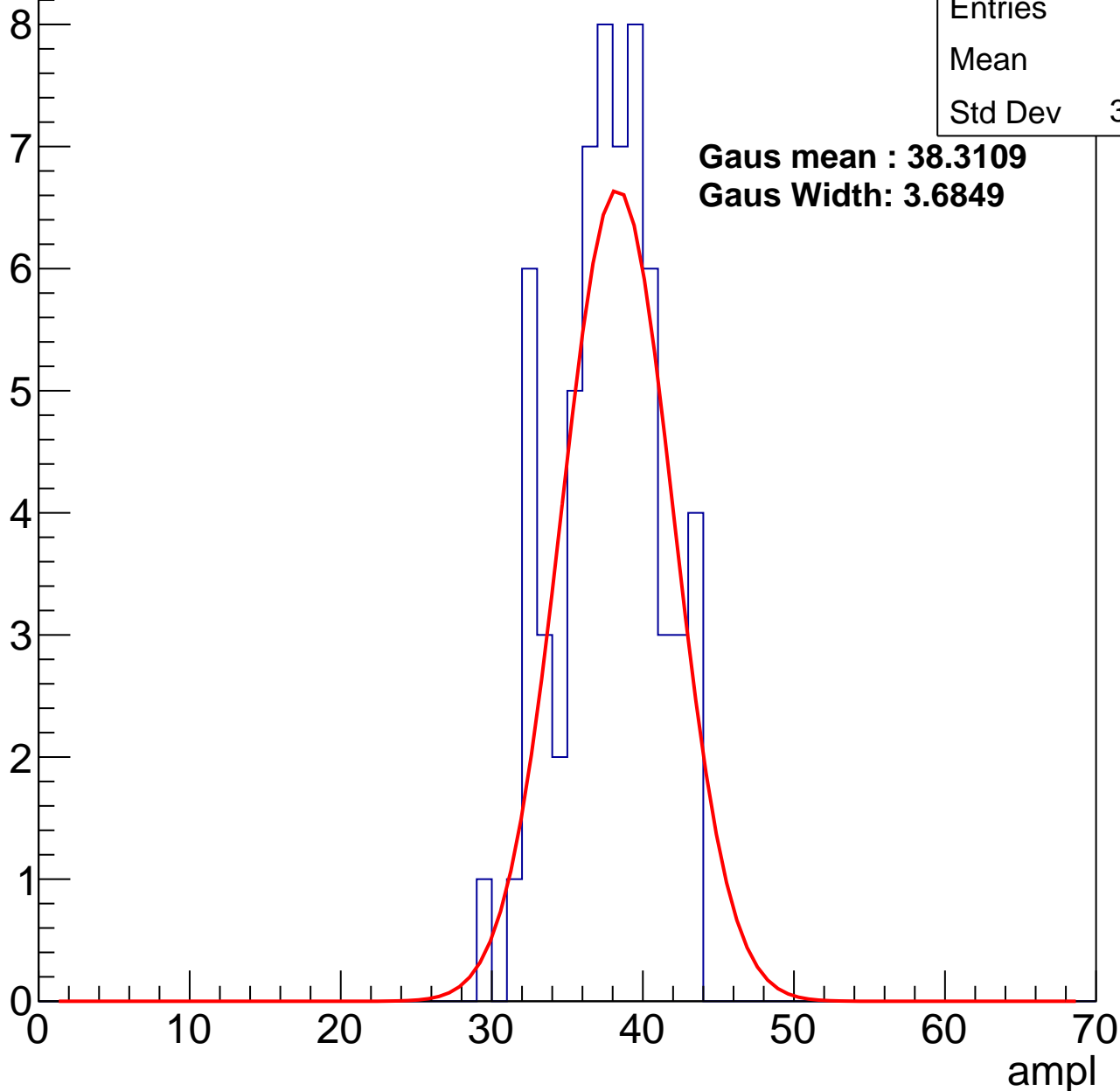
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.2
Std Dev	3.308

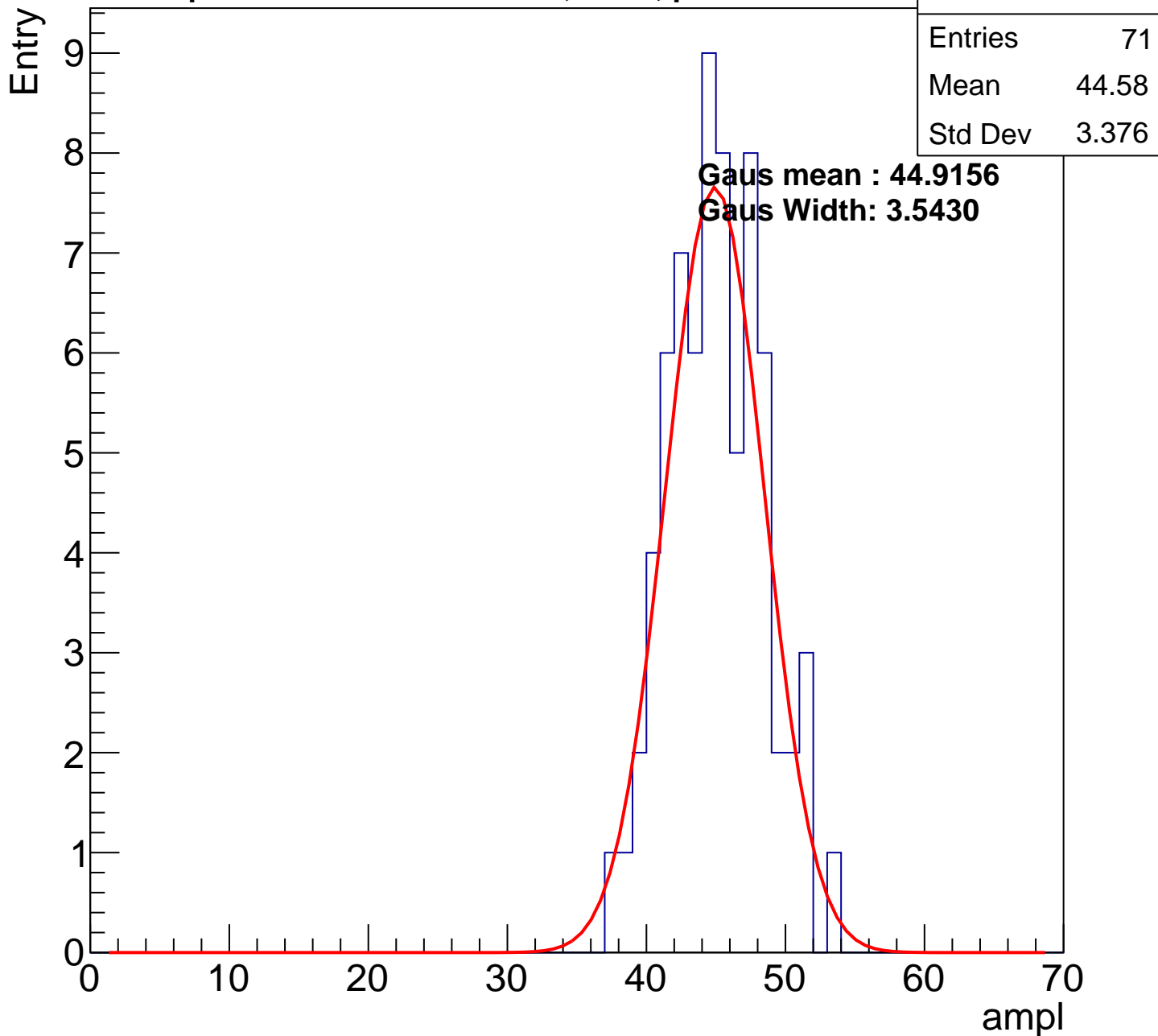
Gaus mean : 38.3109

Gaus Width: 3.6849



B1L101S, U4-ch30, adc2

calib_packv5_042523_0143.root, FC#0, port D2

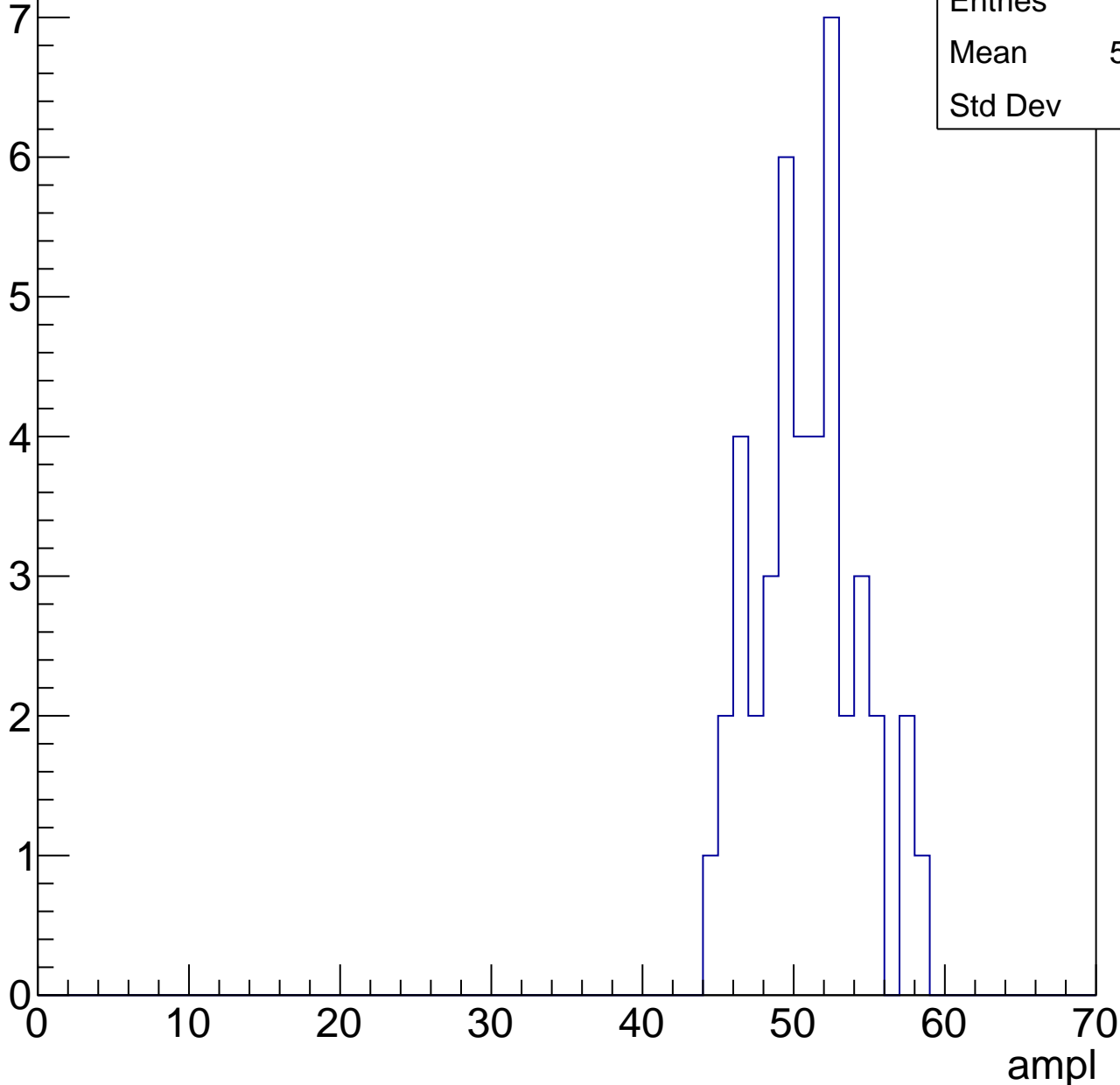


B1L101S, U4-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

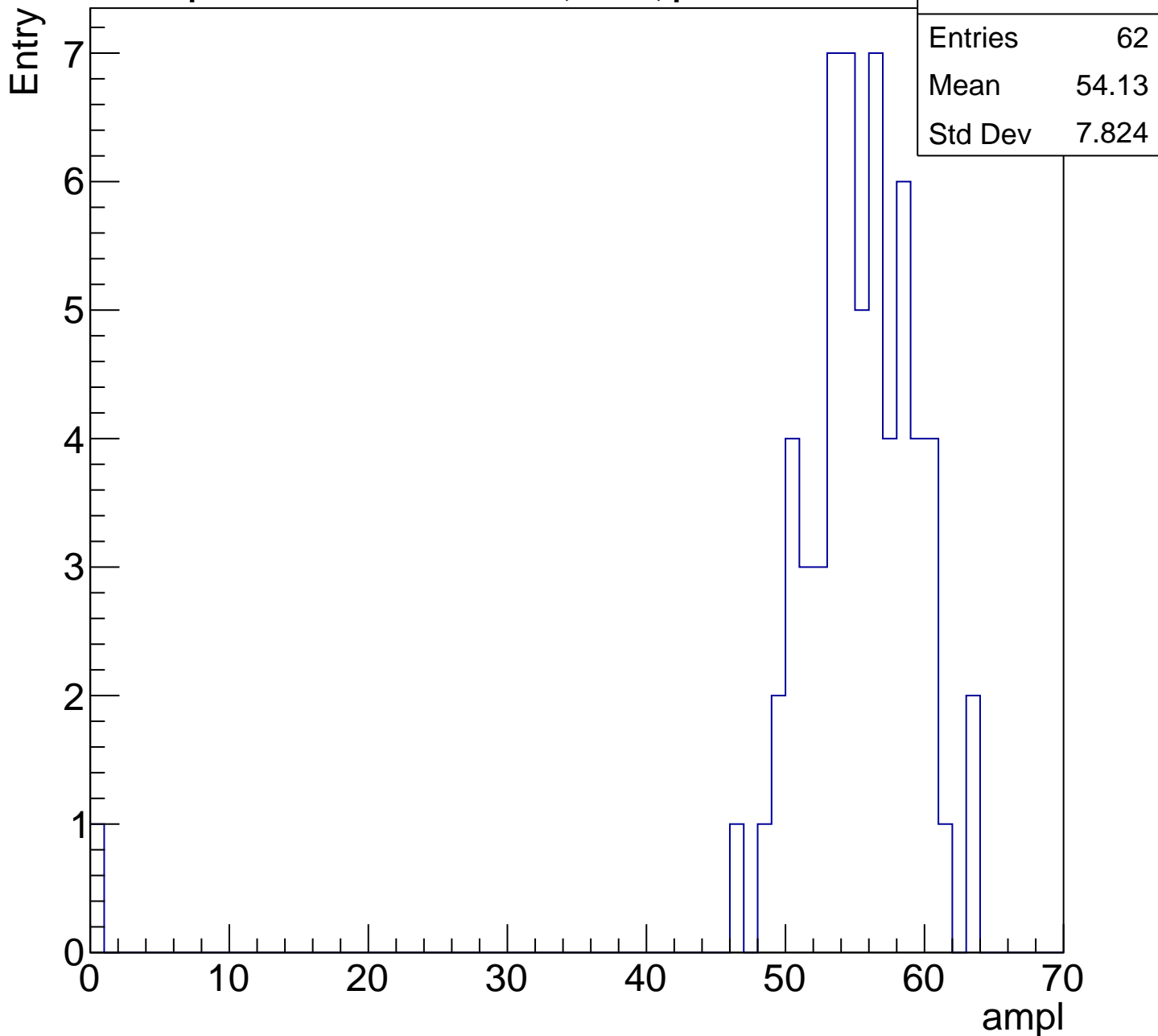
Entry

Entries	43
Mean	50.42
Std Dev	3.37



B1L101S, U4-ch30, adc4

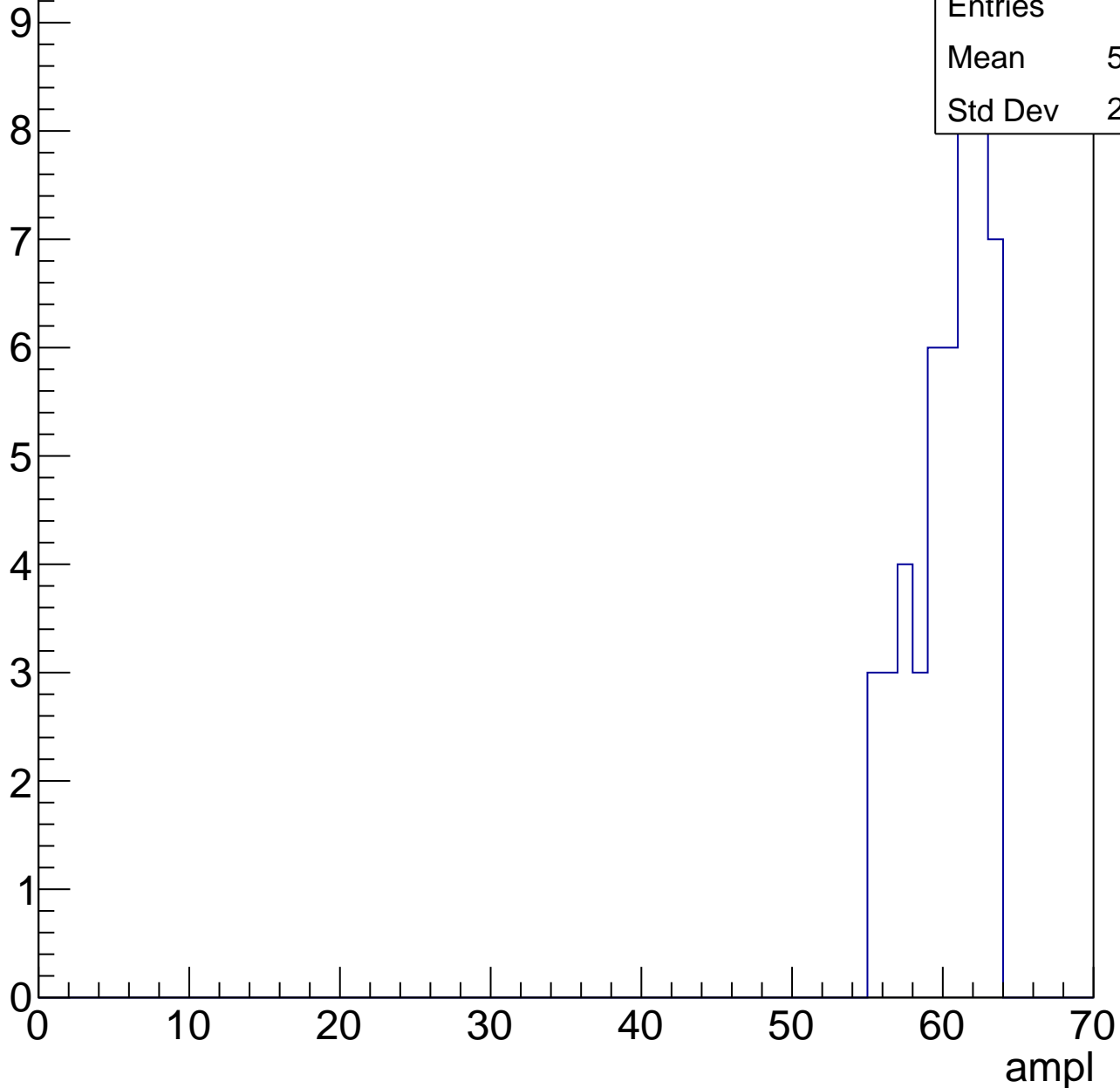
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch31, adc0

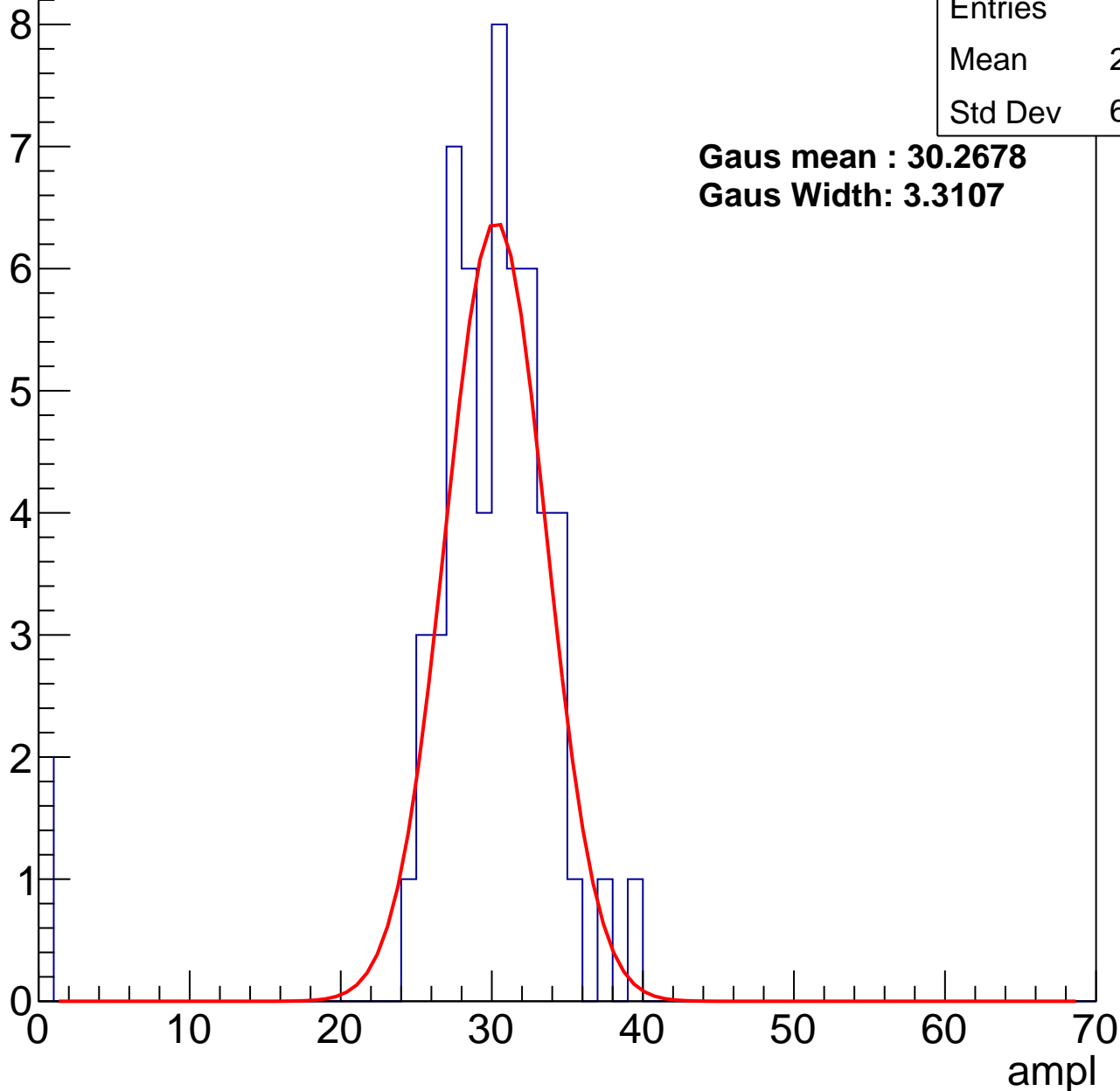
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.89
Std Dev	6.304

Gaus mean : 30.2678

Gaus Width: 3.3107



B1L101S, U4-ch31, adc1

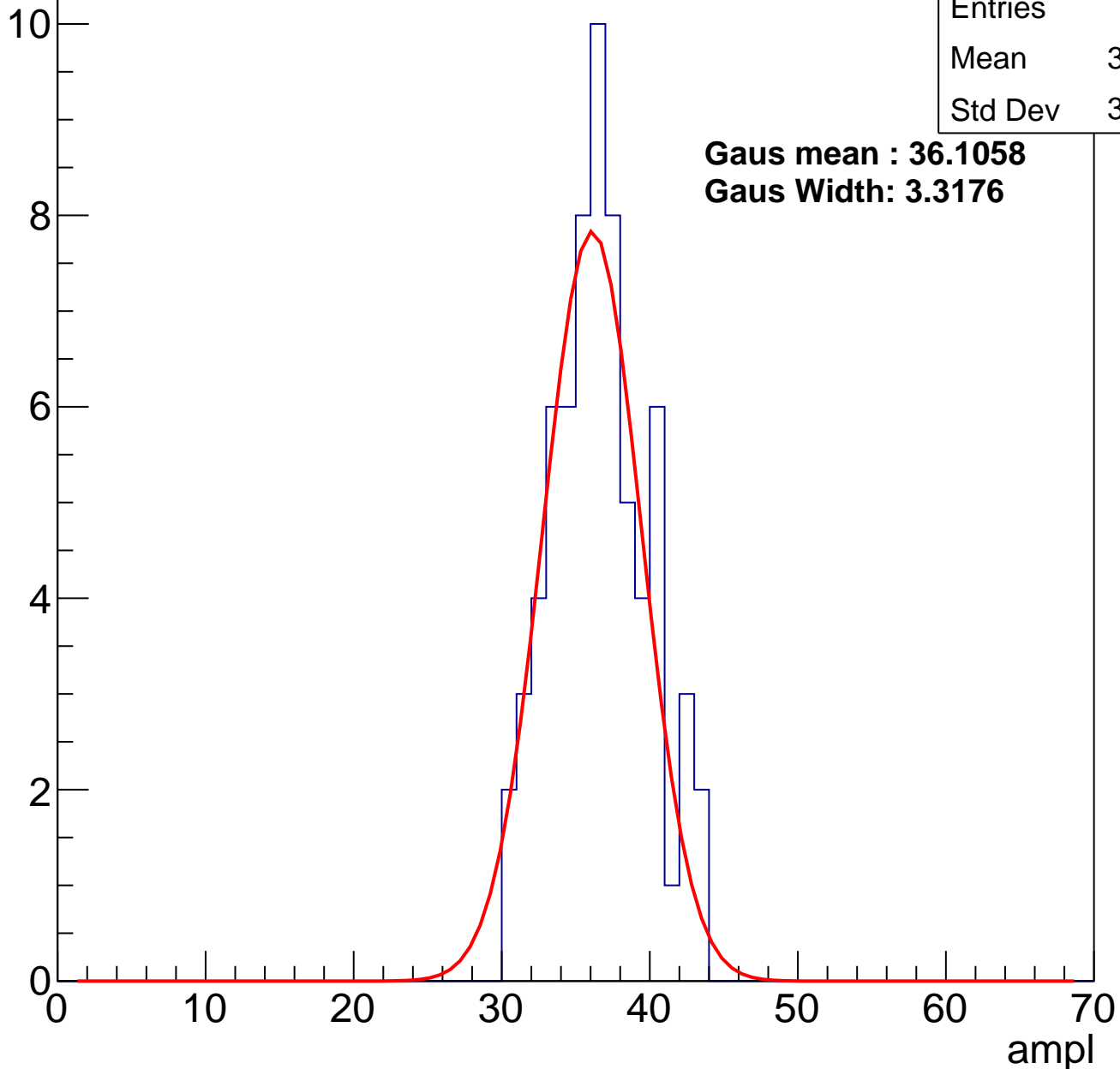
calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	36.15
Std Dev	3.177

Gaus mean : 36.1058

Gaus Width: 3.3176

Entry



B1L101S, U4-ch31, adc2

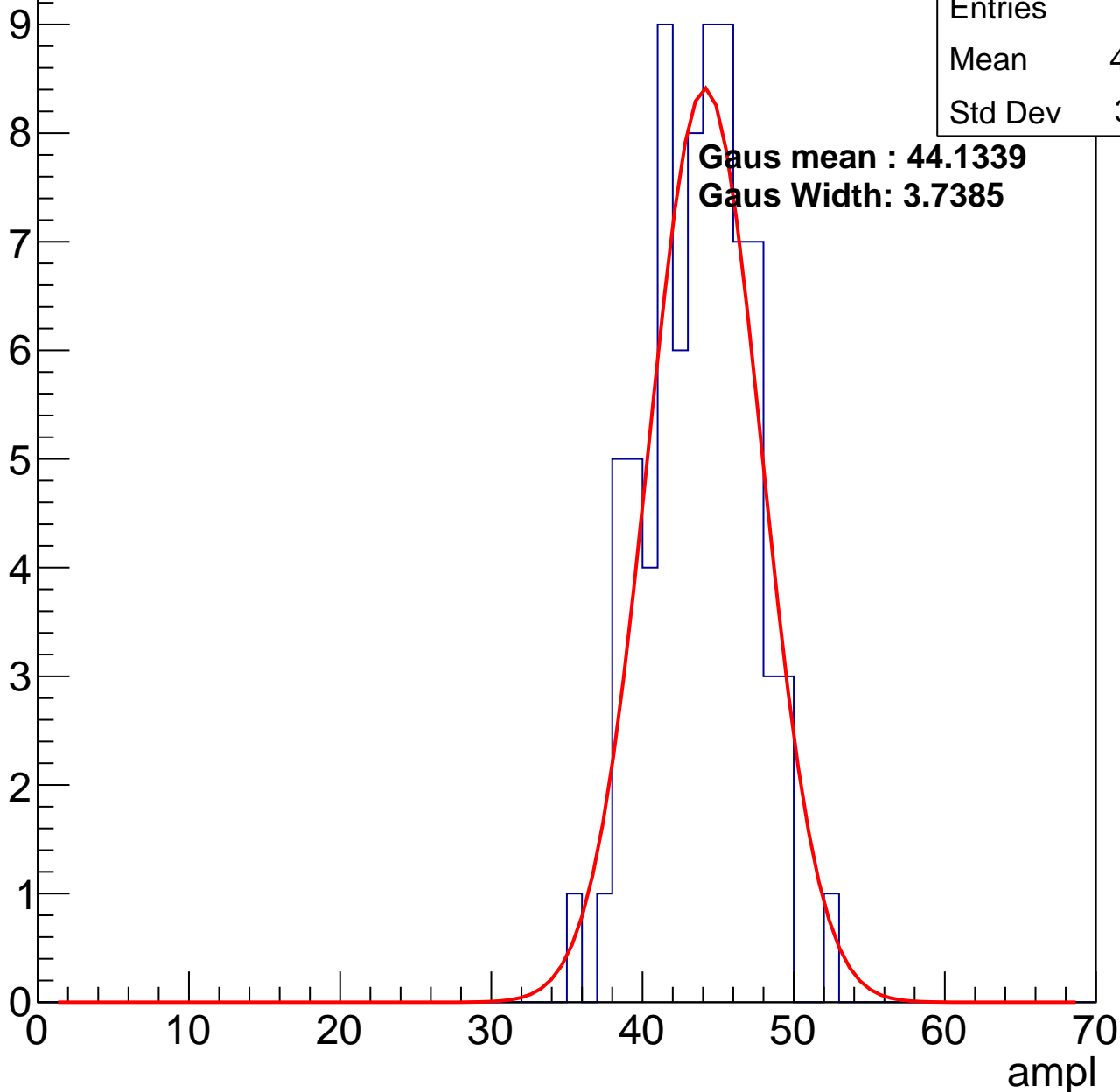
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.29
Std Dev	3.321

Gaus mean : 44.1339

Gaus Width: 3.7385



B1L101S, U4-ch31, adc3

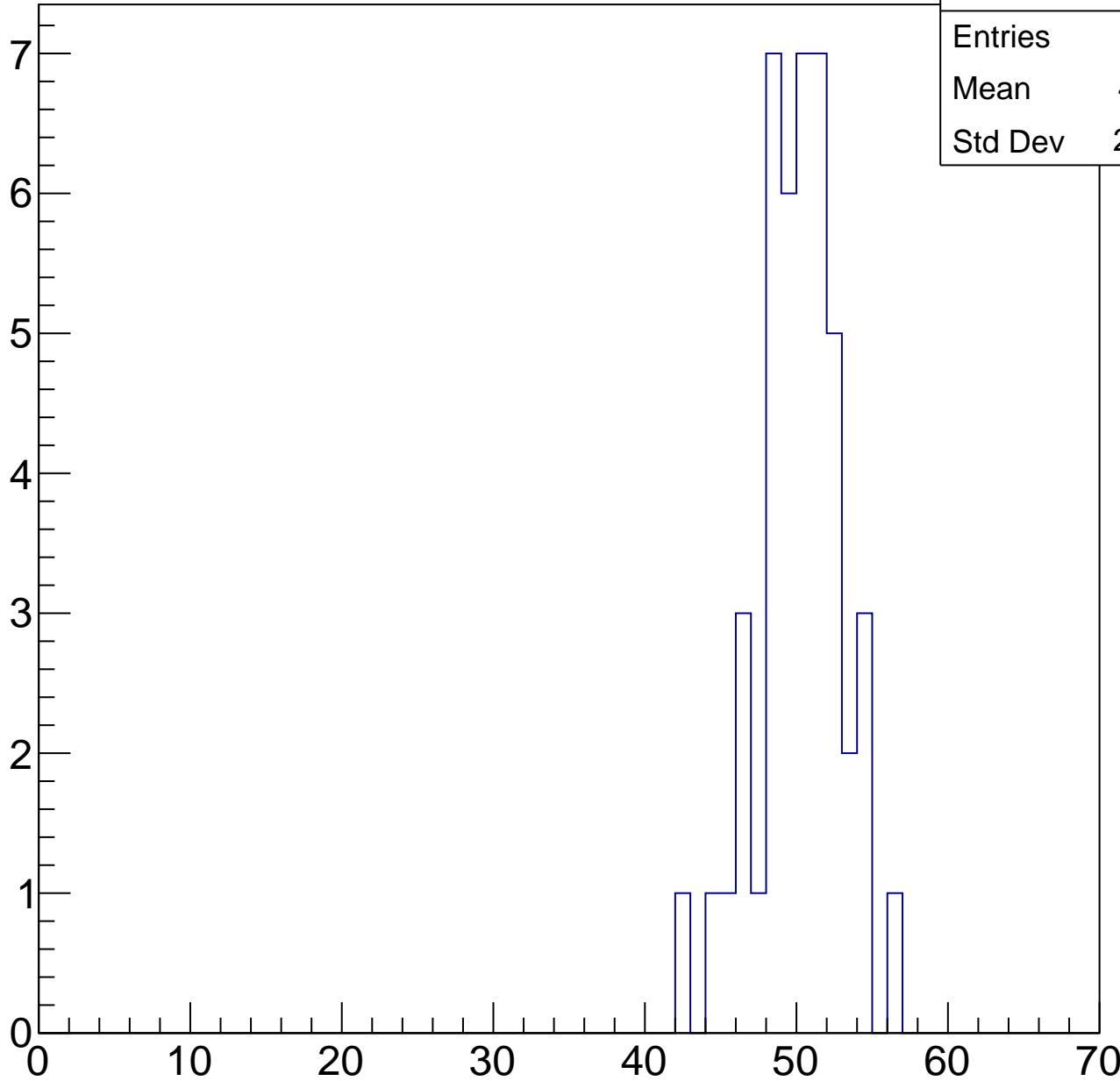
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	49.71
Std Dev	2.754

ampl

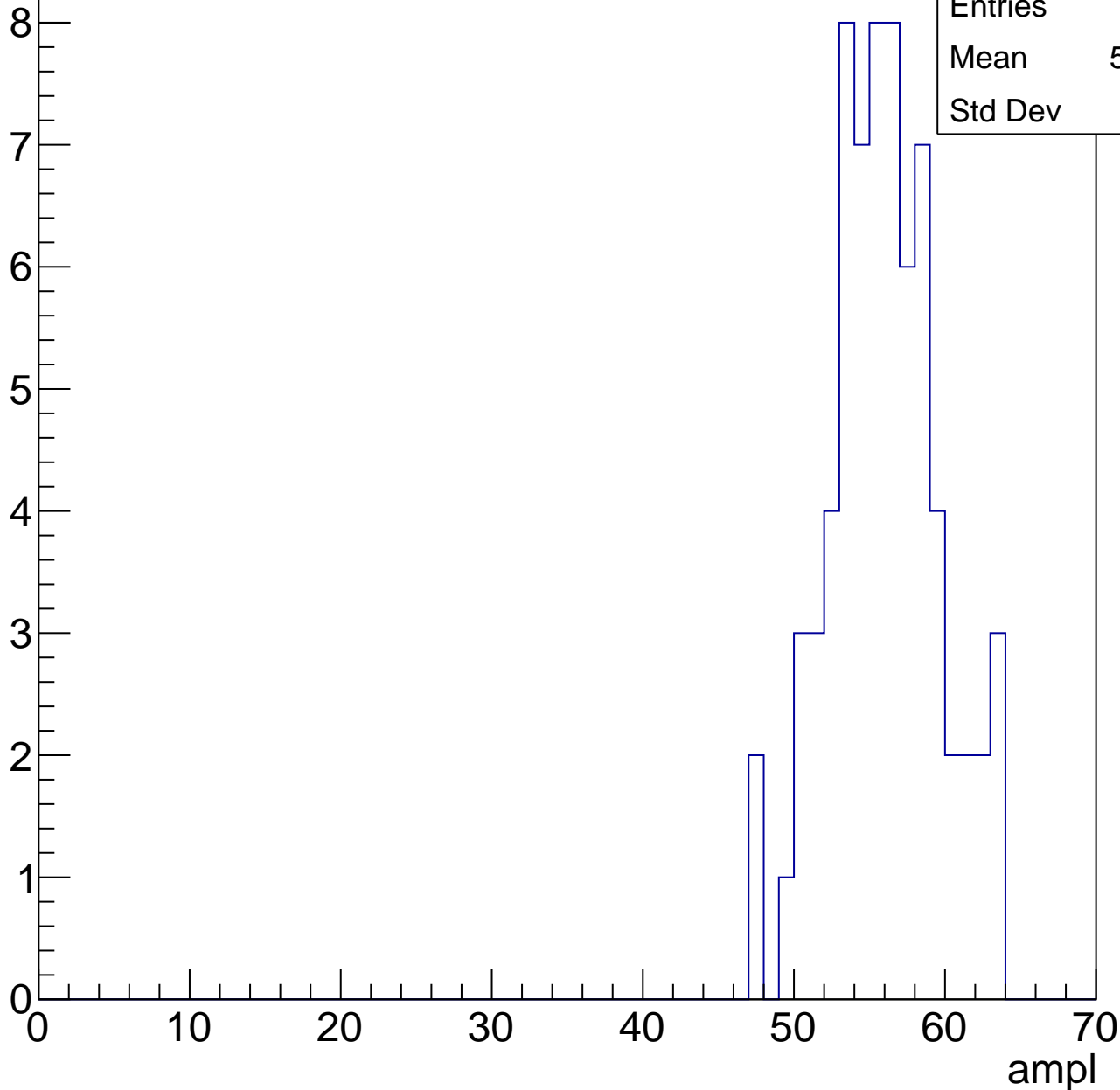


B1L101S, U4-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.47
Std Dev	3.62

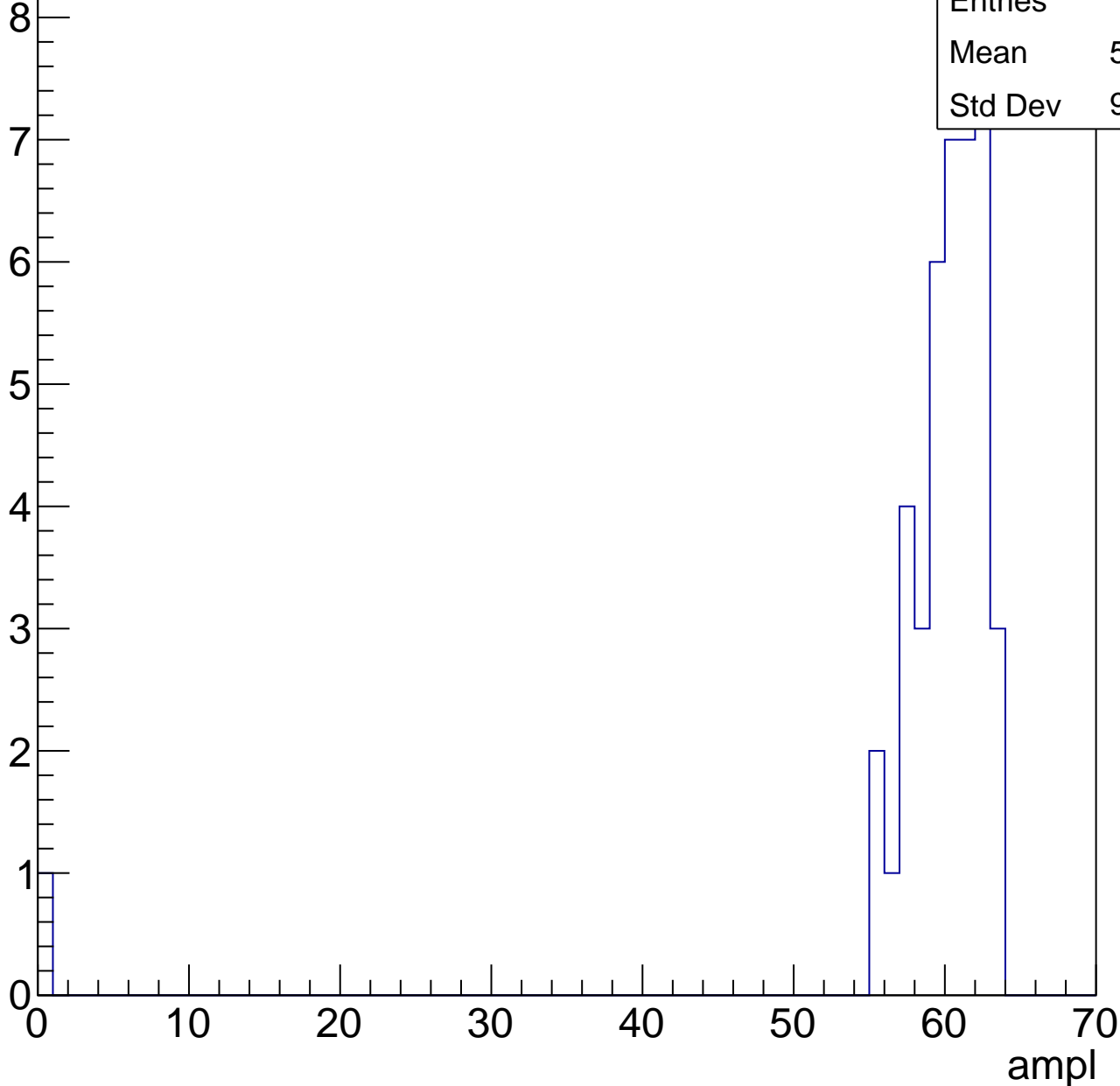


B1L101S, U4-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.43
Std Dev	9.363



B1L101S, U4-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

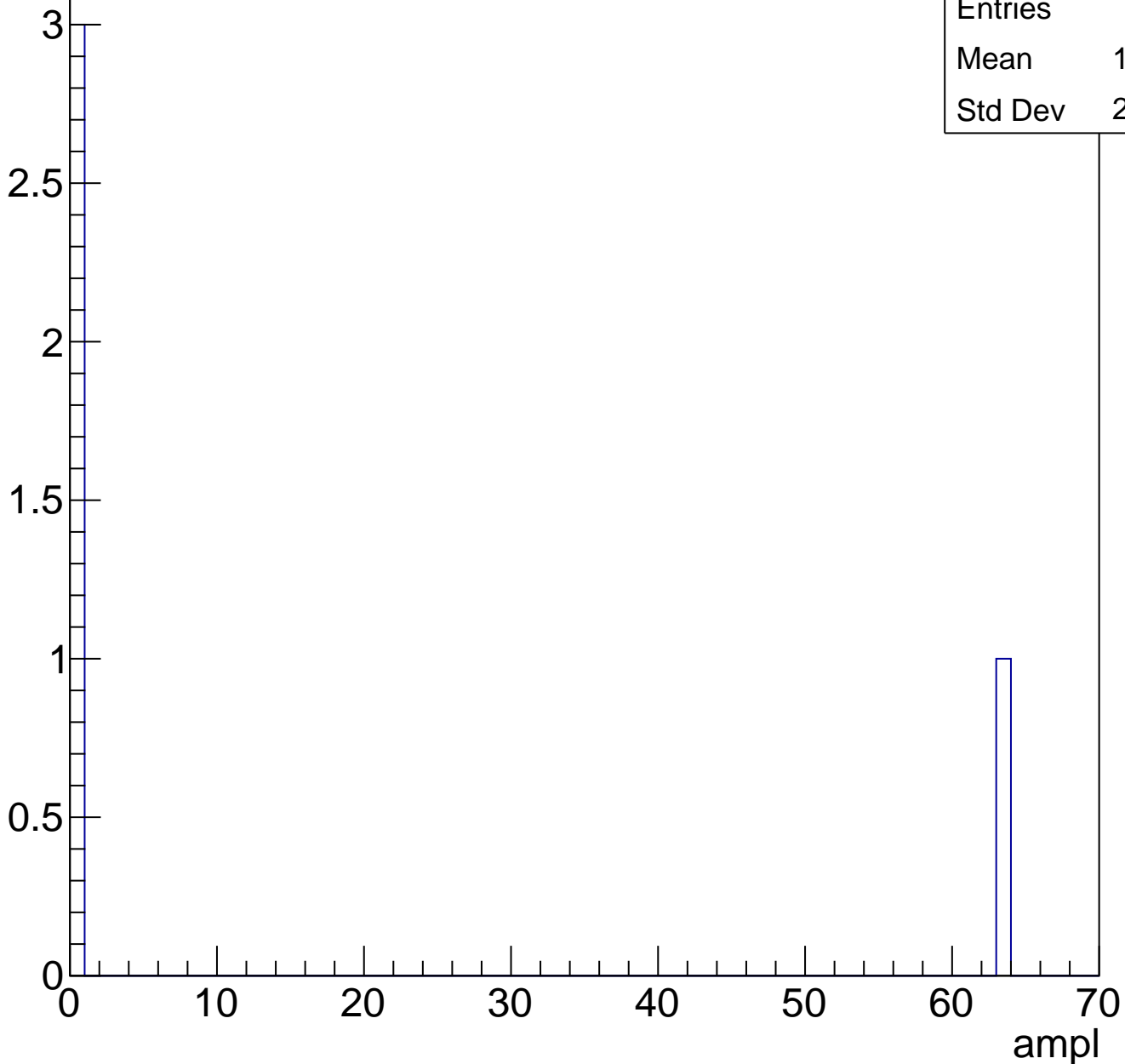
Entry



B1L101S, U4-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch32, adc0

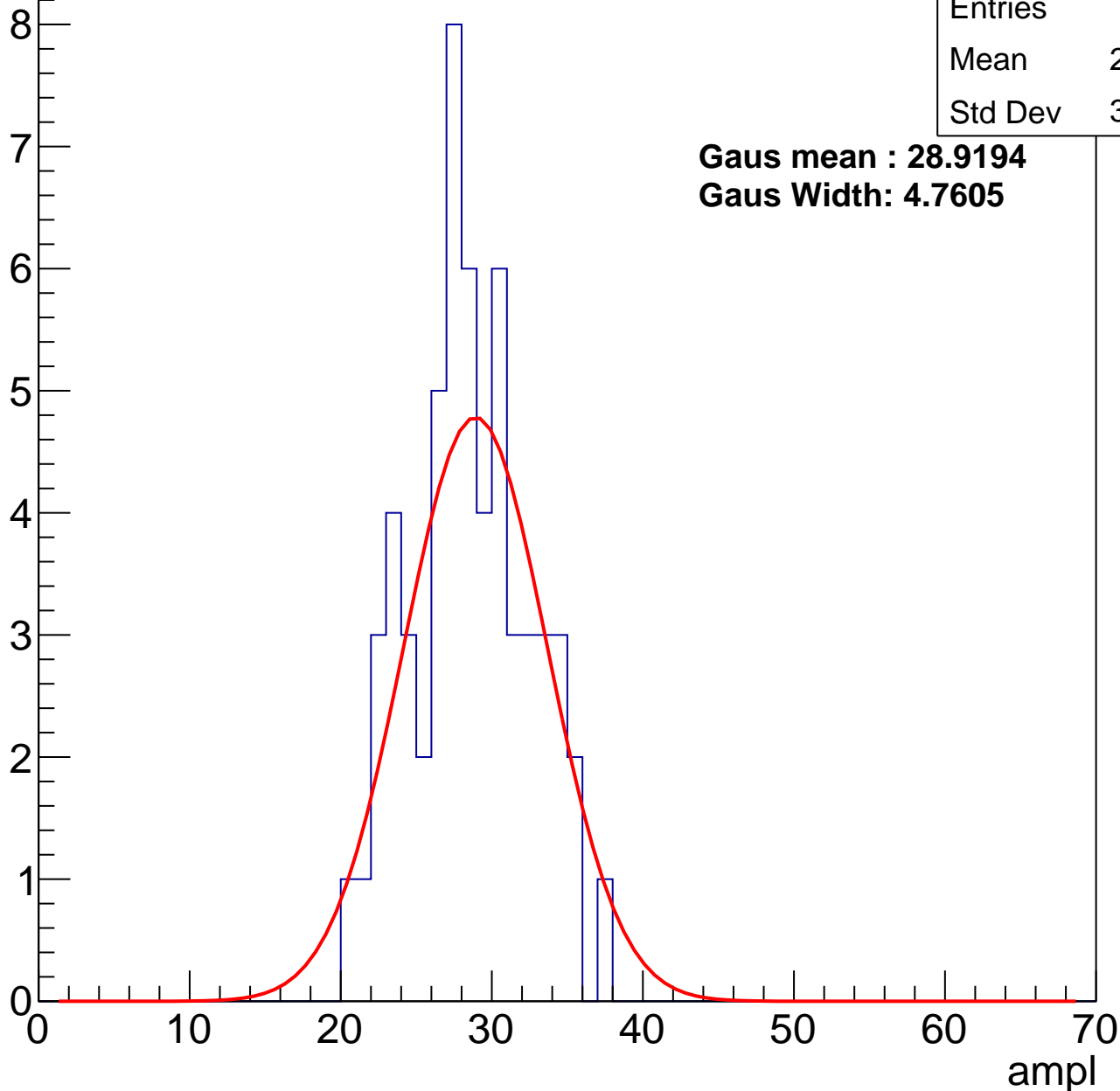
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	28.07
Std Dev	3.895

Gaus mean : 28.9194

Gaus Width: 4.7605



B1L101S, U4-ch32, adc1

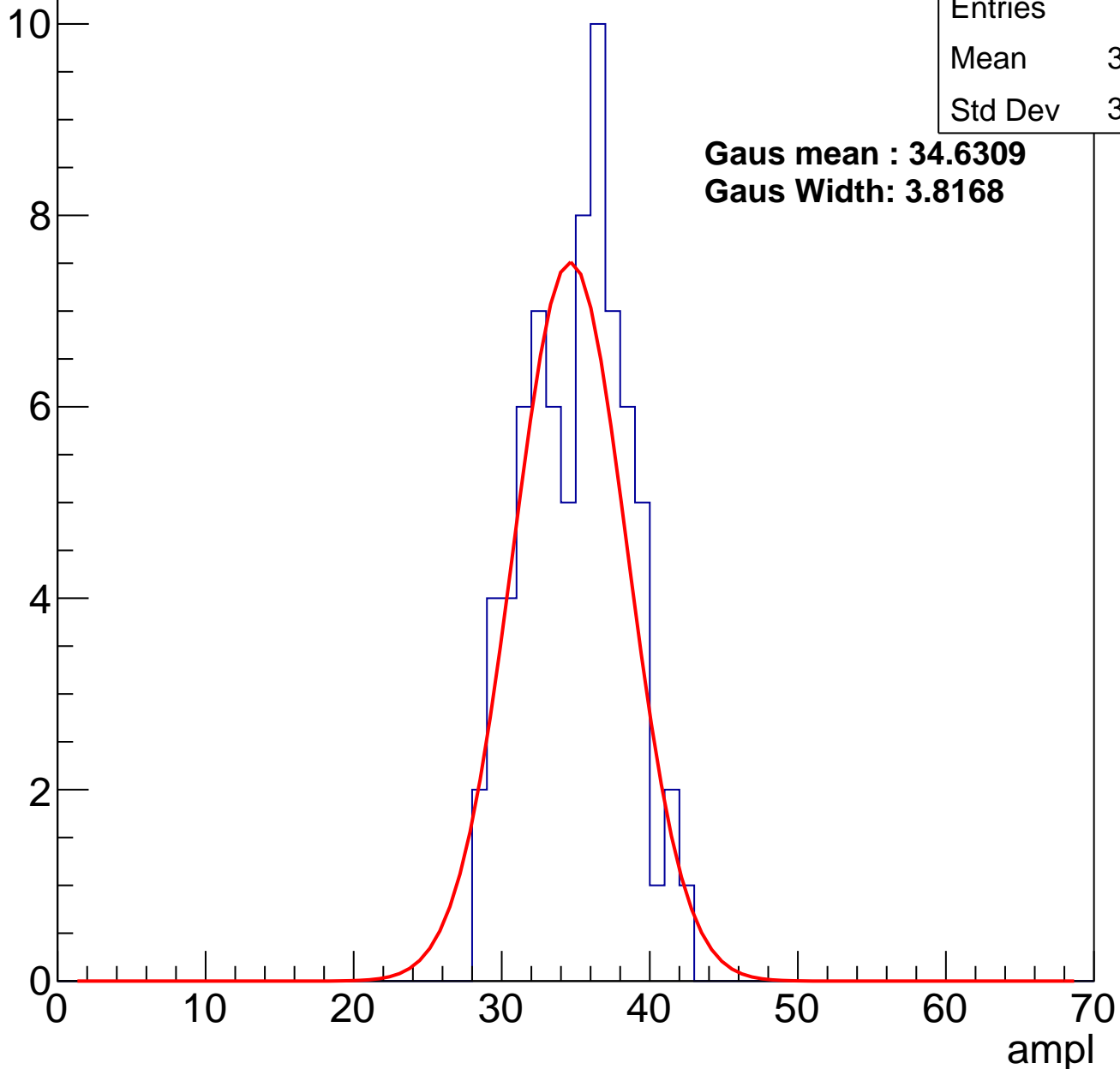
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	34.54
Std Dev	3.354

Gaus mean : 34.6309

Gaus Width: 3.8168

Entry



B1L101S, U4-ch32, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	42.4
Std Dev	3.382

Gaus mean : 42.3825

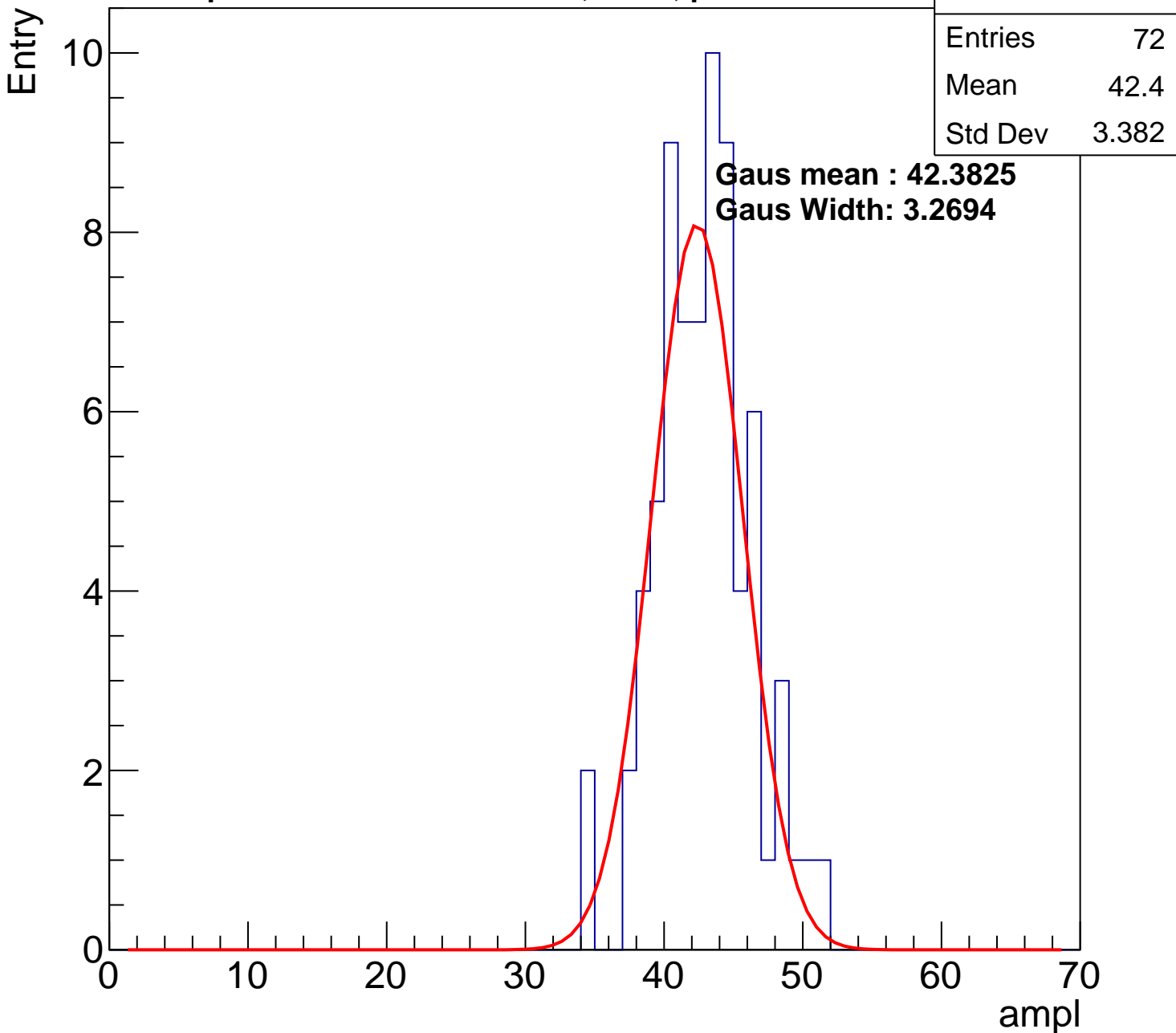
Gaus Width: 3.2694

Entry

10
8
6
4
2
0

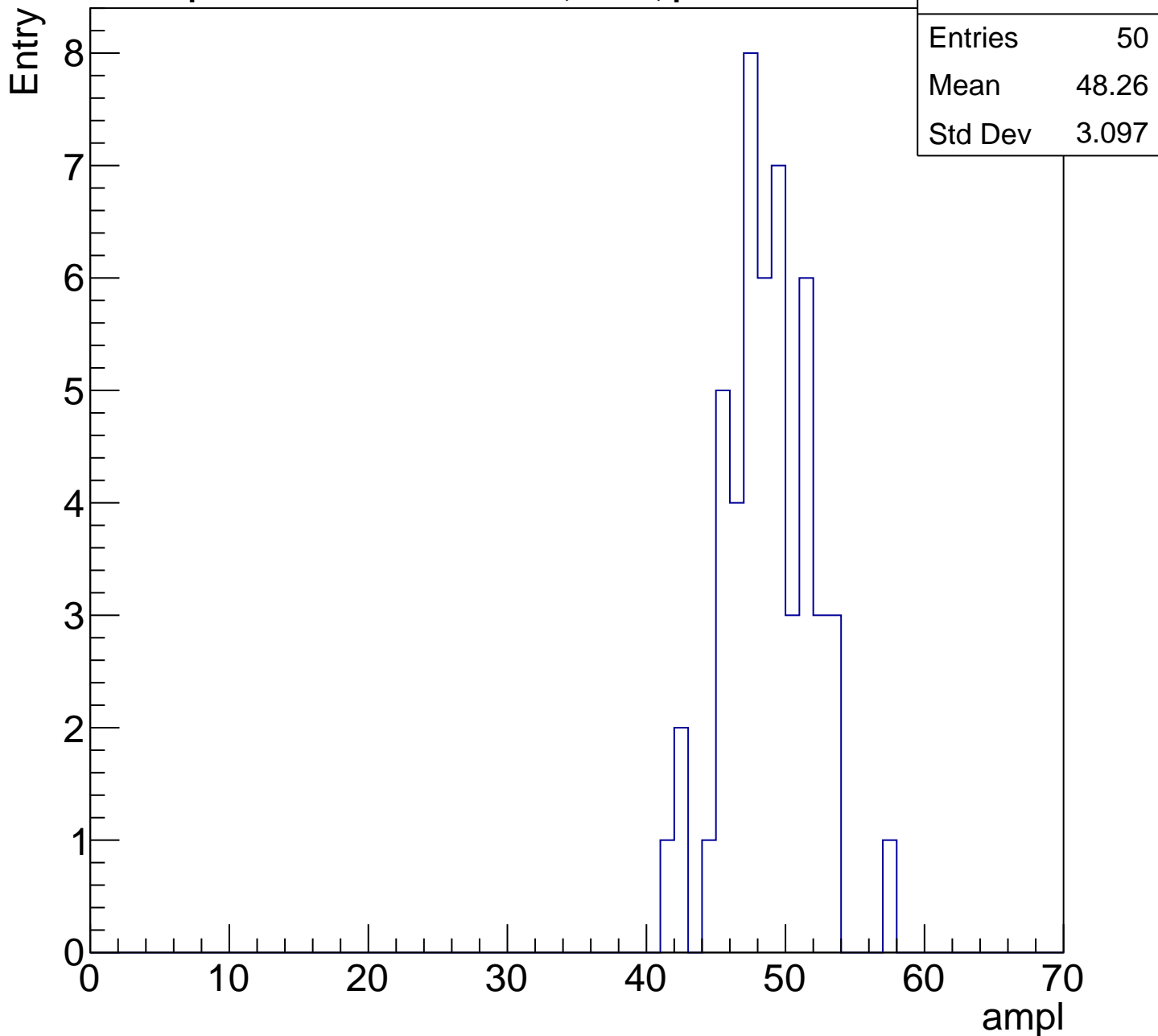
ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

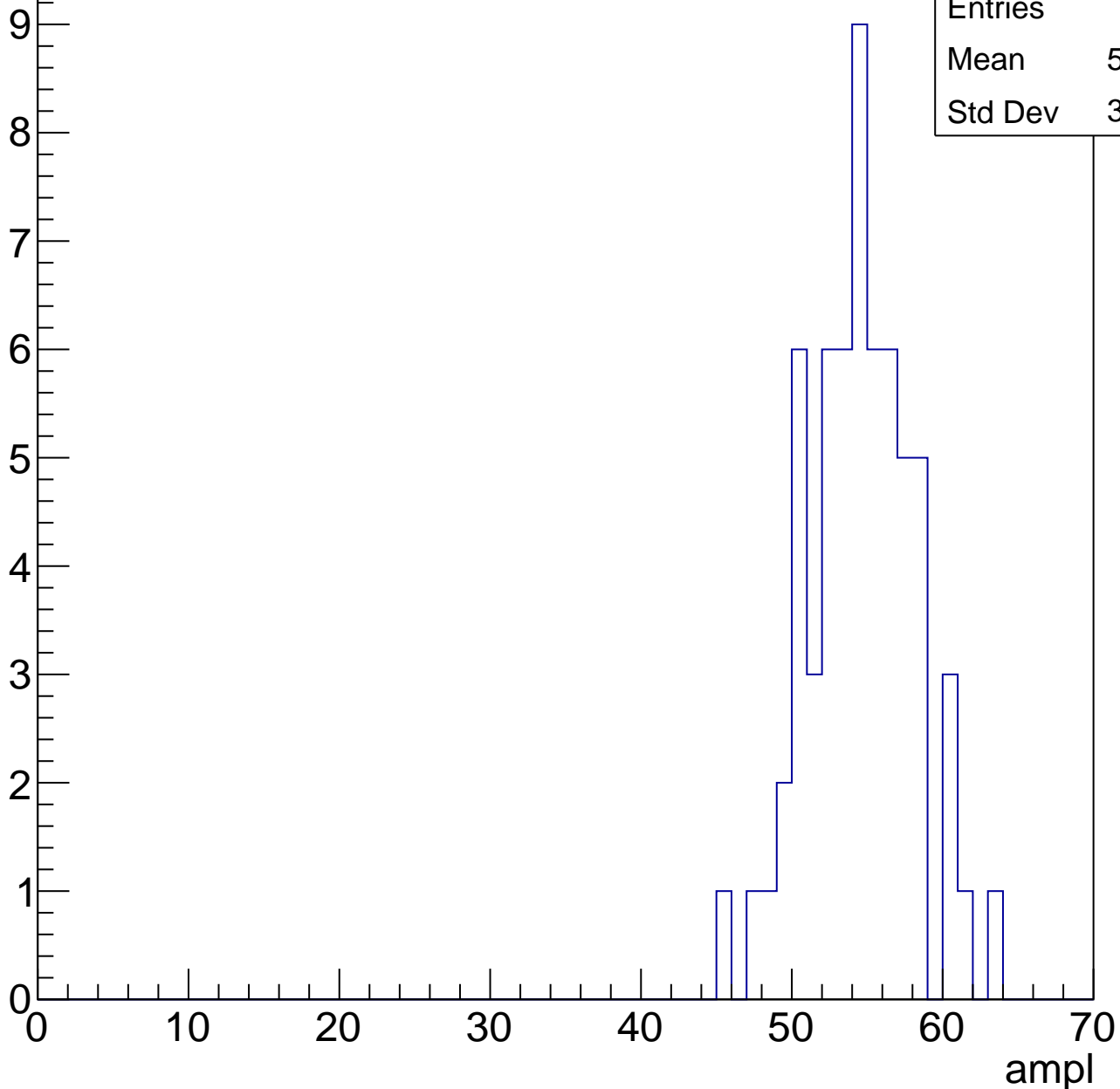


B1L101S, U4-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.06
Std Dev	3.505

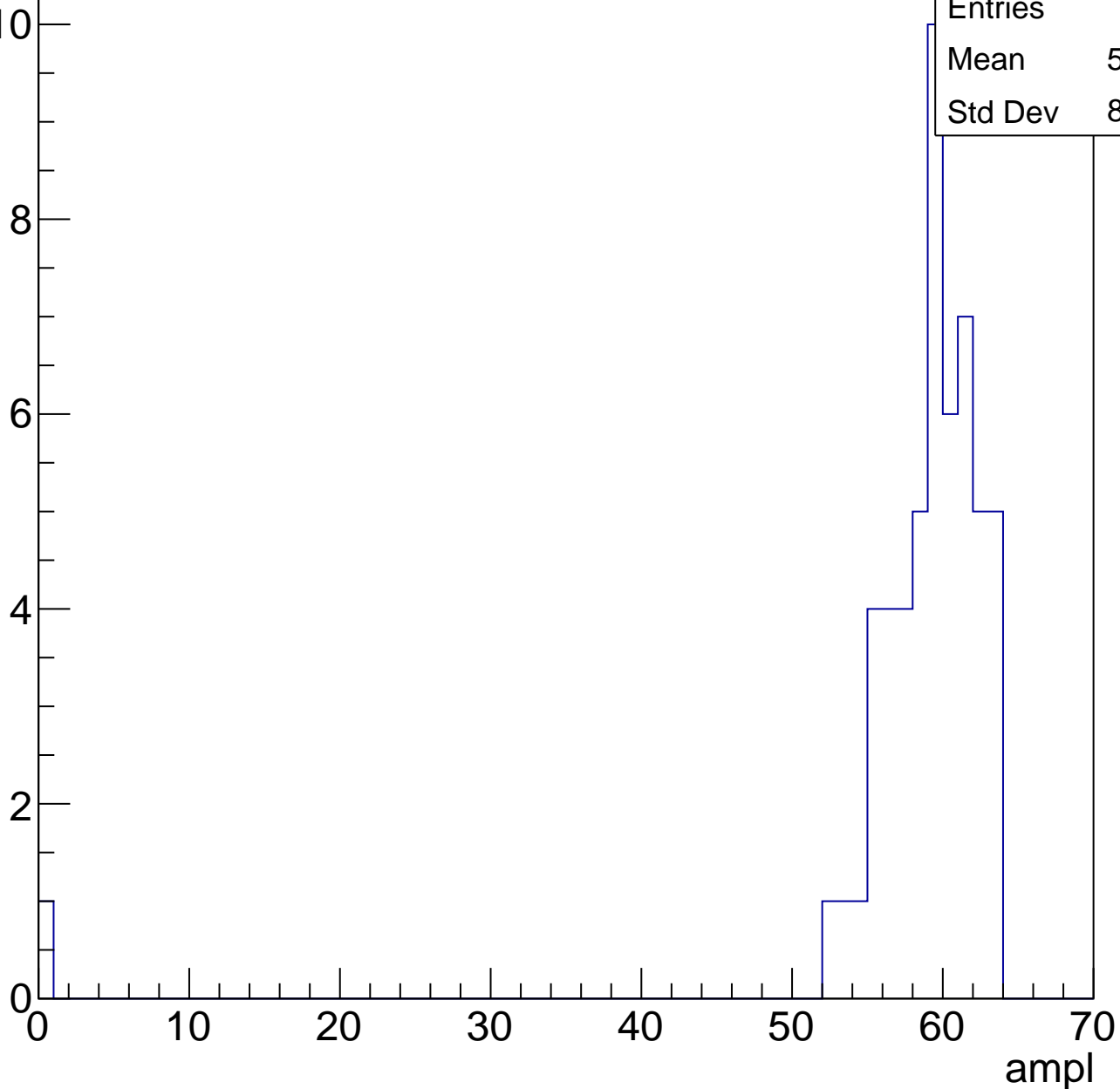


B1L101S, U4-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.83
Std Dev	8.386

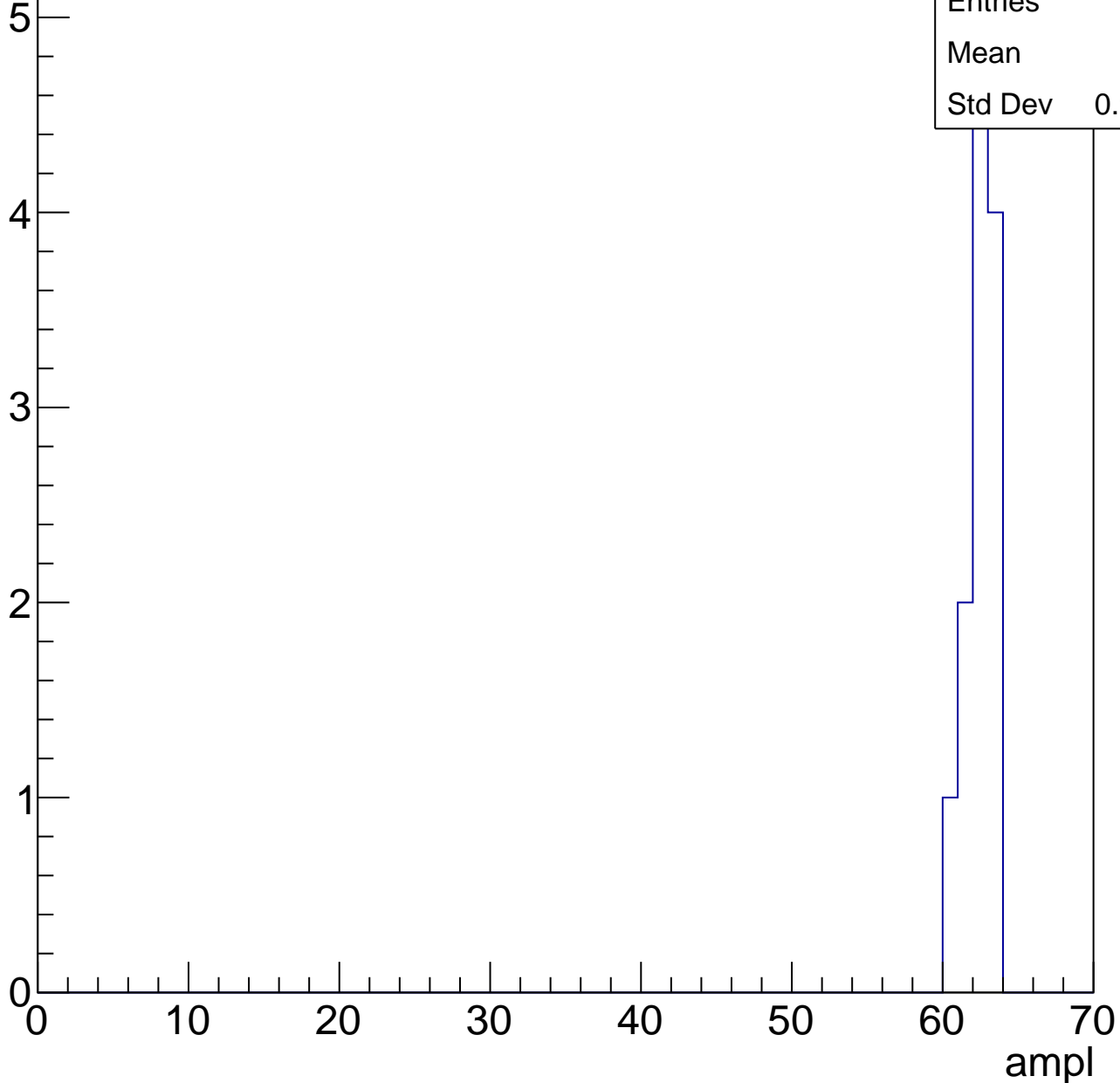


B1L101S, U4-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

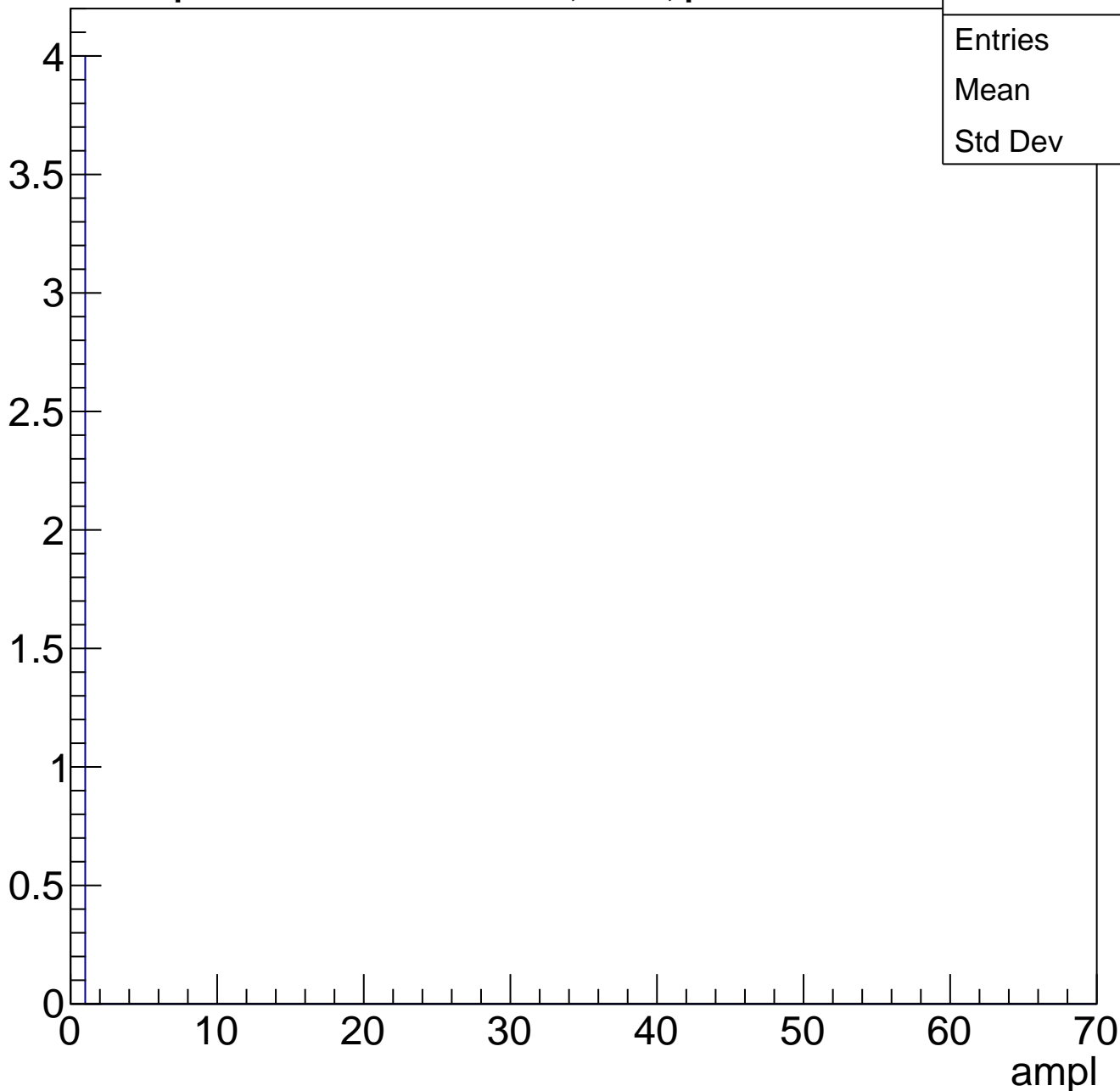
Entries	12
Mean	62
Std Dev	0.9129



B1L101S, U4-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U4-ch33, adc0

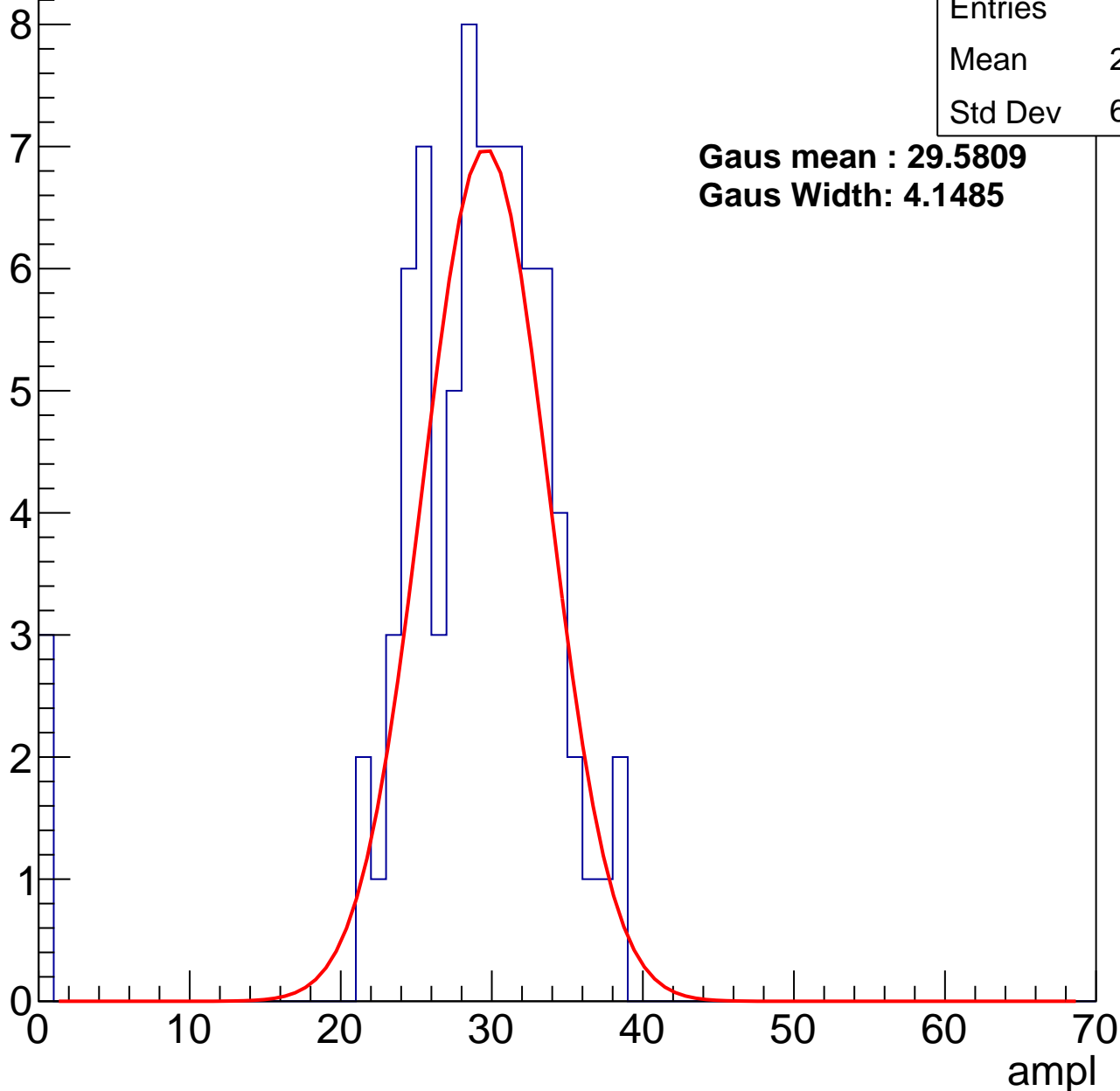
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	27.95
Std Dev	6.729

Gaus mean : 29.5809

Gaus Width: 4.1485



B1L101S, U4-ch33, adc1

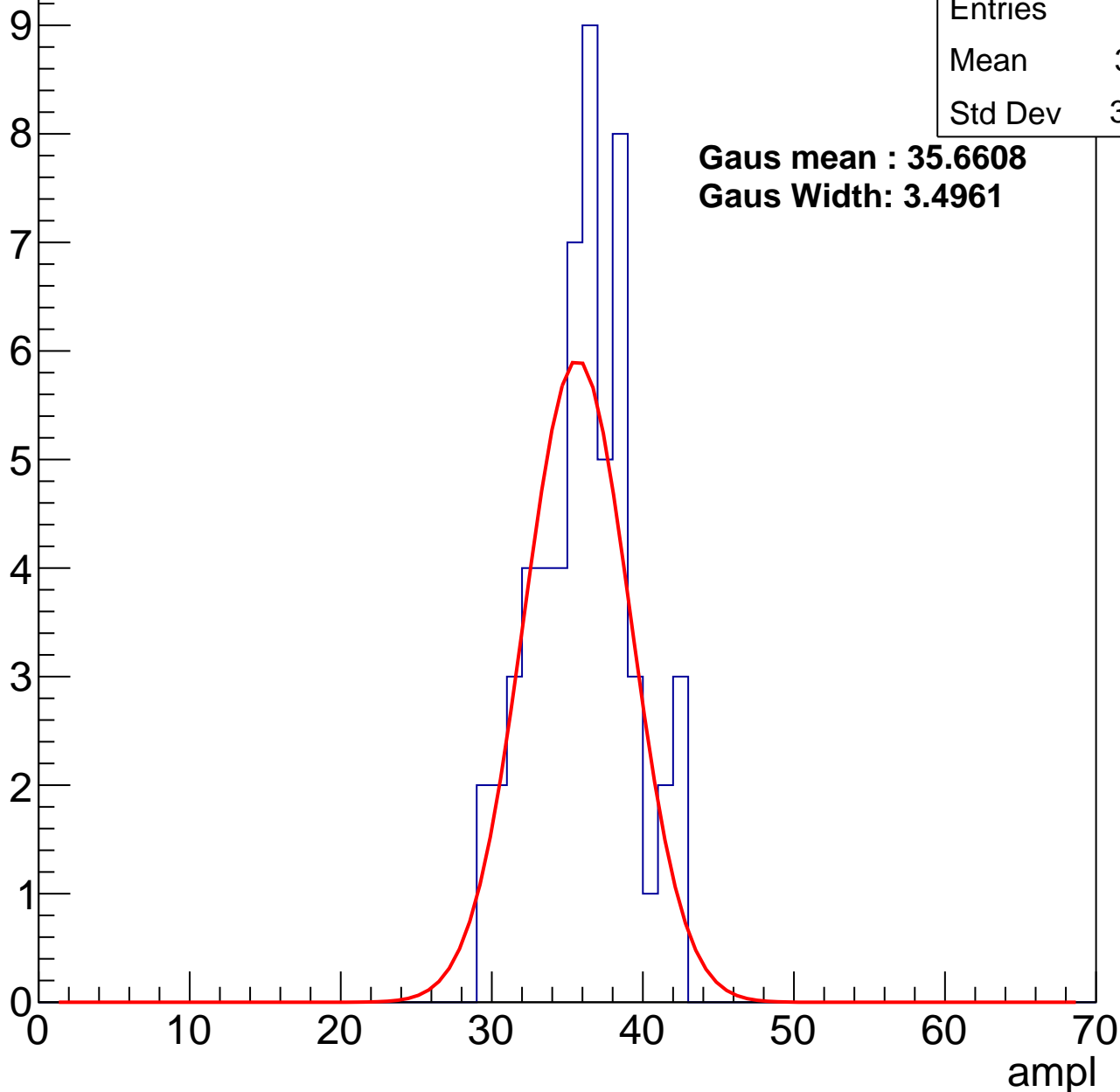
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.61
Std Dev	3.238

Gaus mean : 35.6608

Gaus Width: 3.4961



B1L101S, U4-ch33, adc2

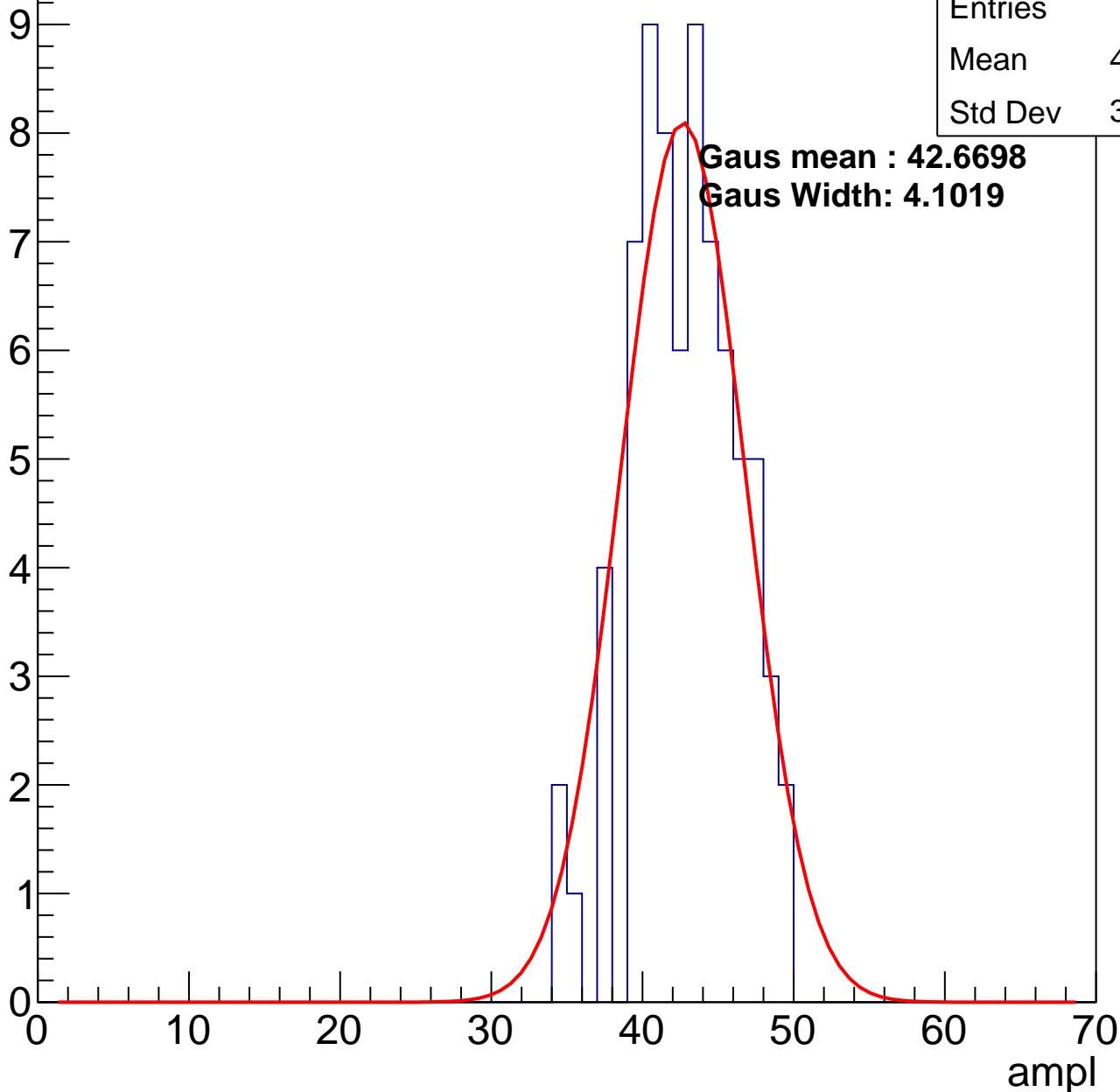
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.38
Std Dev	3.439

Gaus mean : 42.6698

Gaus Width: 4.1019

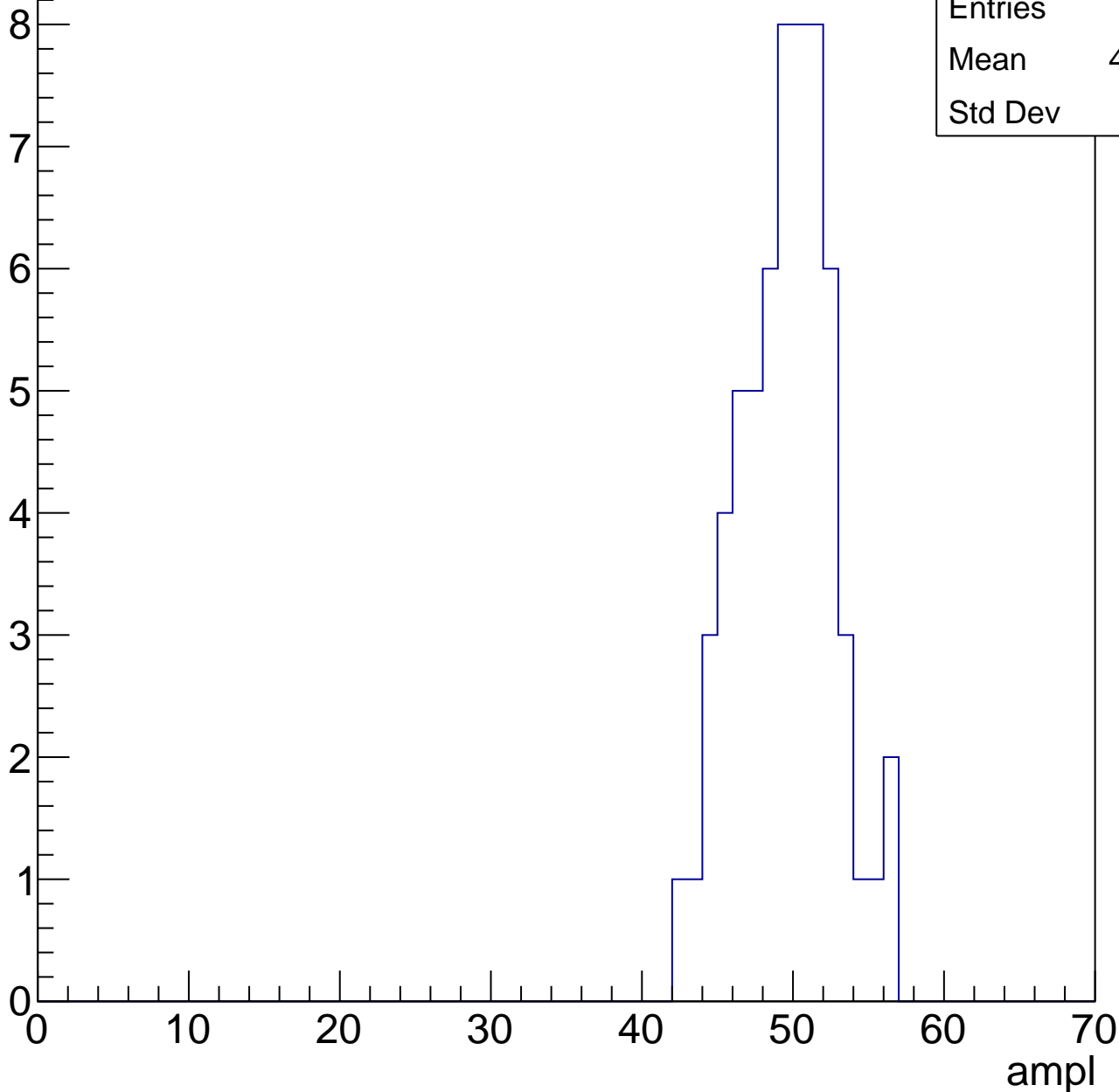


B1L101S, U4-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.06
Std Dev	3.1

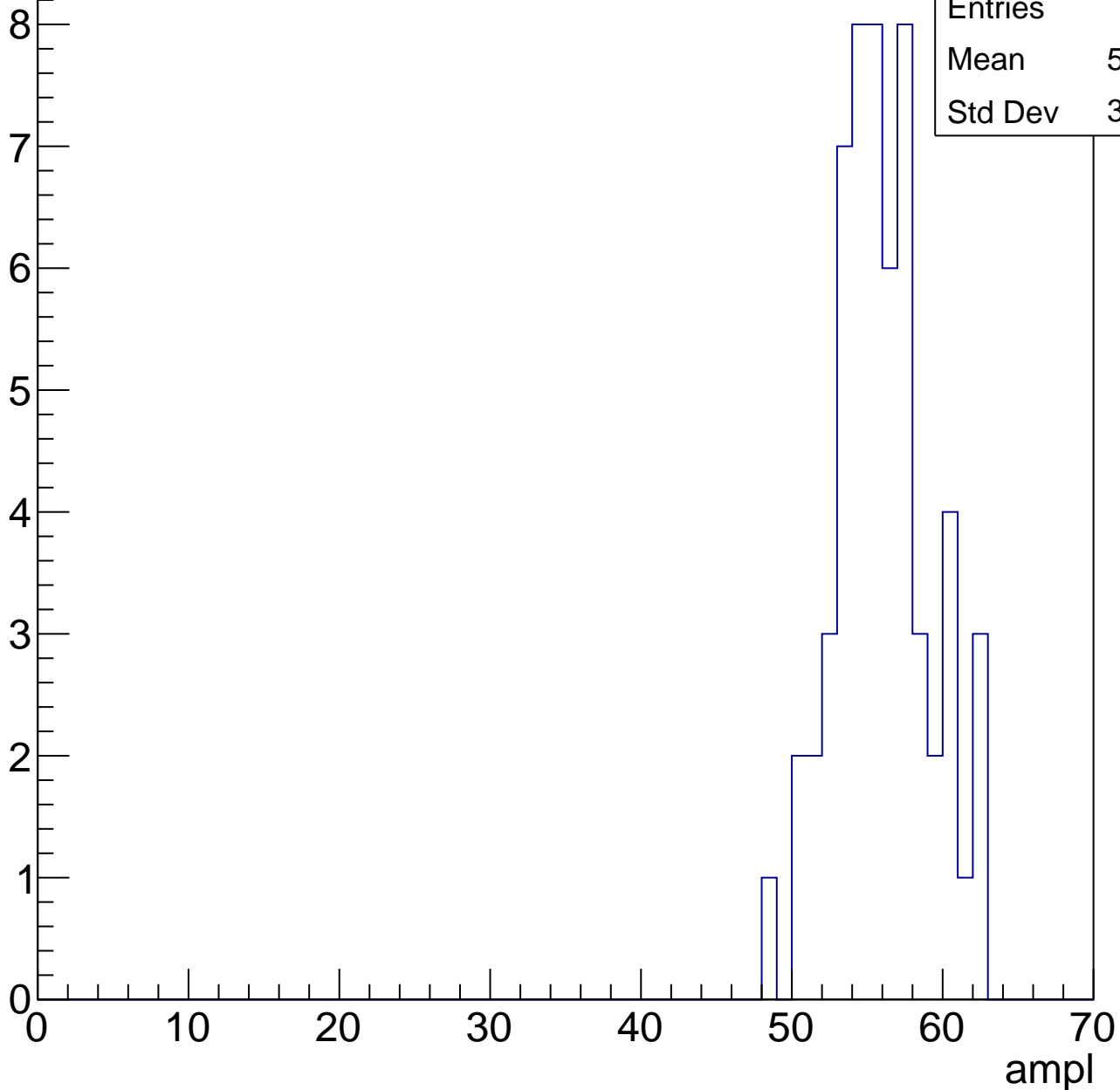


B1L101S, U4-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.52
Std Dev	3.103



B1L101S, U4-ch33, adc5

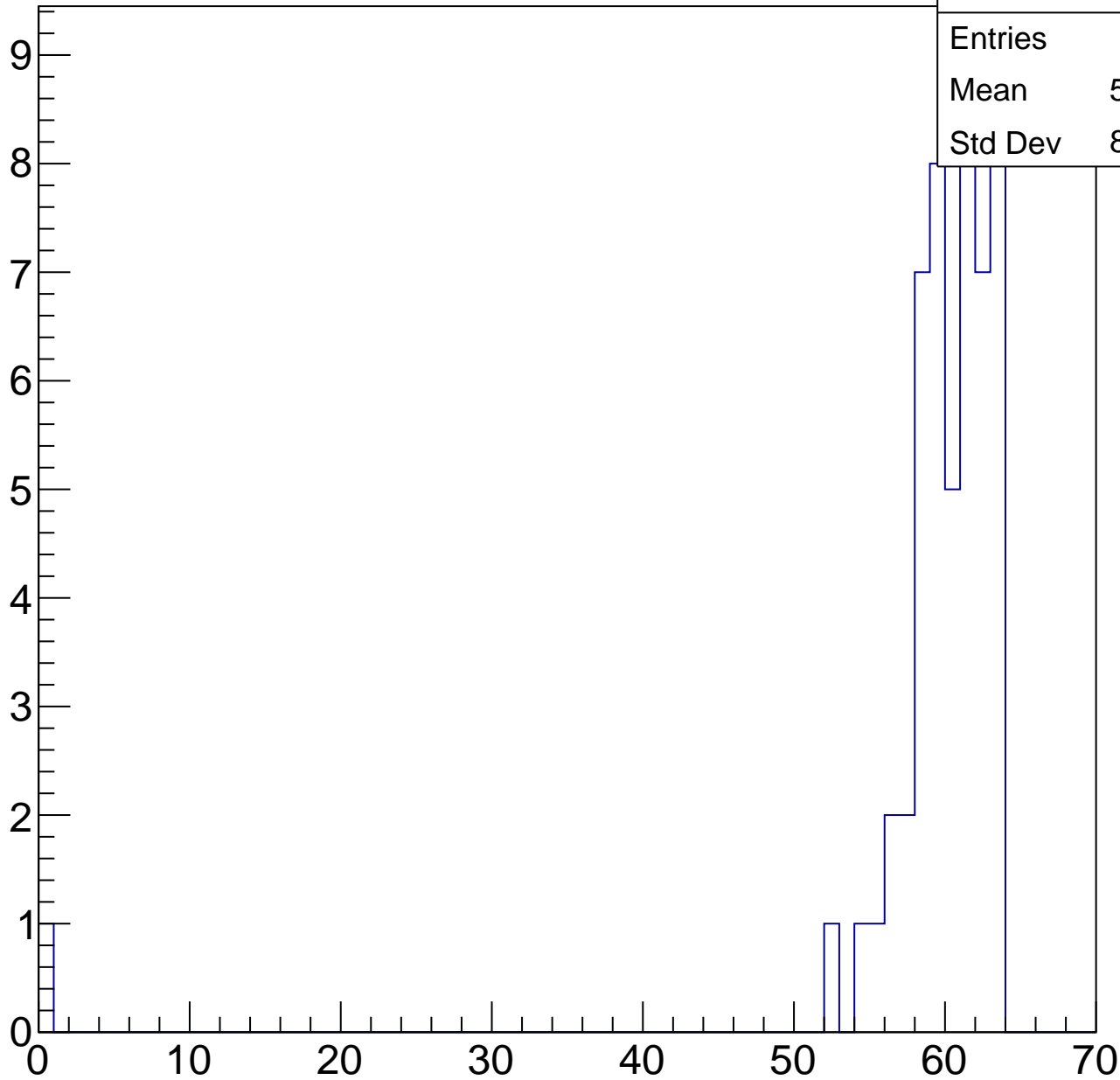
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.69
Std Dev	8.583

ampl



B1L101S, U4-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

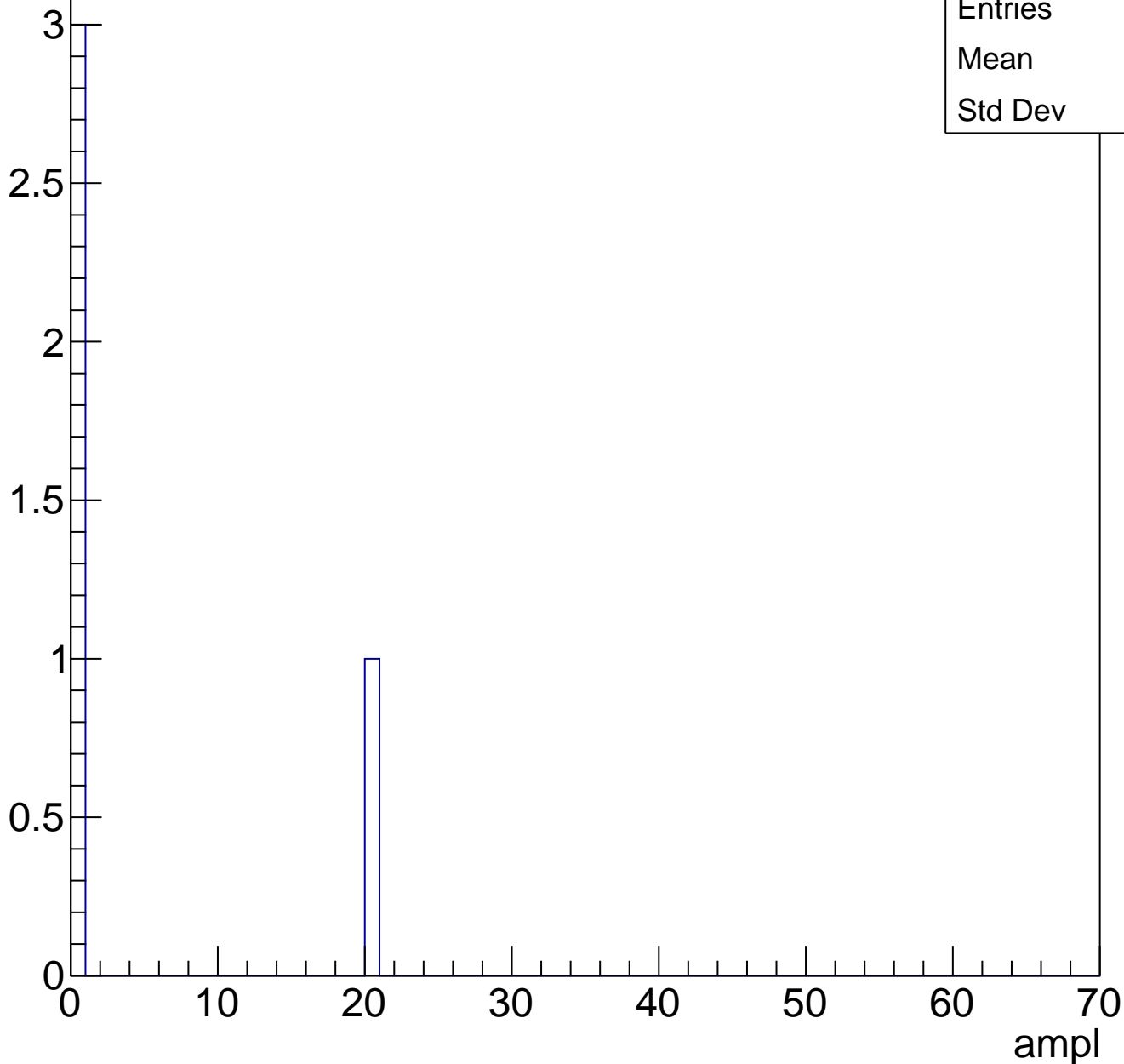
Entry



B1L101S, U4-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch34, adc0

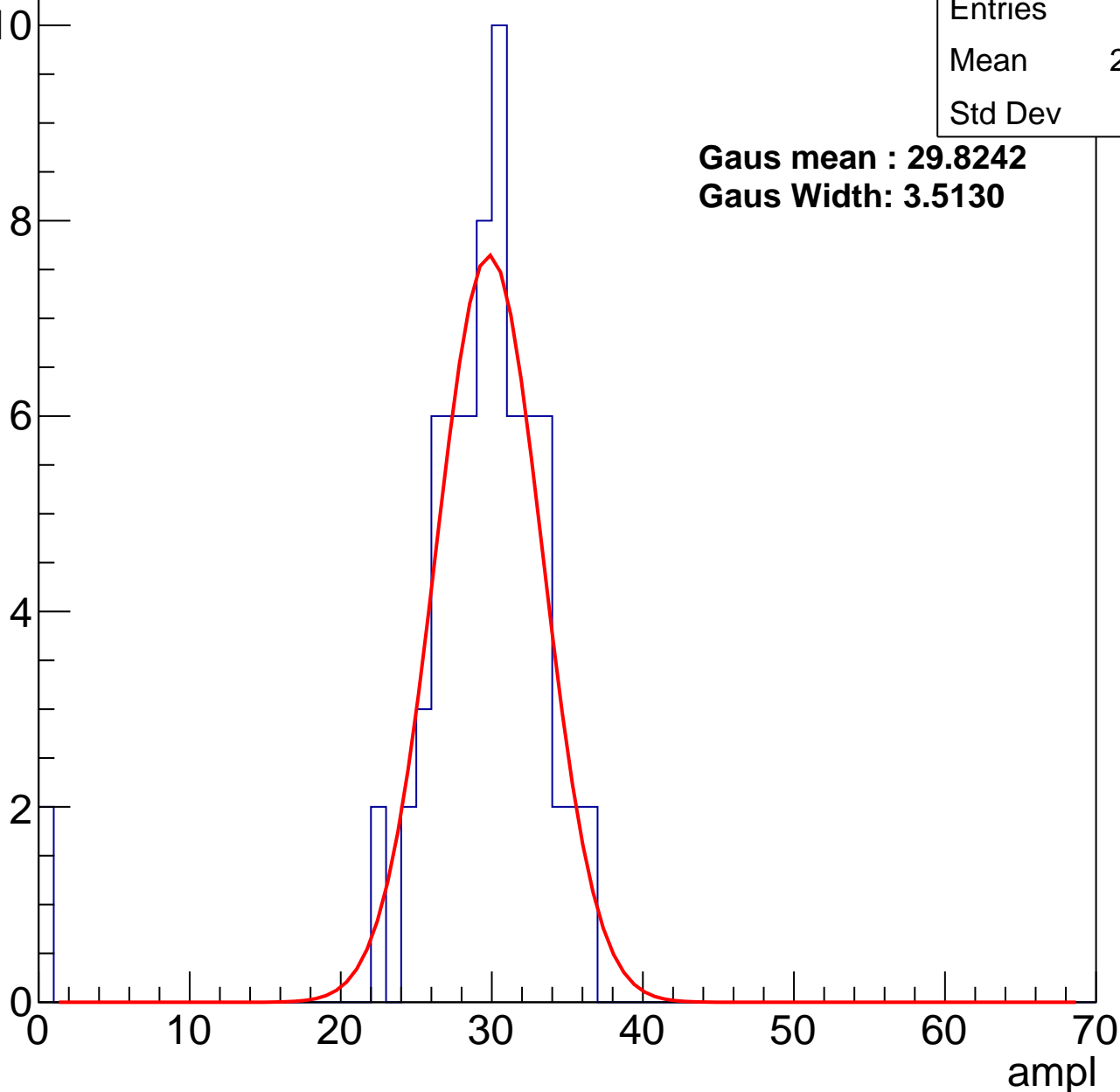
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.57
Std Dev	5.84

Gaus mean : 29.8242

Gaus Width: 3.5130



B1L101S, U4-ch34, adc1

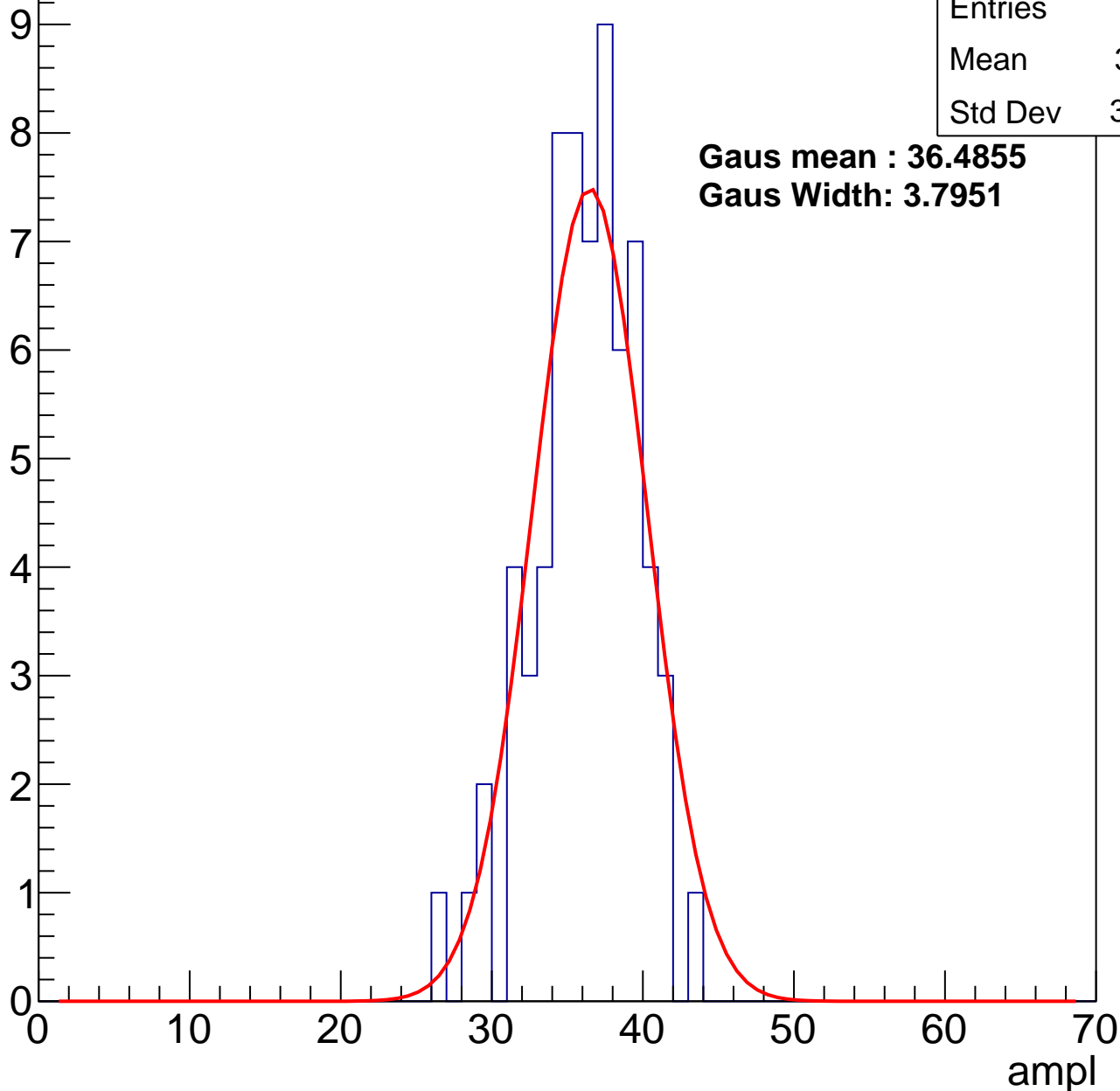
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.71
Std Dev	3.352

Gaus mean : 36.4855

Gaus Width: 3.7951



B1L101S, U4-ch34, adc2

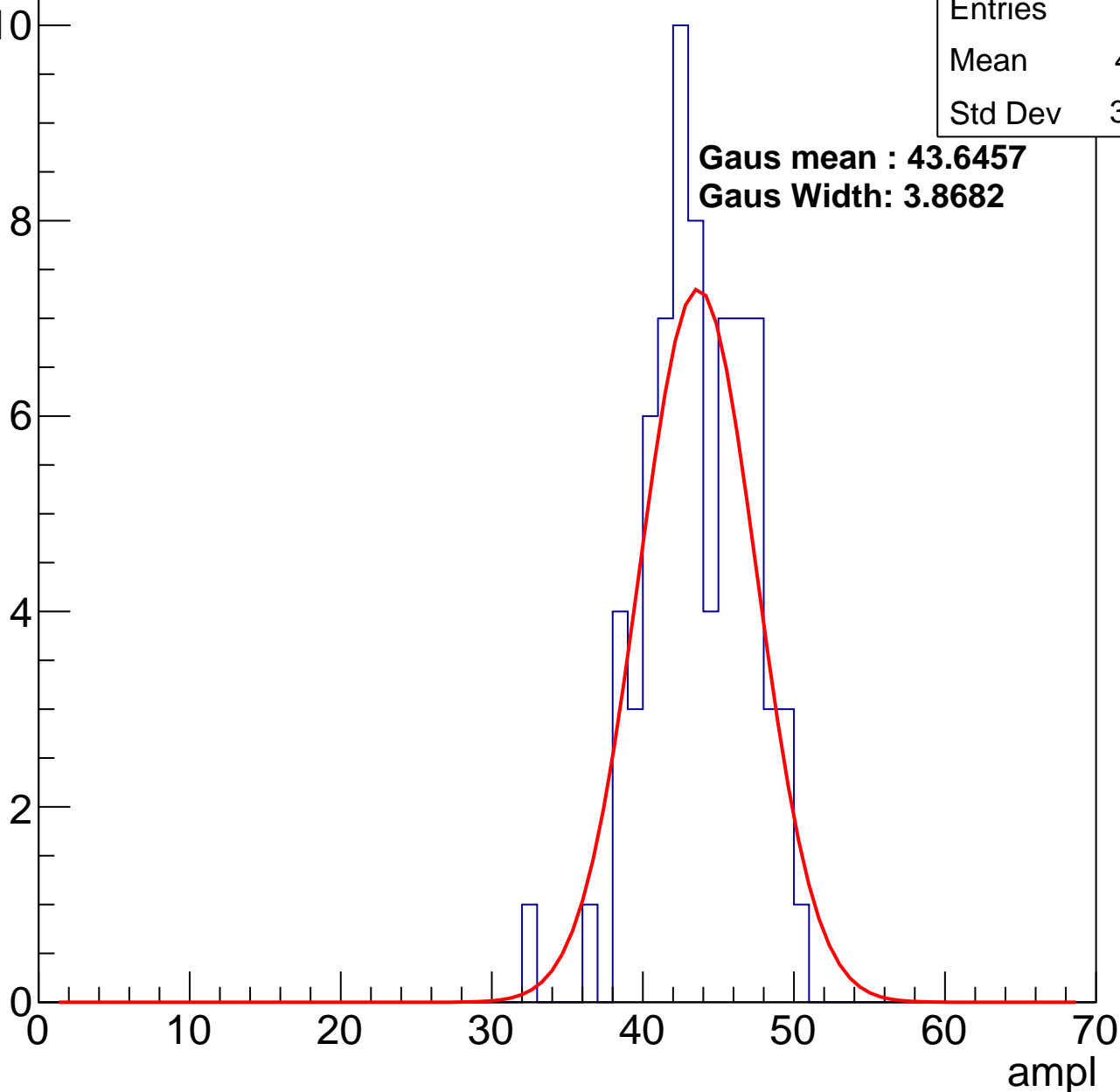
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.21
Std Dev	3.428

Gaus mean : 43.6457

Gaus Width: 3.8682

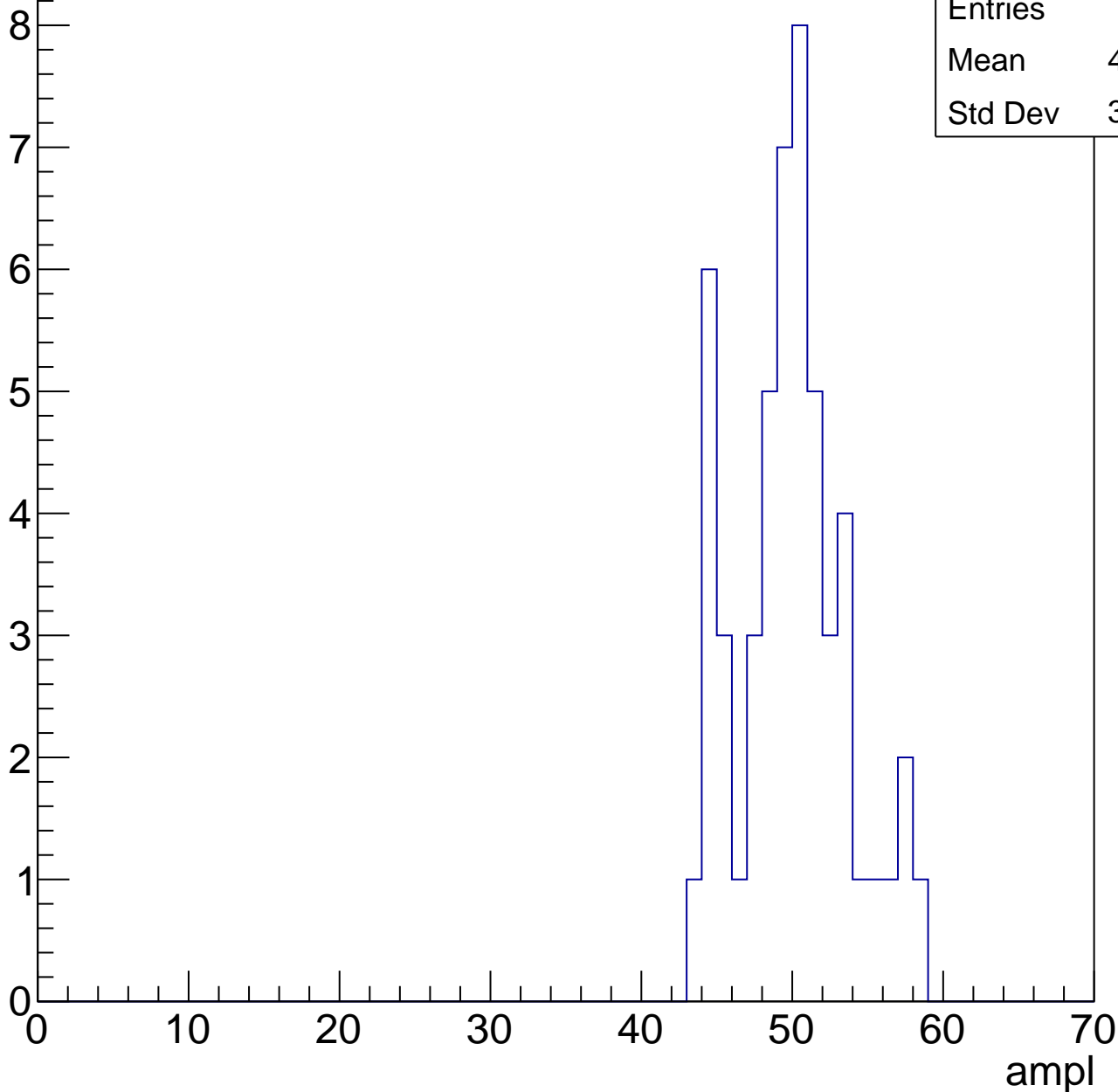


B1L101S, U4-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

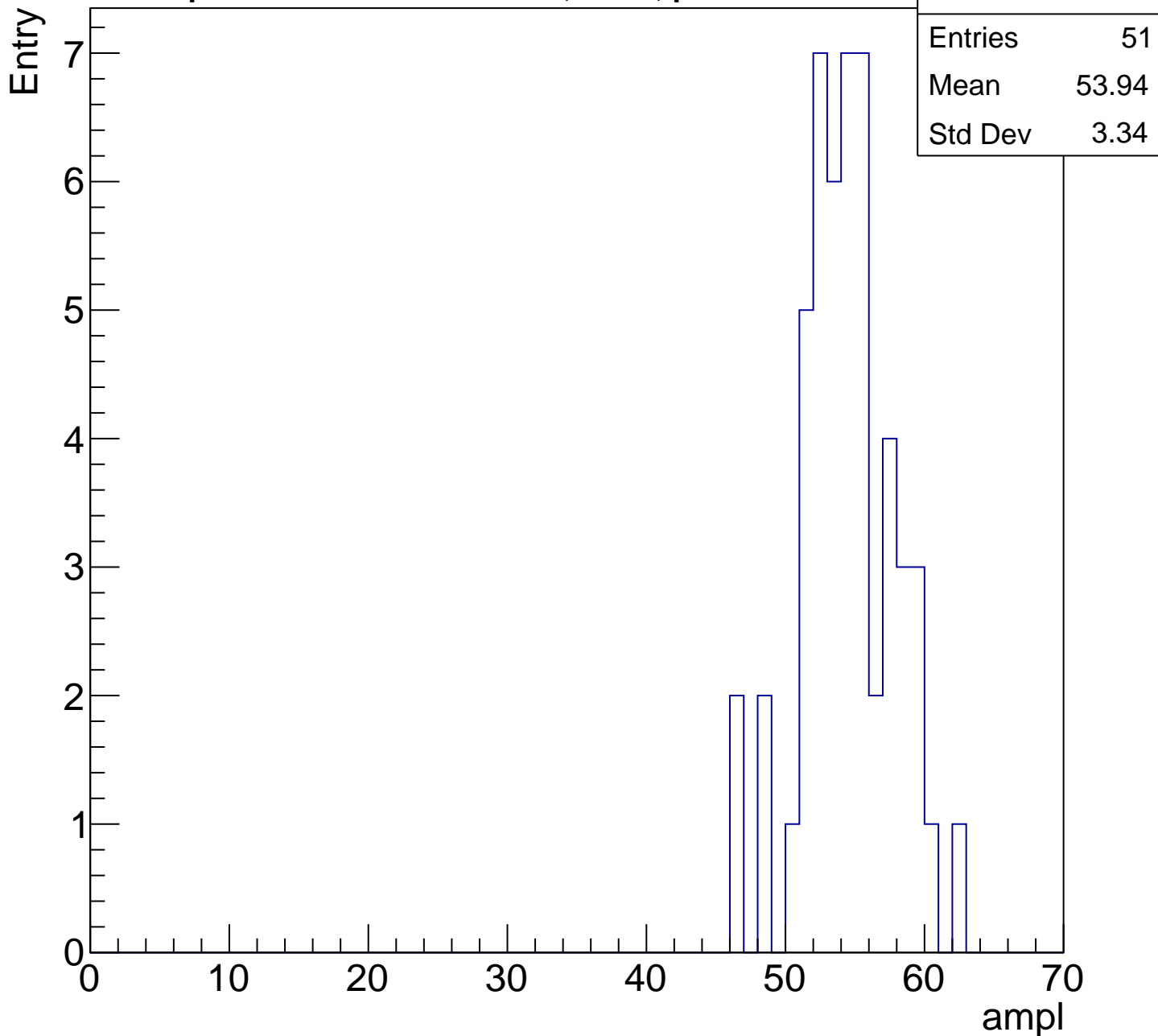
Entry

Entries	52
Mean	49.46
Std Dev	3.635



B1L101S, U4-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

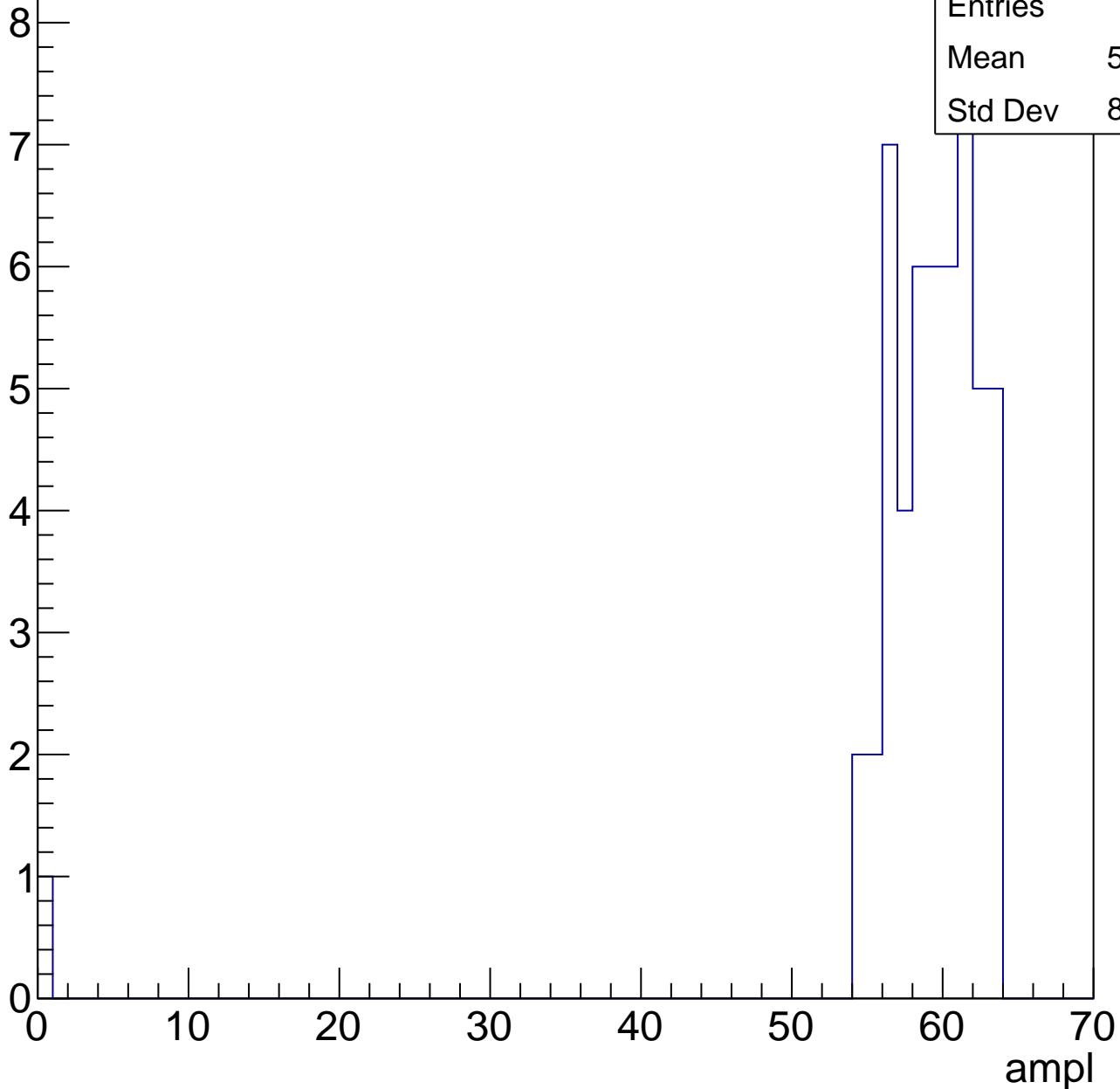


B1L101S, U4-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.94
Std Dev	8.495

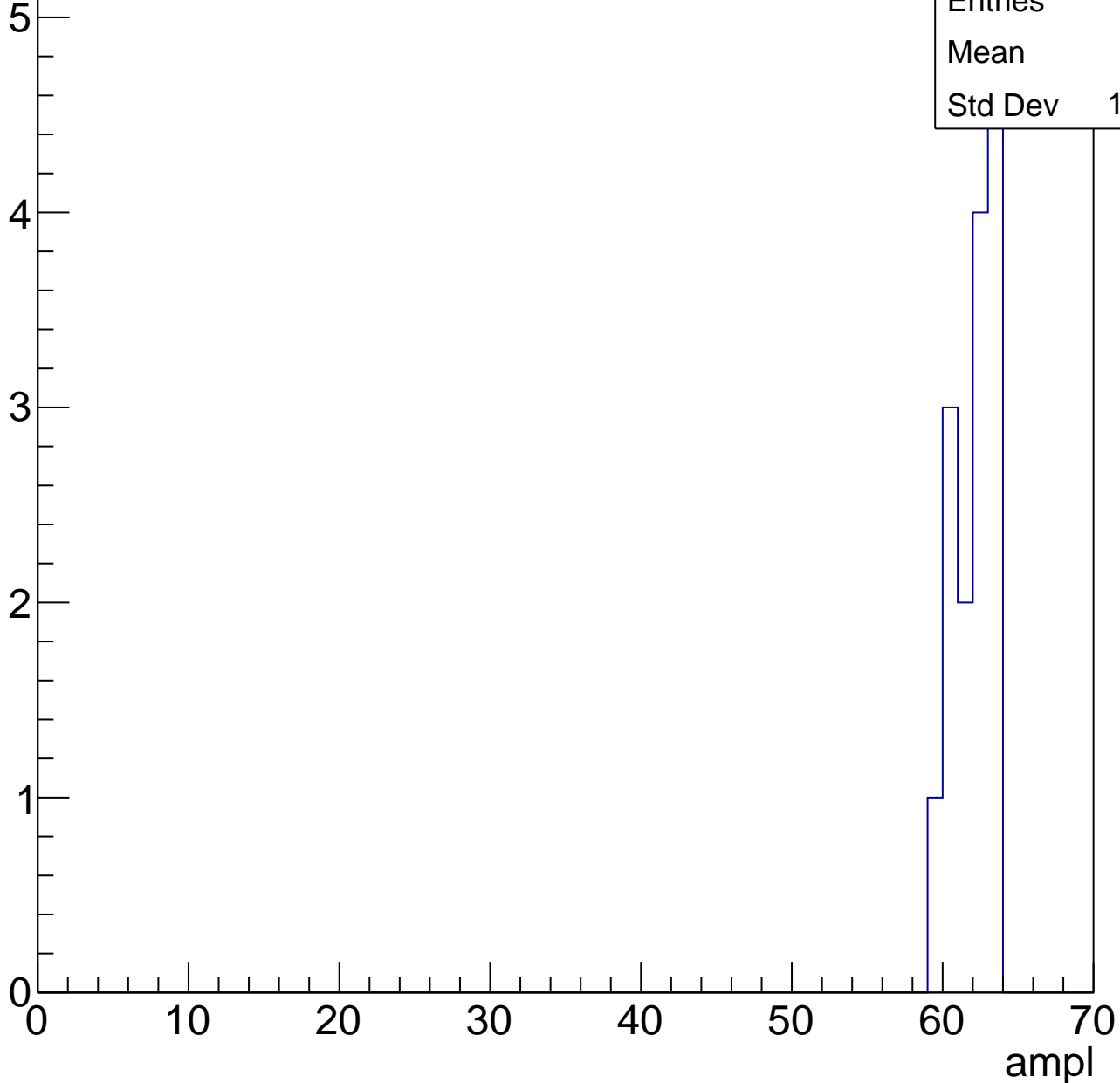


B1L101S, U4-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.6
Std Dev	1.306



B1L101S, U4-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch35, adc0

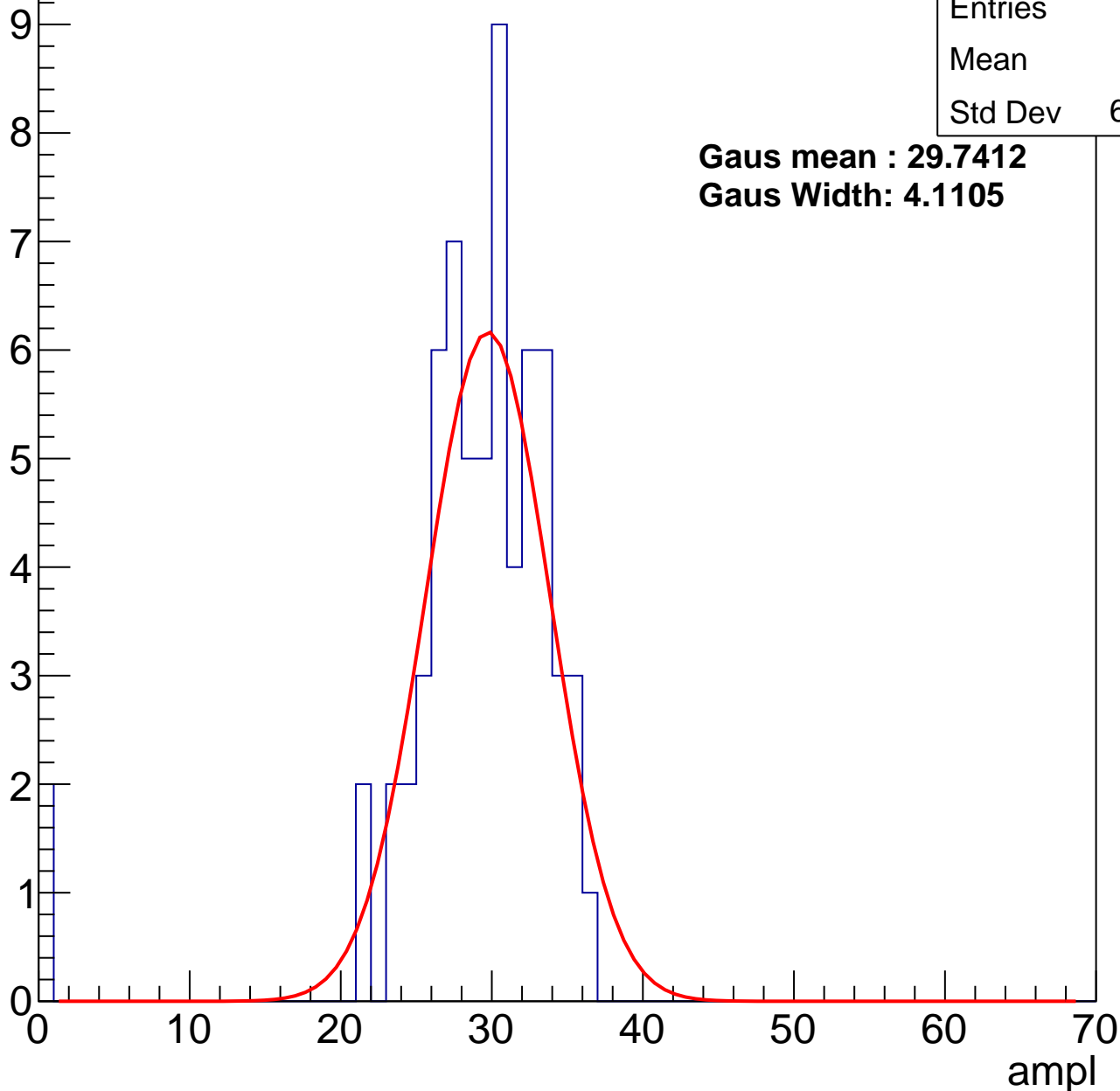
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.3
Std Dev	6.083

Gaus mean : 29.7412

Gaus Width: 4.1105



B1L101S, U4-ch35, adc1

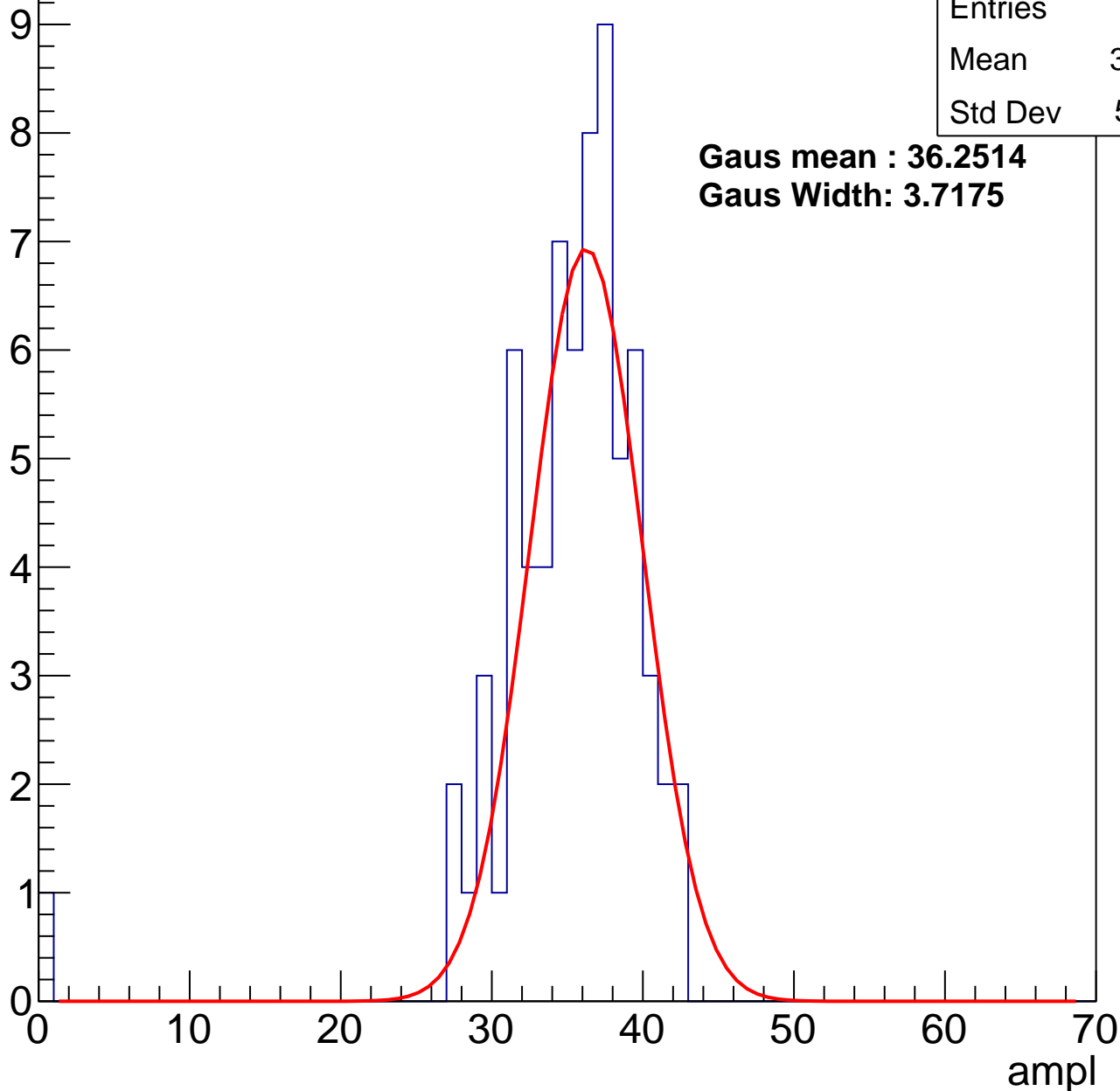
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	34.63
Std Dev	5.491

Gaus mean : 36.2514

Gaus Width: 3.7175



B1L101S, U4-ch35, adc2

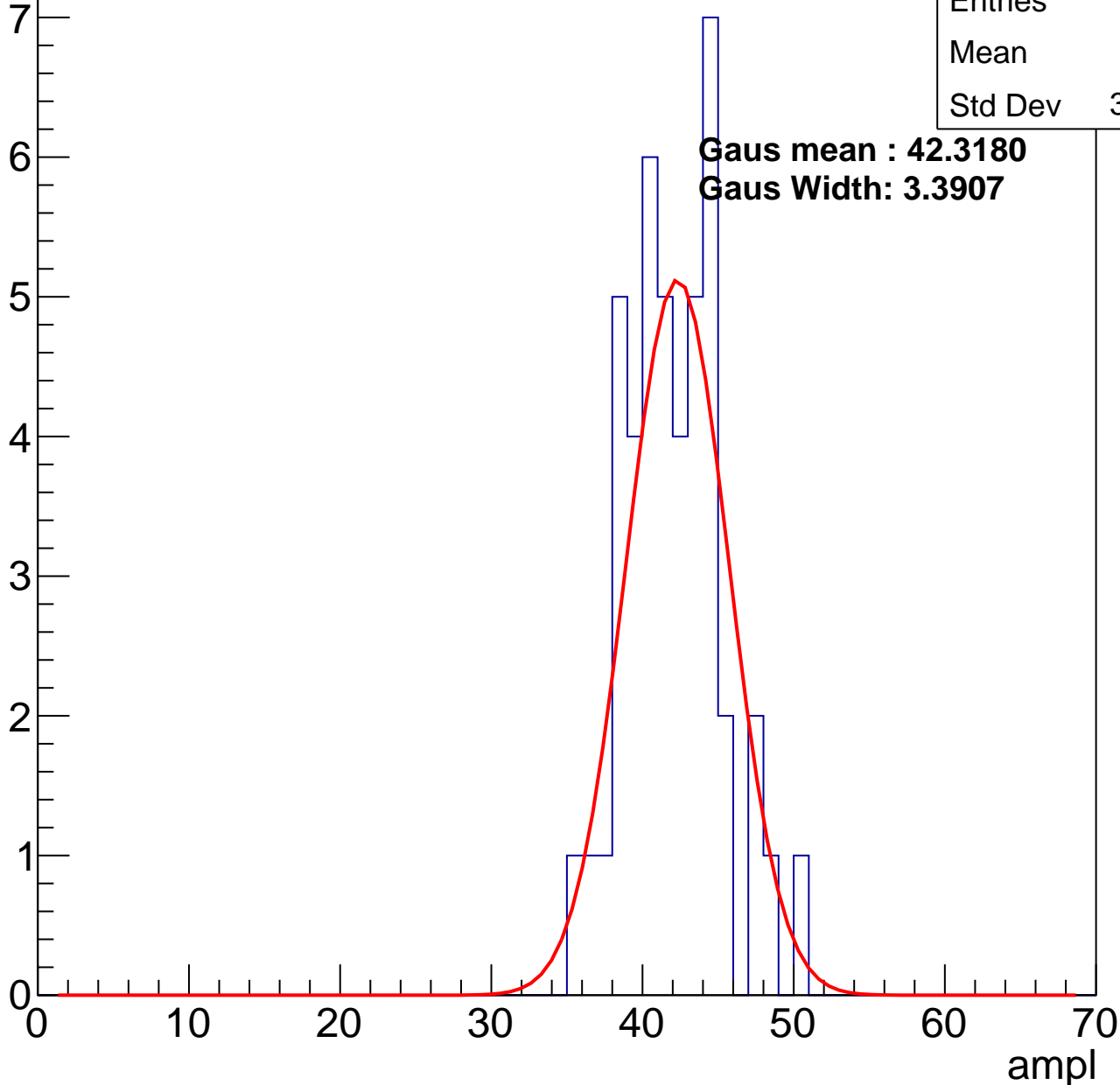
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	41.6
Std Dev	3.158

Gaus mean : 42.3180

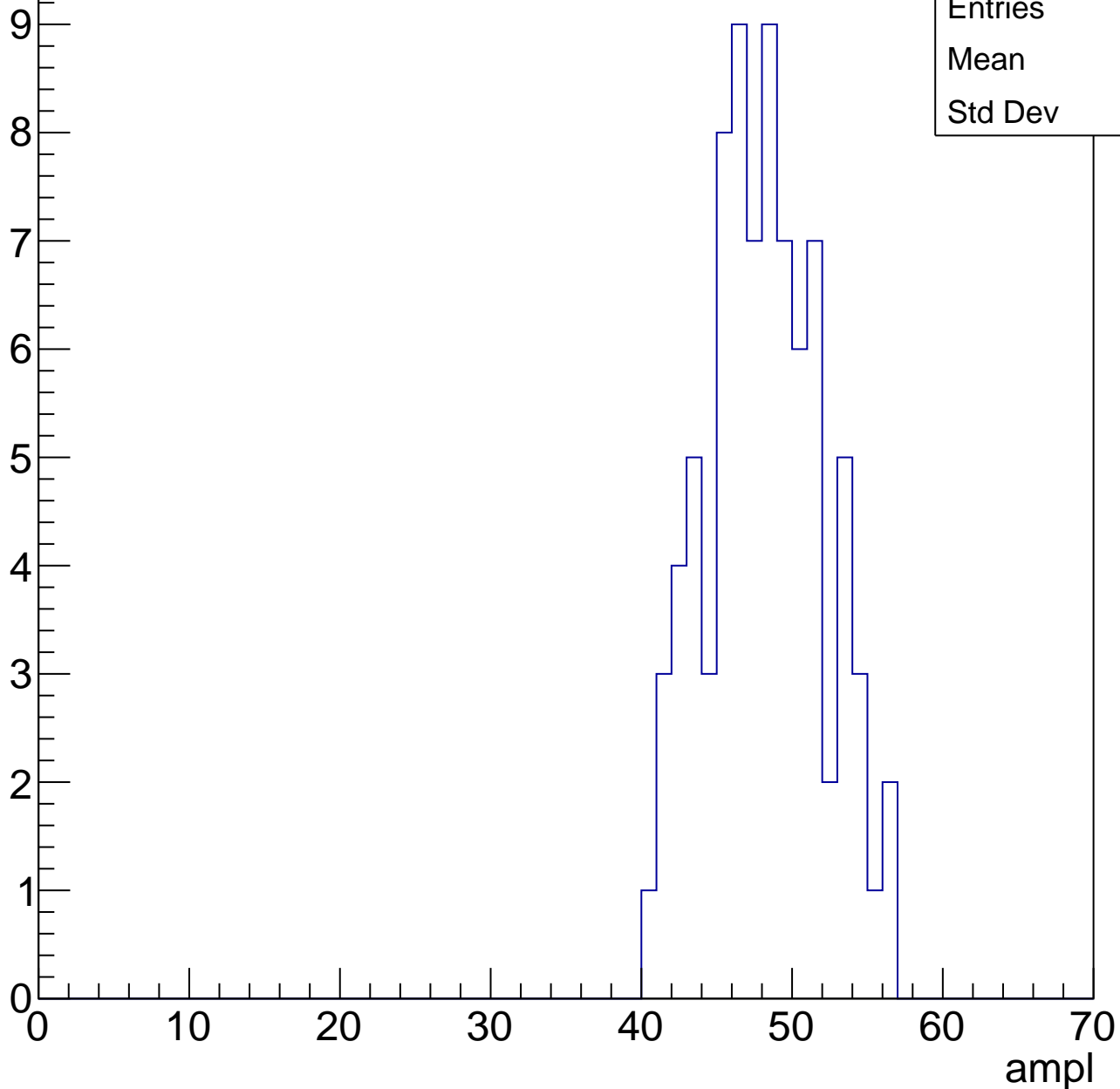
Gaus Width: 3.3907



B1L101S, U4-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



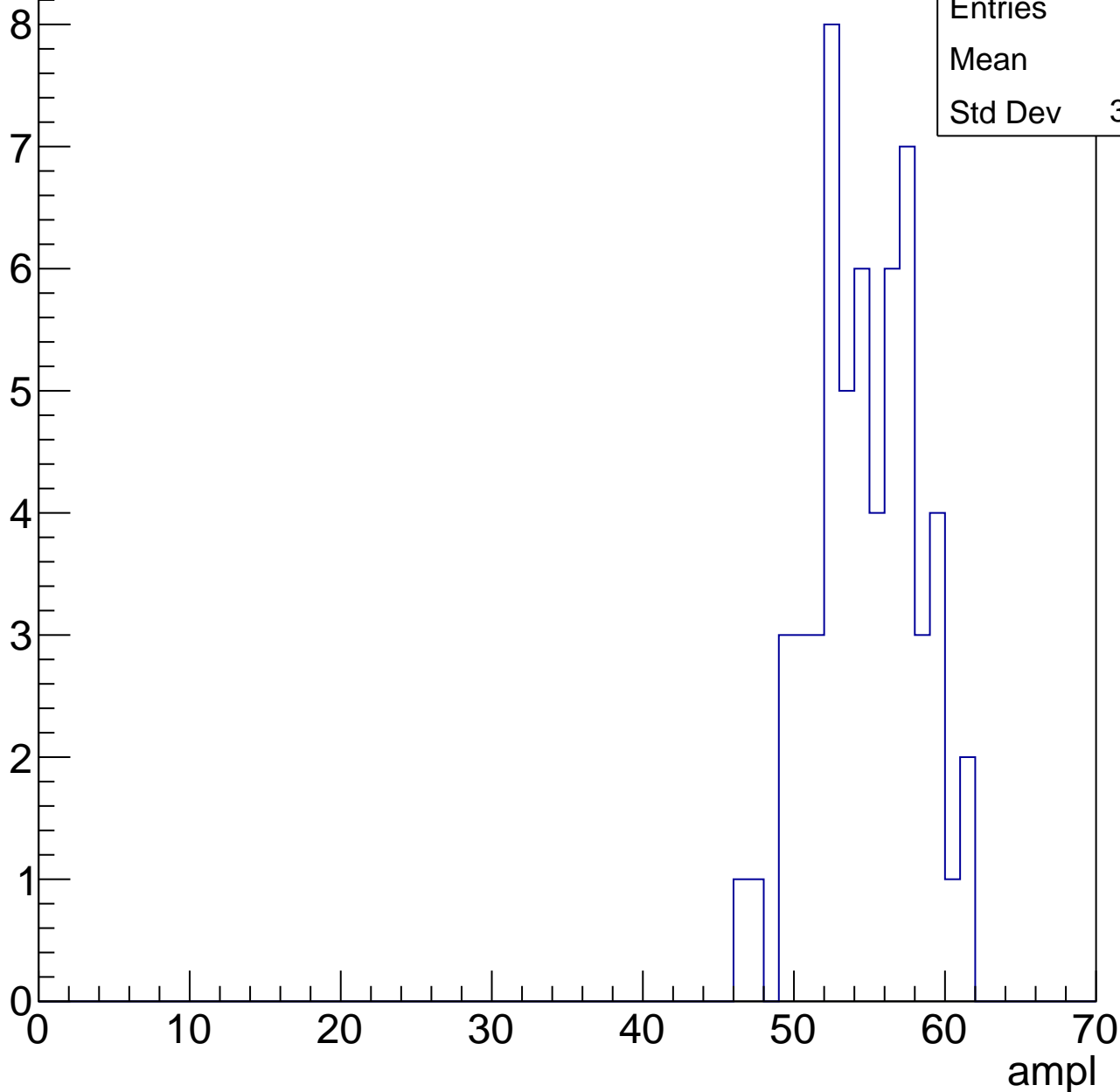
Entries	82
Mean	47.7
Std Dev	3.78

B1L101S, U4-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	54.3
Std Dev	3.423

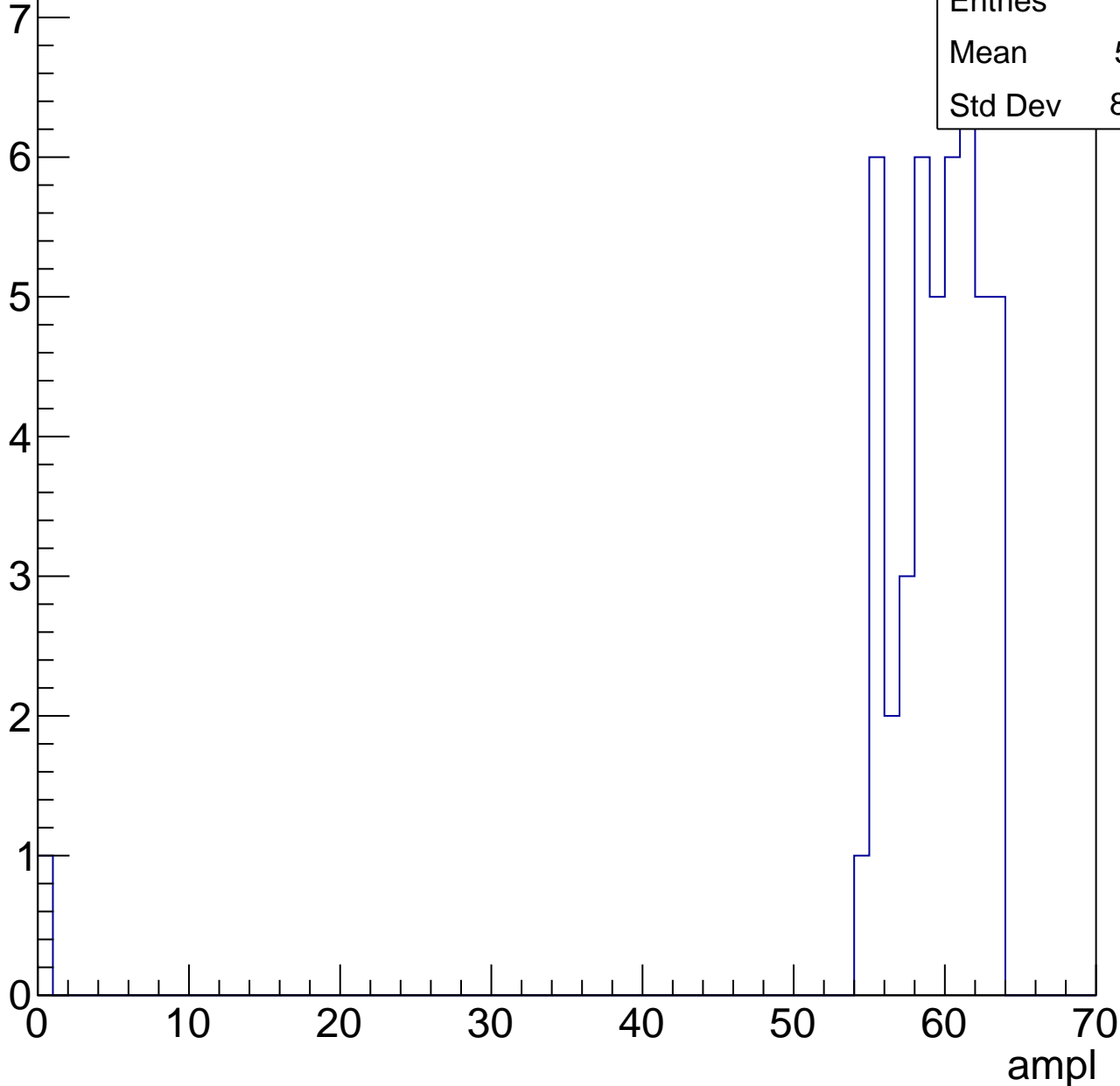


B1L101S, U4-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	57.91
Std Dev	8.922

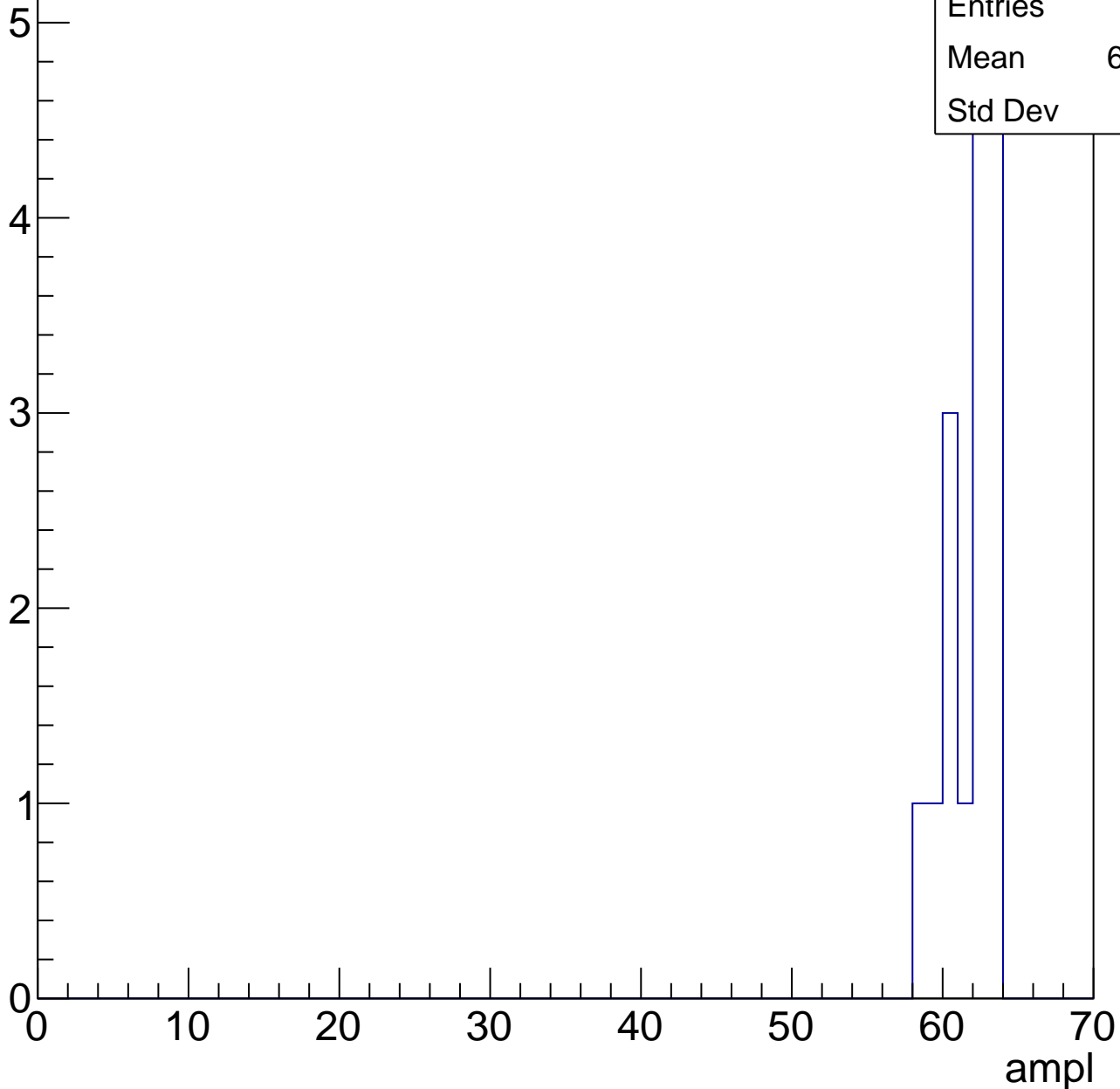


B1L101S, U4-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.44
Std Dev	1.54



B1L101S, U4-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch36, adc0

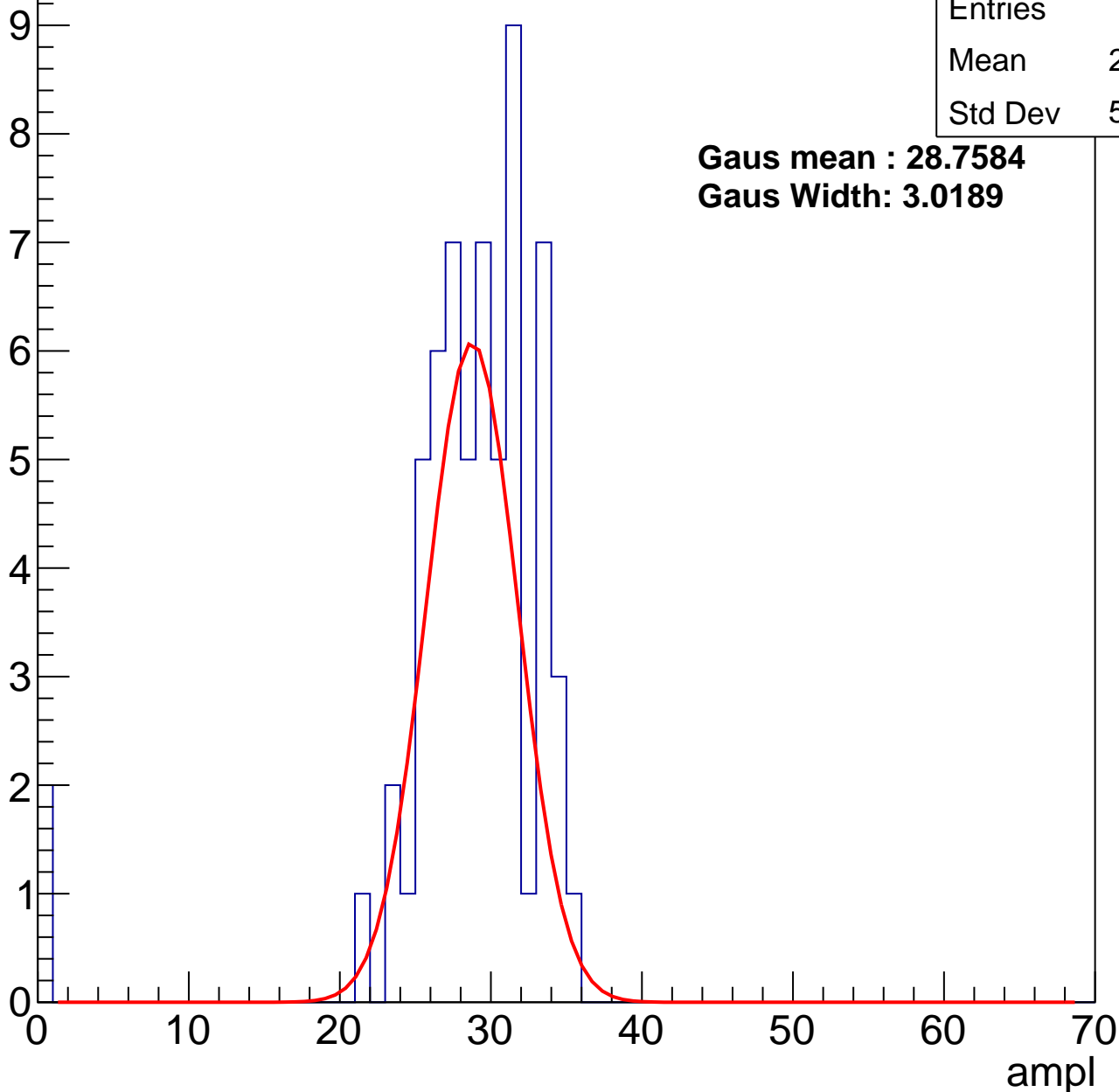
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	27.95
Std Dev	5.977

Gaus mean : 28.7584

Gaus Width: 3.0189



B1L101S, U4-ch36, adc1

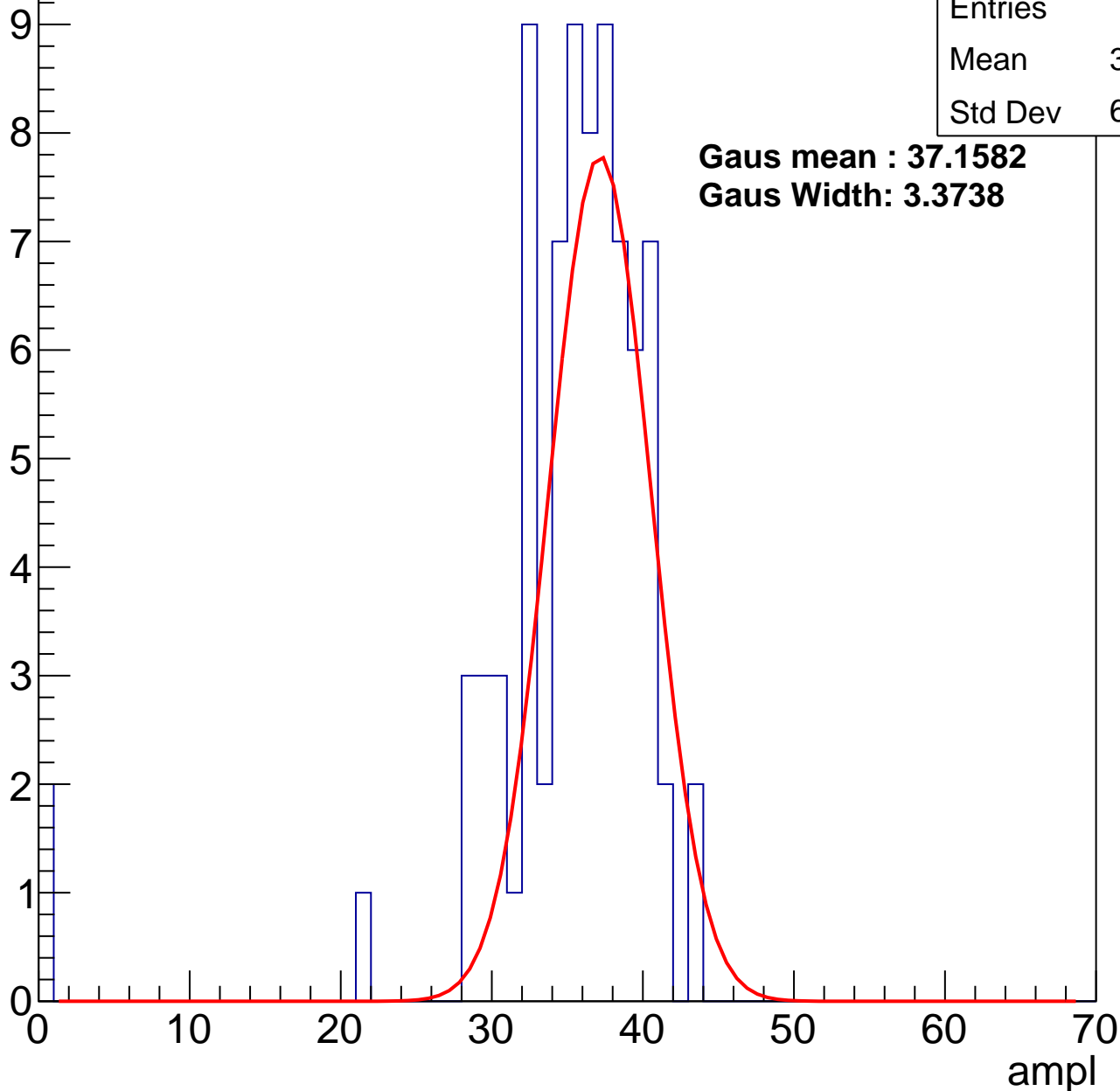
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	34.43
Std Dev	6.703

Gaus mean : 37.1582

Gaus Width: 3.3738



B1L101S, U4-ch36, adc2

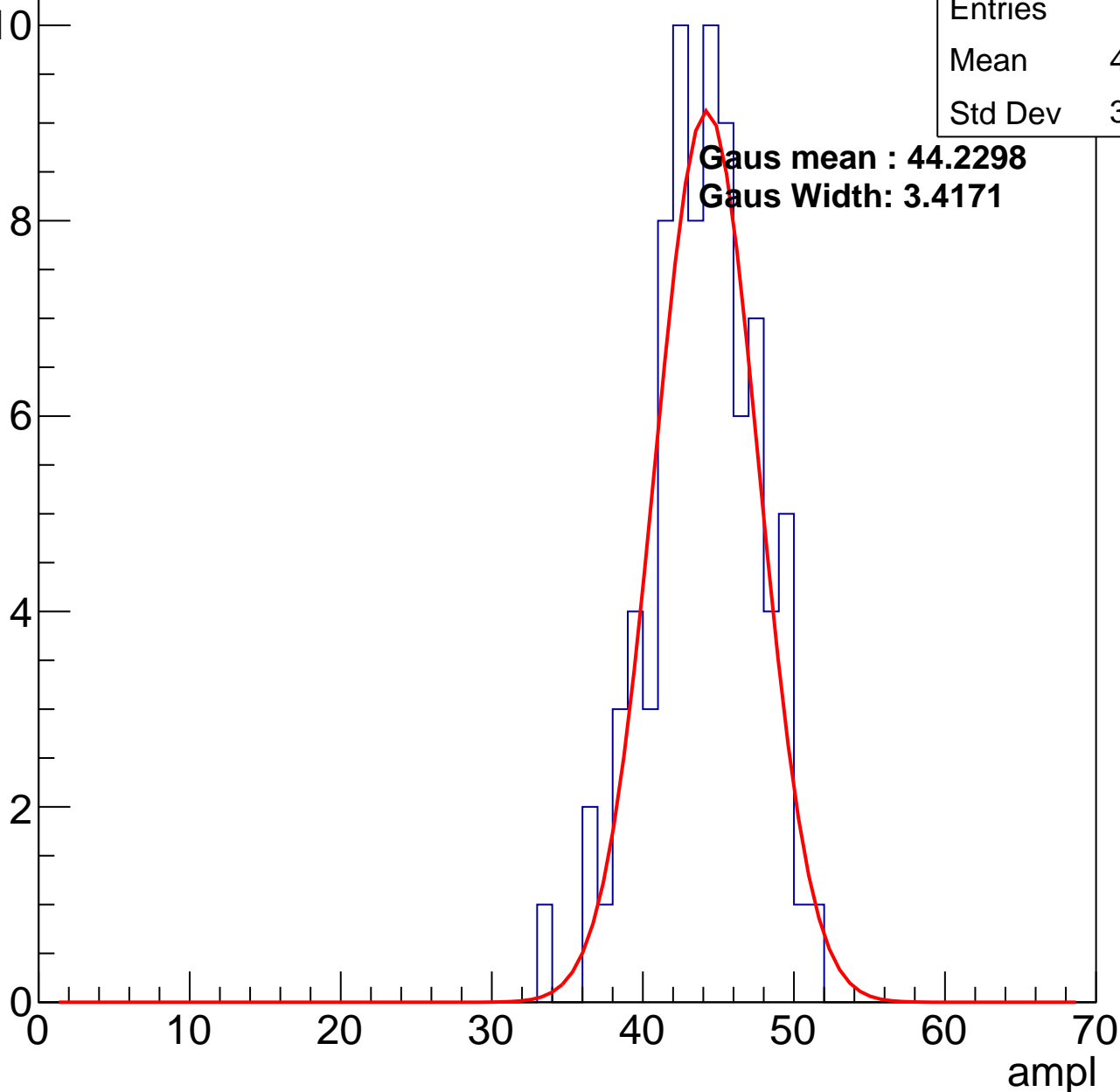
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	43.52
Std Dev	3.514

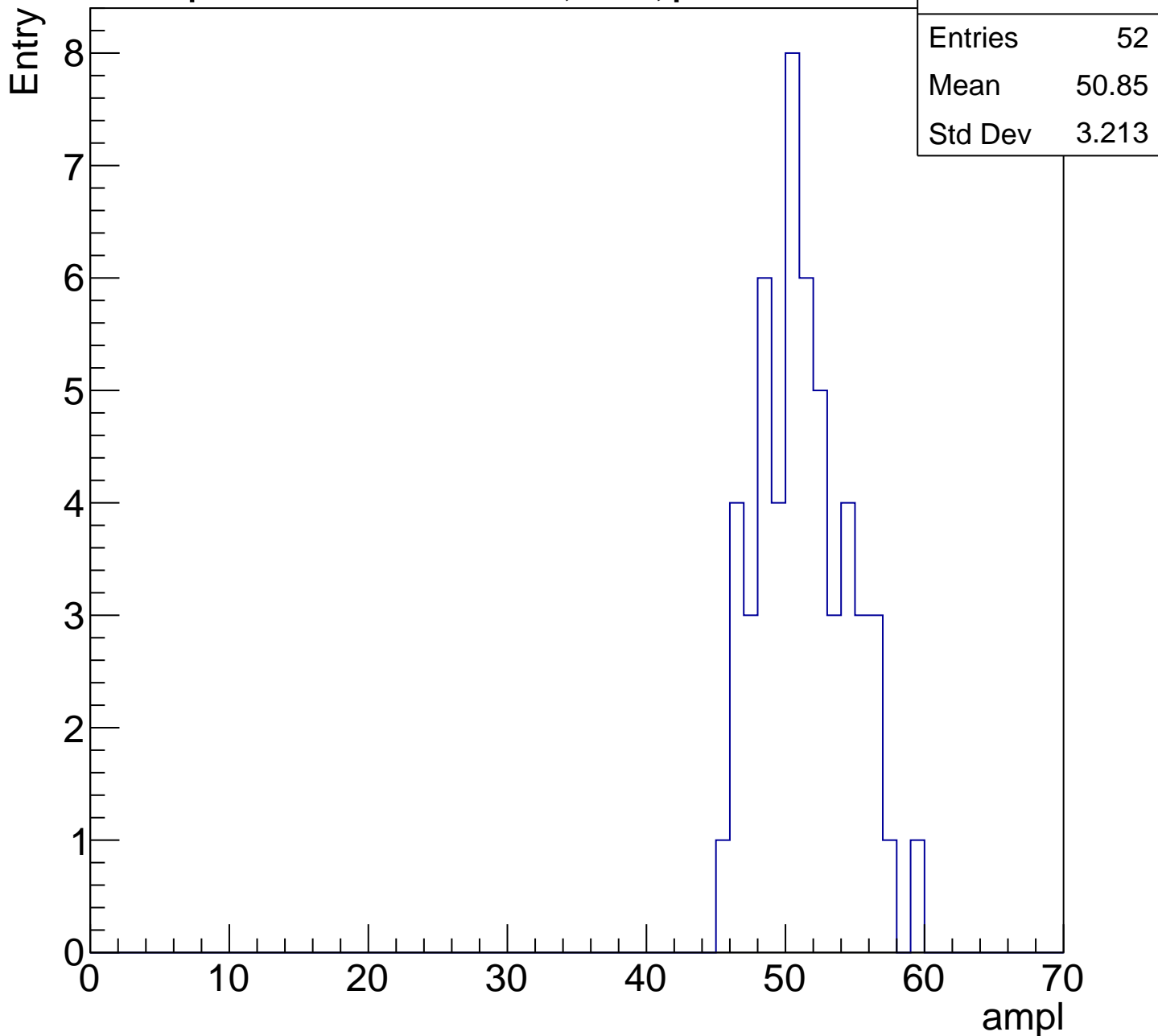
Gaus mean : 44.2298

Gaus Width: 3.4171



B1L101S, U4-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

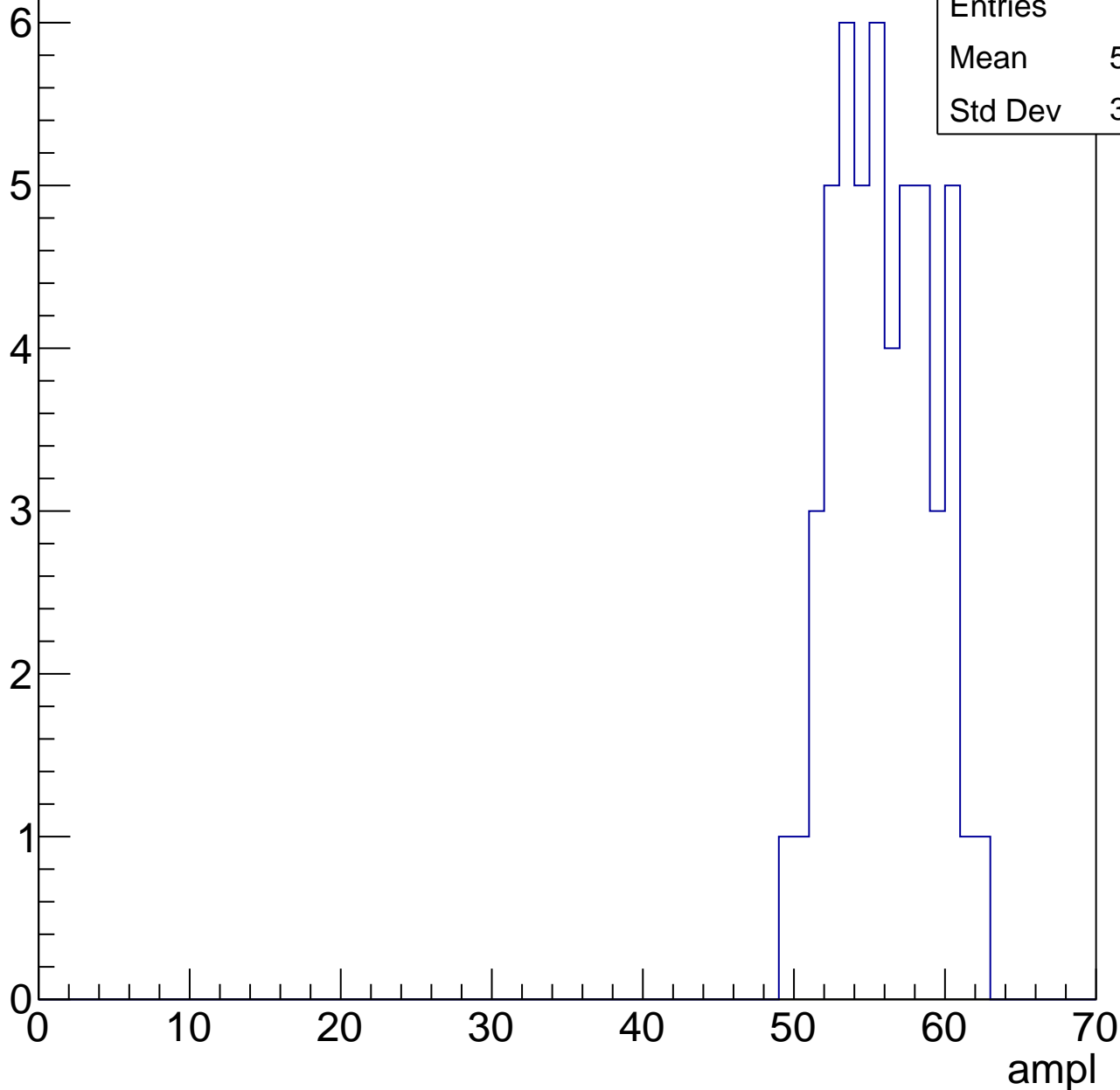


B1L101S, U4-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.47
Std Dev	3.127

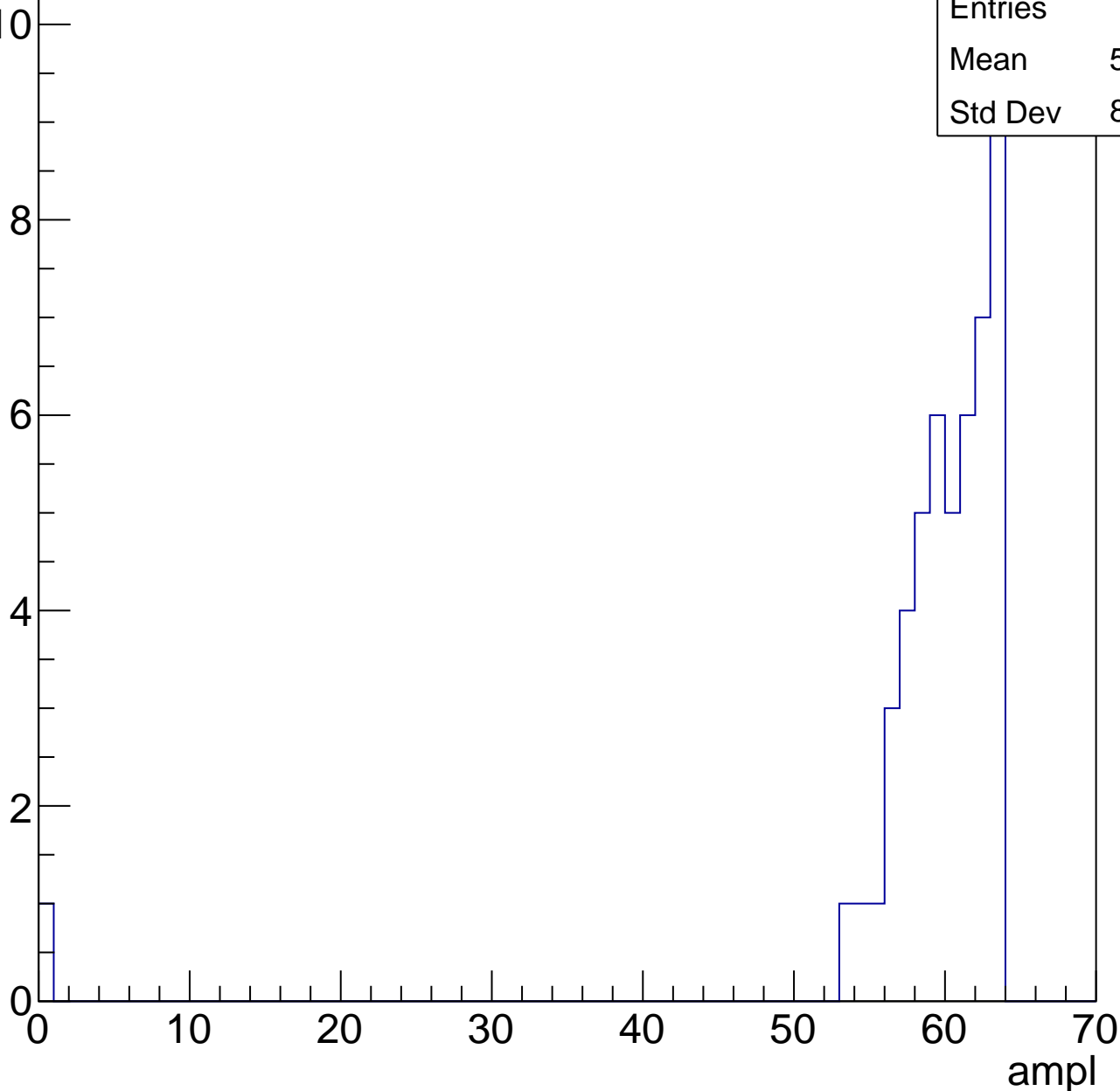


B1L101S, U4-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

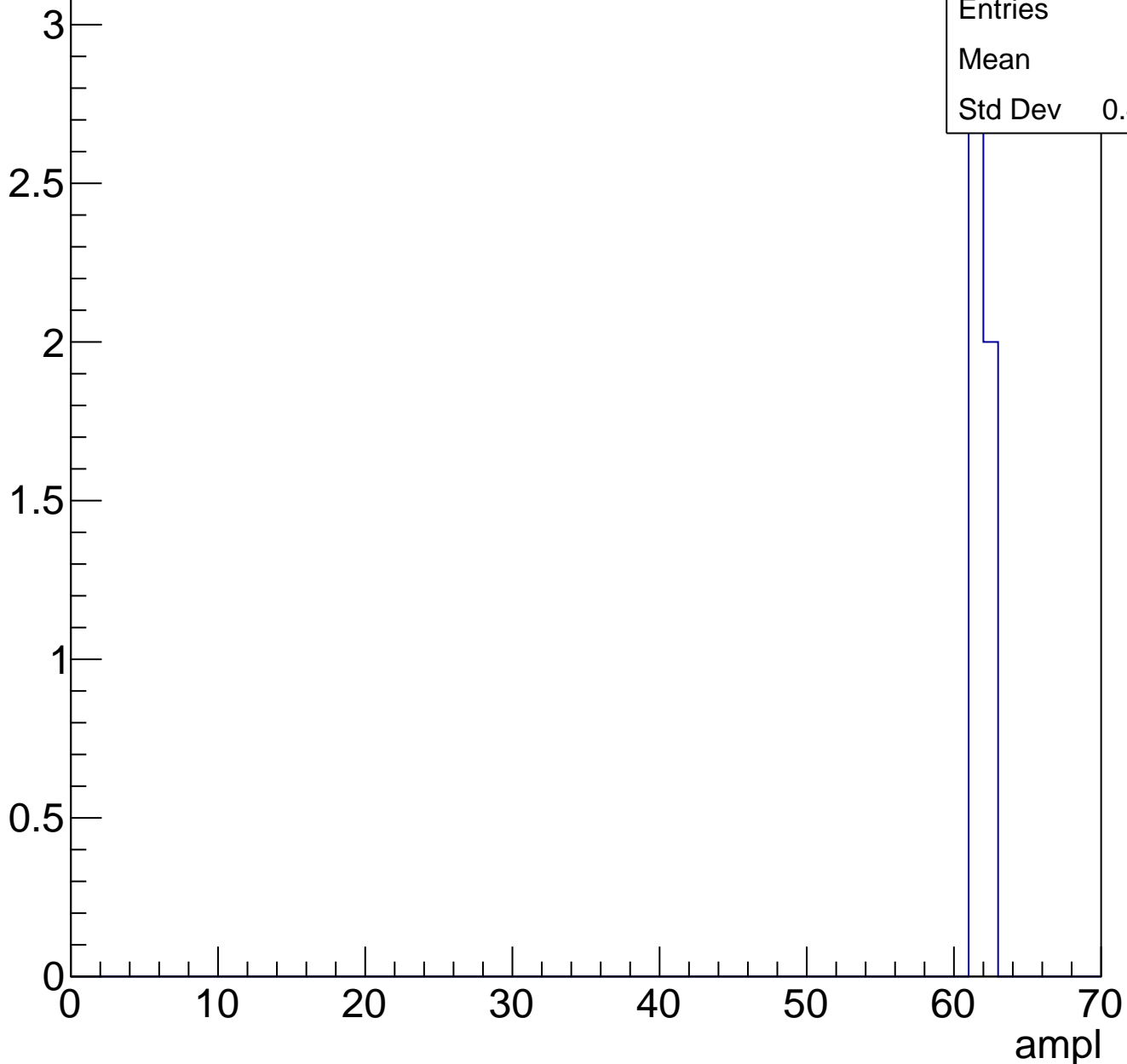
Entries	50
Mean	58.64
Std Dev	8.777



B1L101S, U4-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch37, adc0

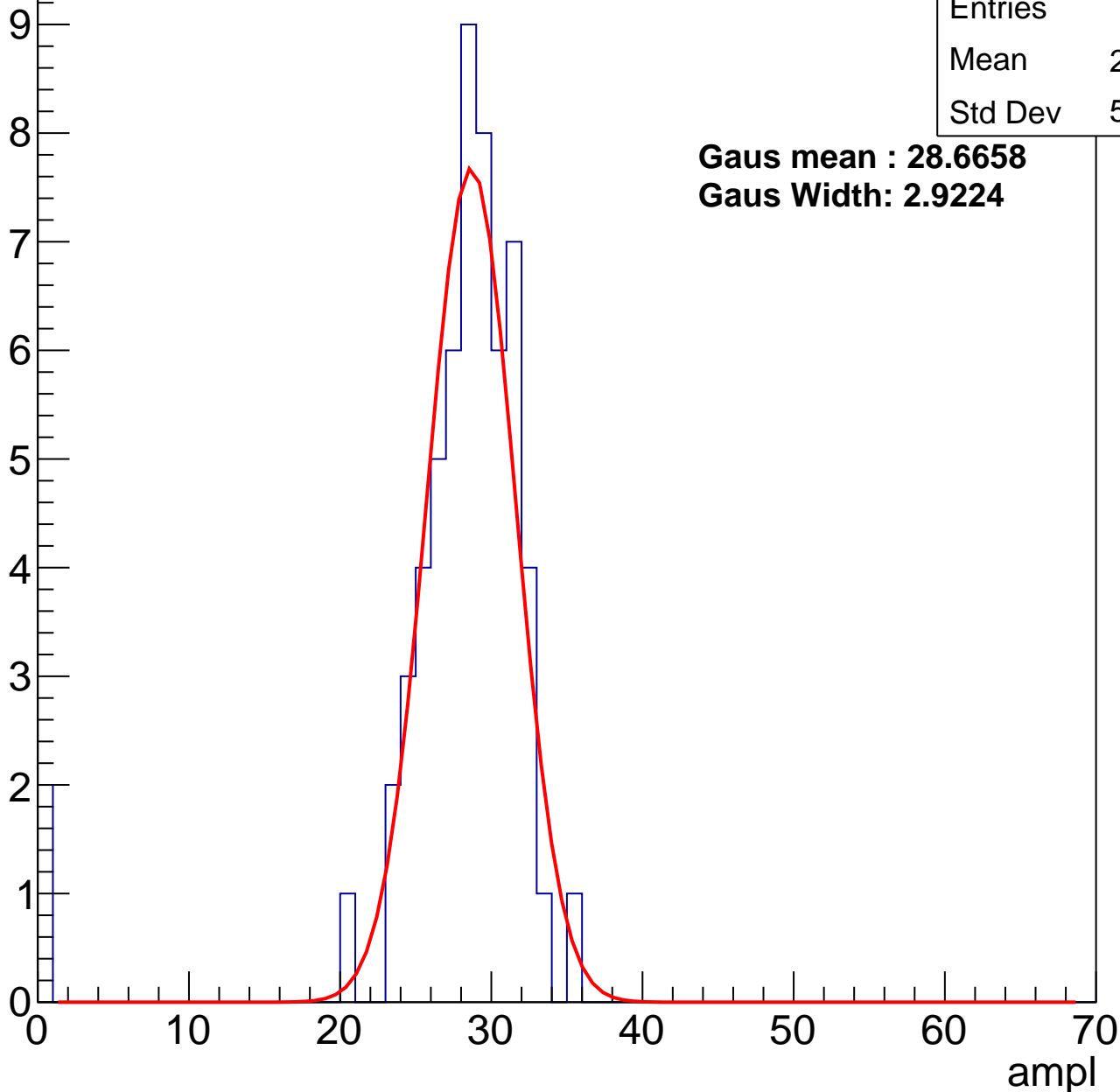
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.24
Std Dev	5.809

Gaus mean : 28.6658

Gaus Width: 2.9224



B1L101S, U4-ch37, adc1

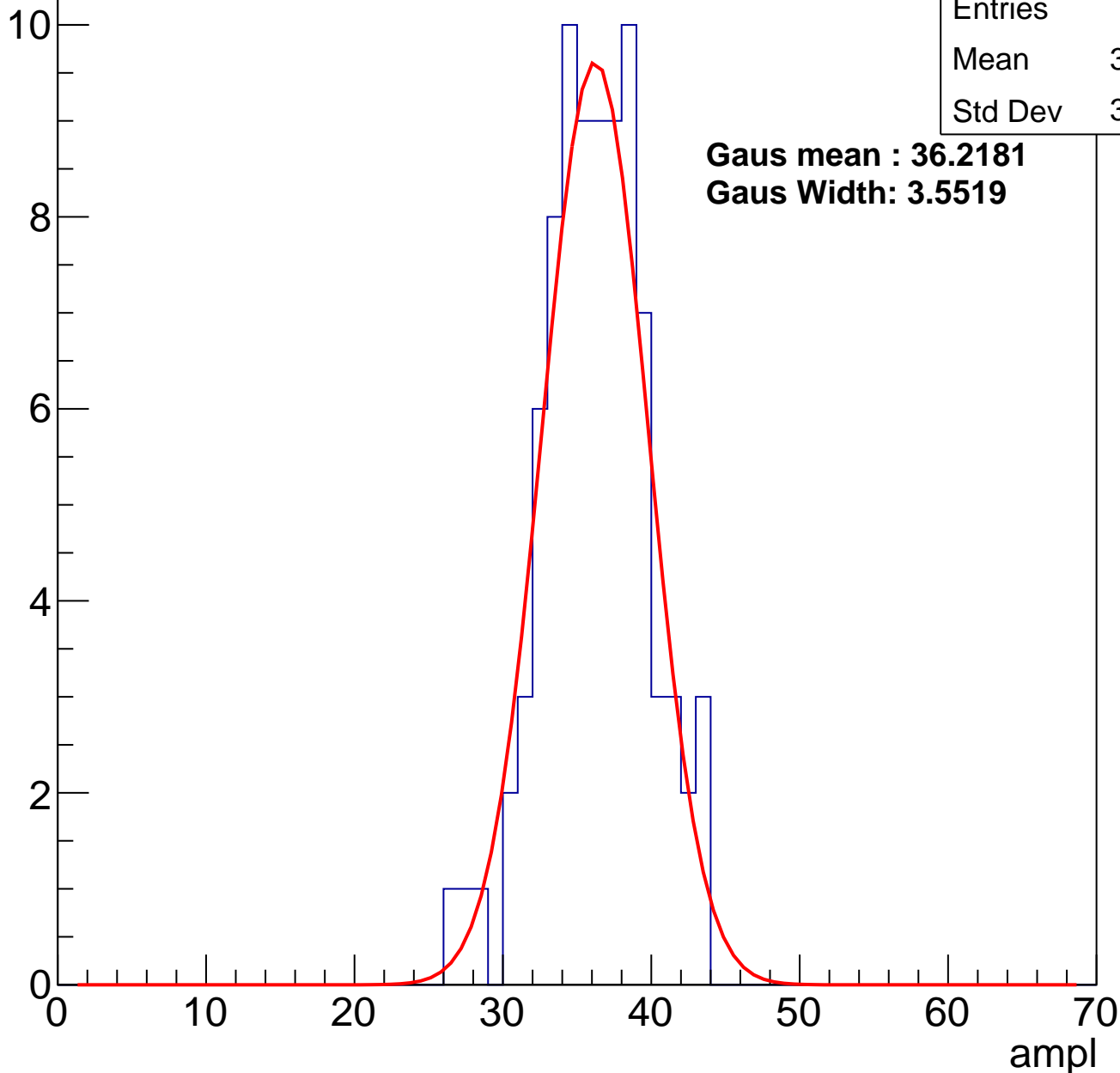
calib_packv5_042523_0143.root, FC#0, port D2

Entries	87
Mean	35.76
Std Dev	3.487

Gaus mean : 36.2181

Gaus Width: 3.5519

Entry



B1L101S, U4-ch37, adc2

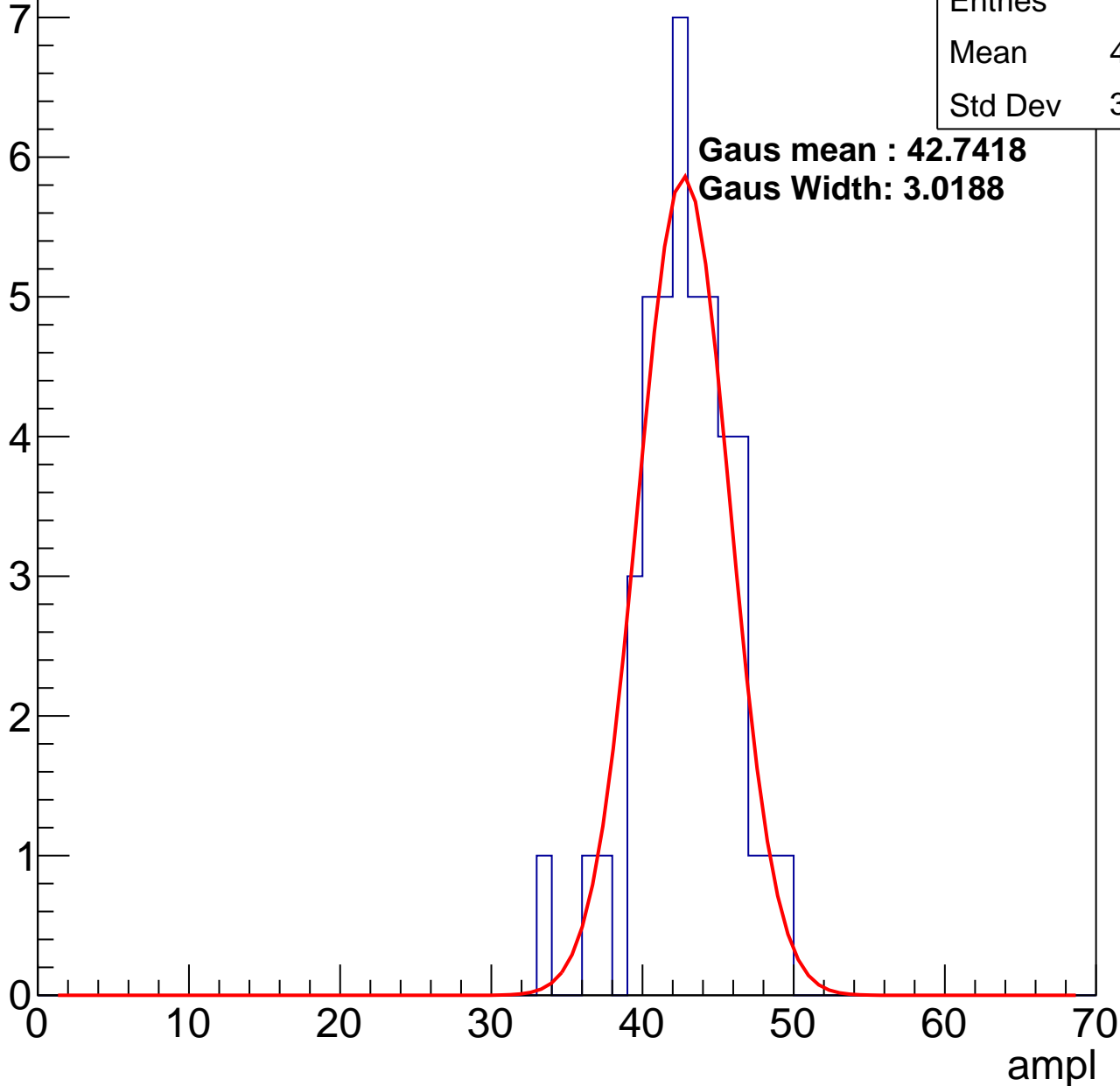
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	42.39
Std Dev	3.099

Gaus mean : 42.7418

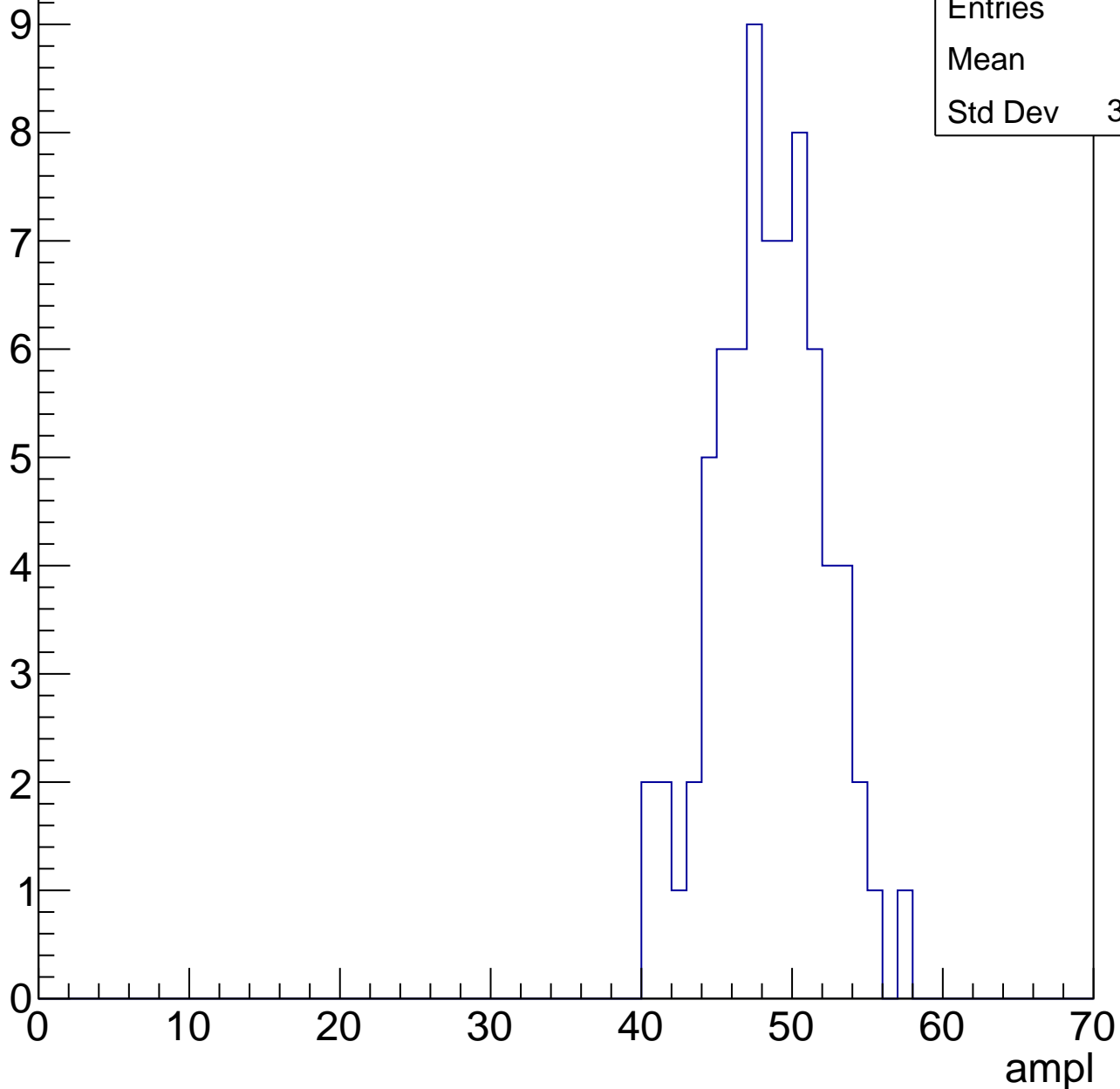
Gaus Width: 3.0188



B1L101S, U4-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

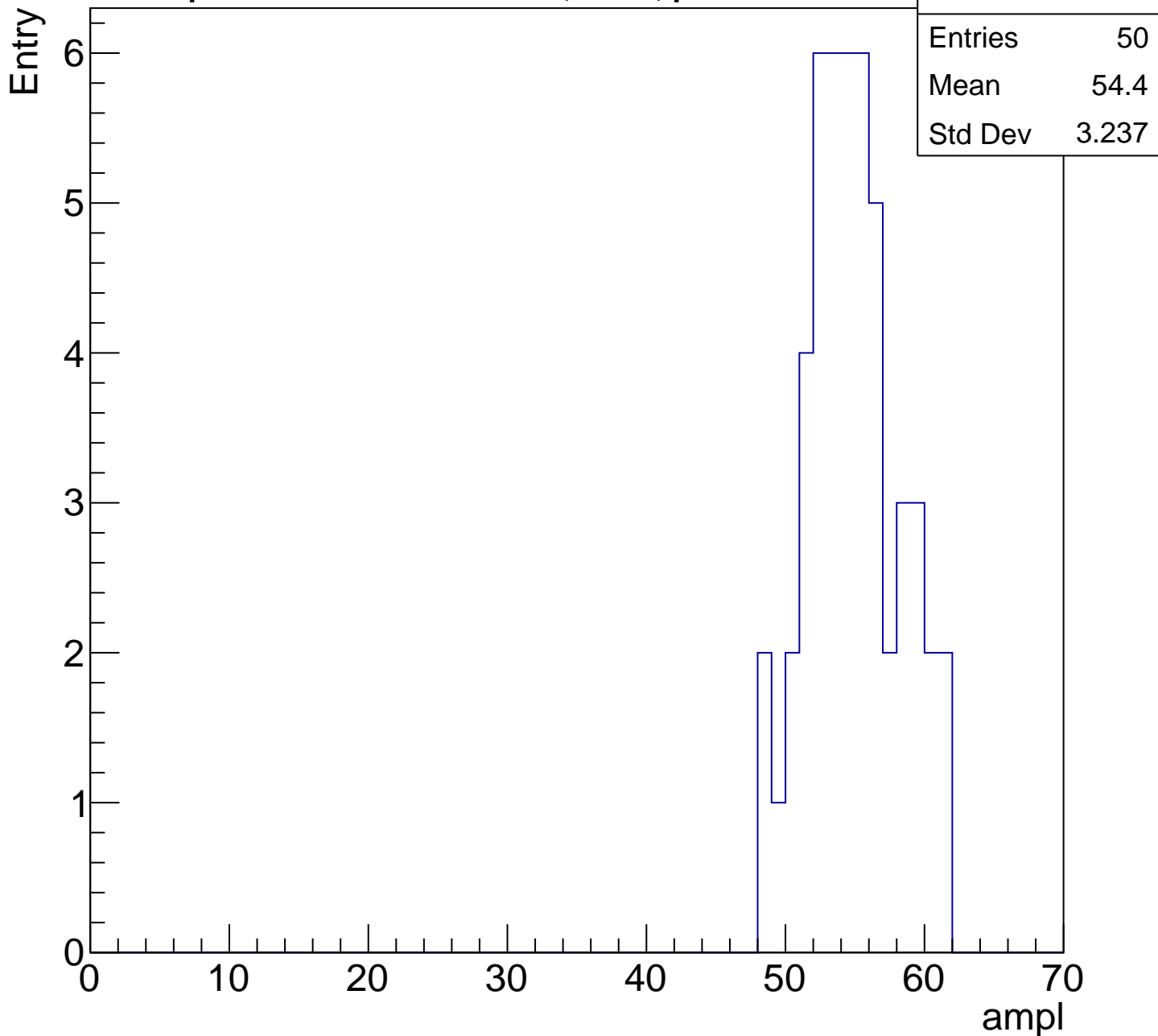
Entry



Entries	73
Mean	48
Std Dev	3.585

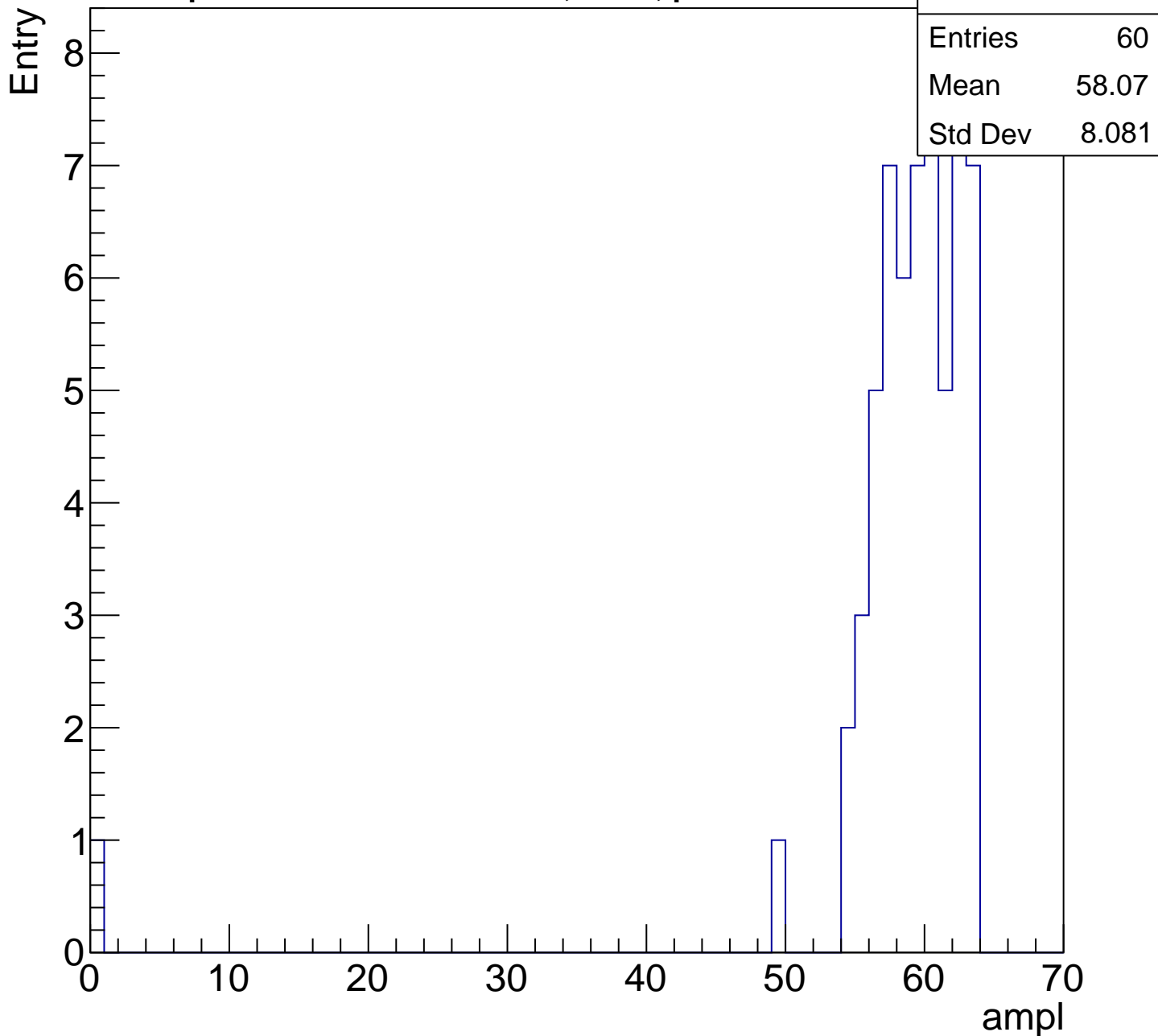
B1L101S, U4-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch37, adc5

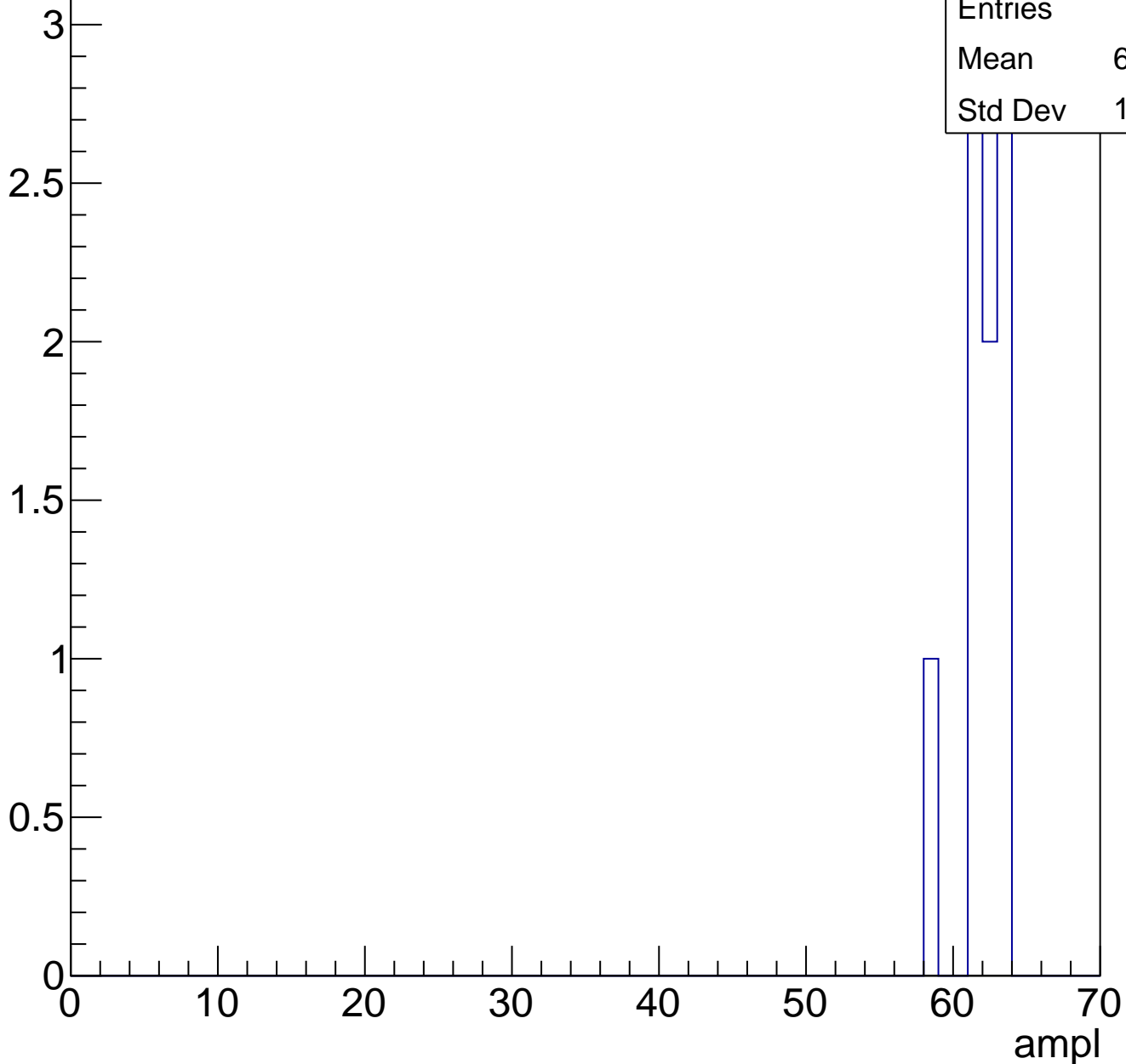
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch38, adc0

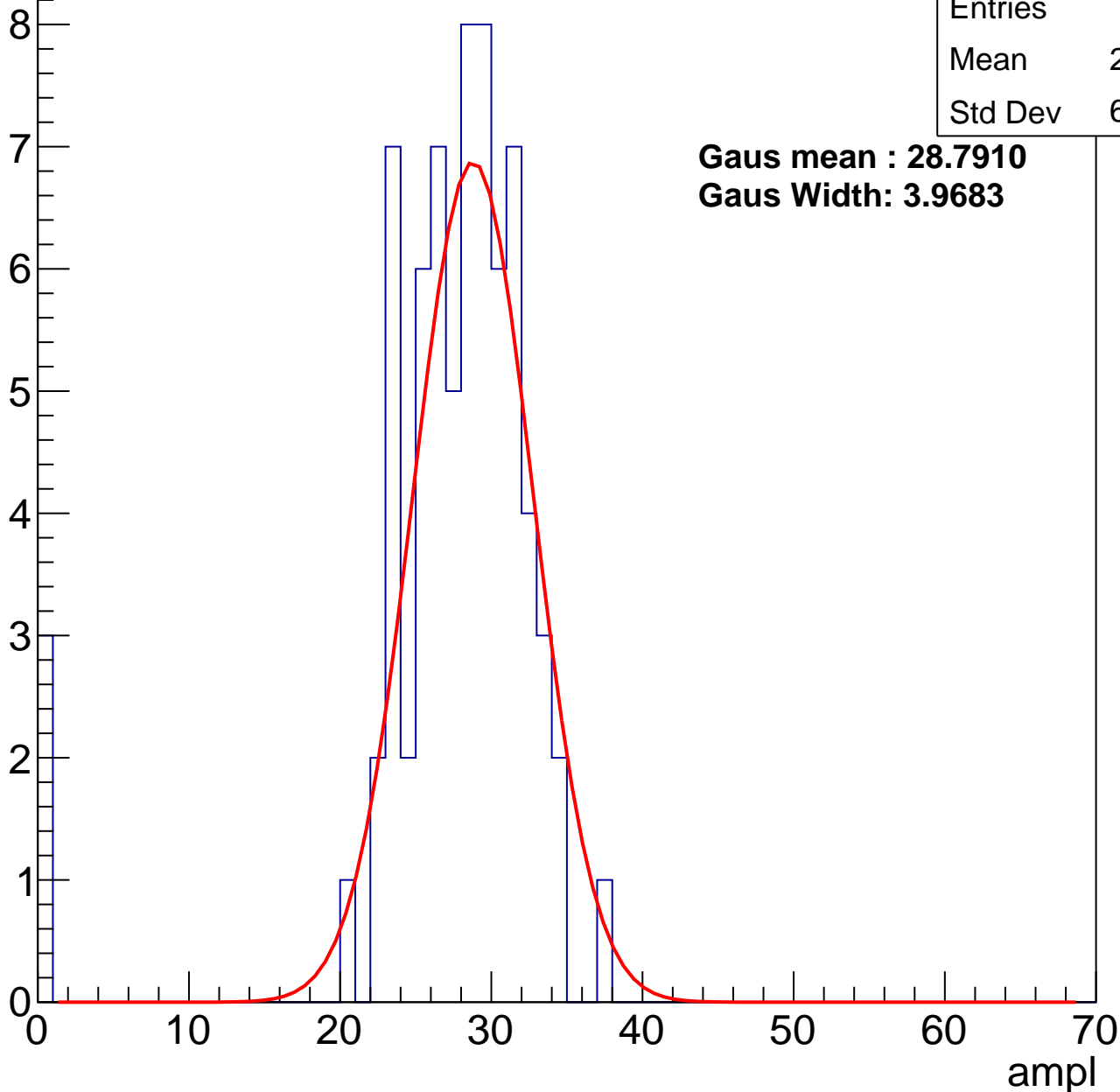
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	26.74
Std Dev	6.513

Gaus mean : 28.7910

Gaus Width: 3.9683



B1L101S, U4-ch38, adc1

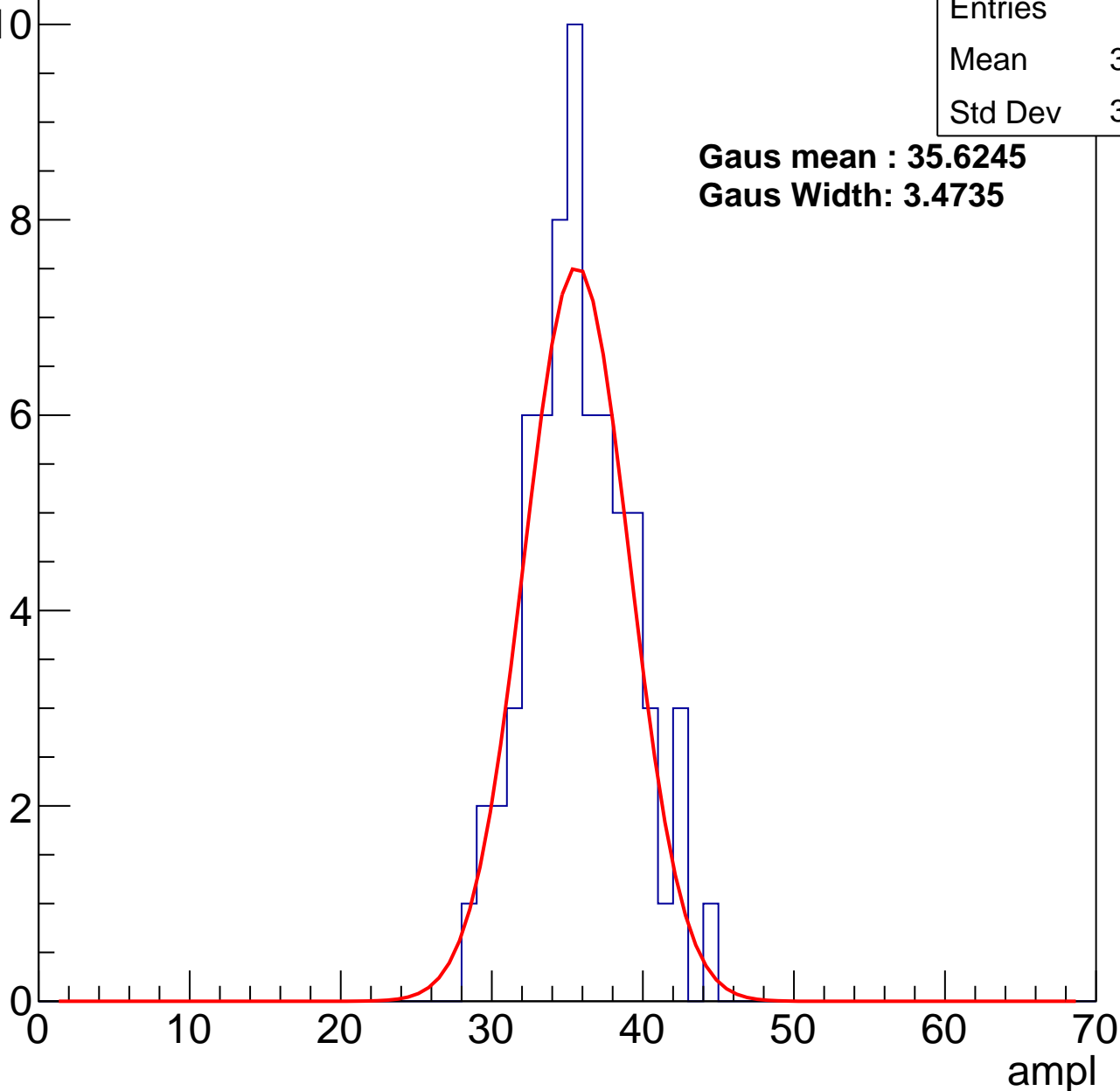
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.37
Std Dev	3.408

Gaus mean : 35.6245

Gaus Width: 3.4735

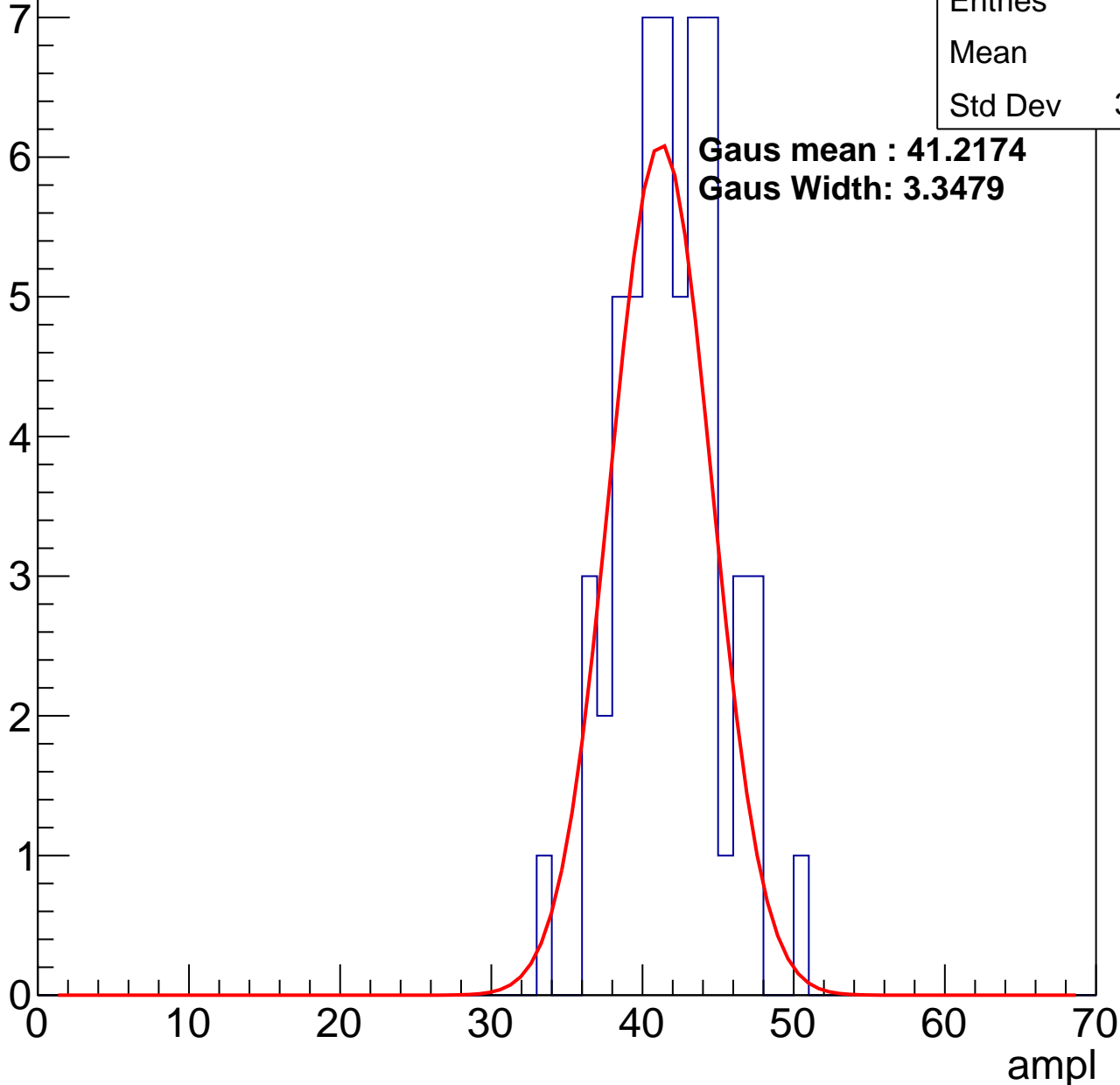


B1L101S, U4-ch38, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.4
Std Dev	3.271

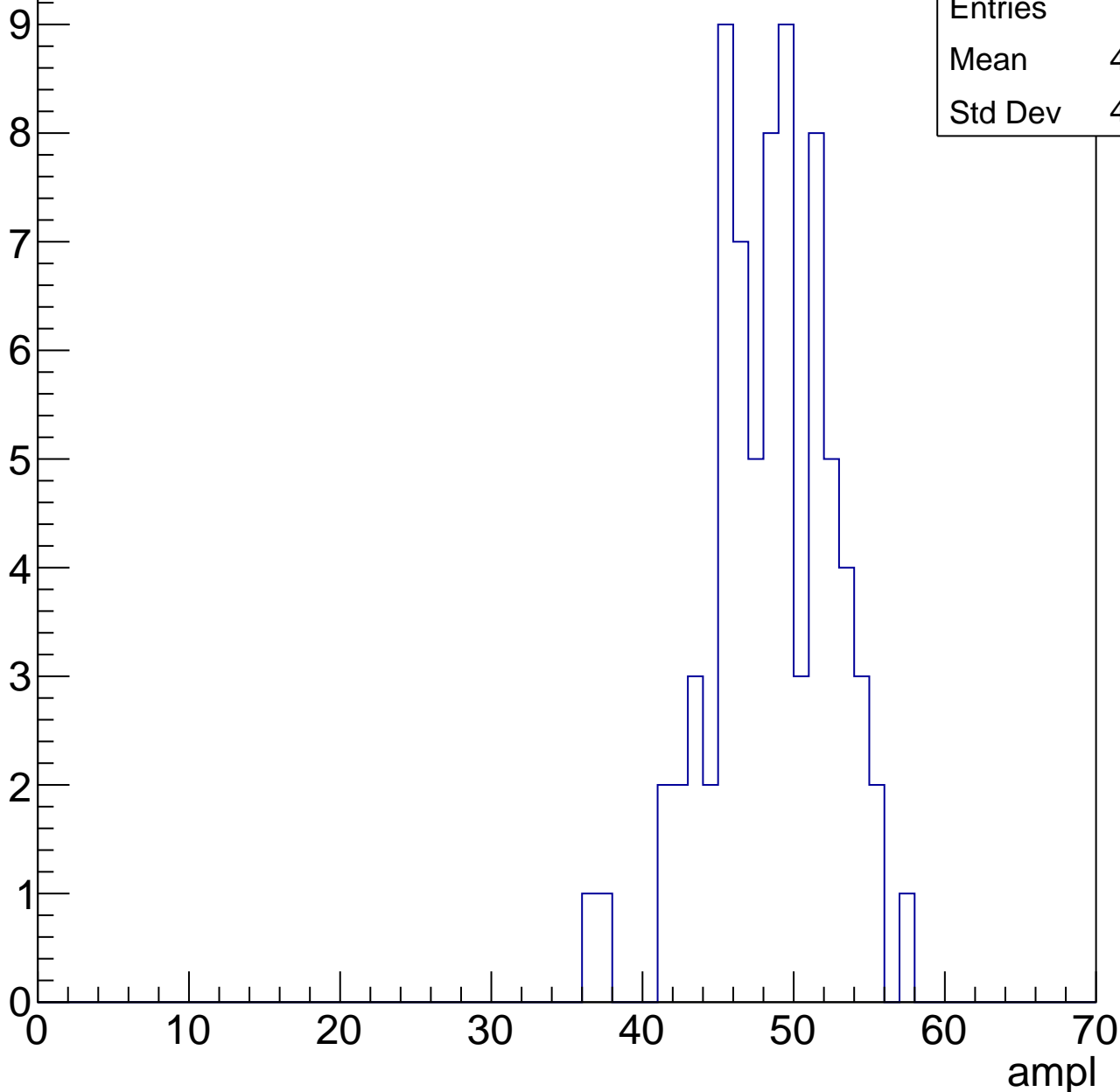


B1L101S, U4-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	48.03
Std Dev	4.026

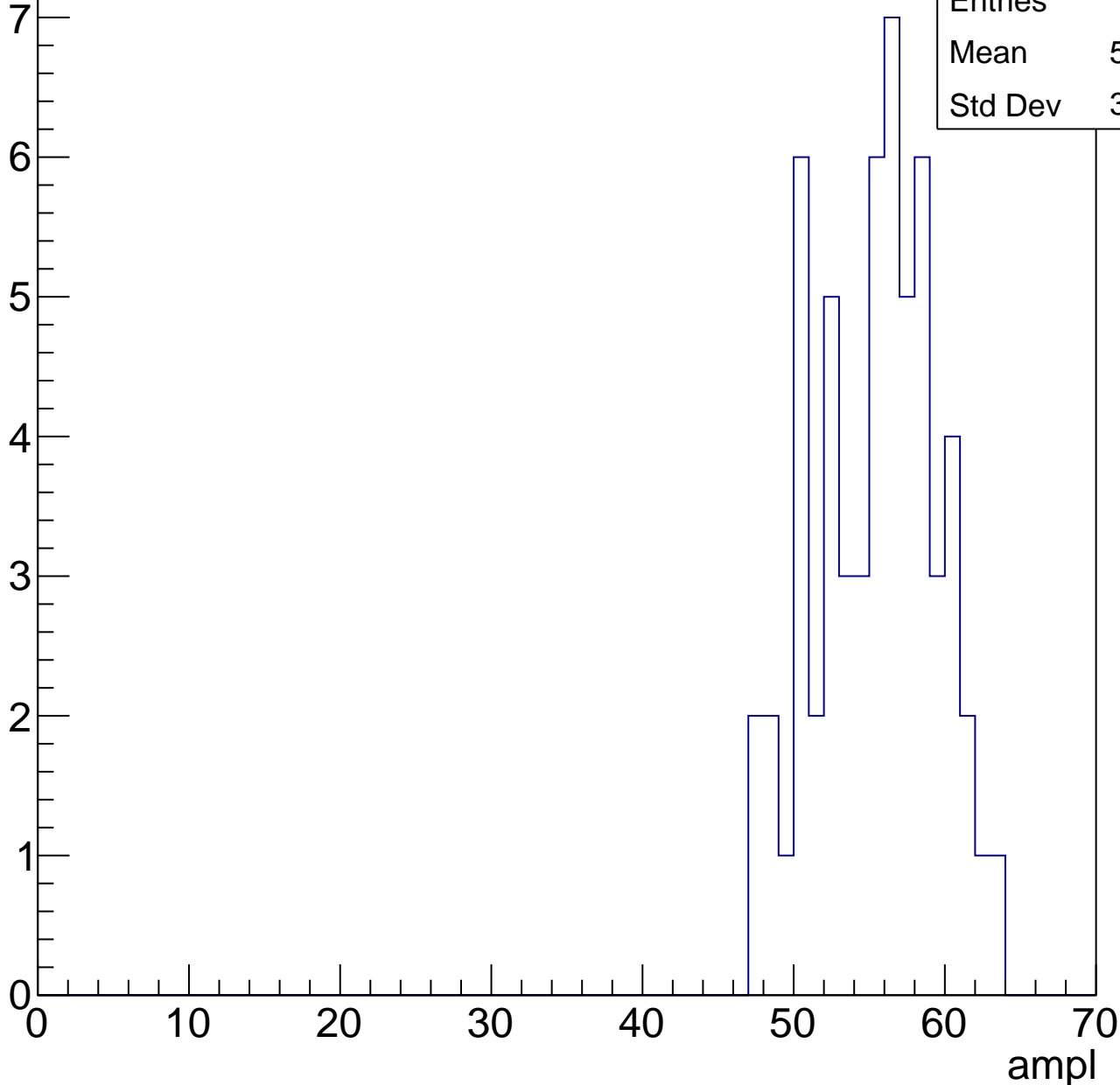


B1L101S, U4-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.93
Std Dev	3.944

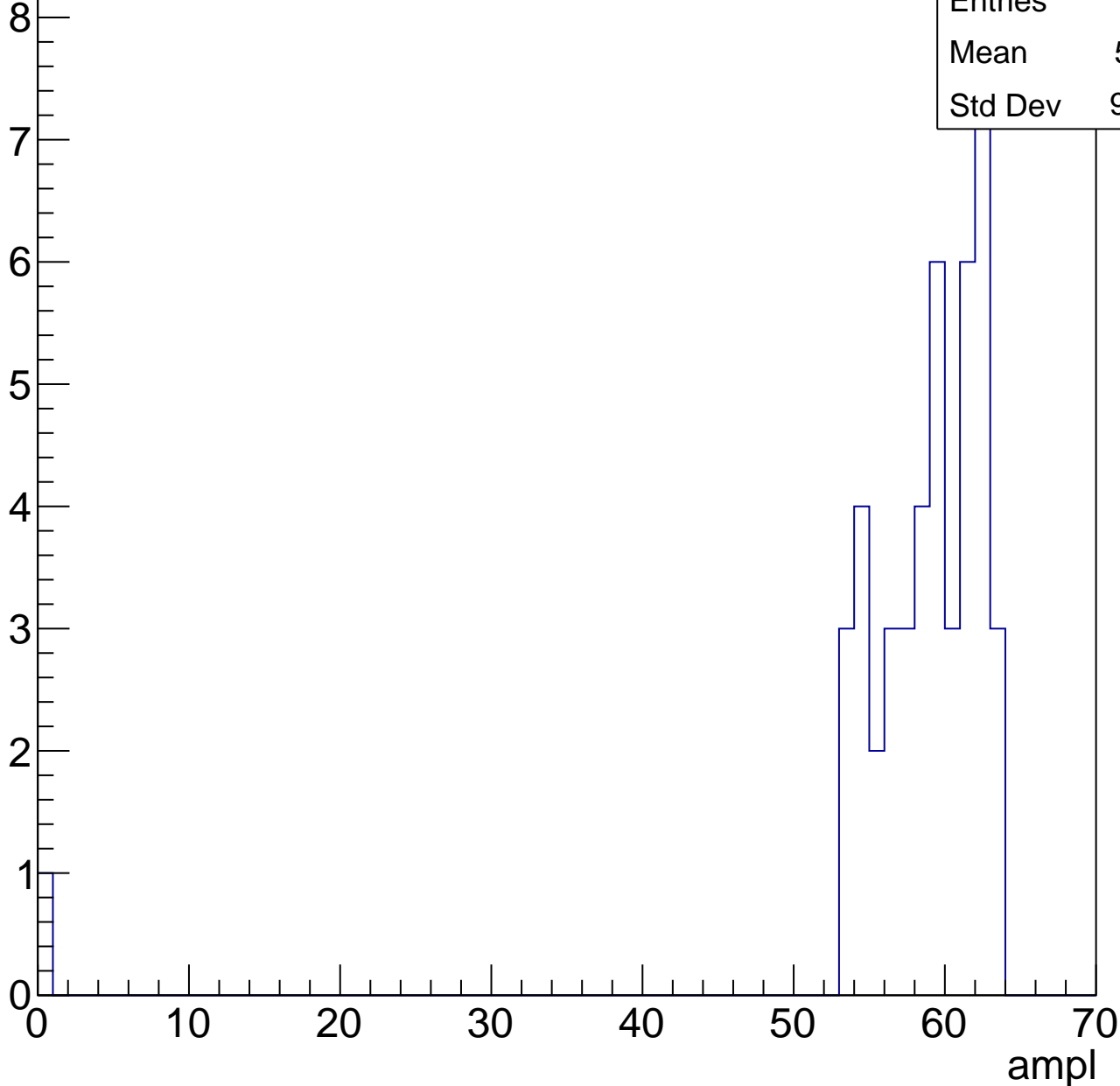


B1L101S, U4-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

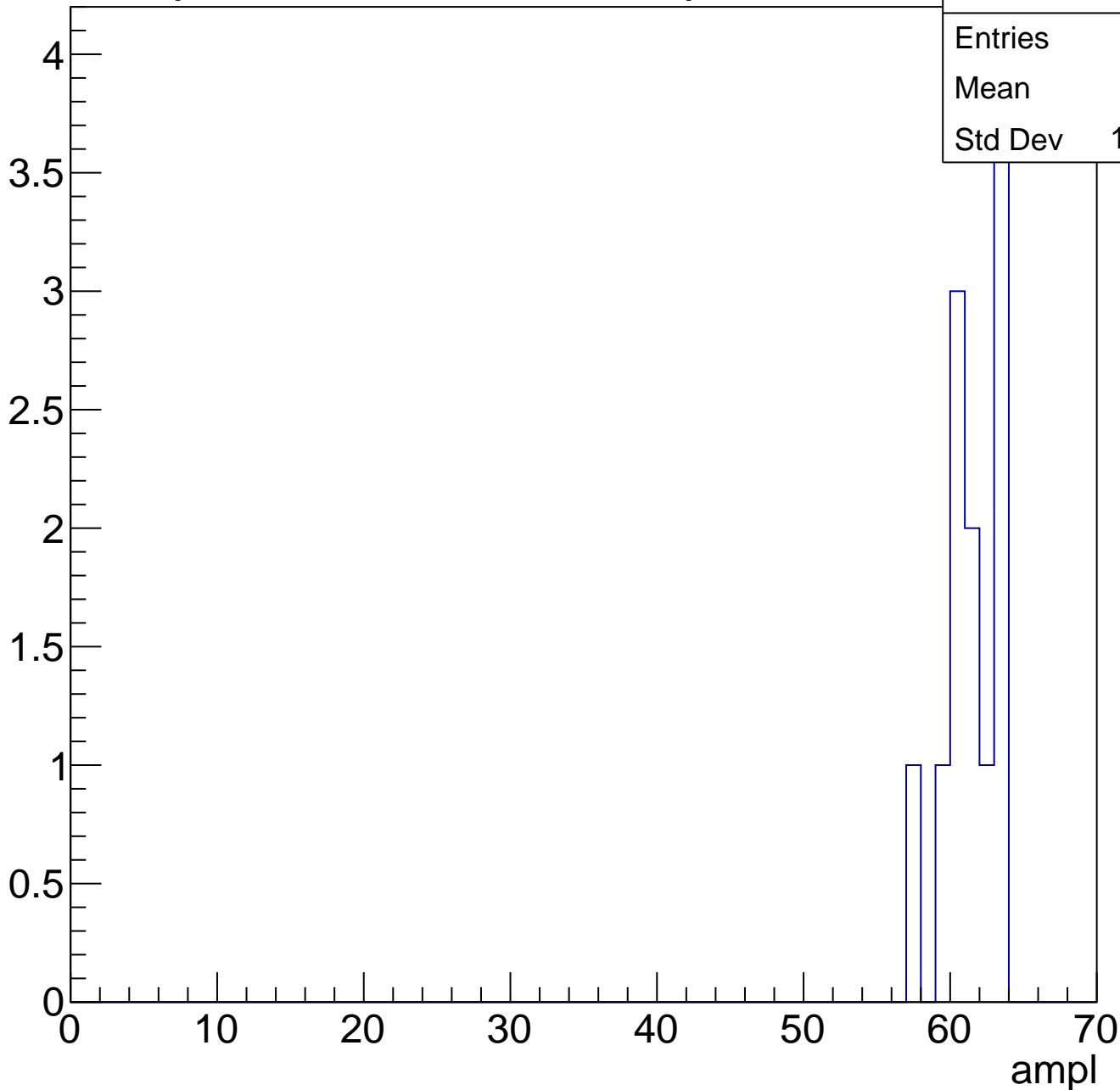
Entries	46
Mean	57.41
Std Dev	9.083



B1L101S, U4-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

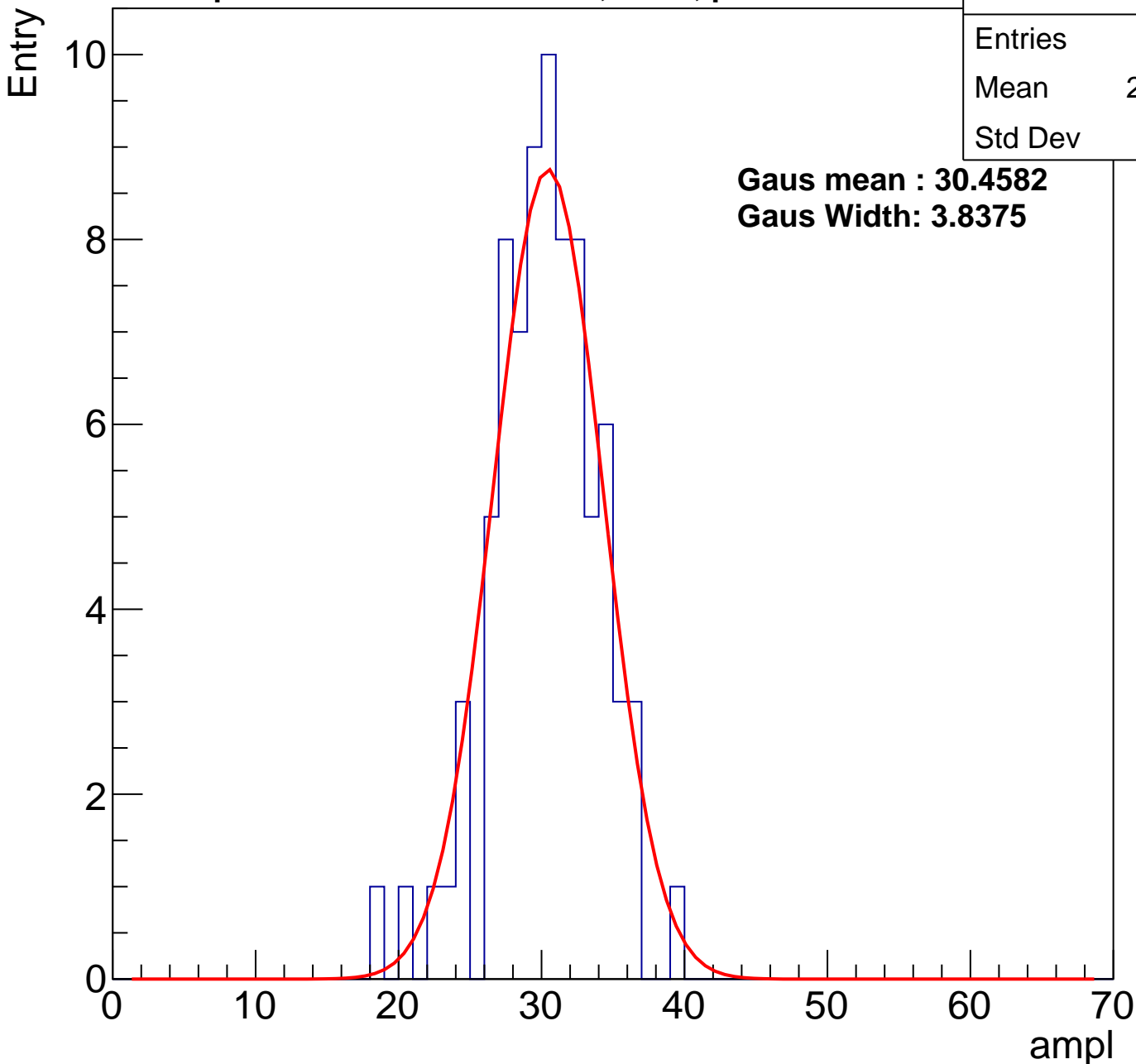


B1L101S, U4-ch39, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	29.79
Std Dev	3.7

Gaus mean : 30.4582
Gaus Width: 3.8375



B1L101S, U4-ch39, adc1

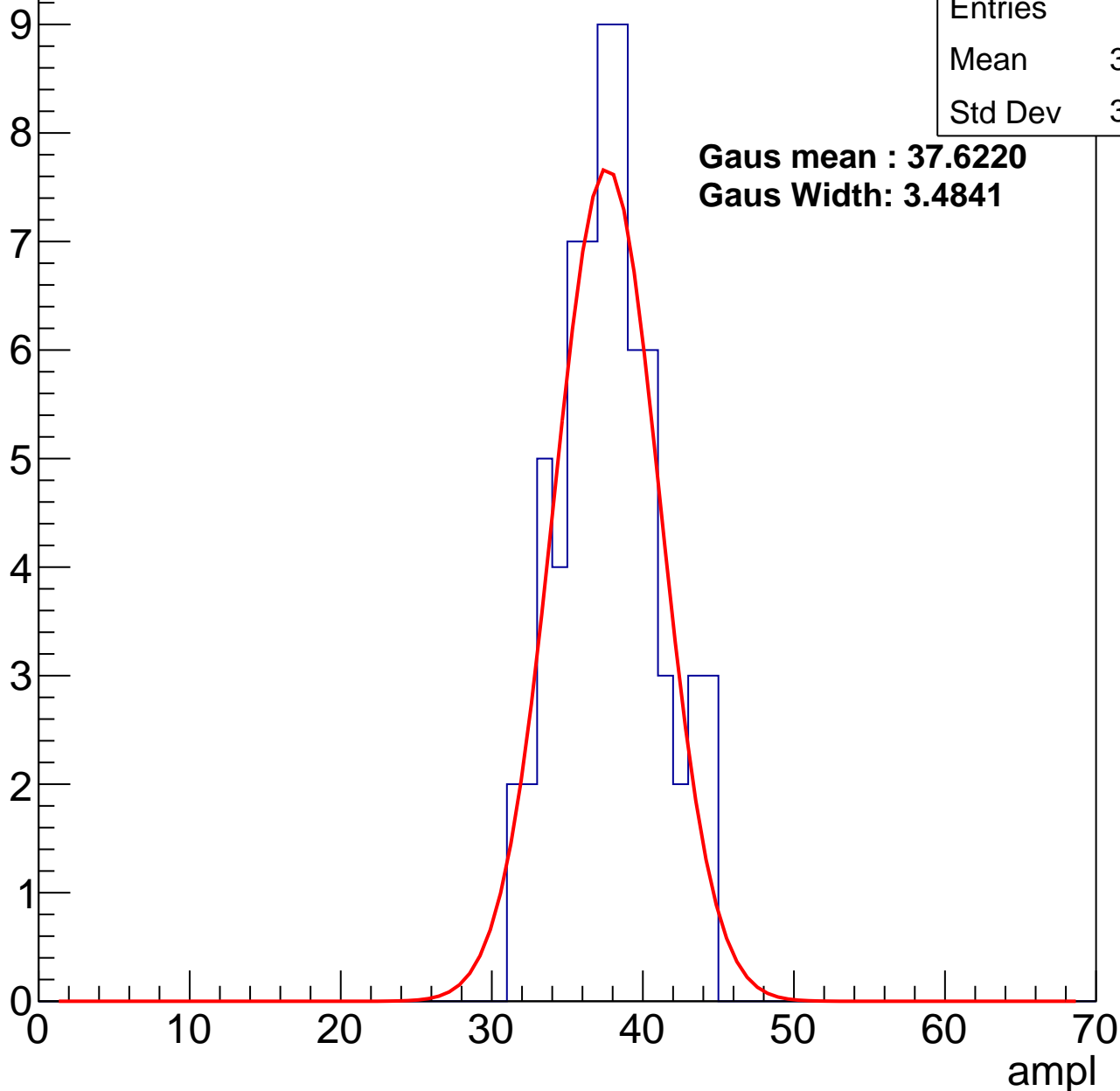
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.37
Std Dev	3.217

Gaus mean : 37.6220

Gaus Width: 3.4841

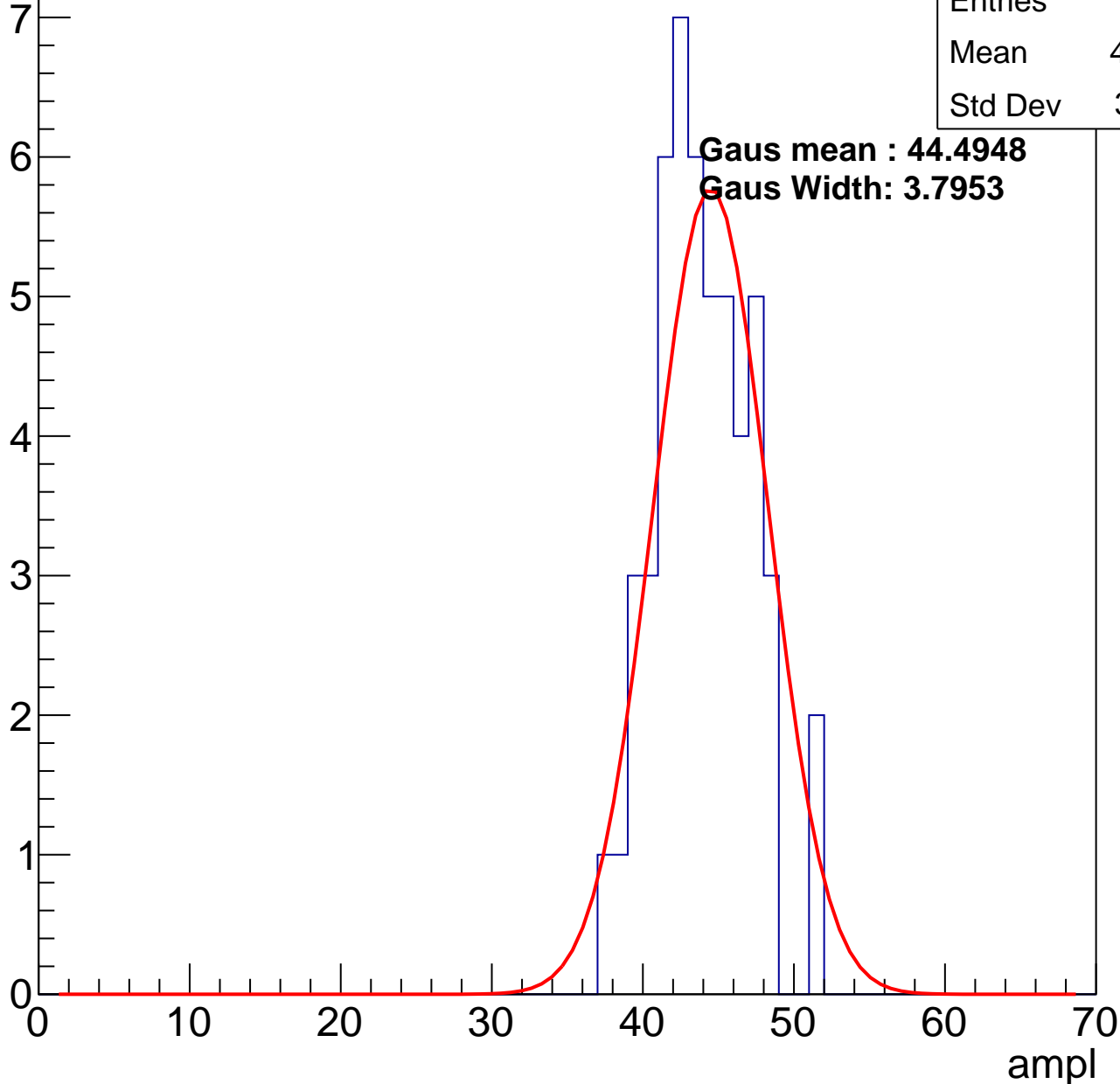


B1L101S, U4-ch39, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	43.53
Std Dev	3.121

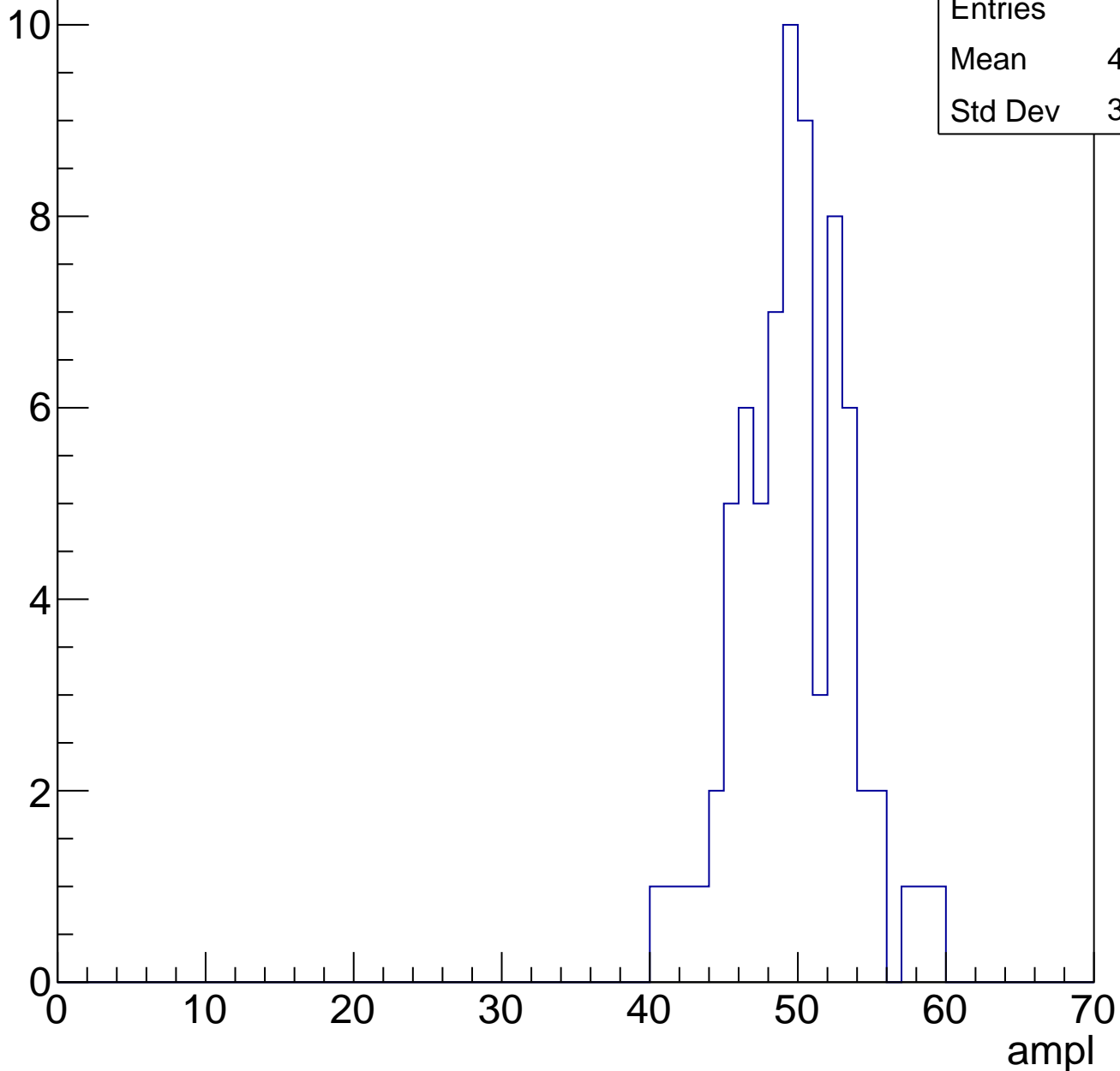


B1L101S, U4-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	49.24
Std Dev	3.714

Entry

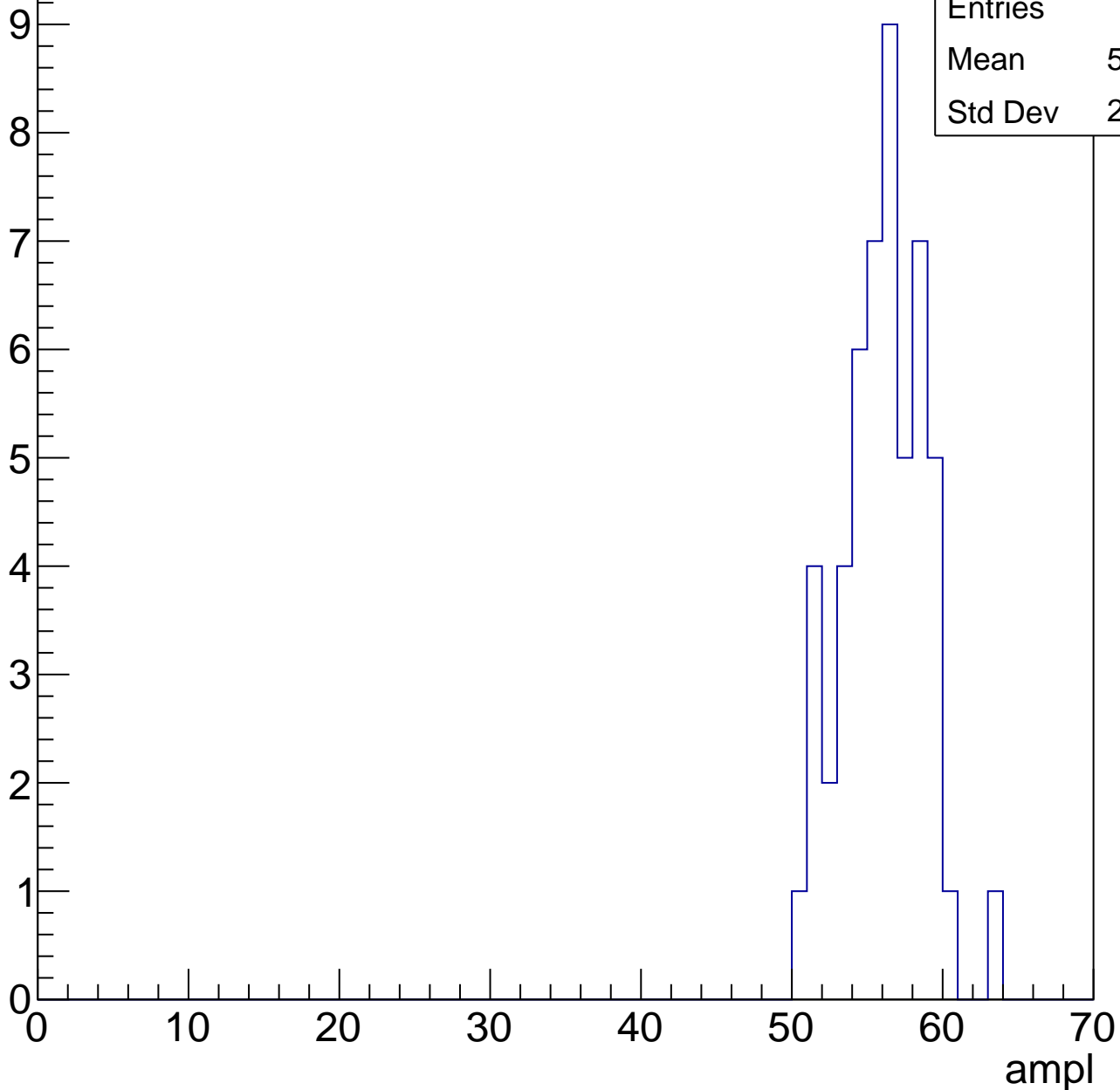


B1L101S, U4-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	55.62
Std Dev	2.669



B1L101S, U4-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

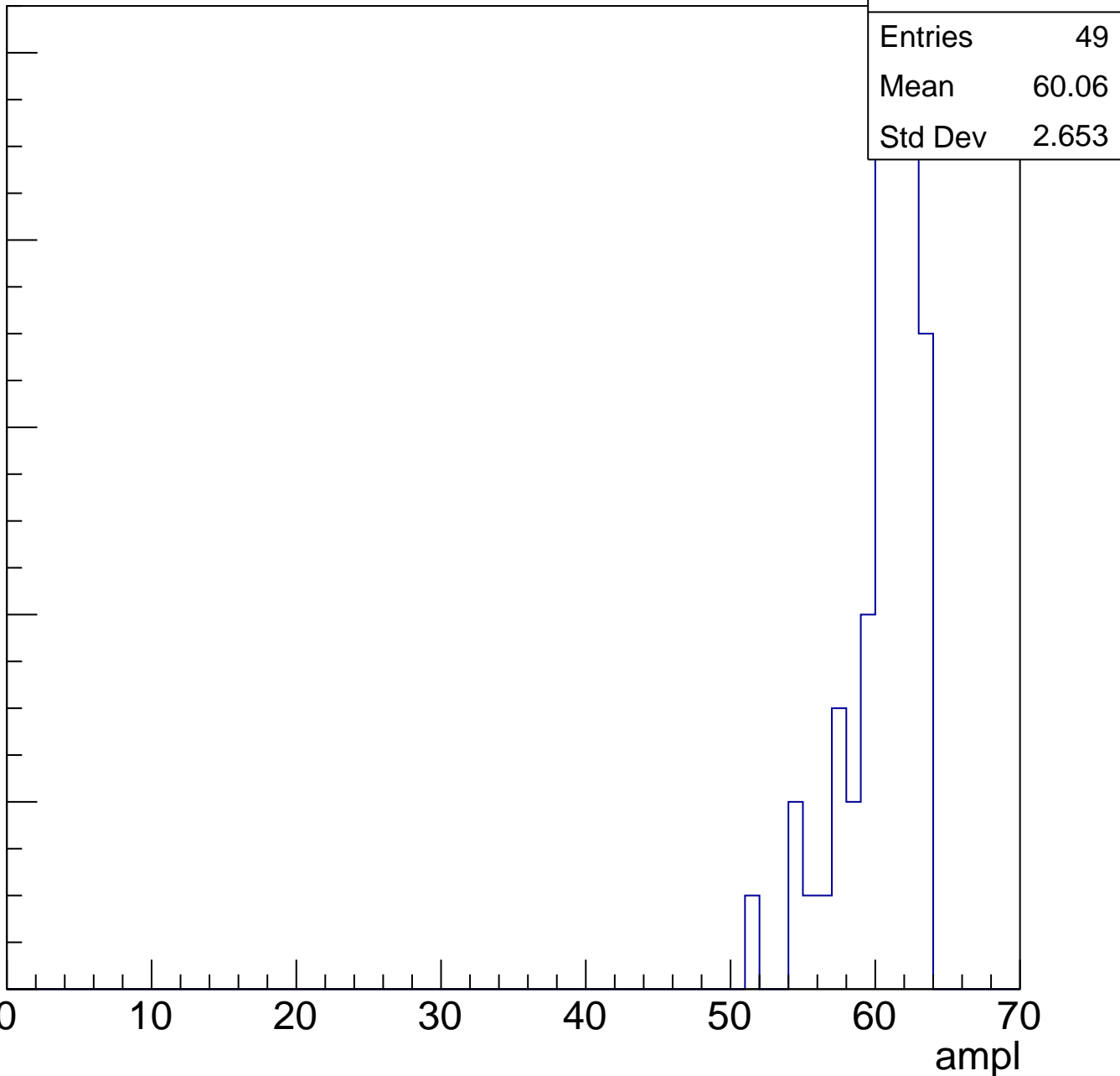
40

50

60

ampl

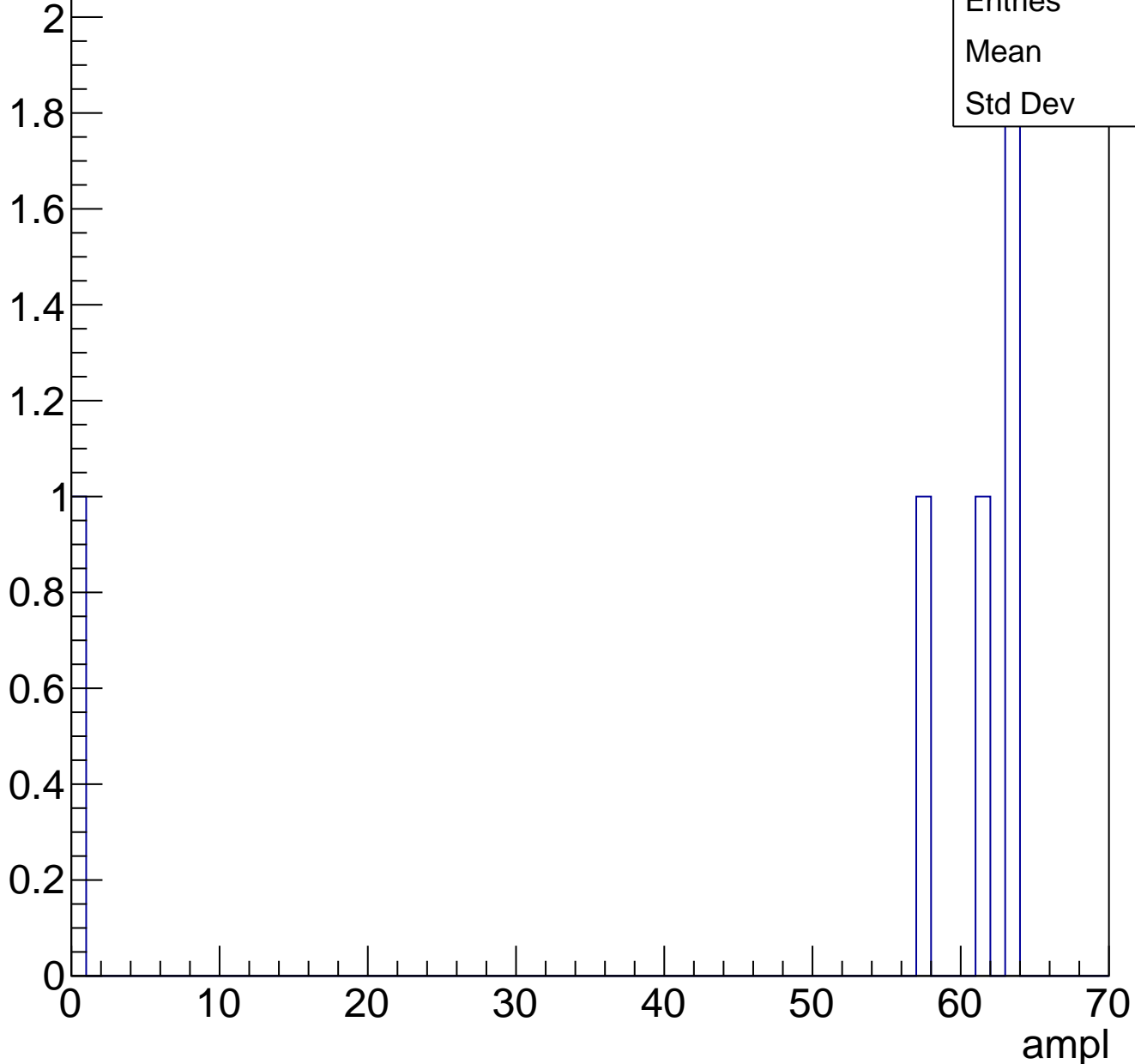
Entries	49
Mean	60.06
Std Dev	2.653



B1L101S, U4-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	48.8
Std Dev	24.5

B1L101S, U4-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch40, adc0

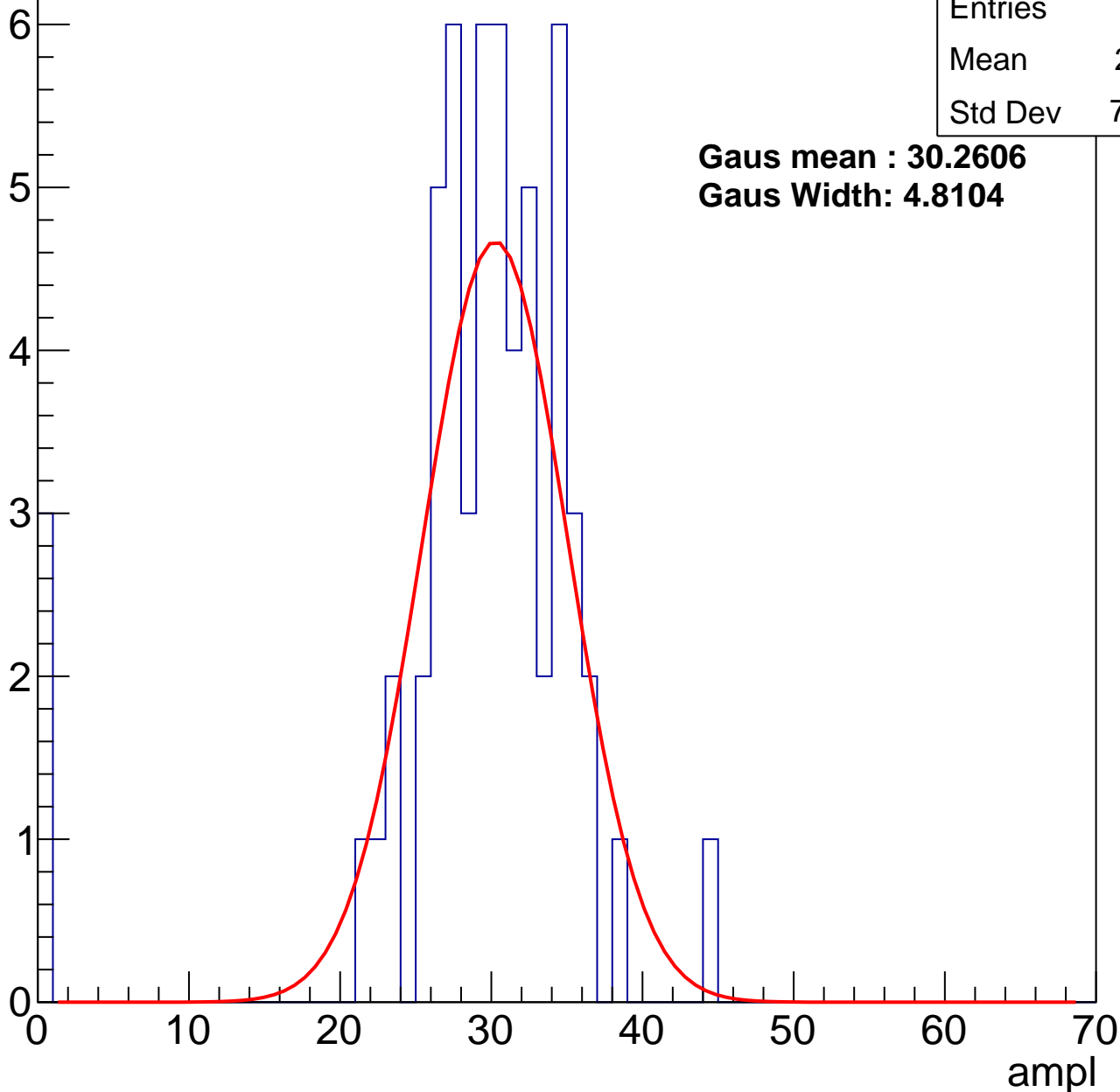
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.51
Std Dev	7.766

Gaus mean : 30.2606

Gaus Width: 4.8104



B1L101S, U4-ch40, adc1

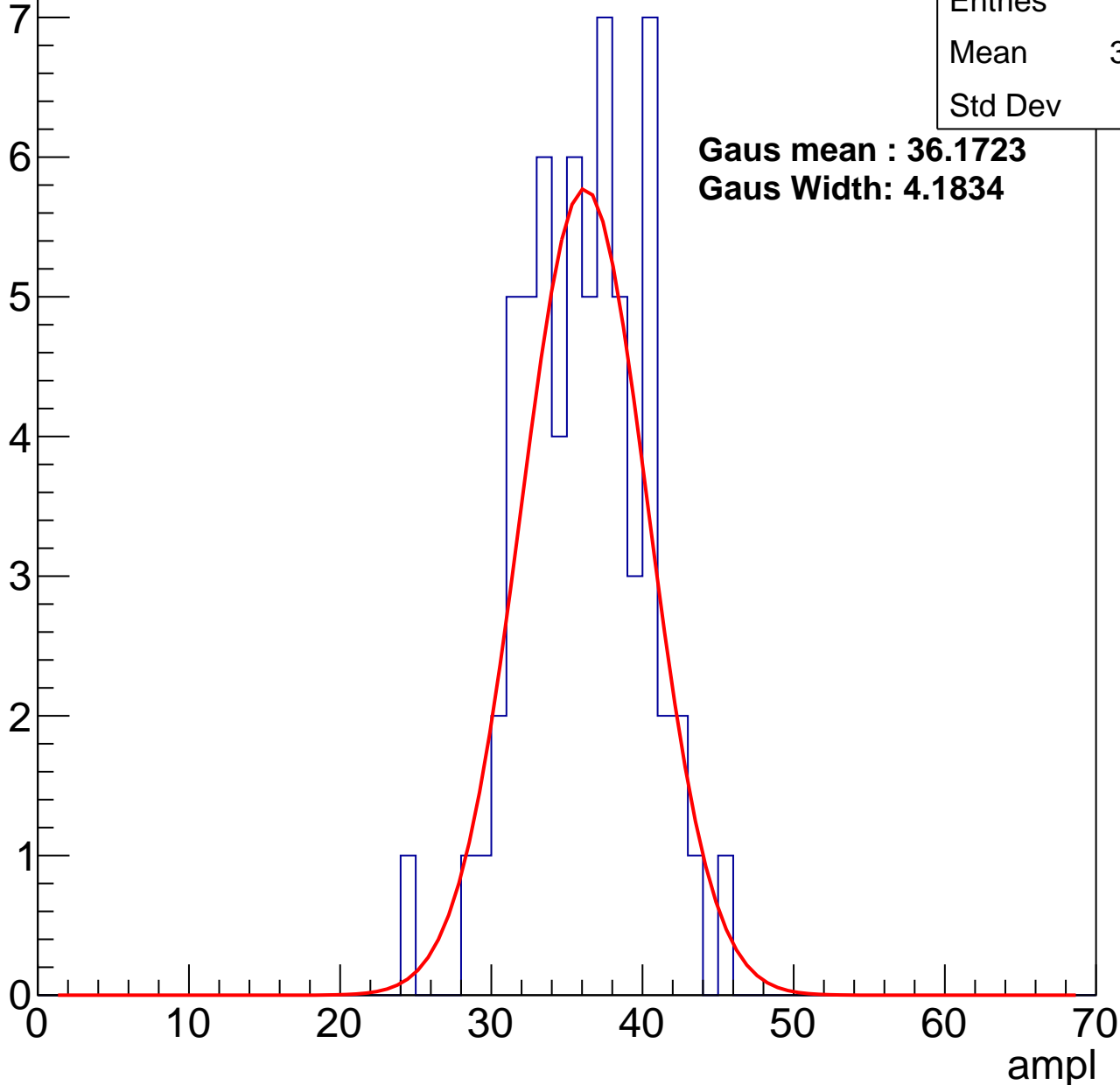
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.62
Std Dev	3.99

Gaus mean : 36.1723

Gaus Width: 4.1834



B1L101S, U4-ch40, adc2

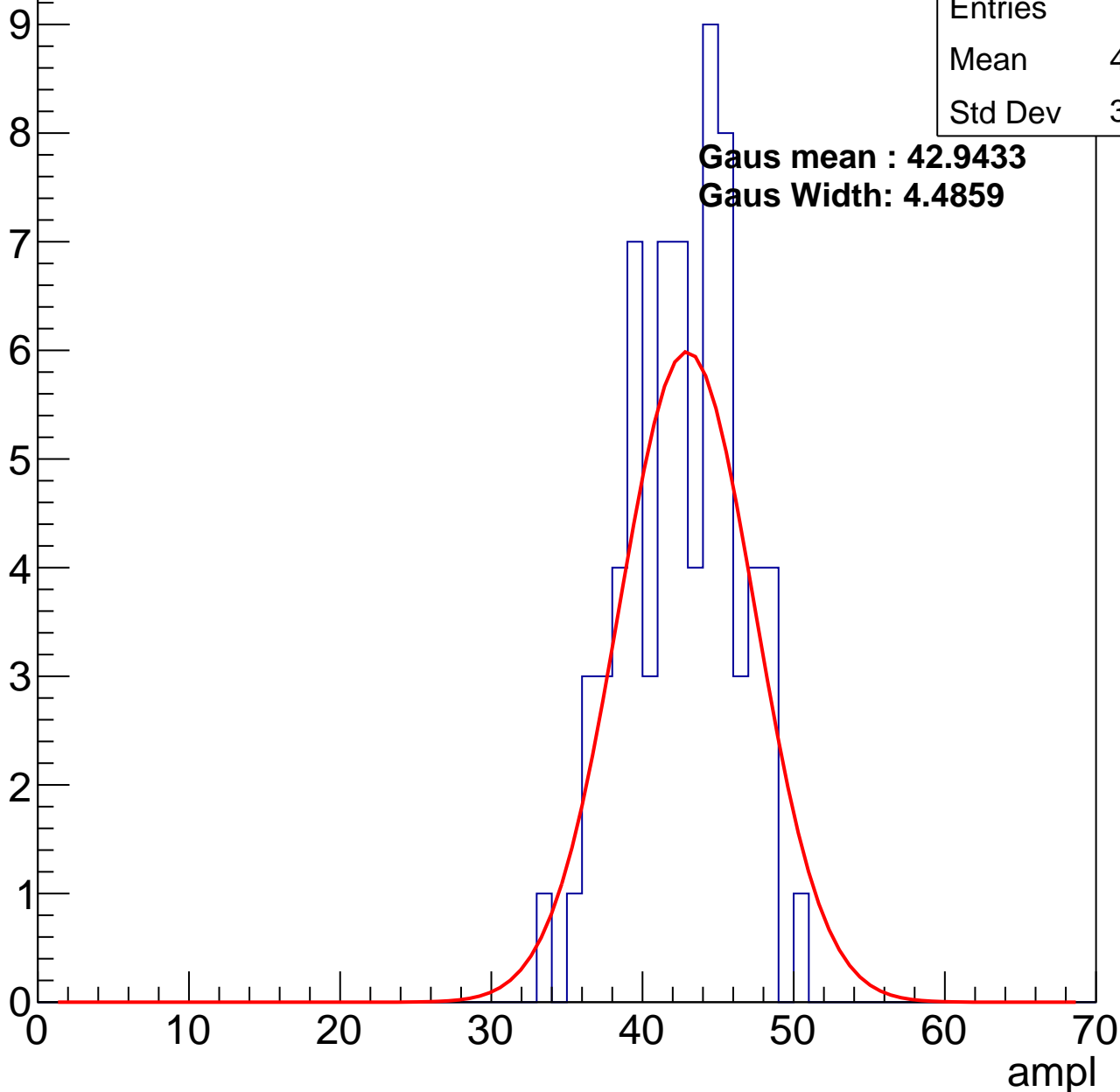
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.16
Std Dev	3.662

Gaus mean : 42.9433

Gaus Width: 4.4859

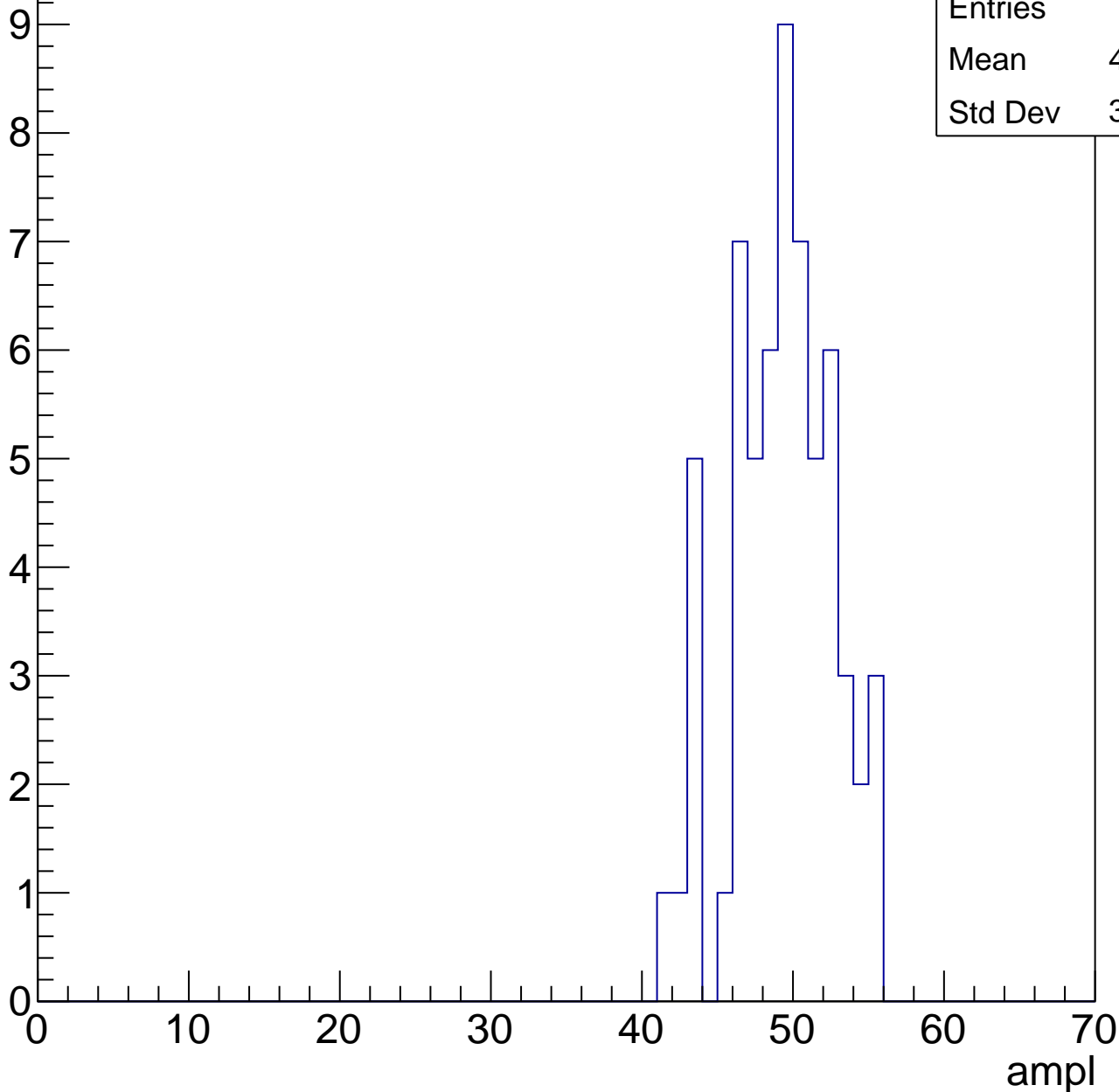


B1L101S, U4-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

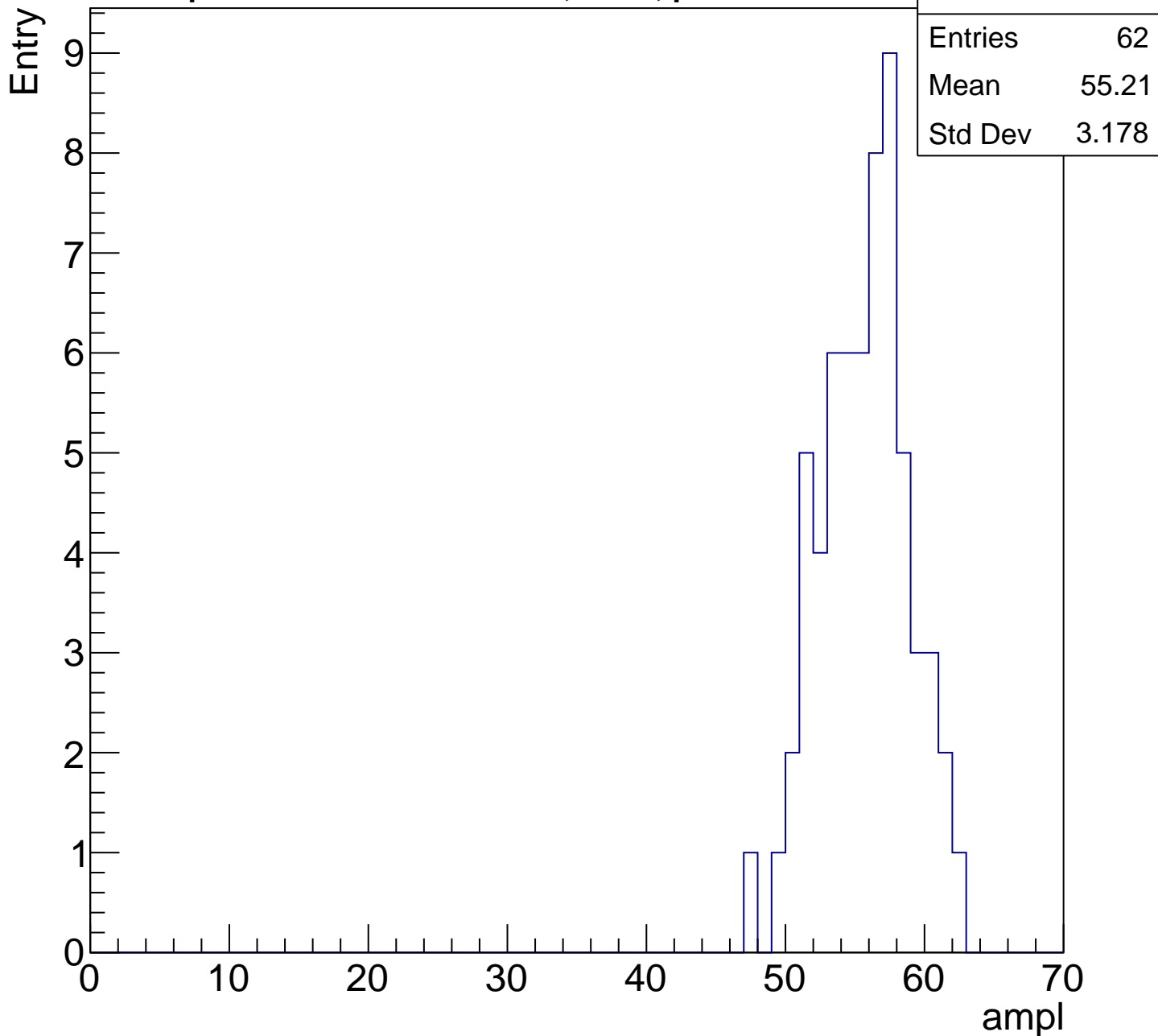
Entry

Entries	61
Mean	48.82
Std Dev	3.346



B1L101S, U4-ch40, adc4

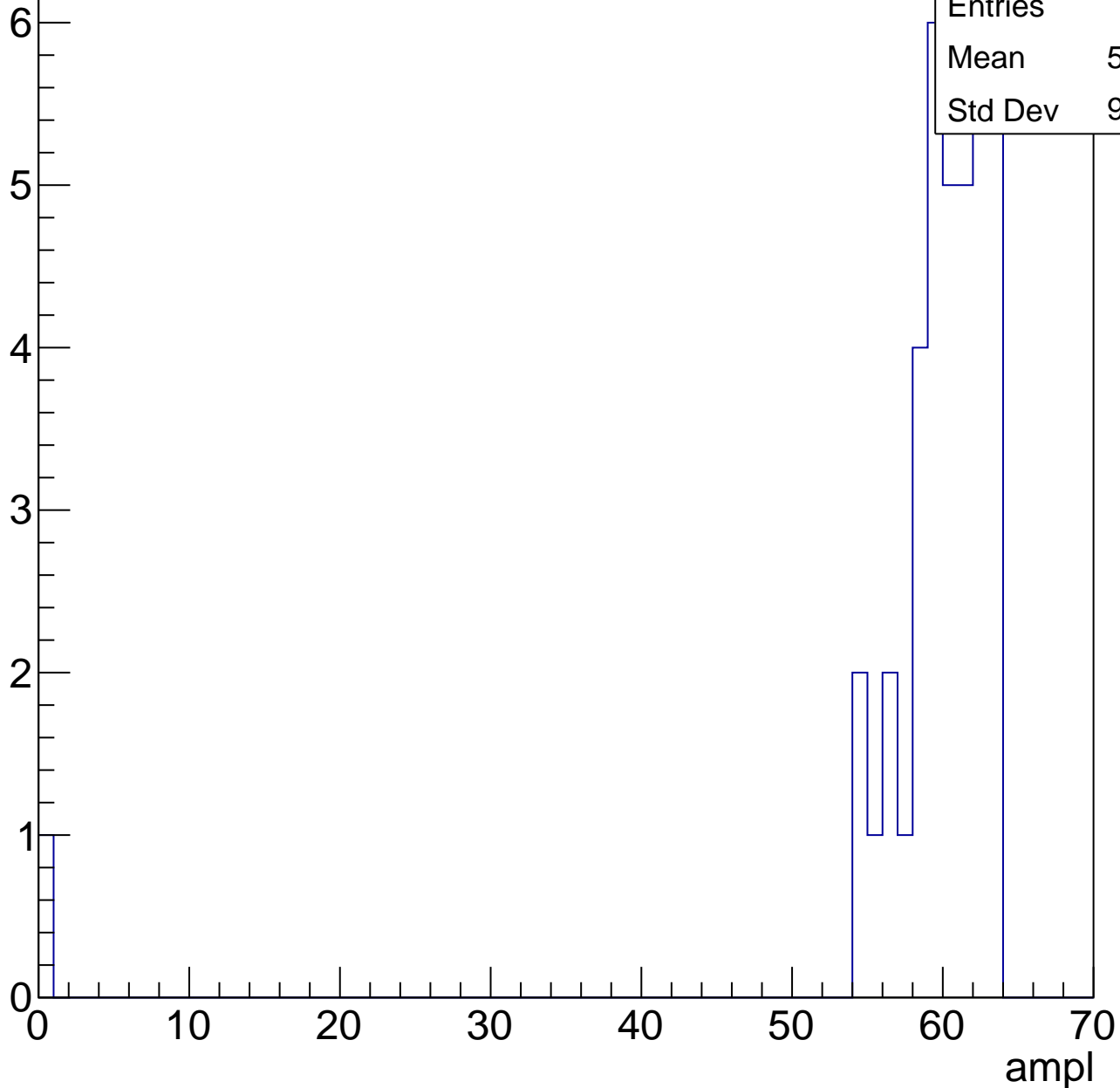
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

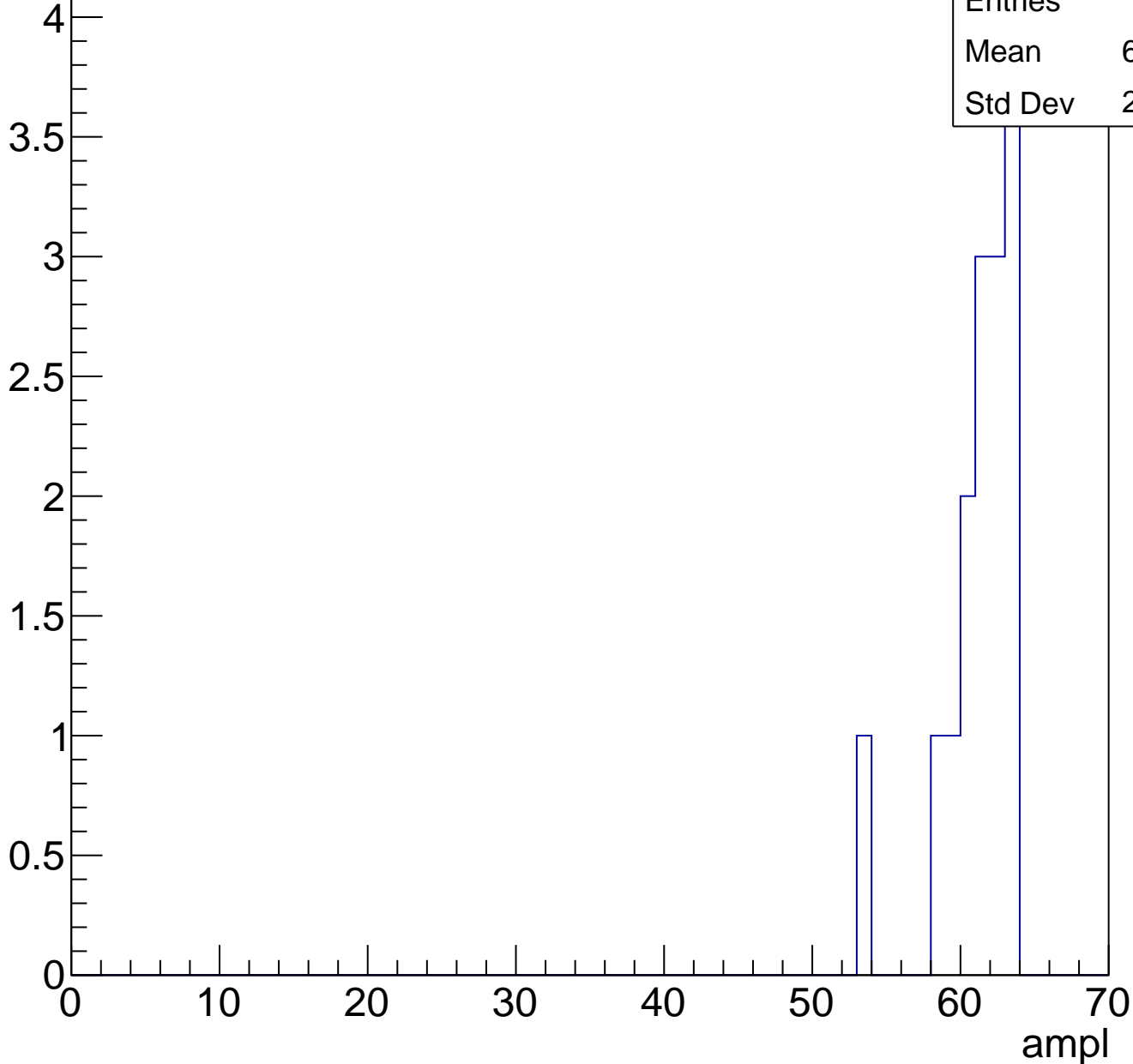
Entry



B1L101S, U4-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch41, adc0

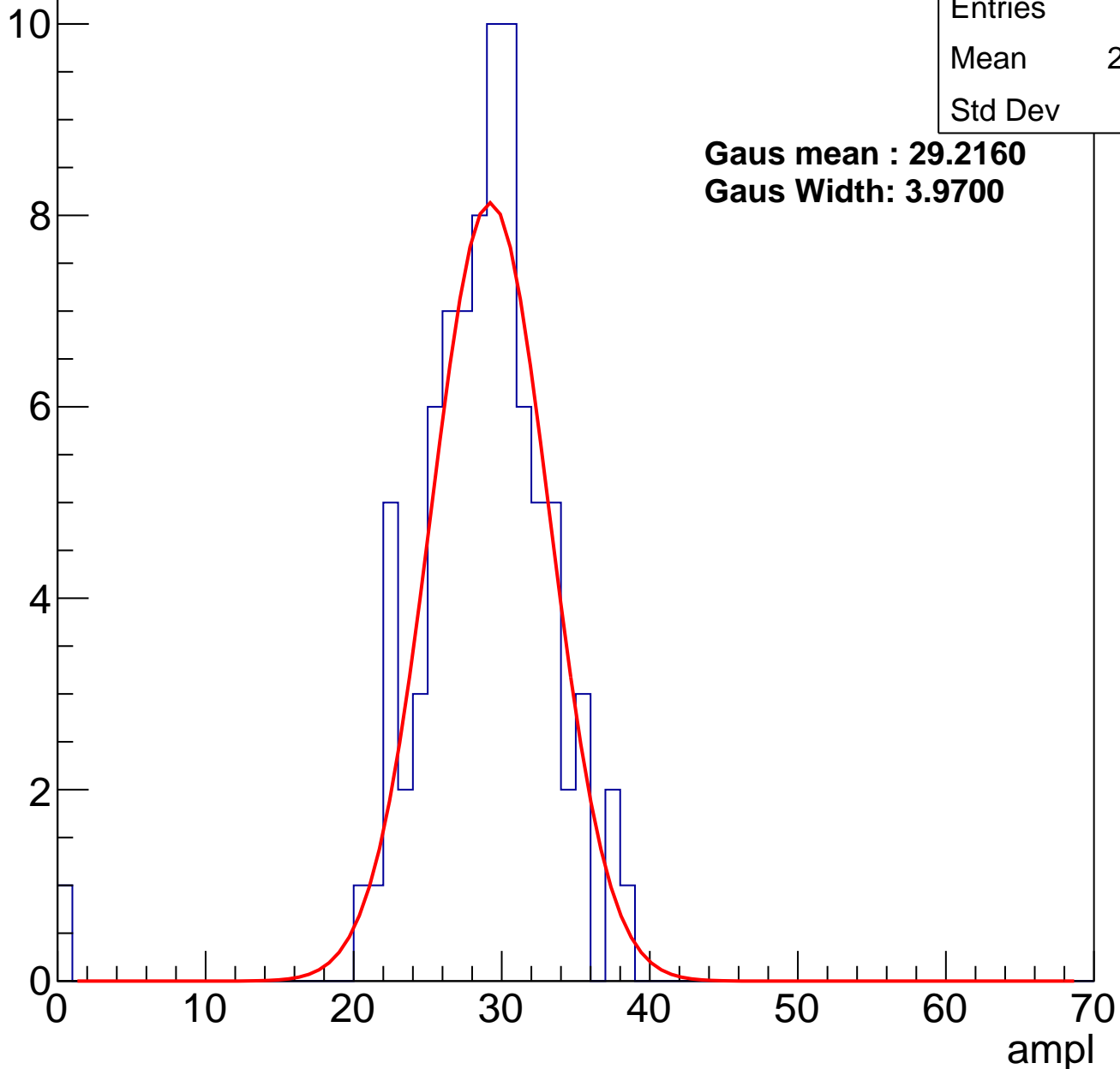
calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	28.24
Std Dev	4.91

Gaus mean : 29.2160

Gaus Width: 3.9700

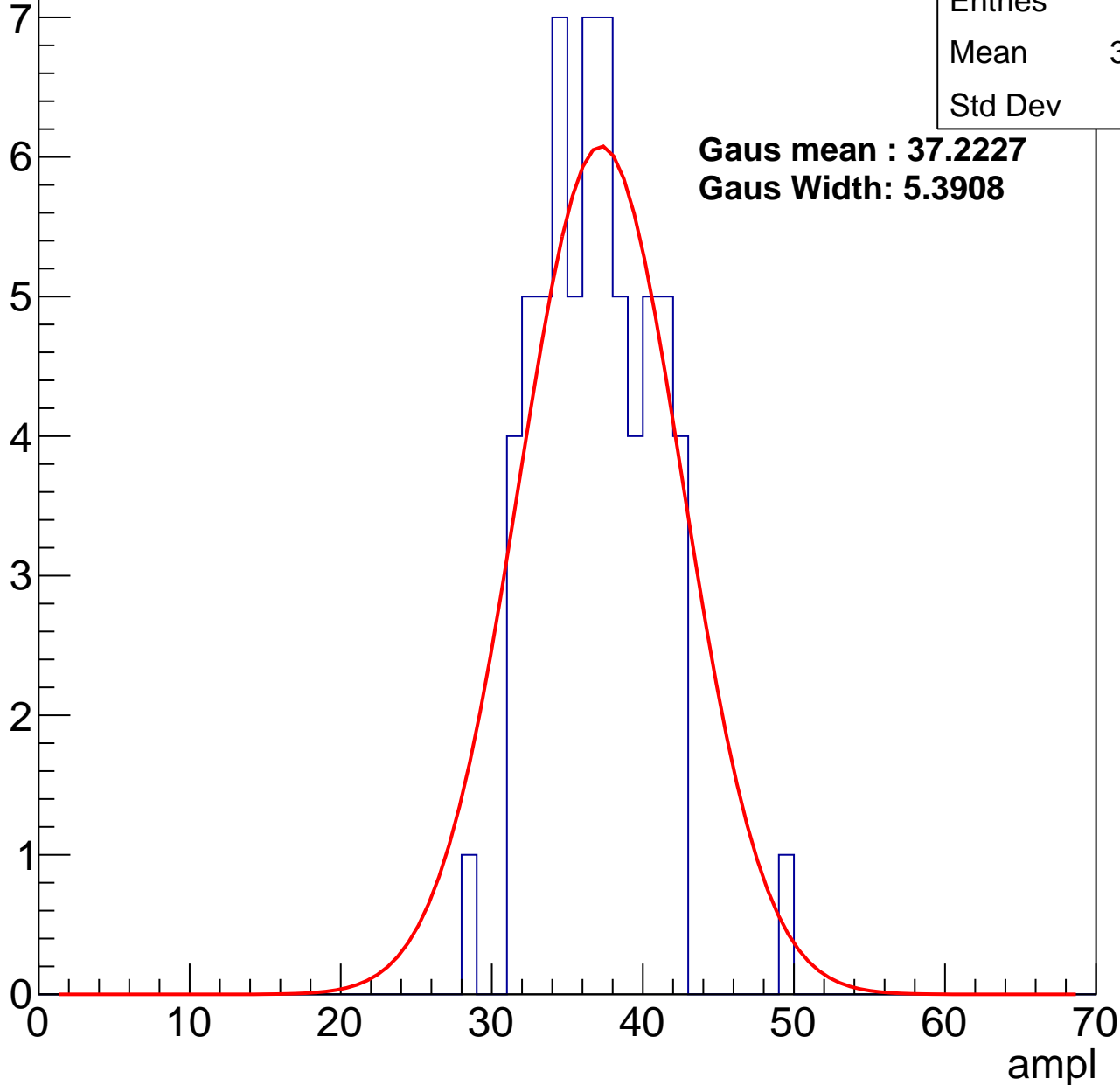
Entry



B1L101S, U4-ch41, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch41, adc2

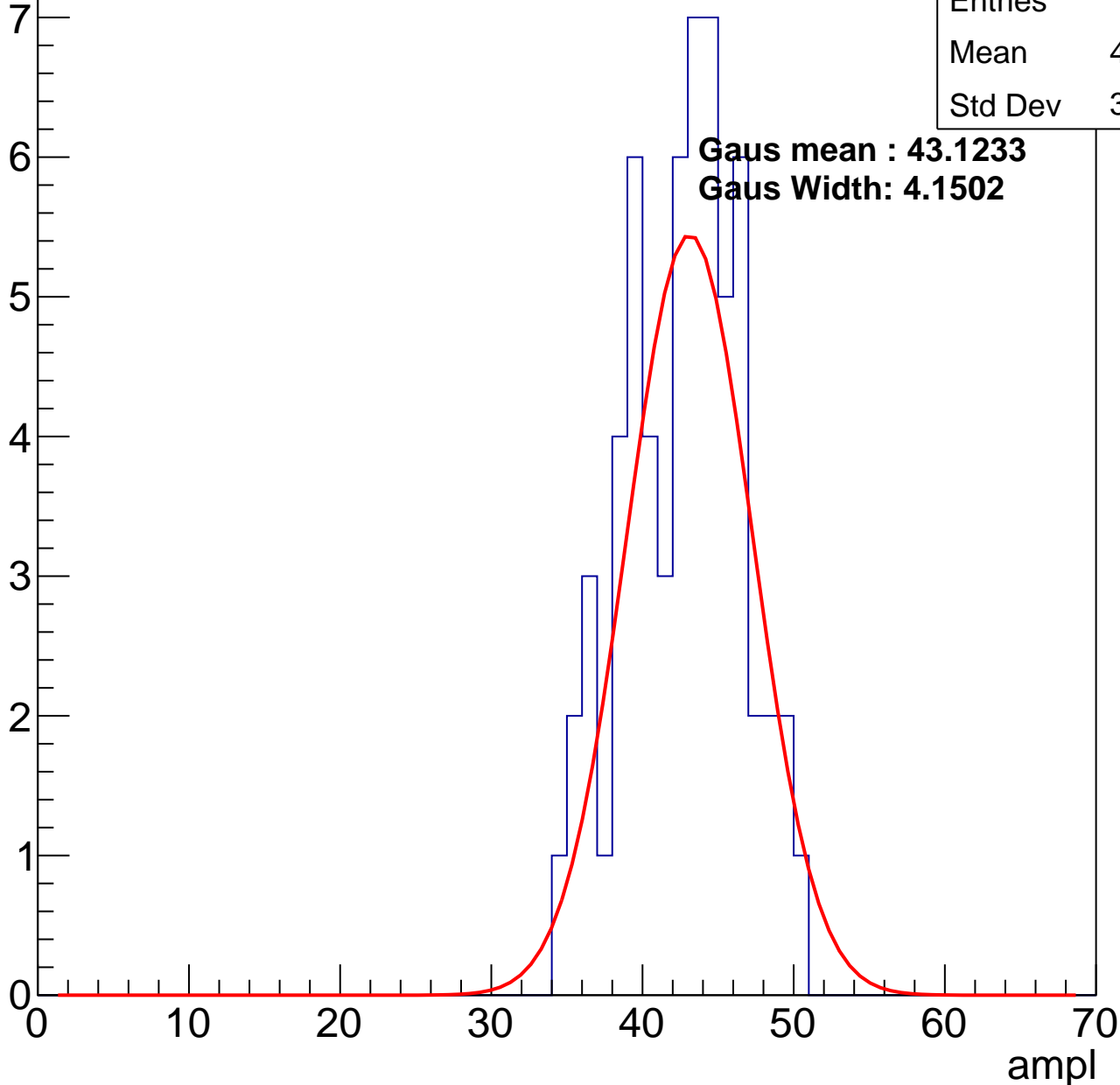
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.23
Std Dev	3.786

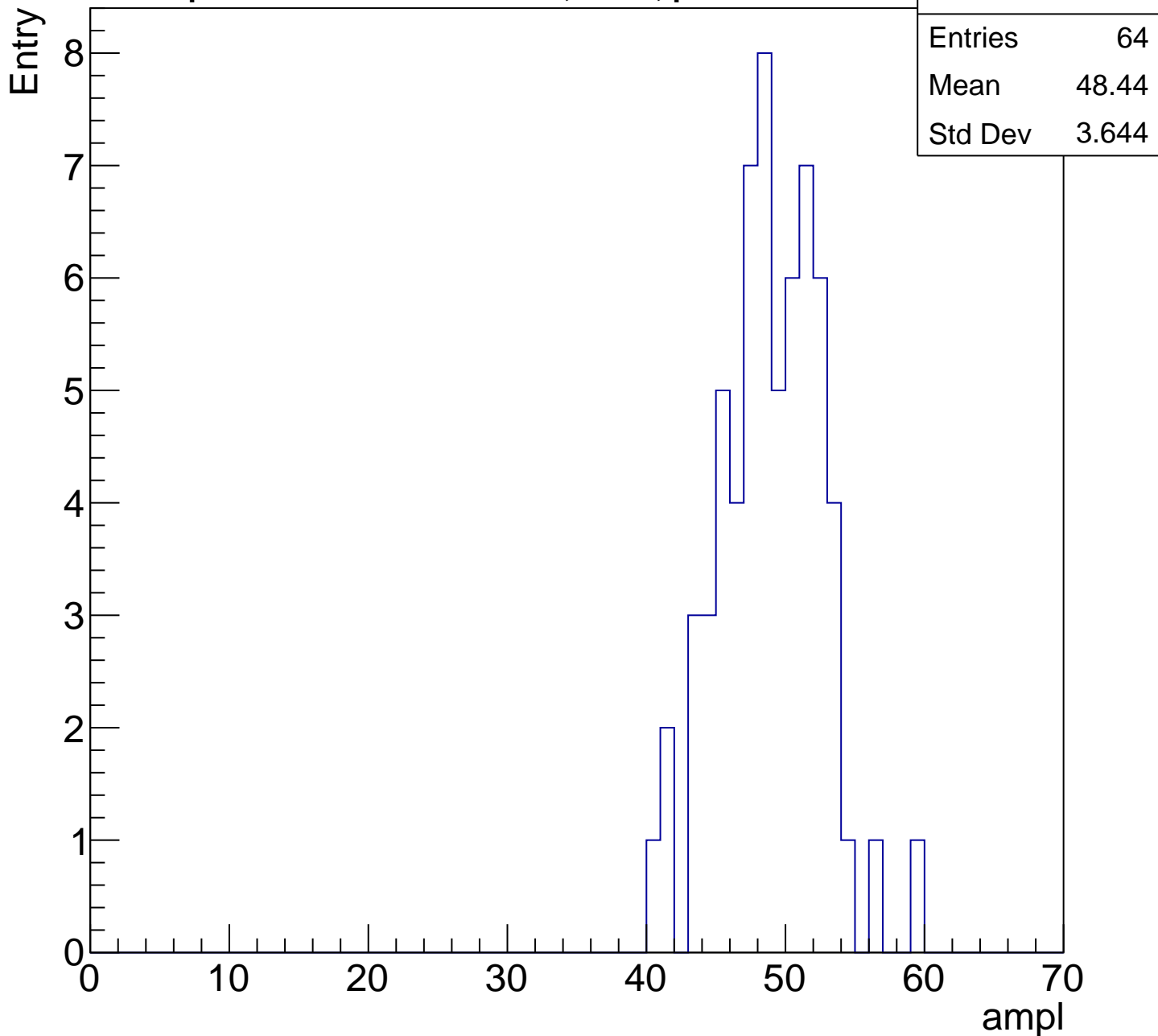
Gaus mean : 43.1233

Gaus Width: 4.1502



B1L101S, U4-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

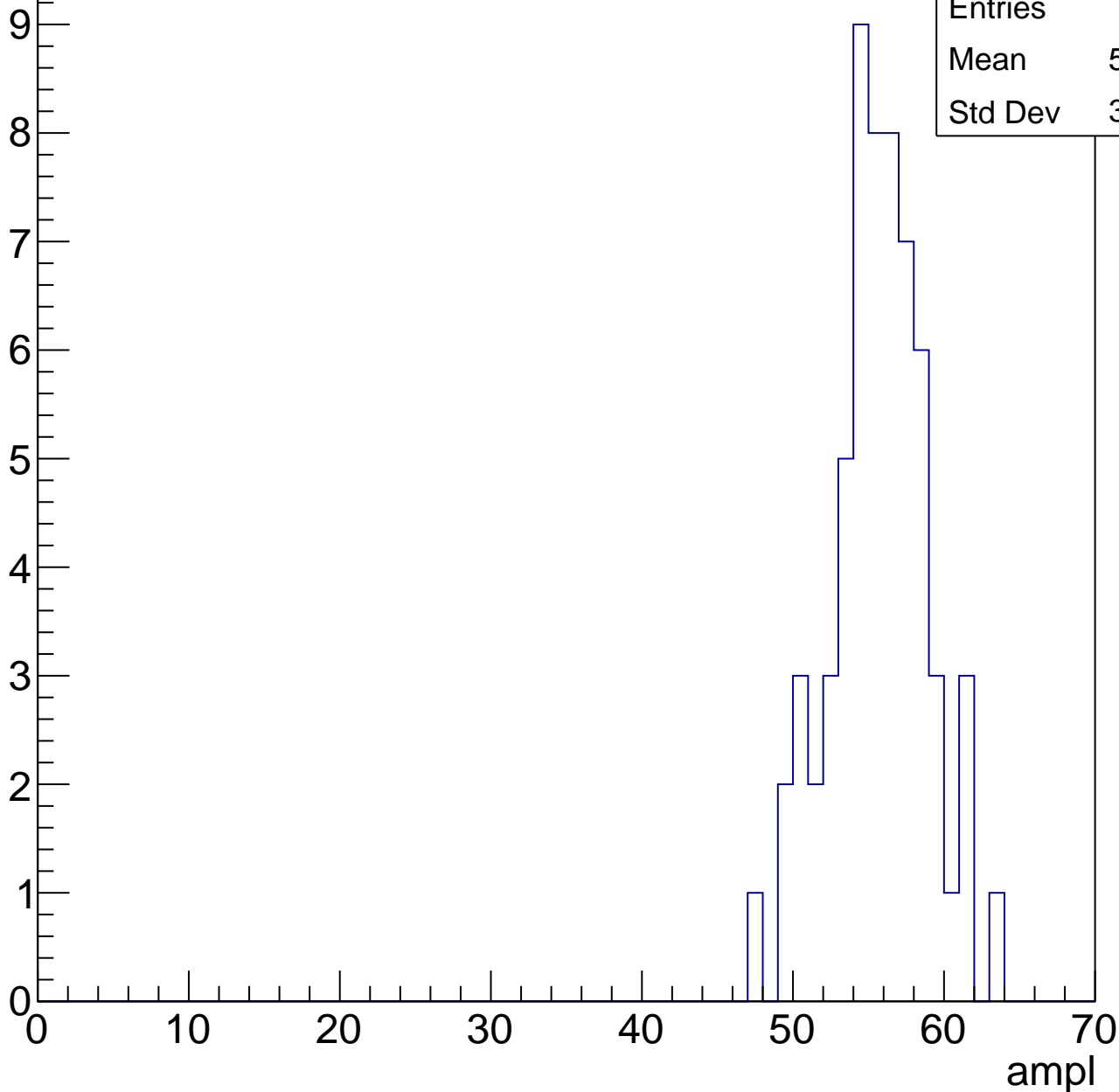


B1L101S, U4-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.19
Std Dev	3.192

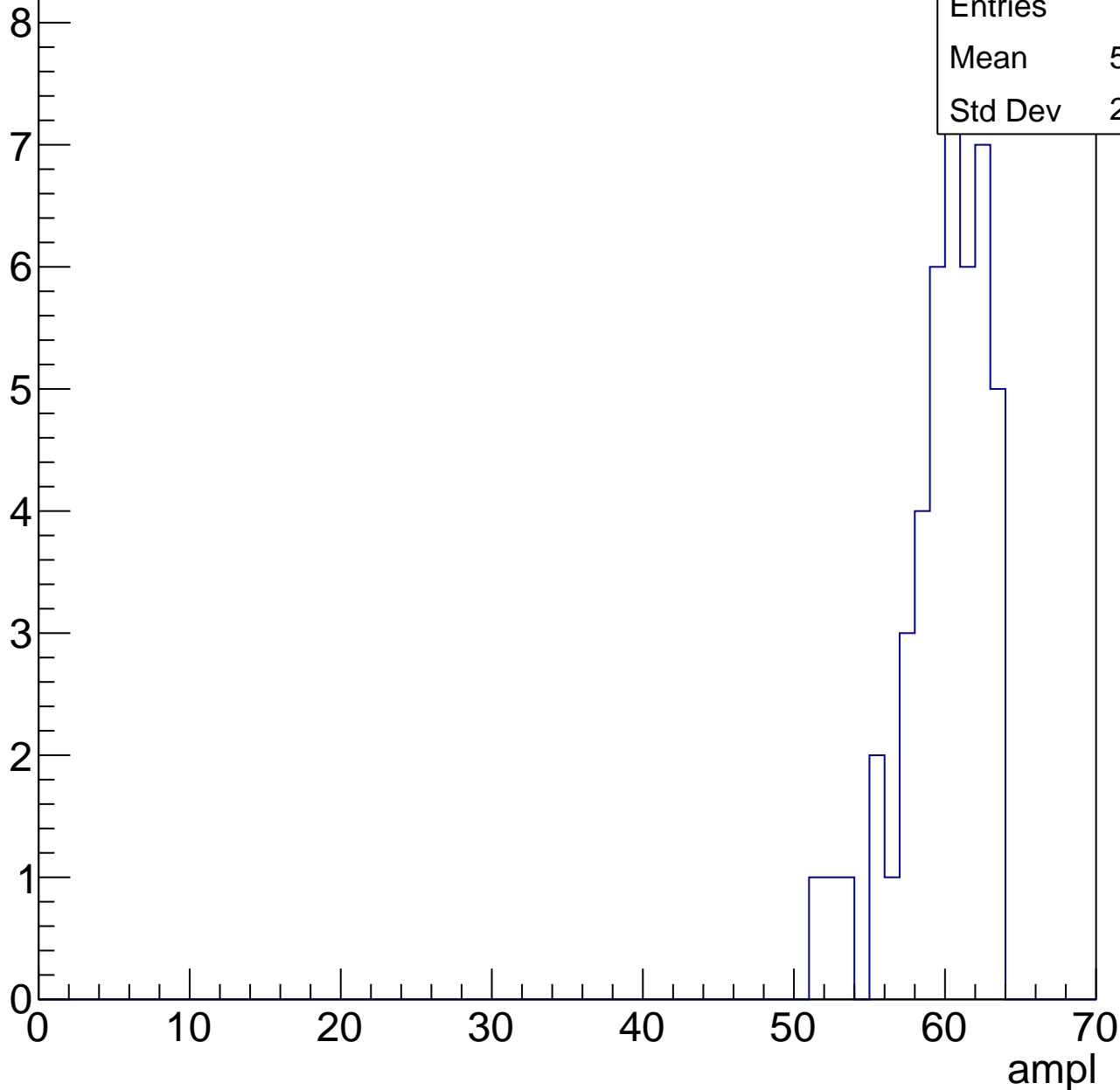


B1L101S, U4-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

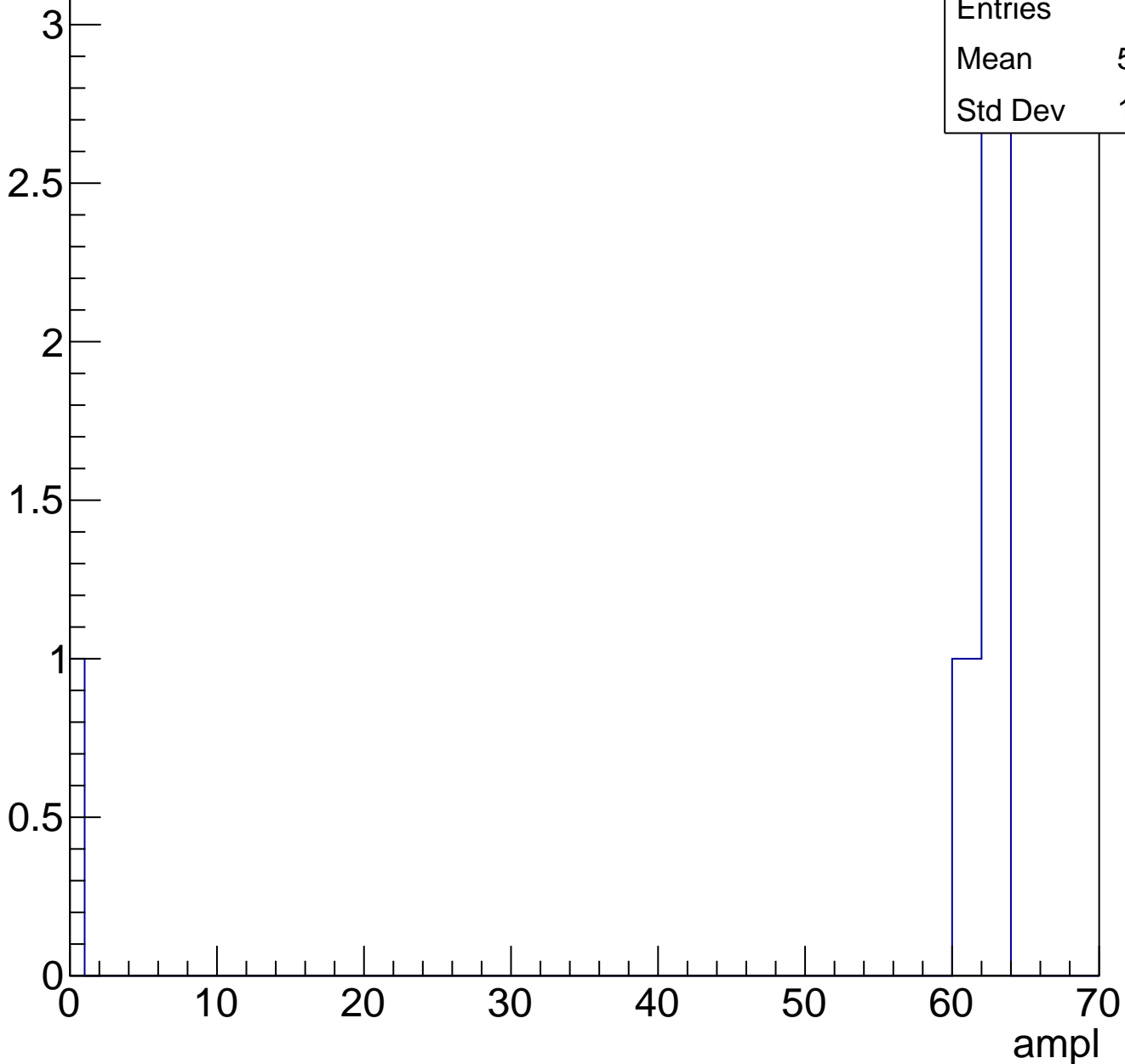
Entries	45
Mean	59.42
Std Dev	2.879



B1L101S, U4-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch42, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	28.14
Std Dev	7.18

Gaus mean : 30.1421

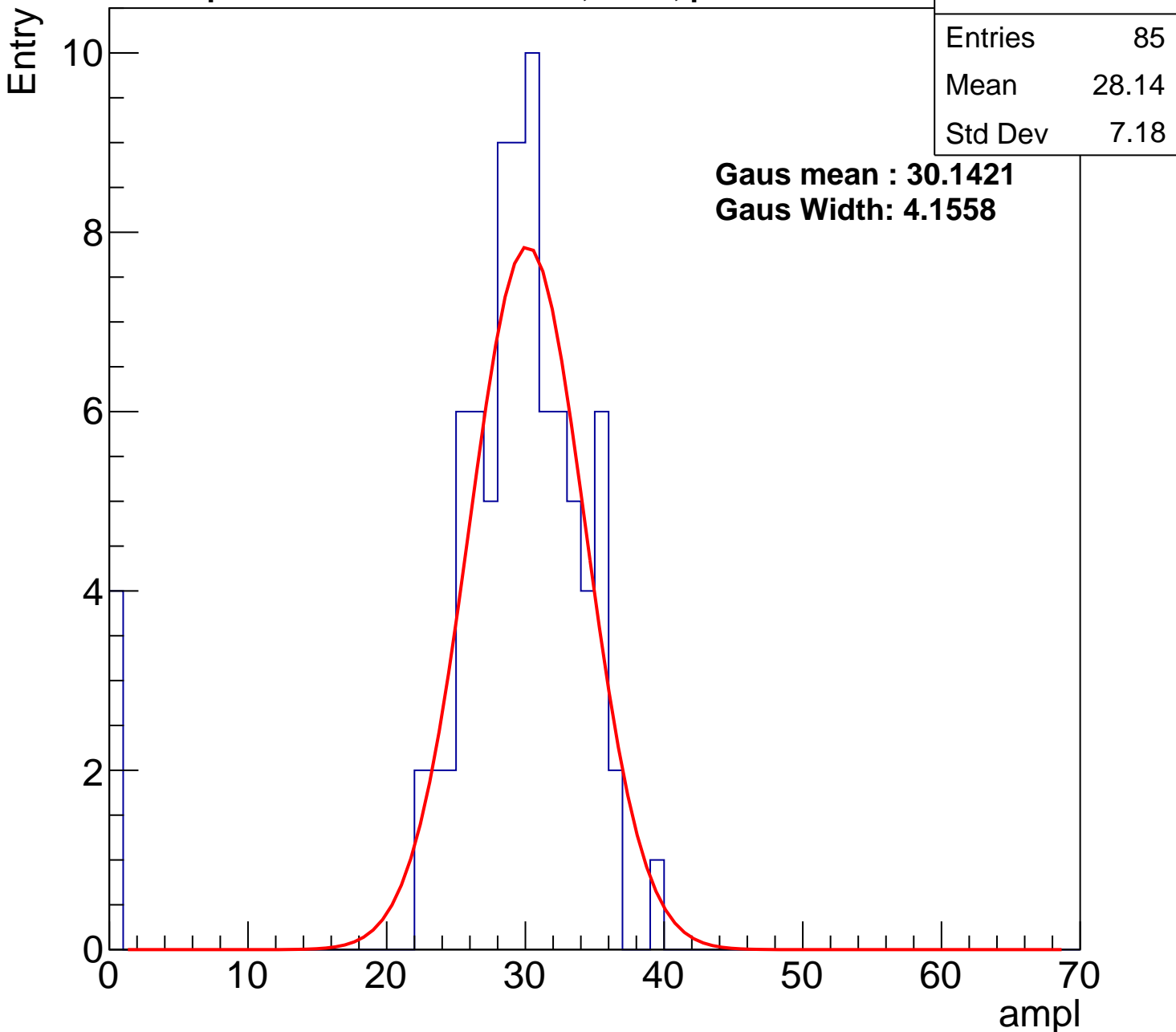
Gaus Width: 4.1558

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch42, adc1

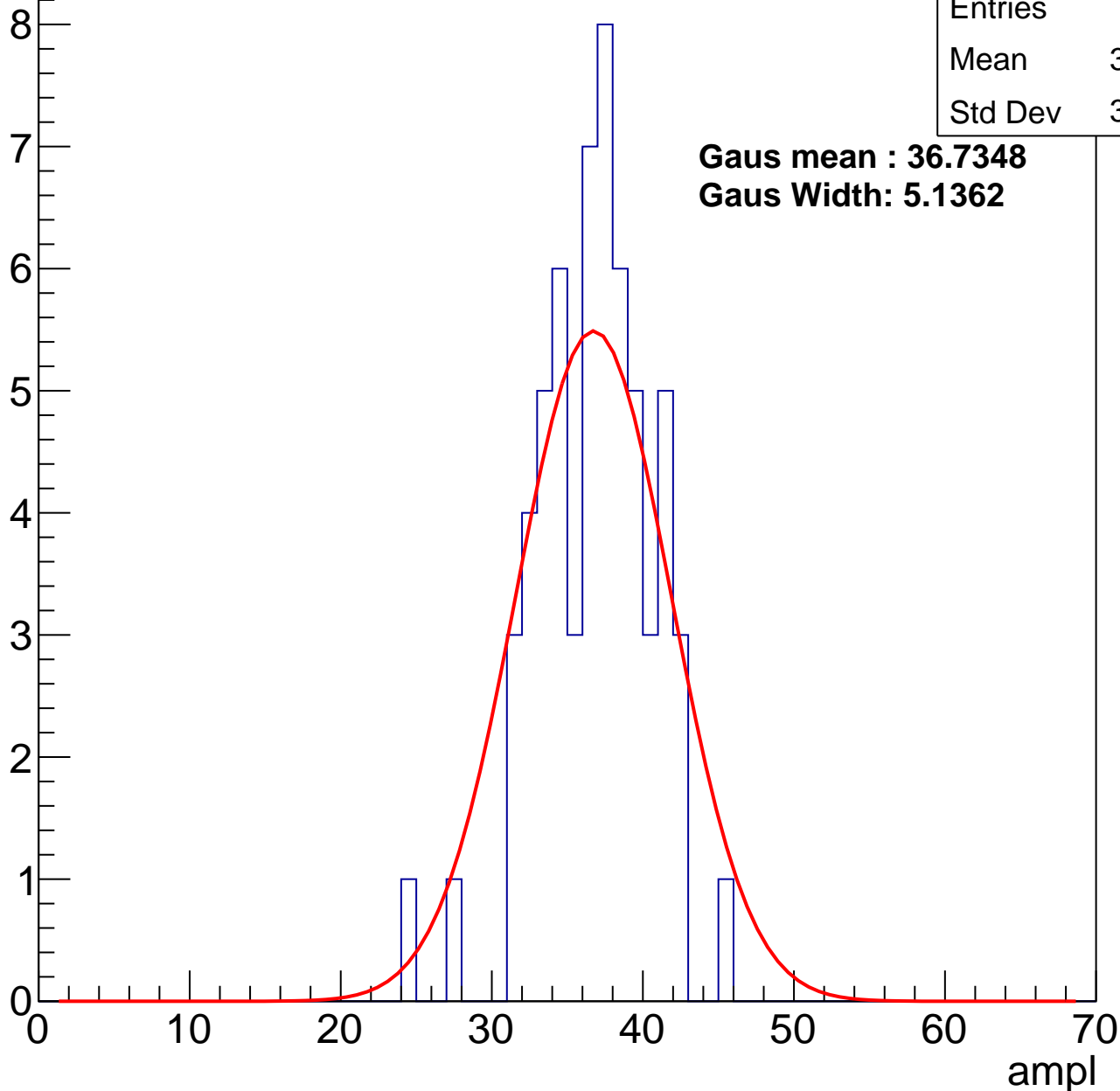
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.28
Std Dev	3.777

Gaus mean : 36.7348

Gaus Width: 5.1362



B1L101S, U4-ch42, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	42.2
Std Dev	3.124

Gaus mean : 42.6303

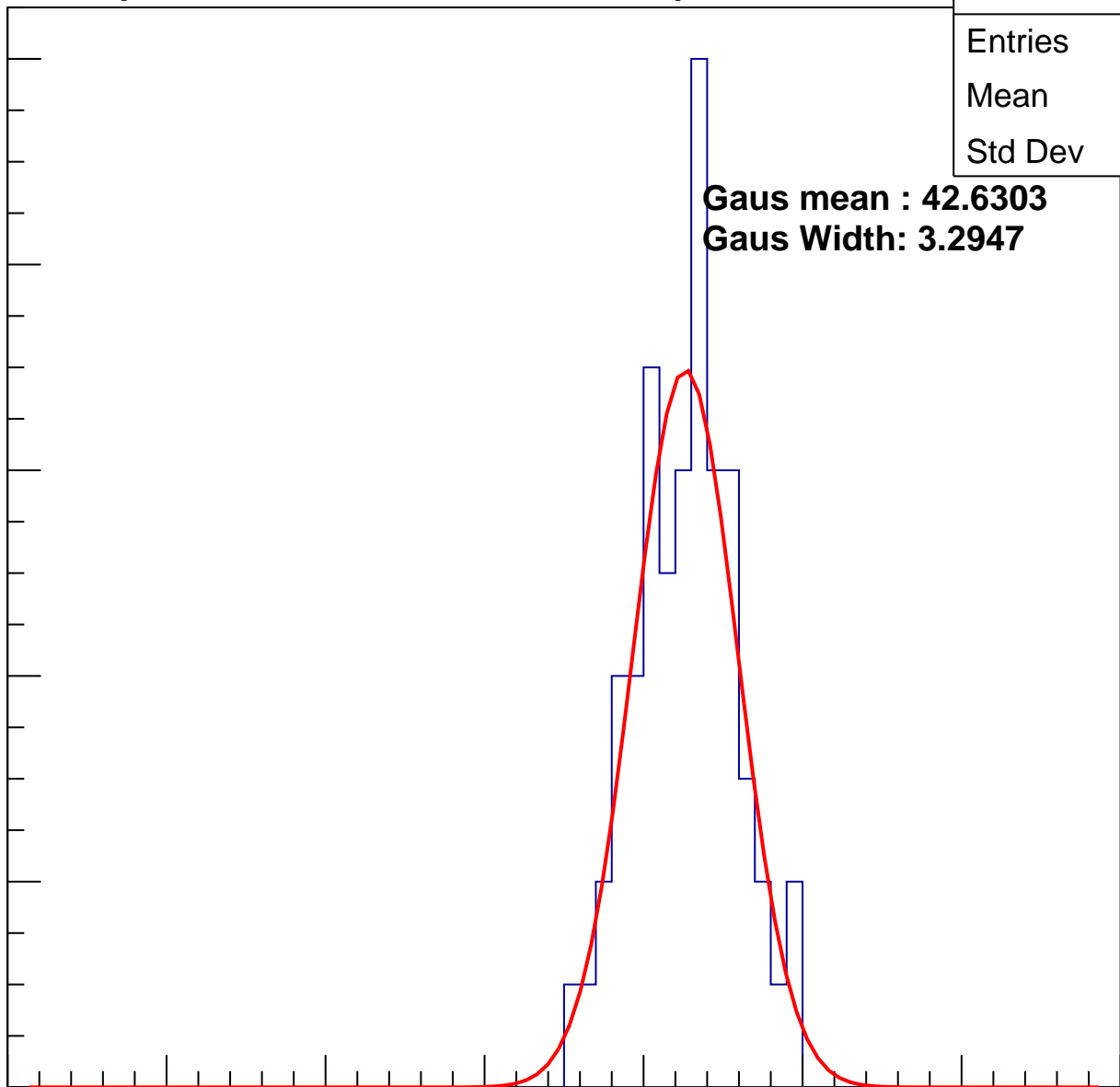
Gaus Width: 3.2947

Entry

10
8
6
4
2
0

ampl

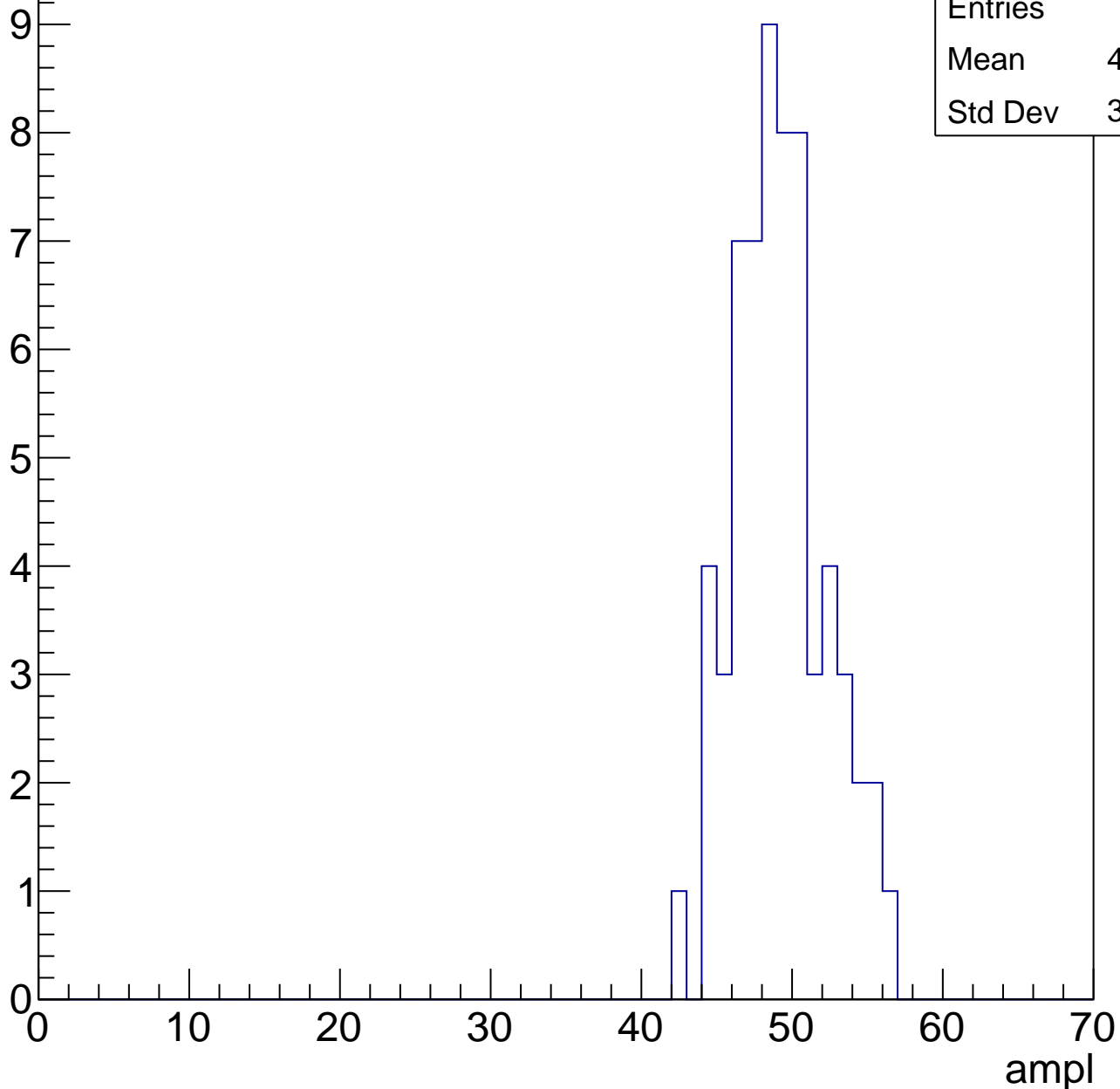
0 10 20 30 40 50 60 70



B1L101S, U4-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

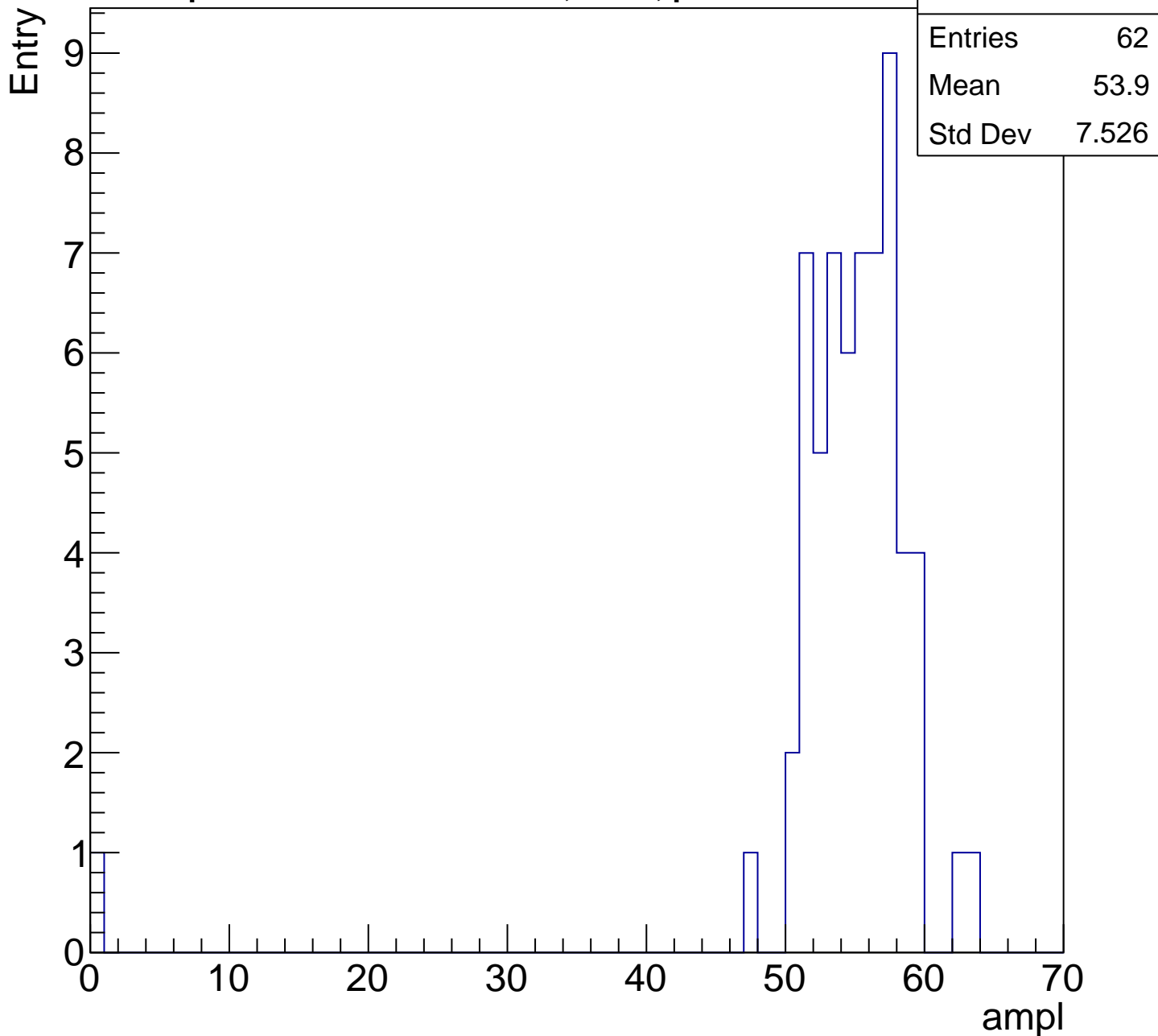
Entry



Entries	62
Mean	48.74
Std Dev	3.026

B1L101S, U4-ch42, adc4

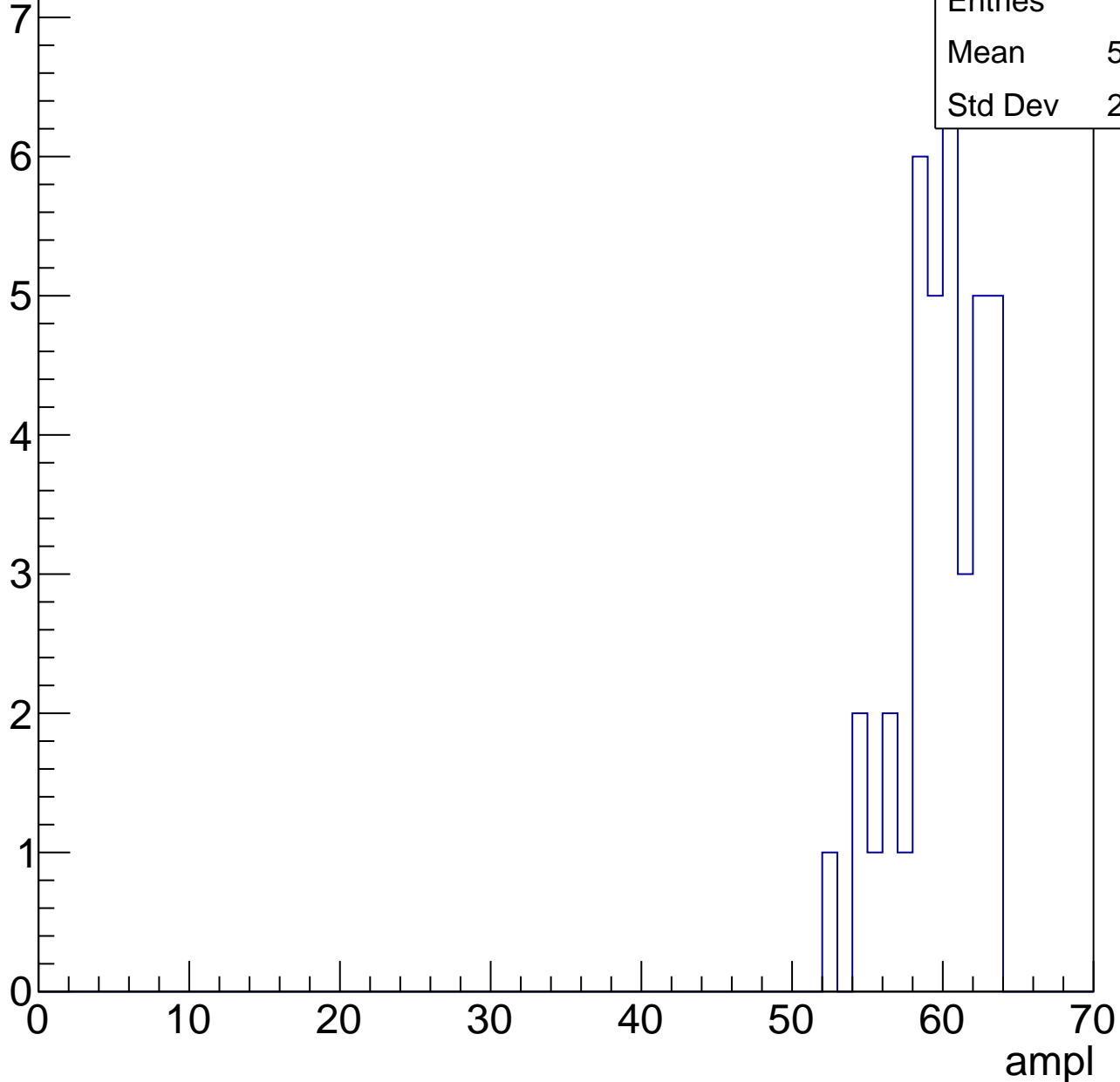
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

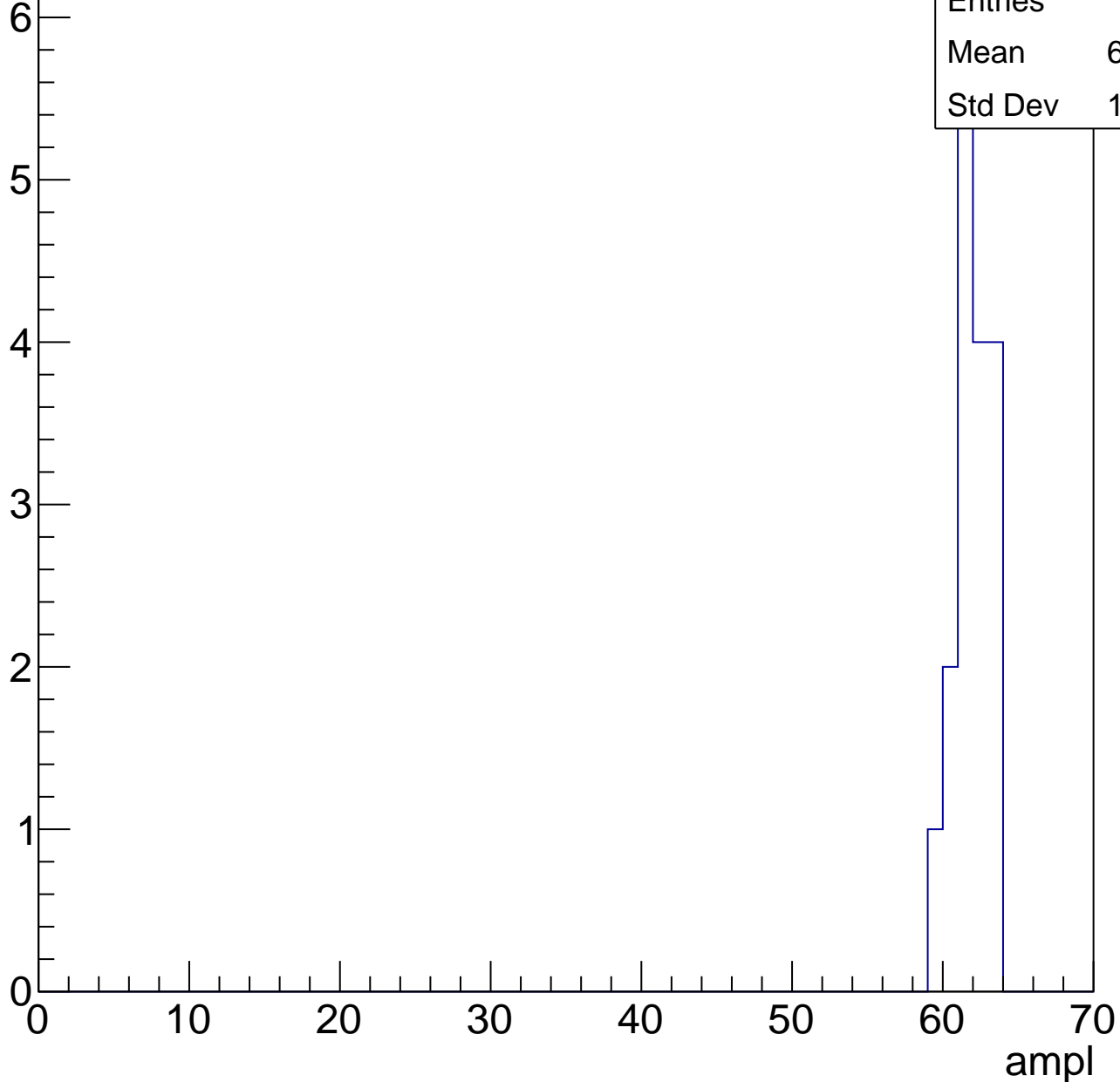
Entry



B1L101S, U4-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

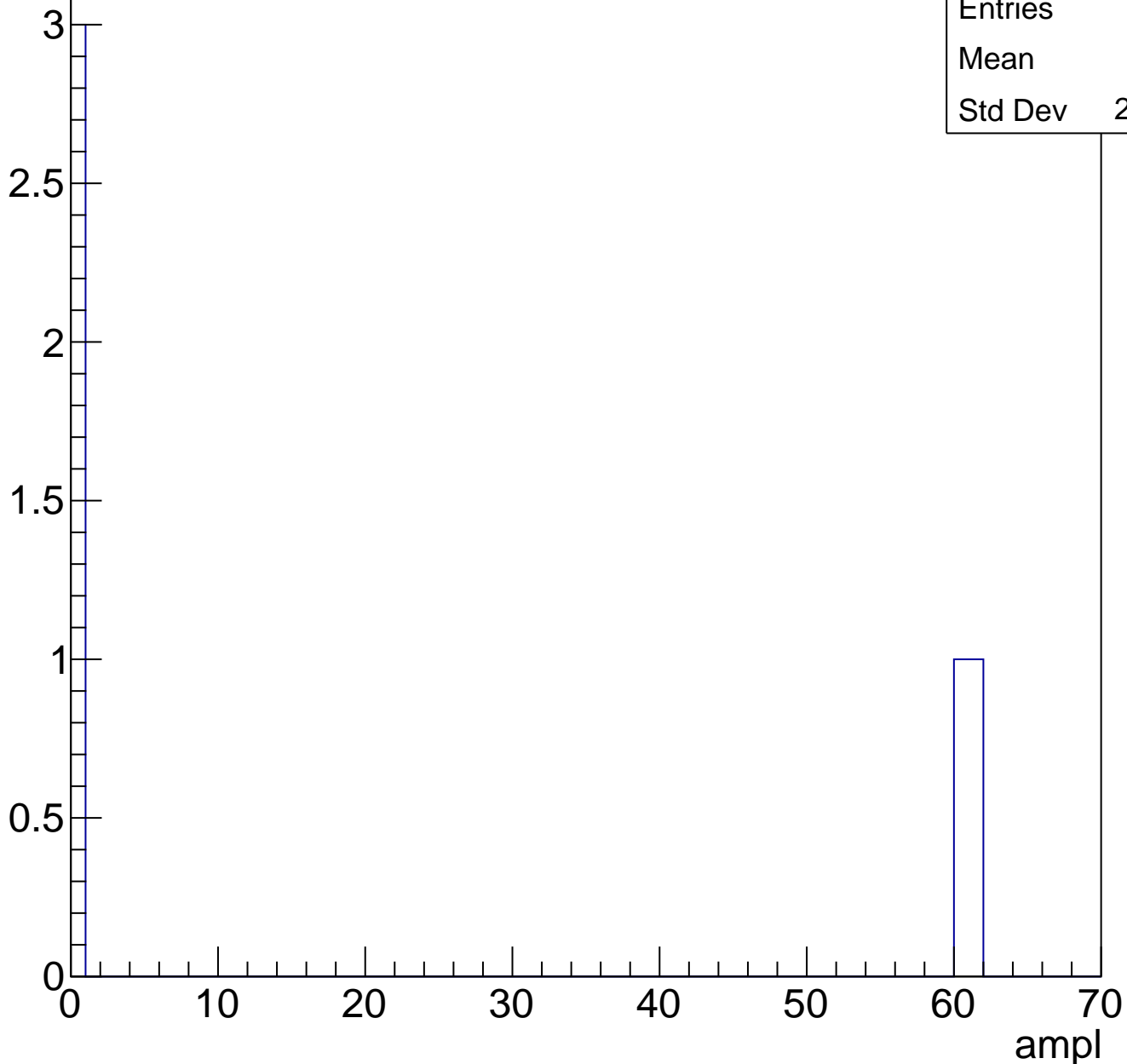
Entry



B1L101S, U4-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch43, adc0

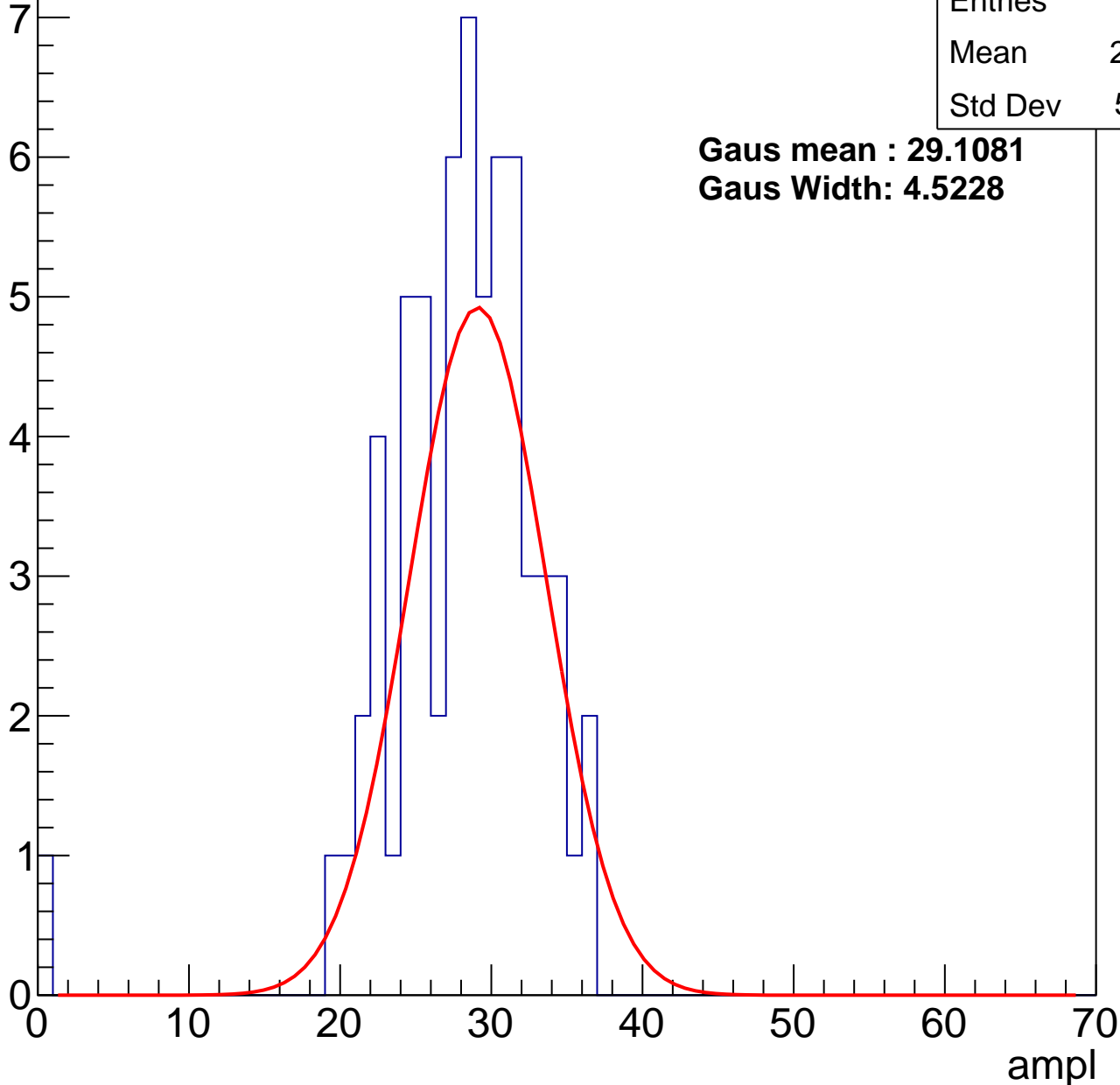
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.53
Std Dev	5.321

Gaus mean : 29.1081

Gaus Width: 4.5228



B1L101S, U4-ch43, adc1

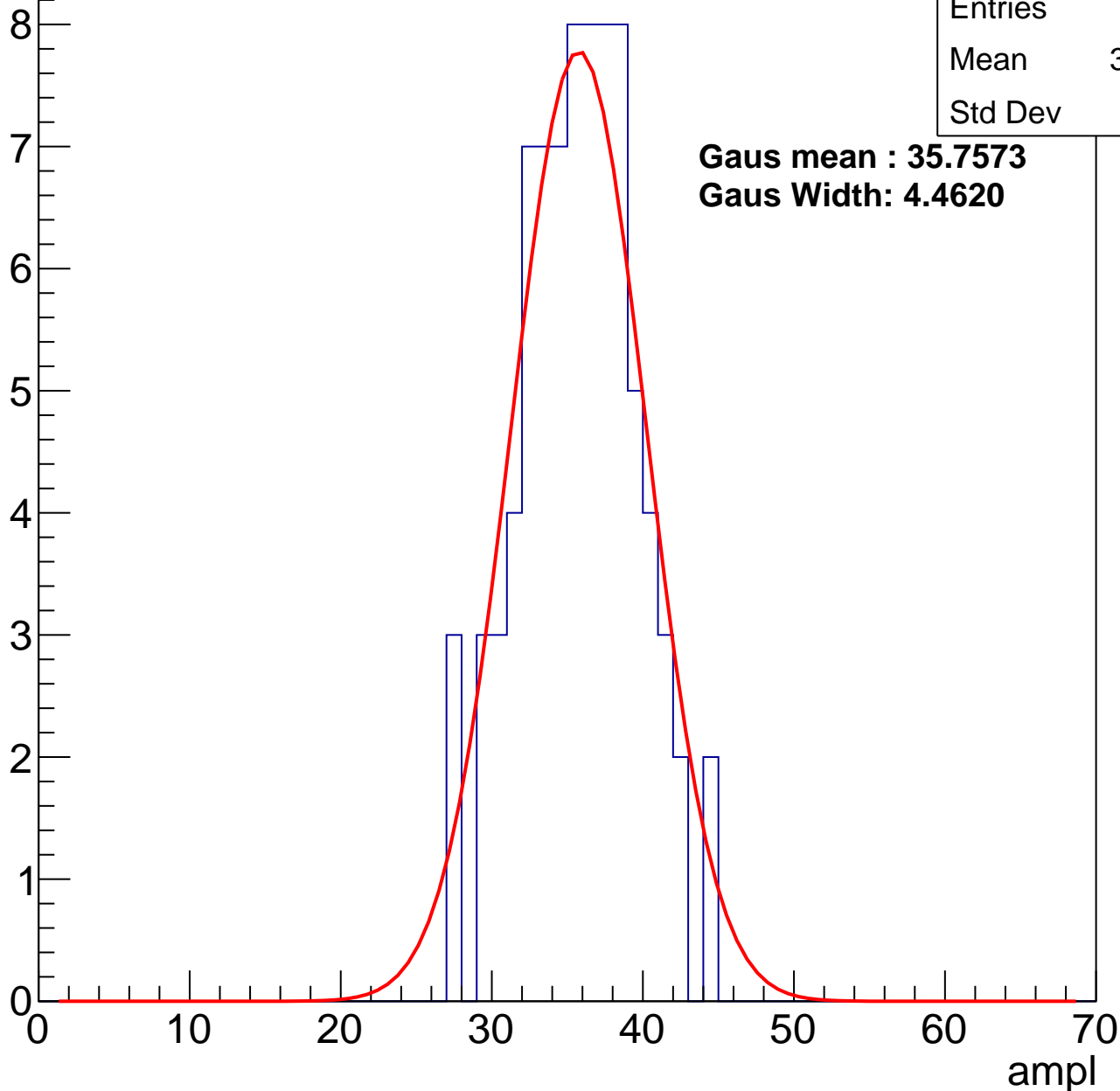
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	35.28
Std Dev	3.81

Gaus mean : 35.7573

Gaus Width: 4.4620



B1L101S, U4-ch43, adc2

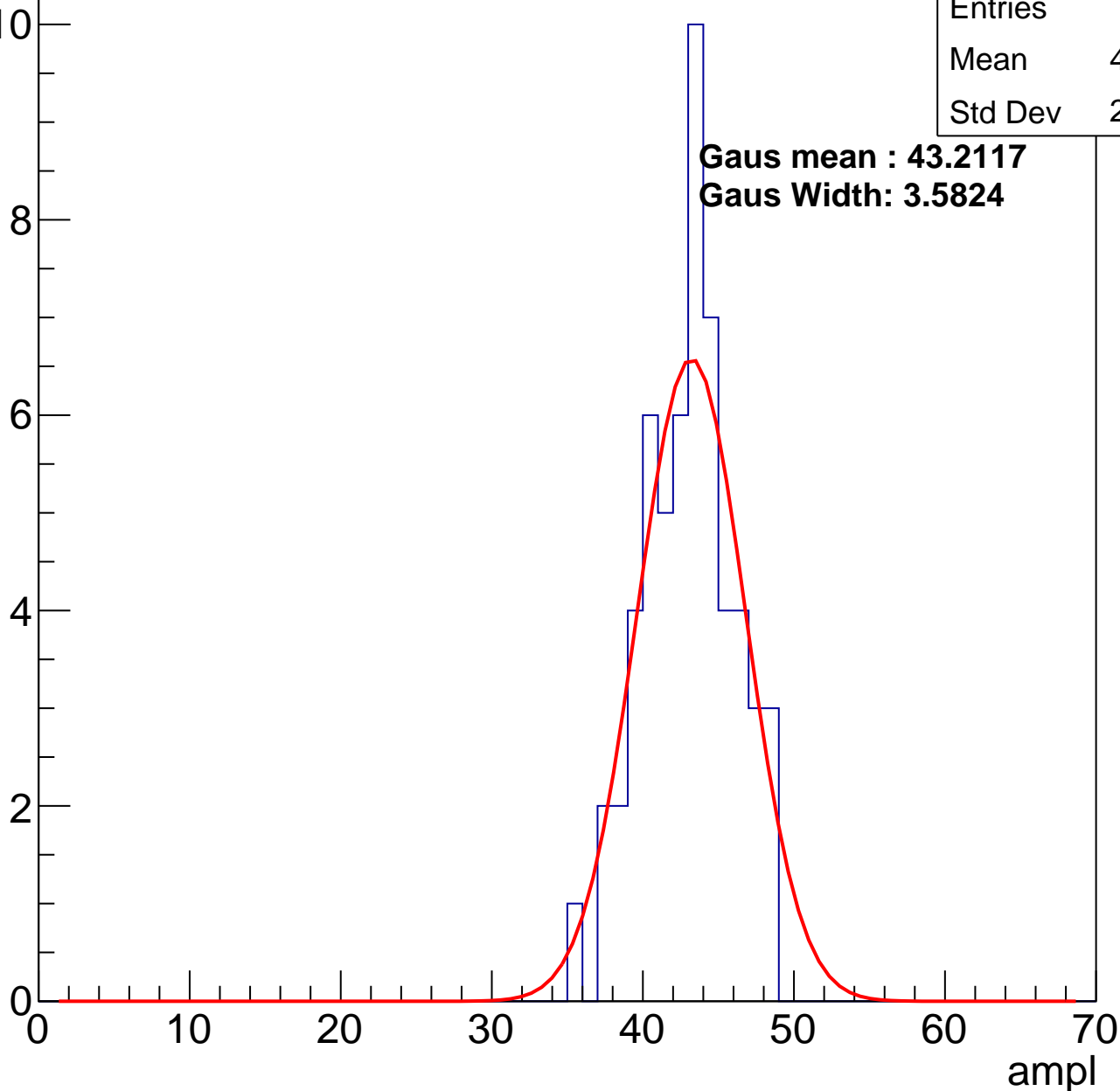
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.54
Std Dev	2.962

Gaus mean : 43.2117

Gaus Width: 3.5824

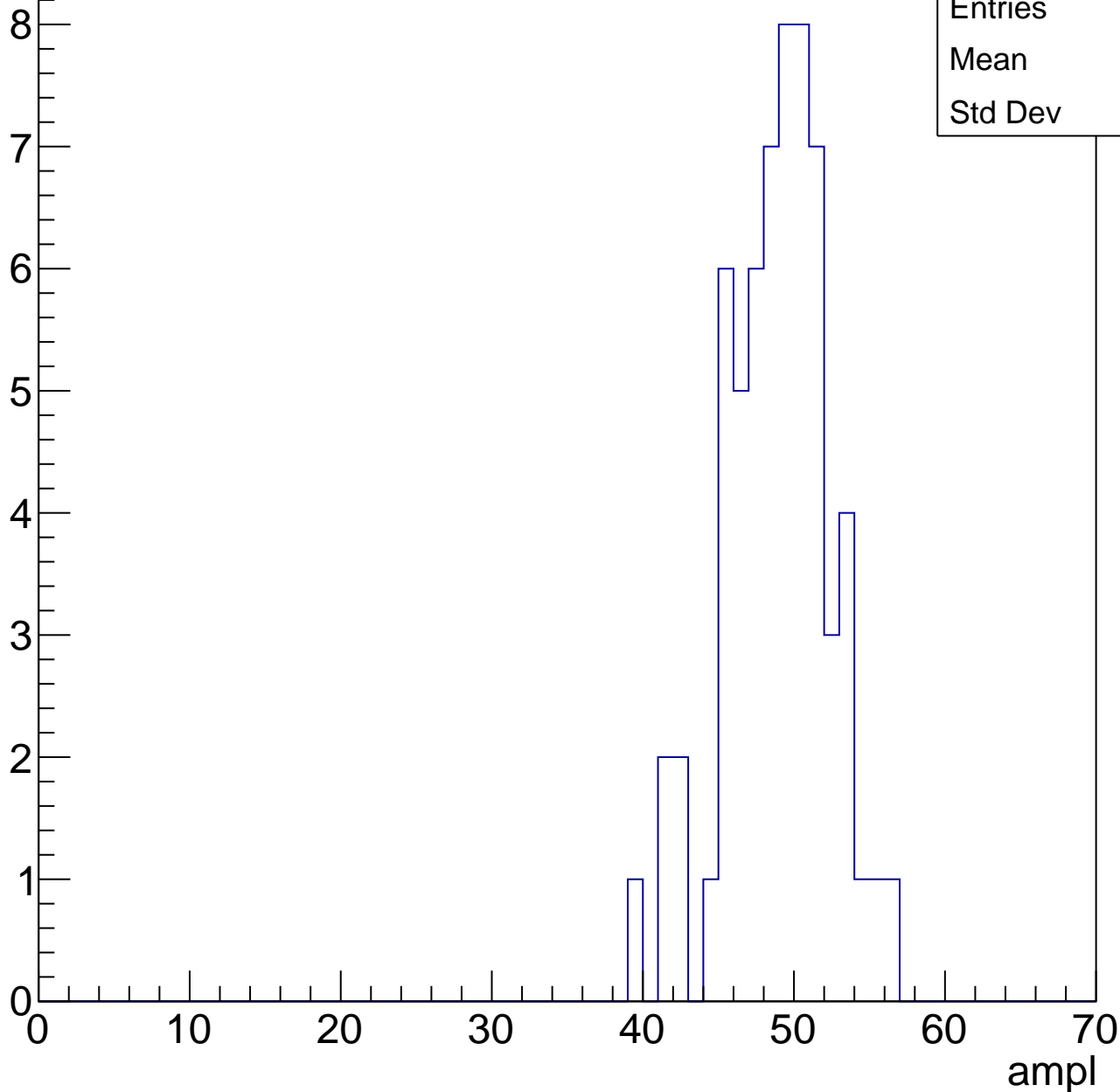


B1L101S, U4-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	48.4
Std Dev	3.42

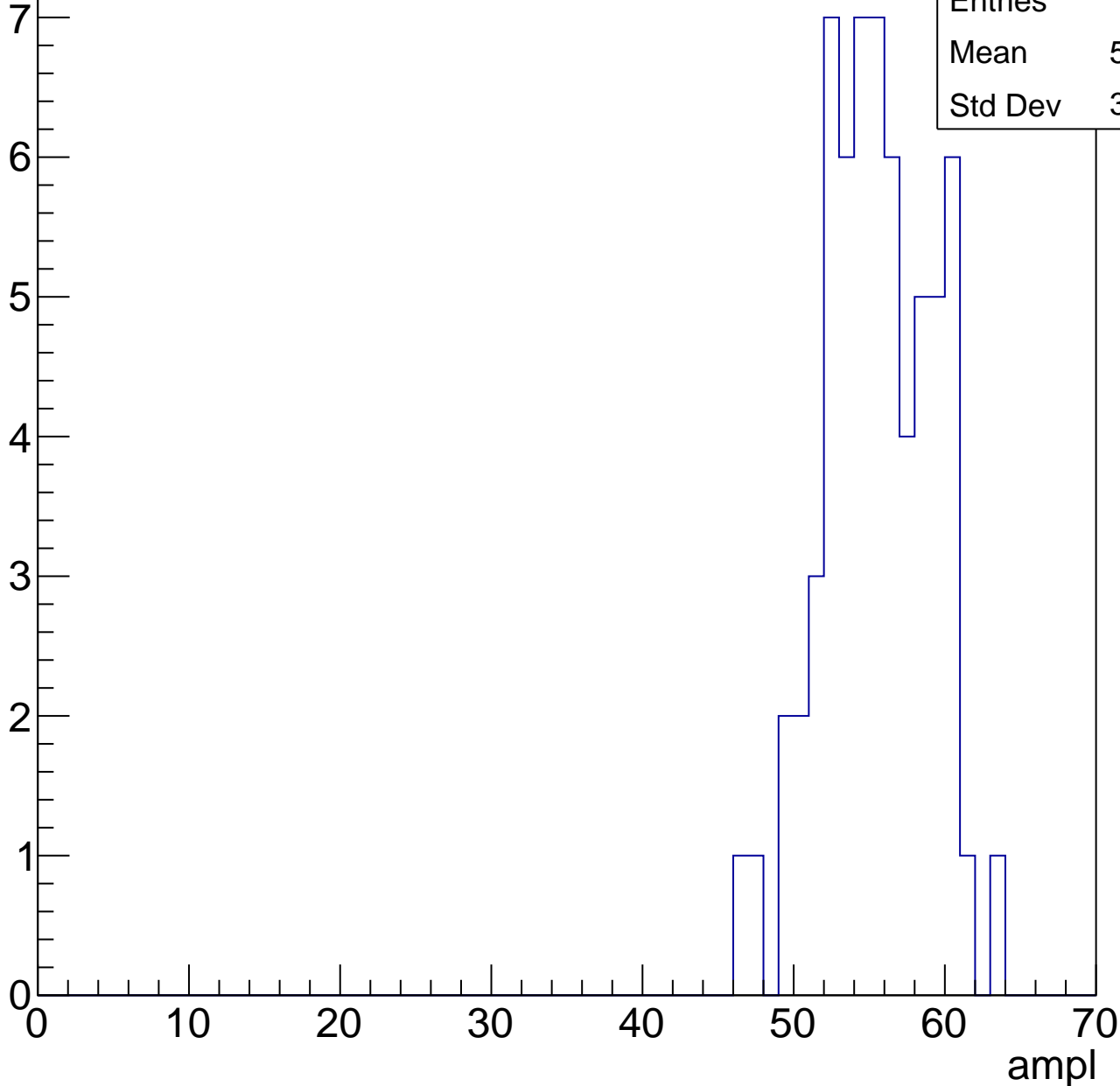


B1L101S, U4-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	55.03
Std Dev	3.549

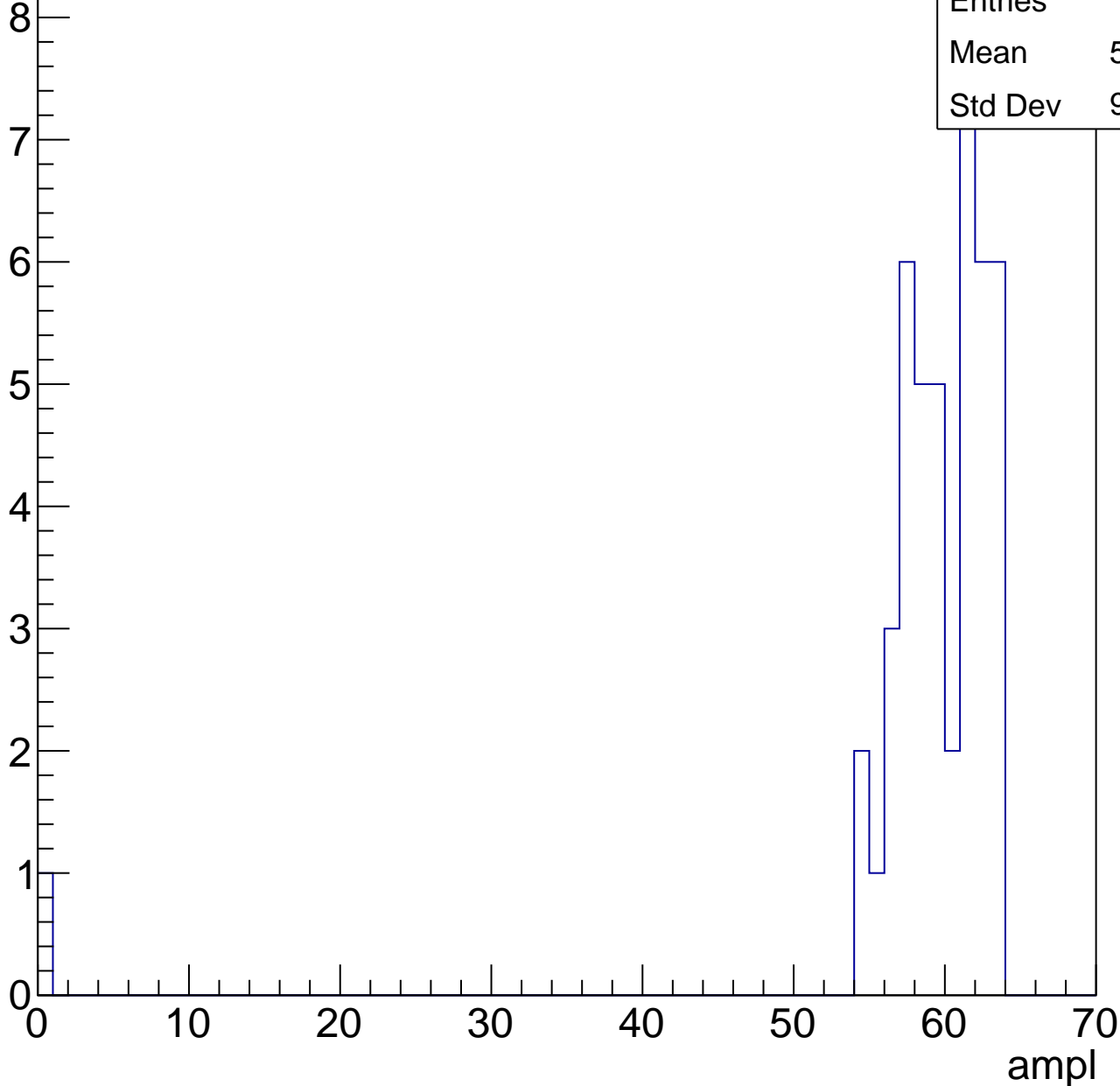


B1L101S, U4-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

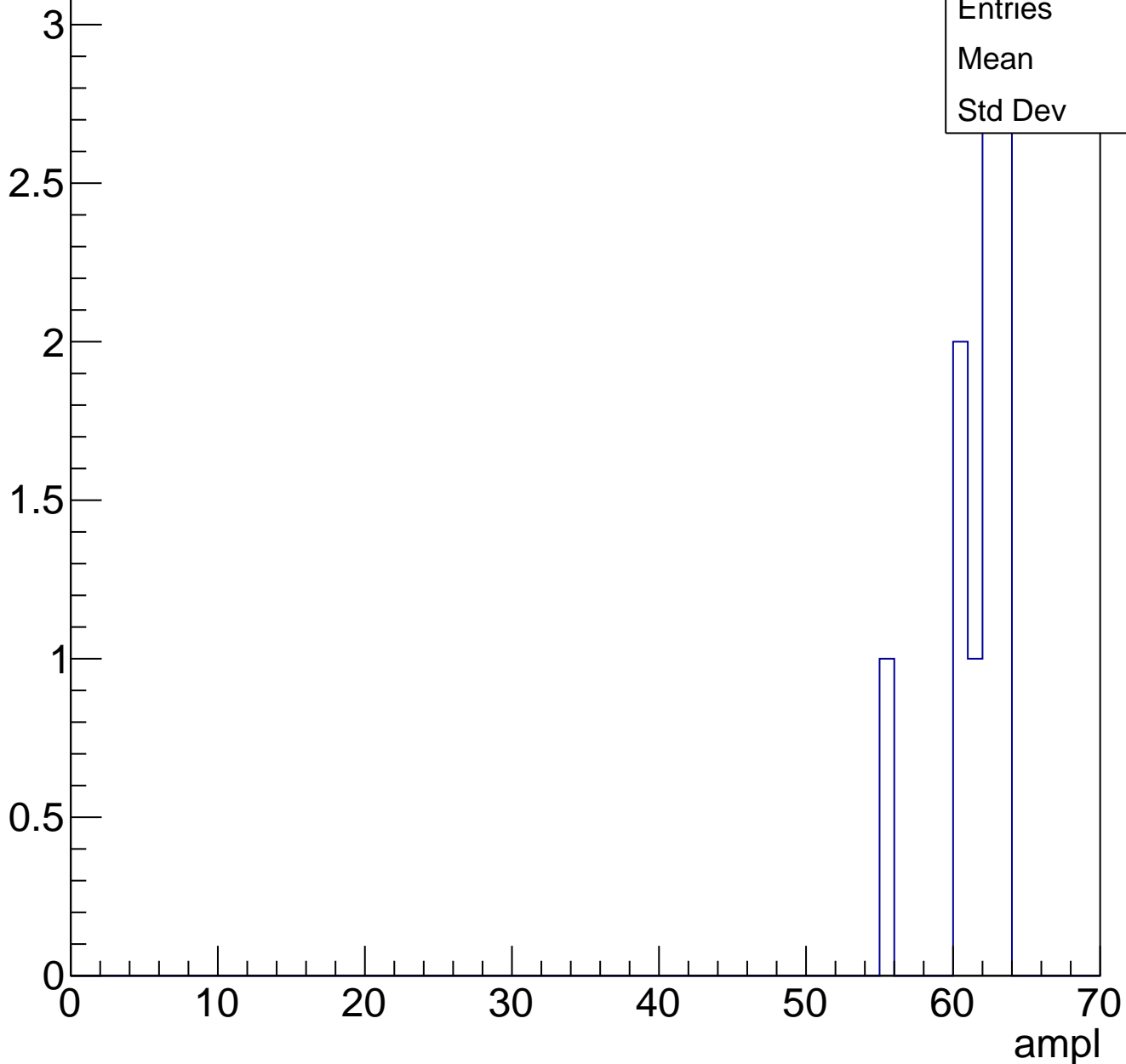
Entries	45
Mean	58.13
Std Dev	9.133



B1L101S, U4-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

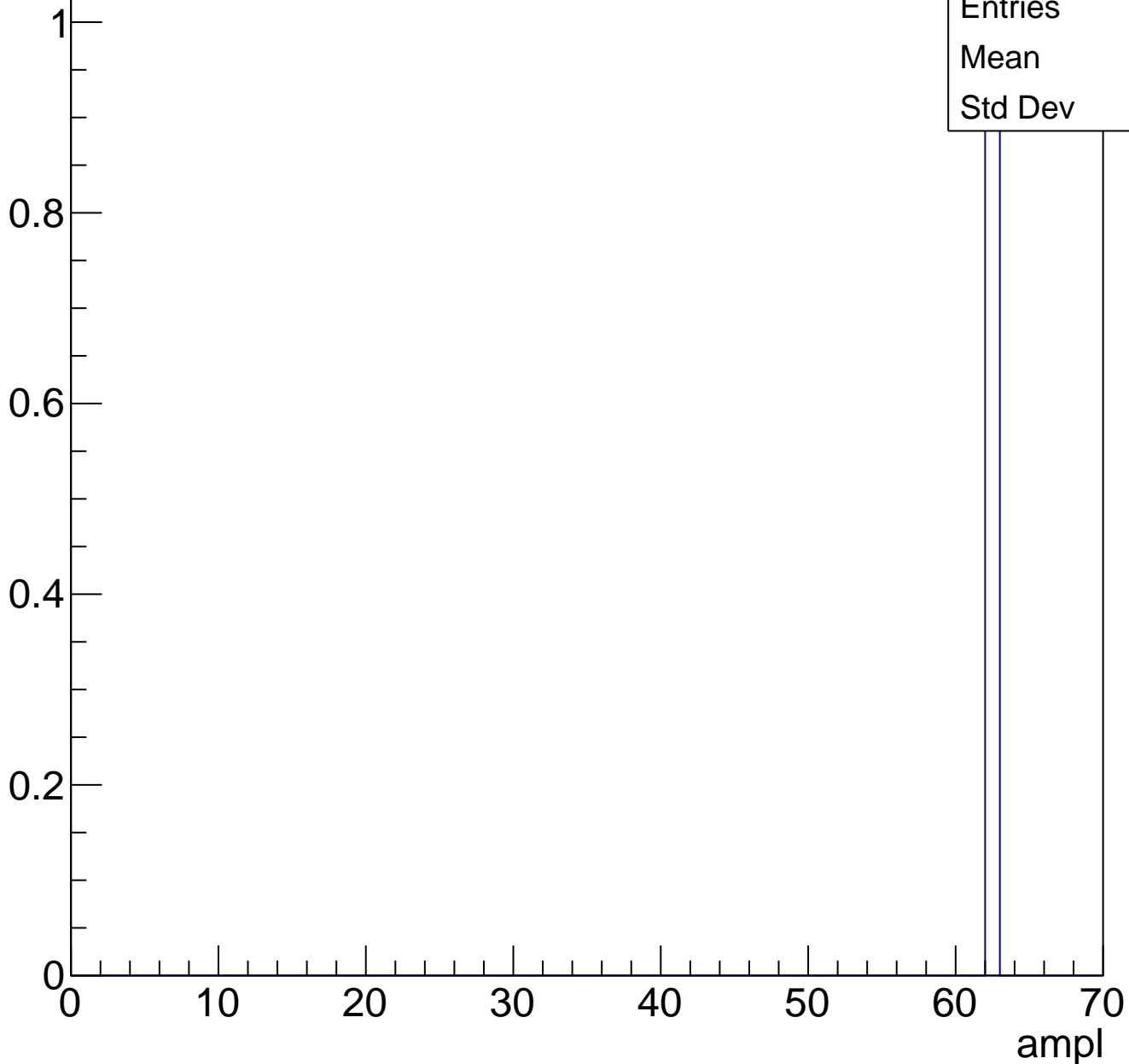
Entry



B1L101S, U4-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch44, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	101
Mean	28.66
Std Dev	6.997

Gaus mean : 29.8728

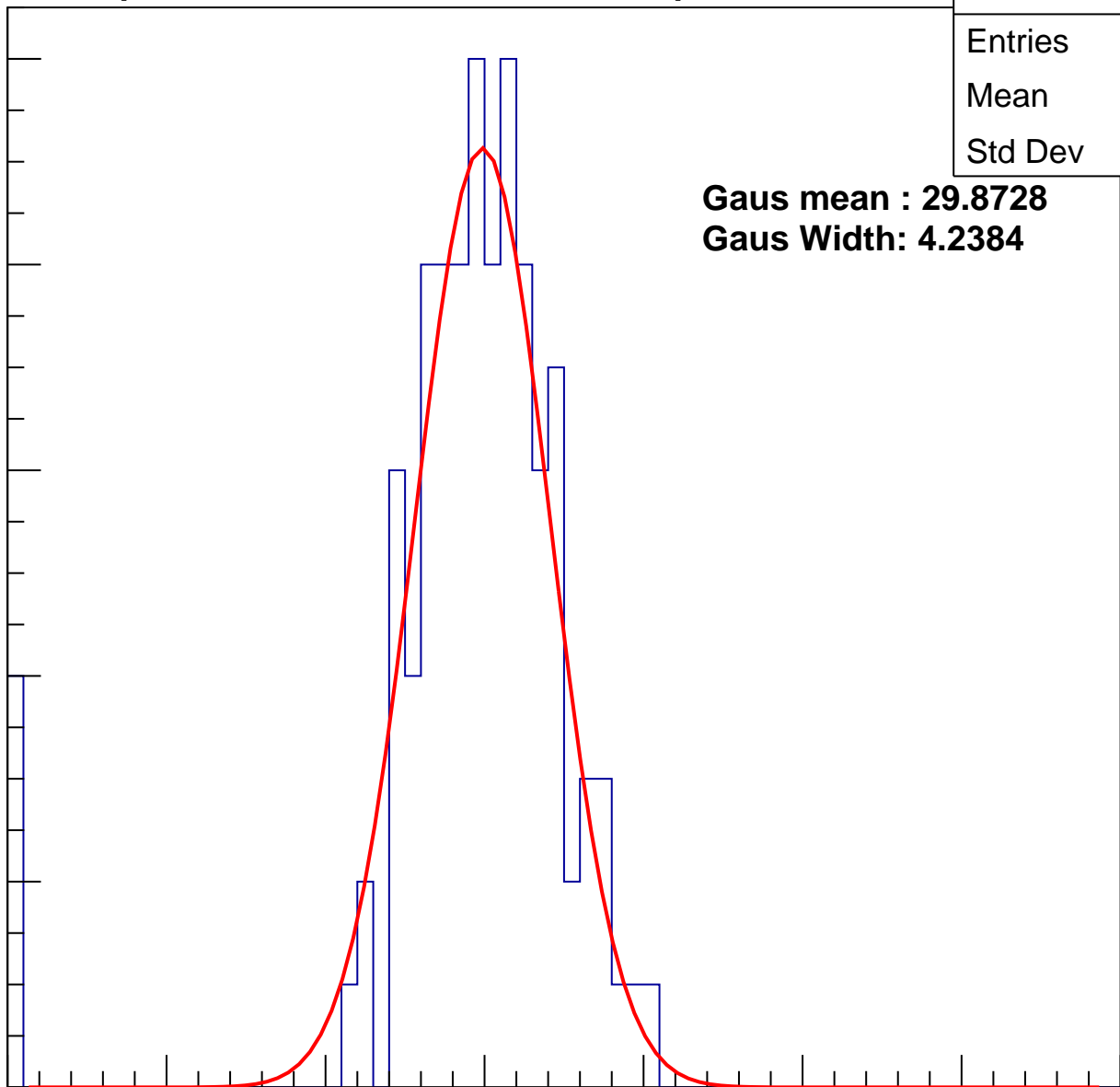
Gaus Width: 4.2384

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch44, adc1

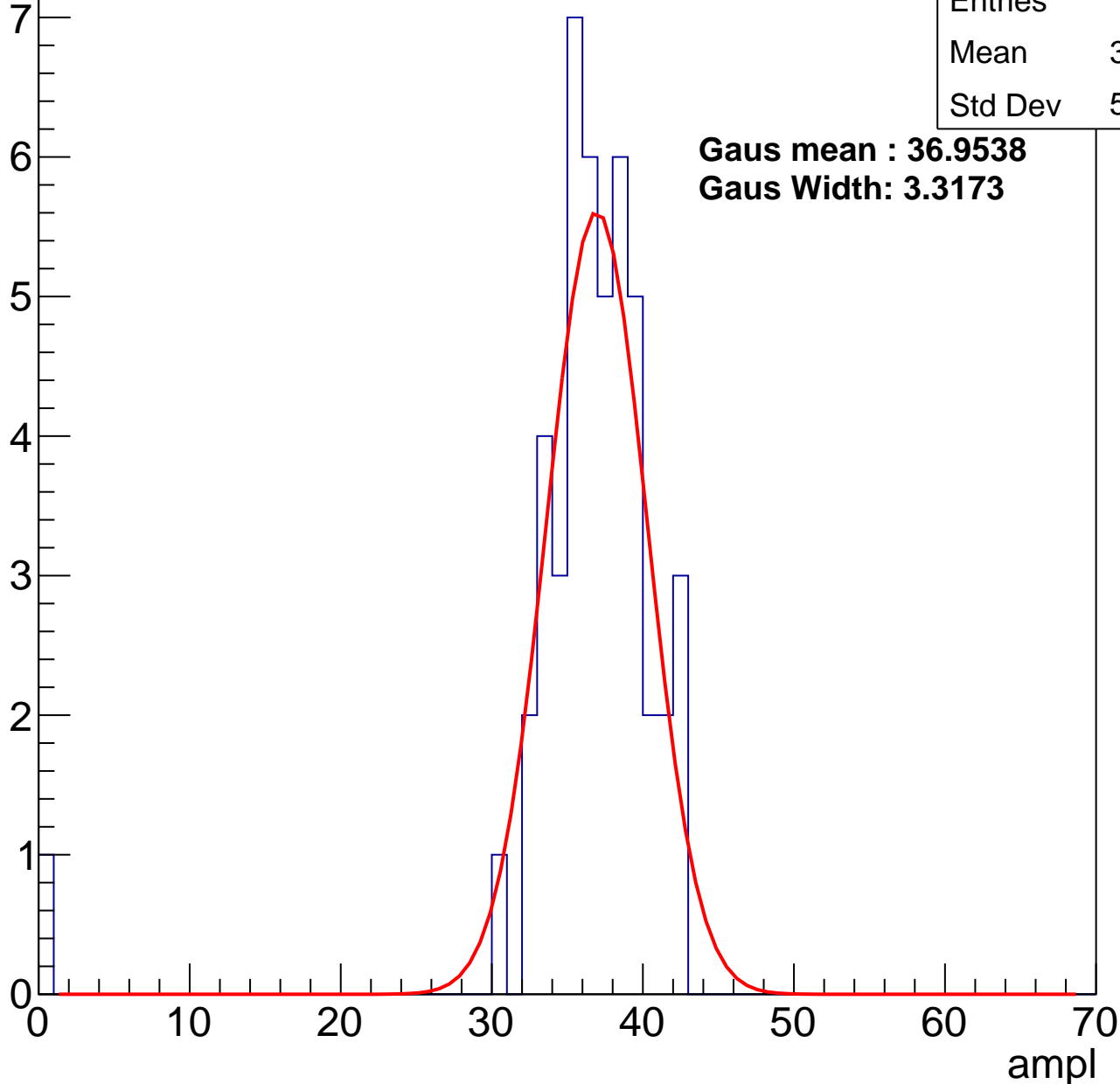
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	35.85
Std Dev	5.982

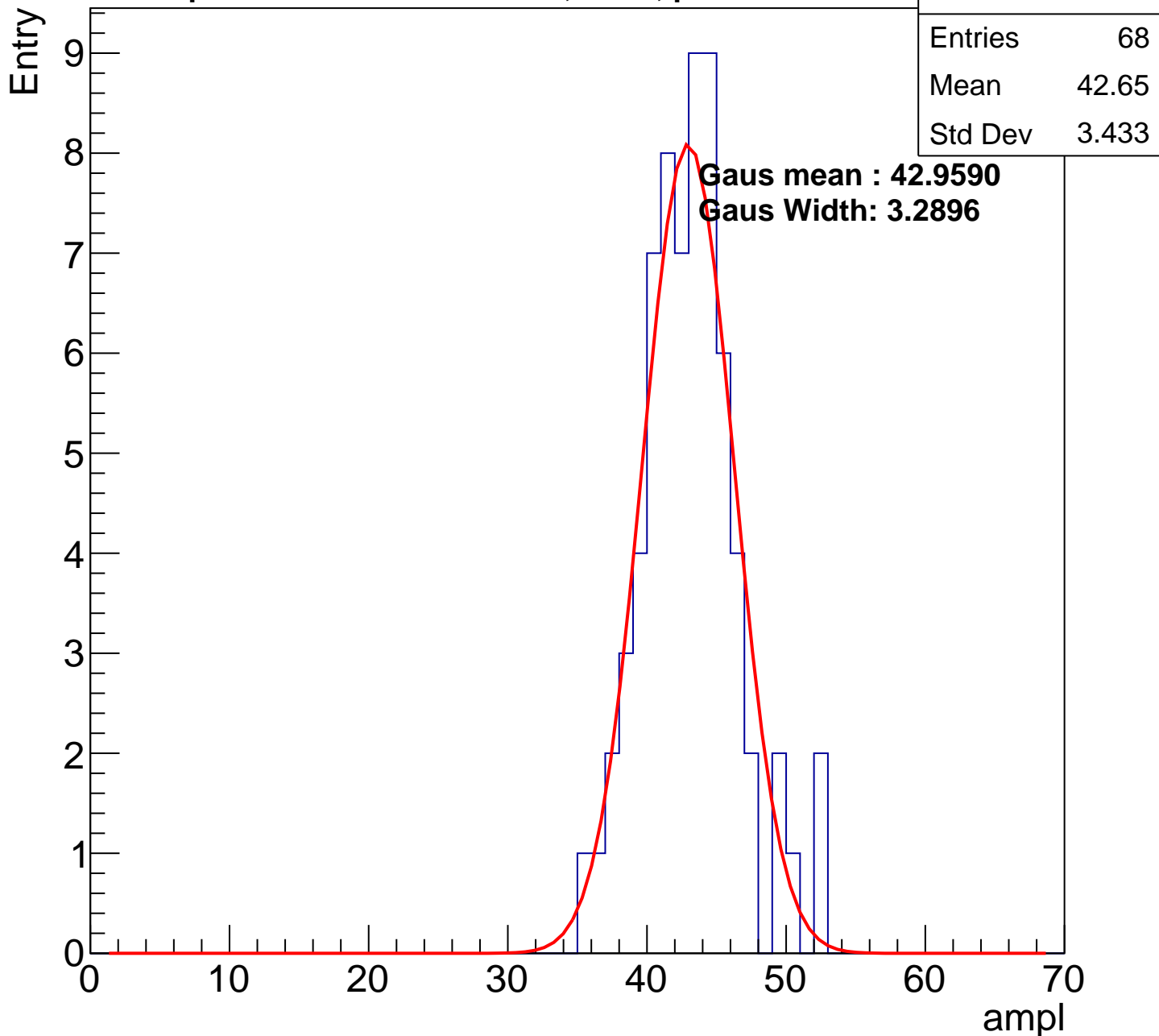
Gaus mean : 36.9538

Gaus Width: 3.3173



B1L101S, U4-ch44, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	49.09
Std Dev	2.891

Entry

10

8

6

4

2

0

0

10

20

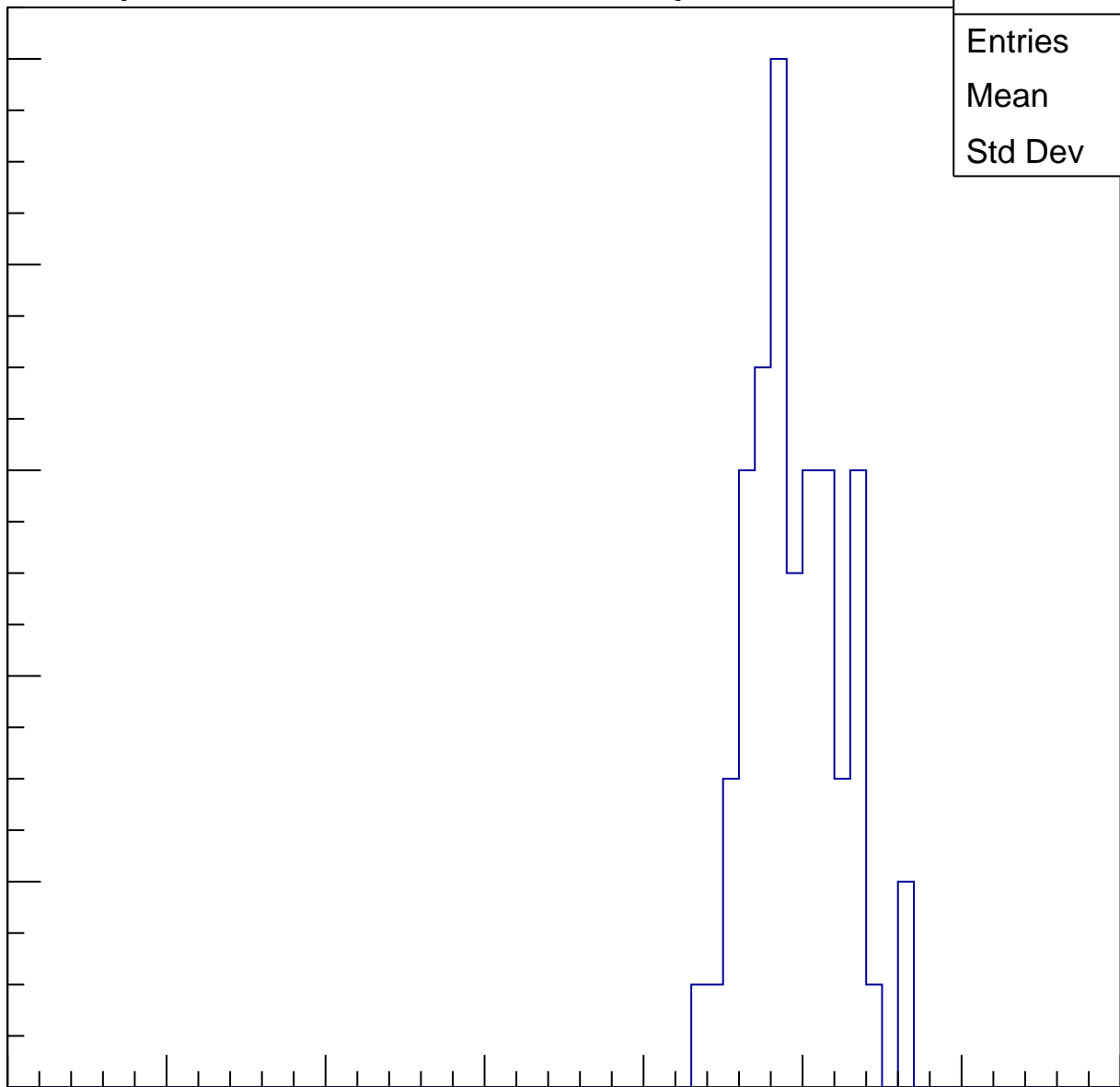
30

40

50

60

ampl

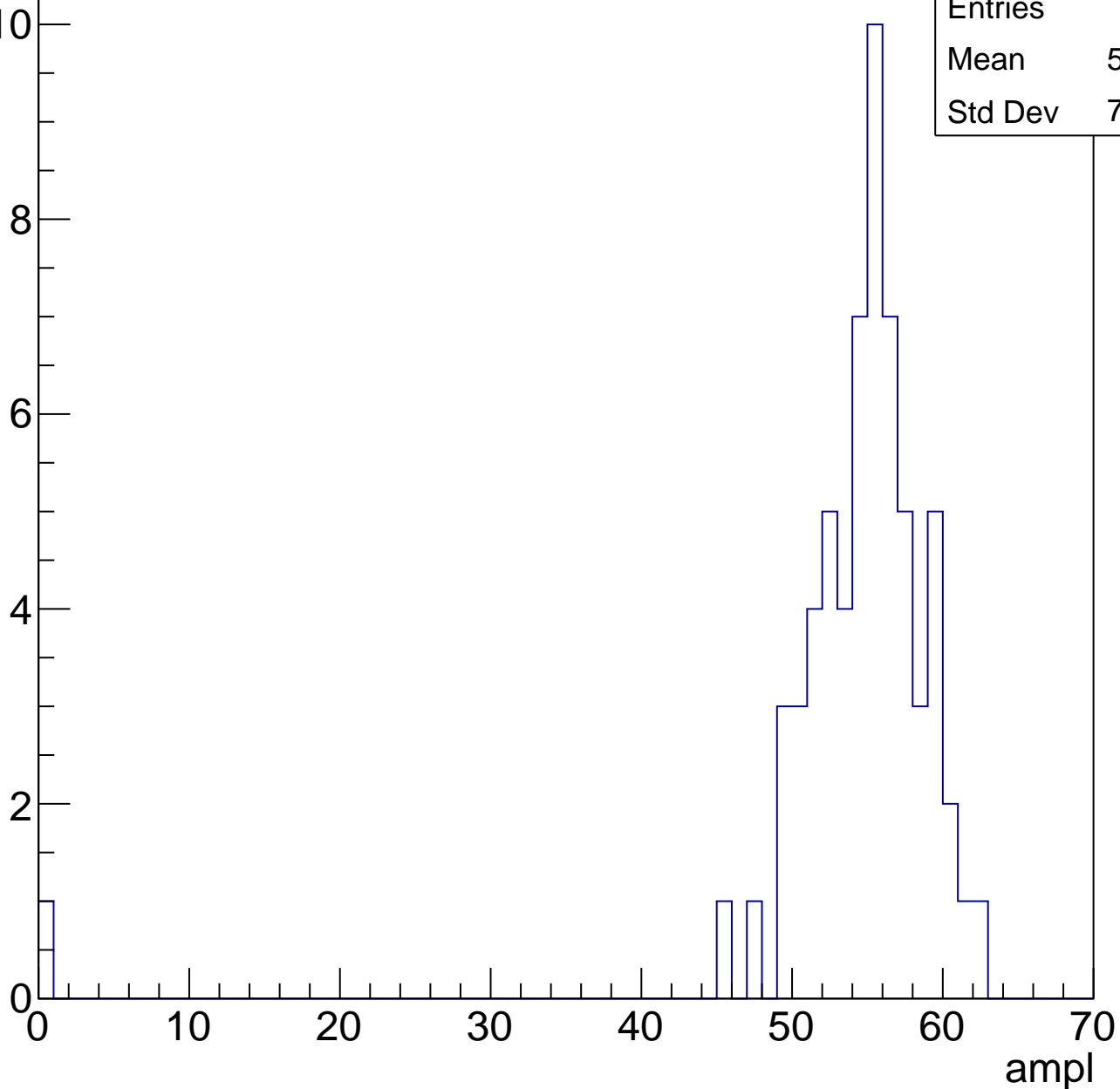


B1L101S, U4-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	53.68
Std Dev	7.626

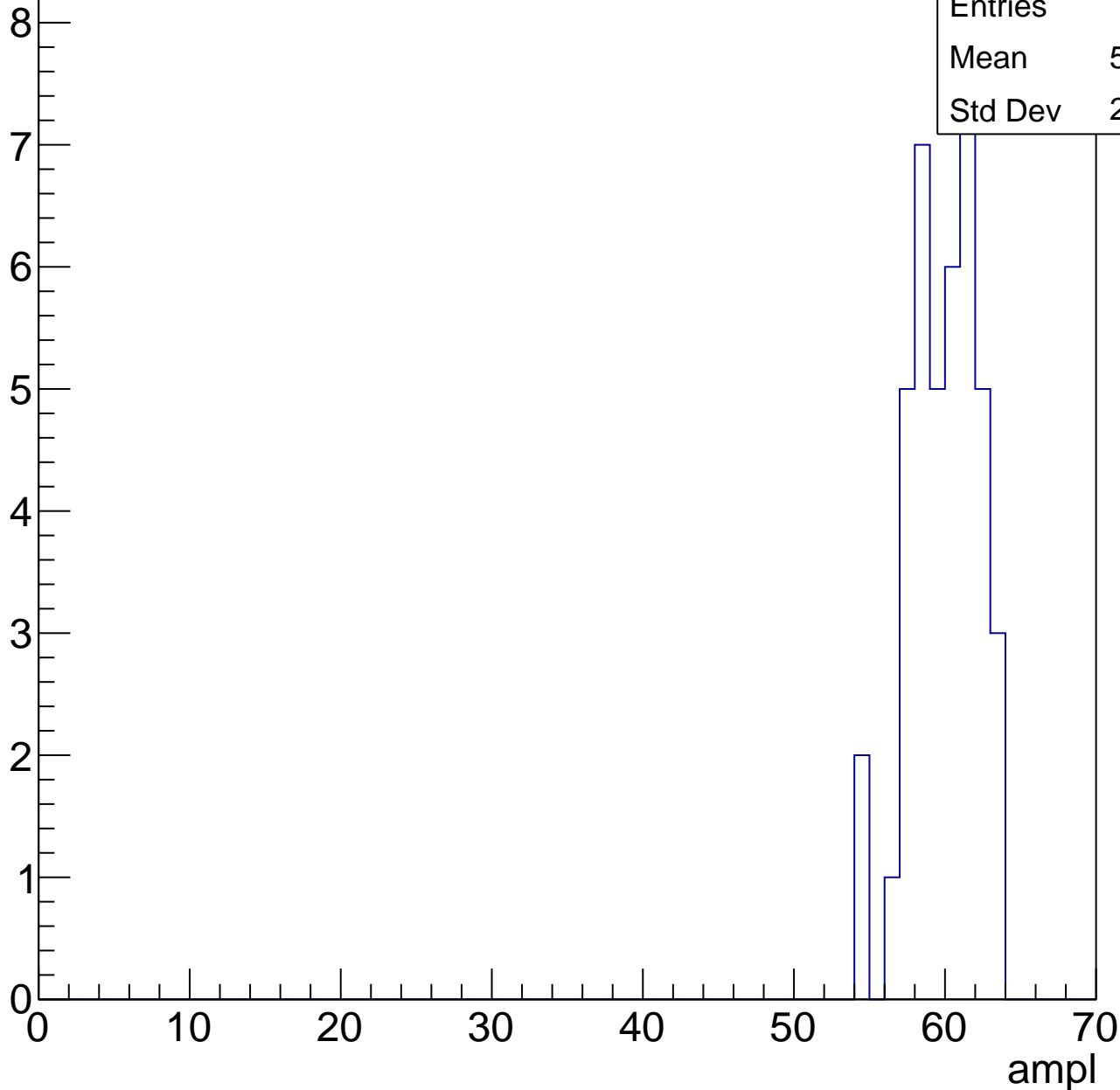


B1L101S, U4-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.45
Std Dev	2.228

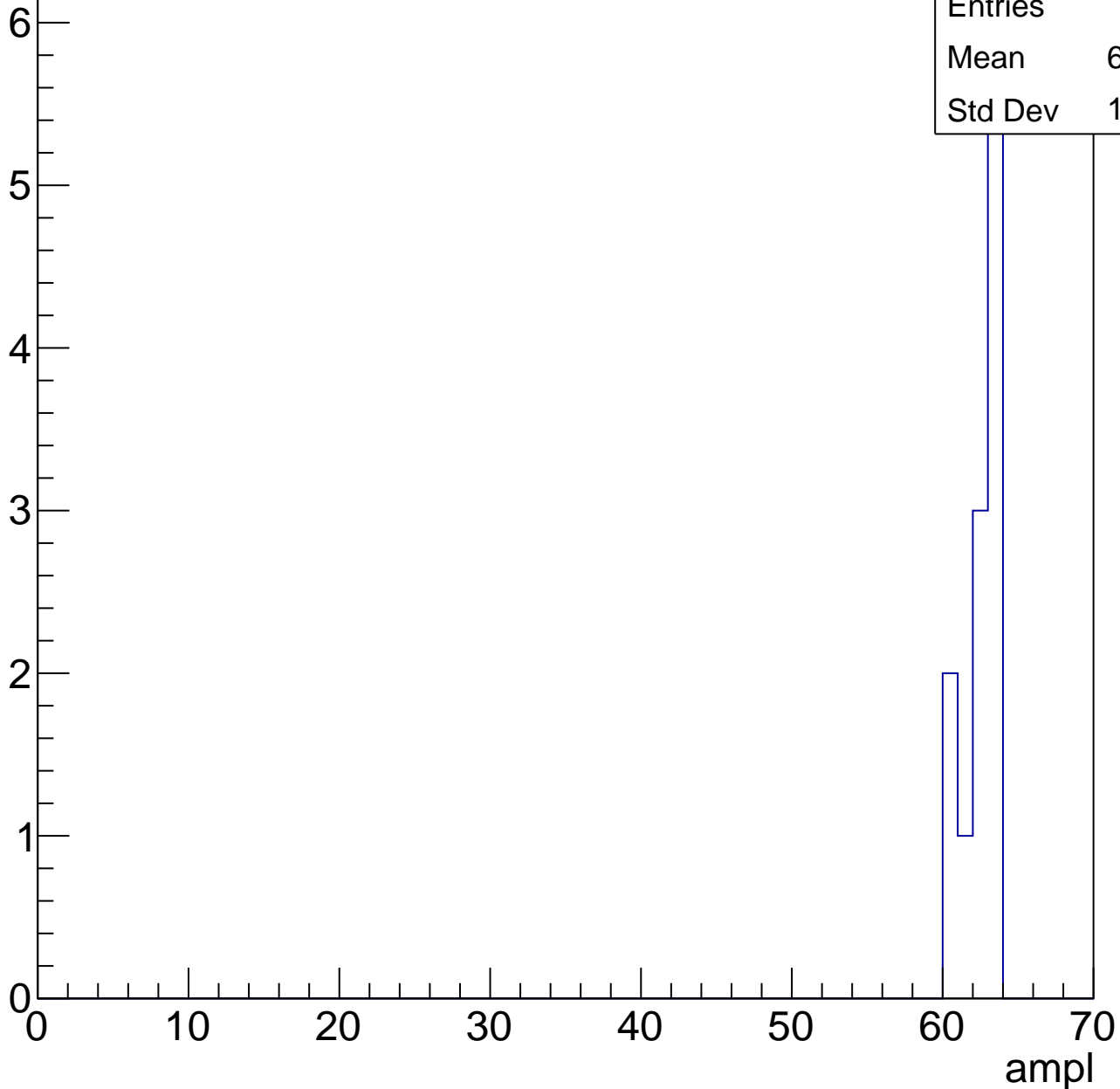


B1L101S, U4-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

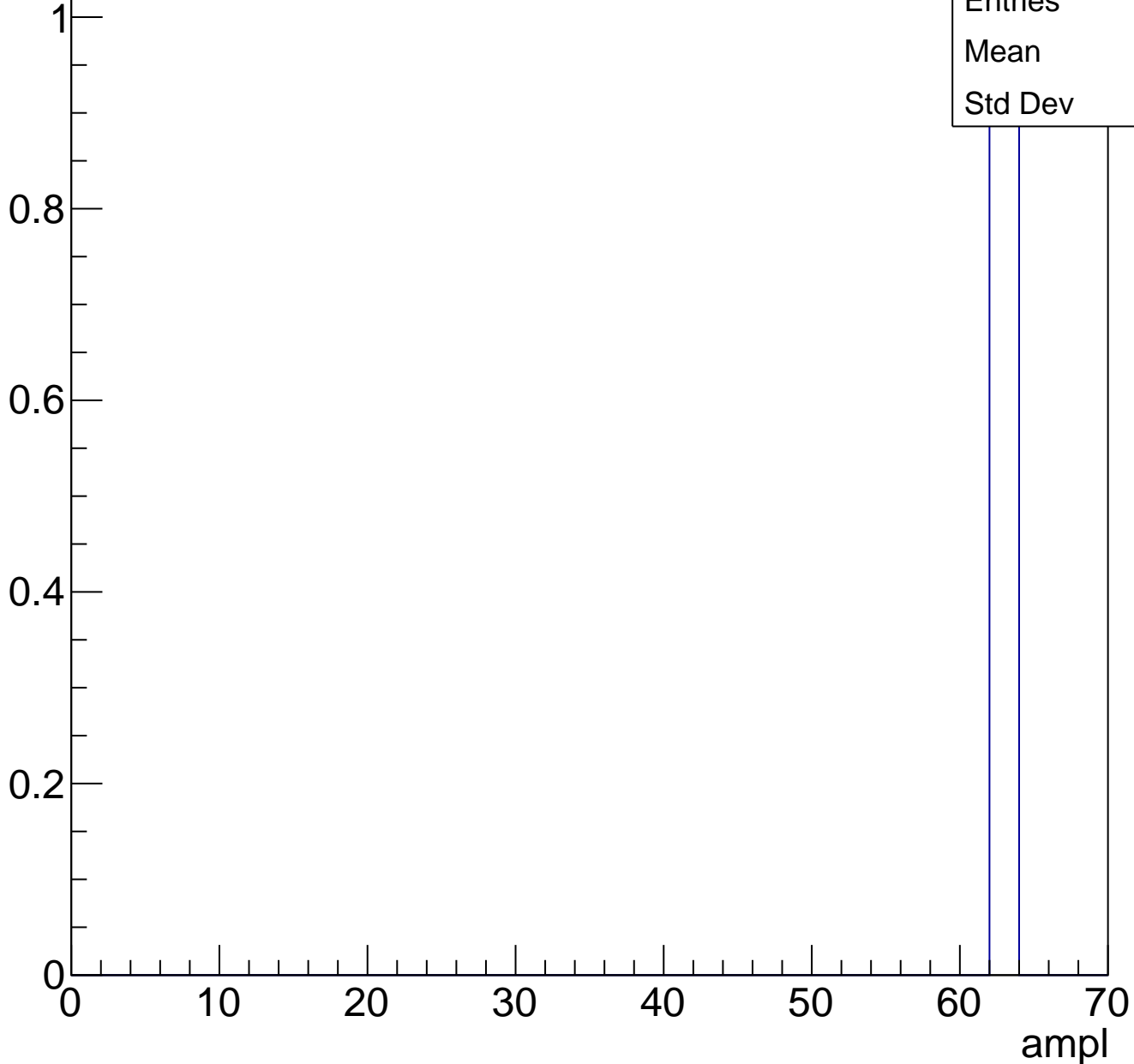
Entries	12
Mean	62.08
Std Dev	1.115



B1L101S, U4-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch45, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.28
Std Dev	4.868

Gaus mean : 29.6059

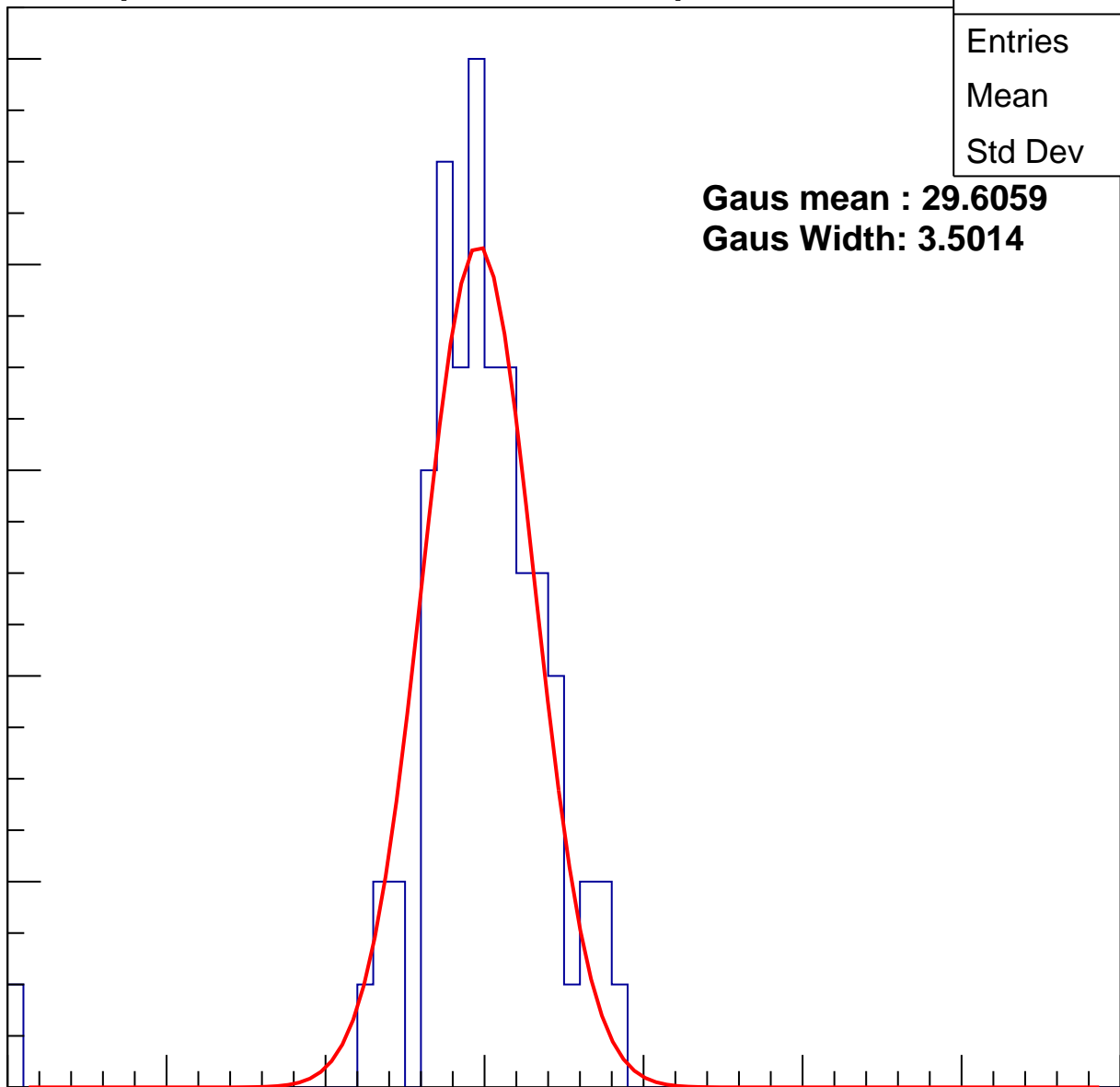
Gaus Width: 3.5014

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch45, adc1

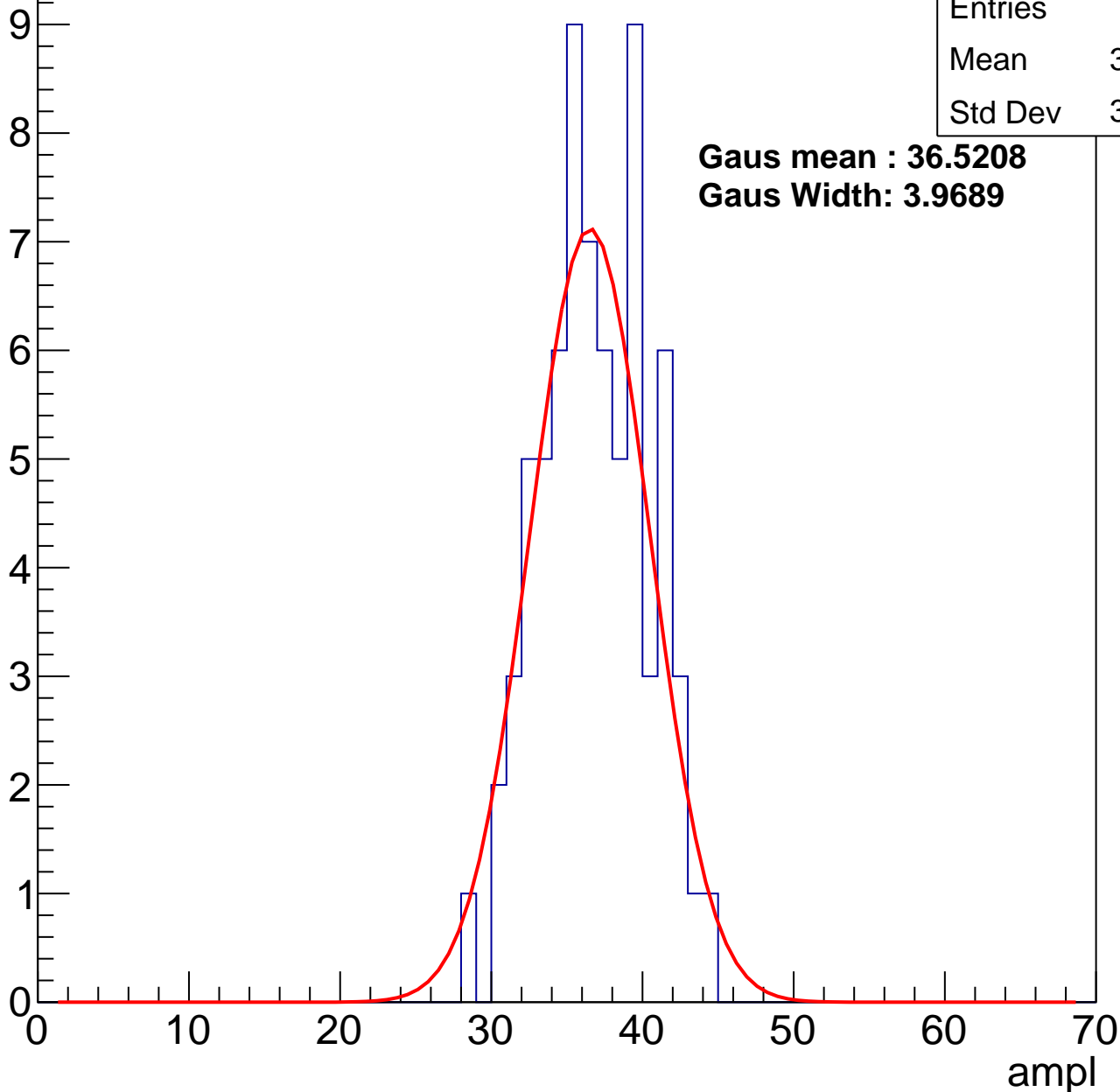
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.38
Std Dev	3.506

Gaus mean : 36.5208

Gaus Width: 3.9689



B1L101S, U4-ch45, adc2

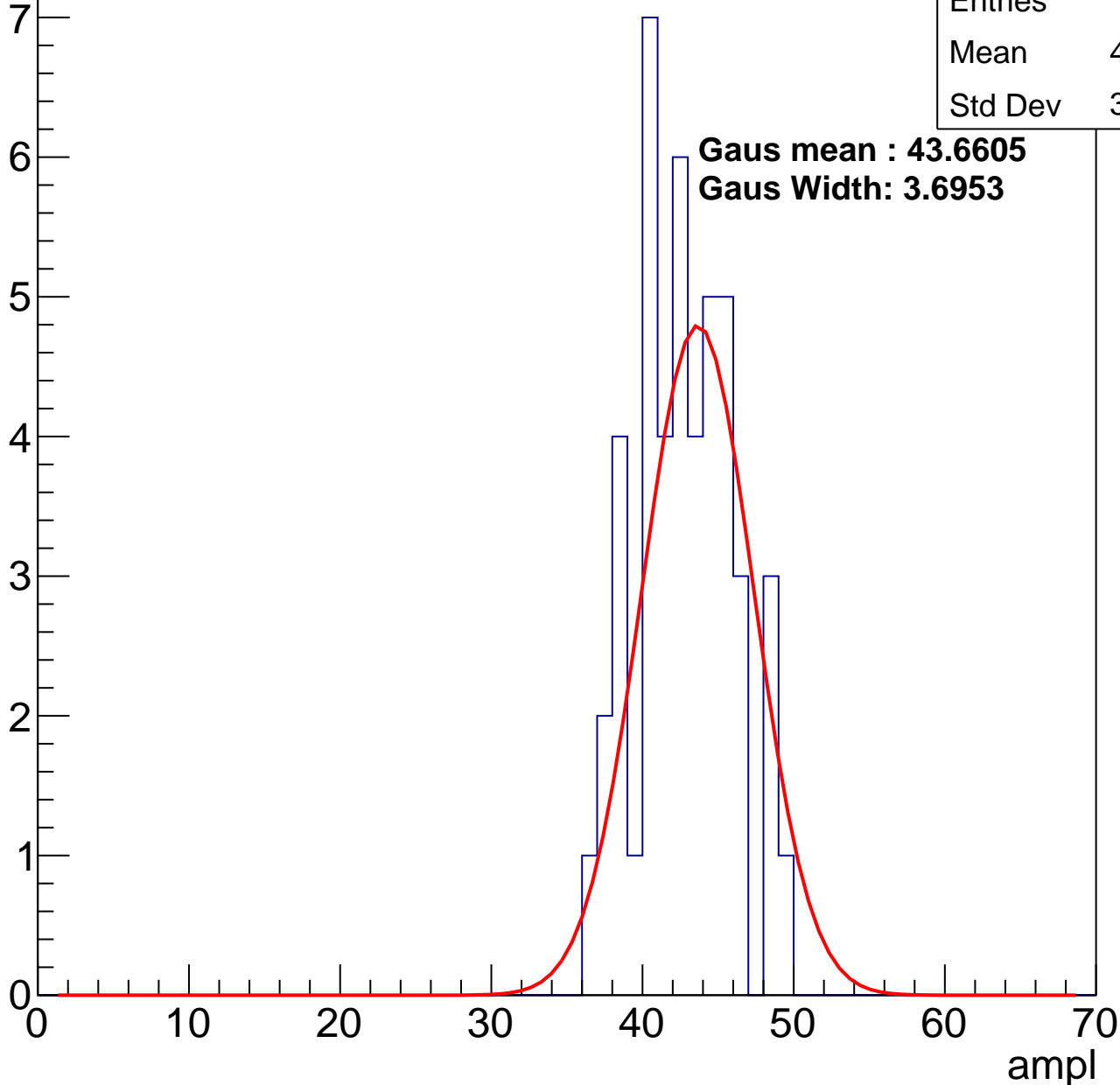
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	42.28
Std Dev	3.167

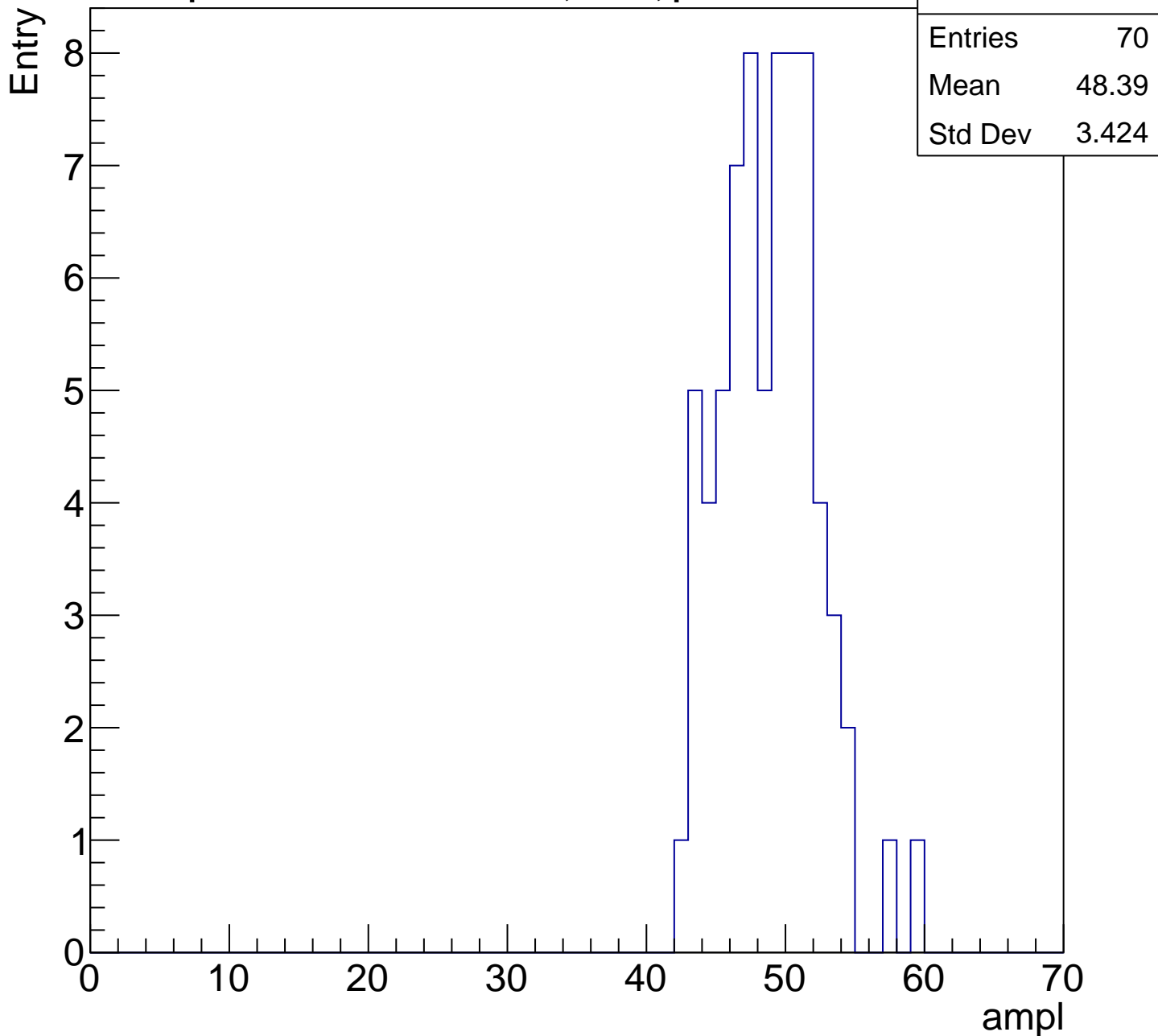
Gaus mean : 43.6605

Gaus Width: 3.6953



B1L101S, U4-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

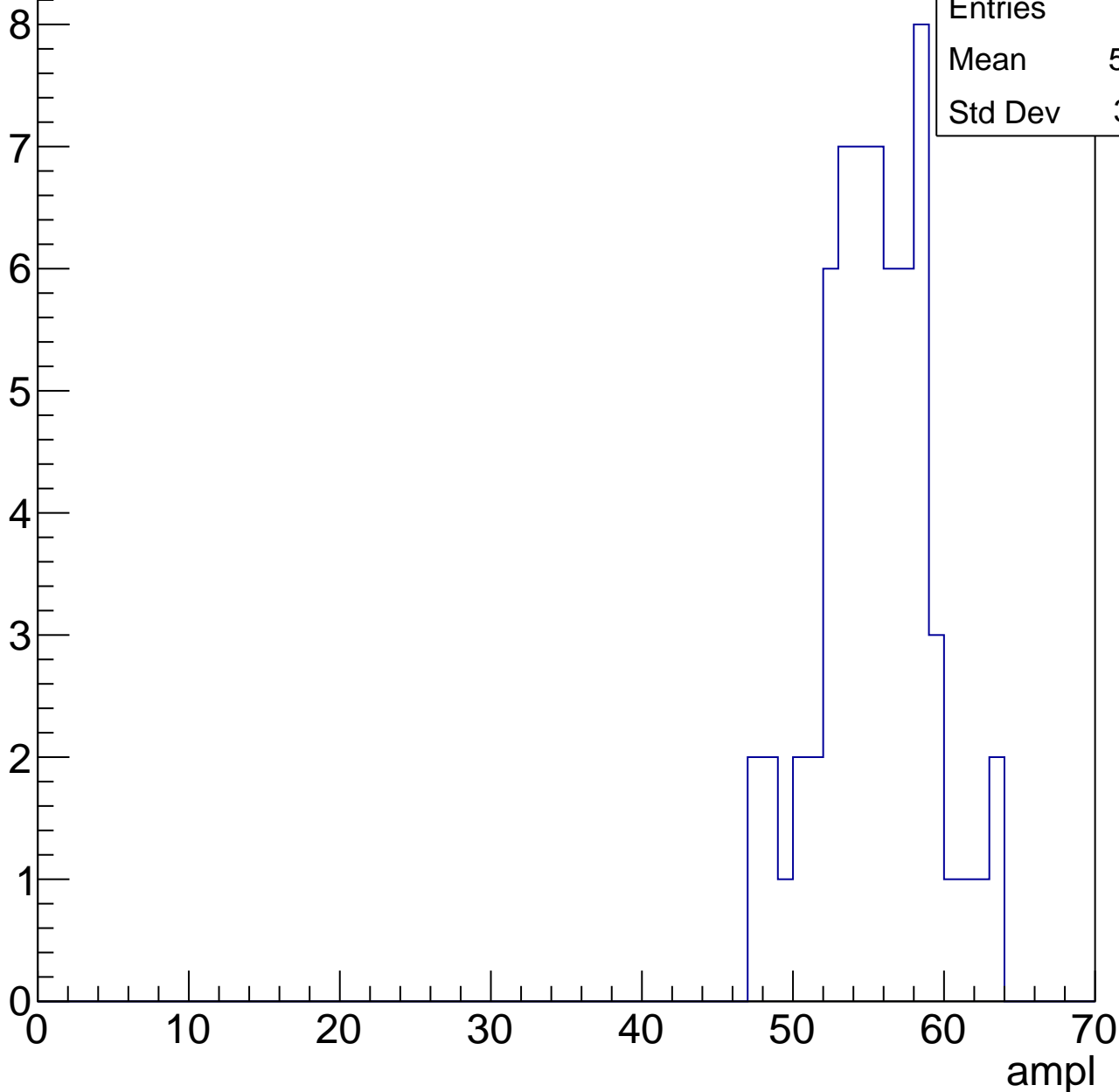


B1L101S, U4-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	54.92
Std Dev	3.581

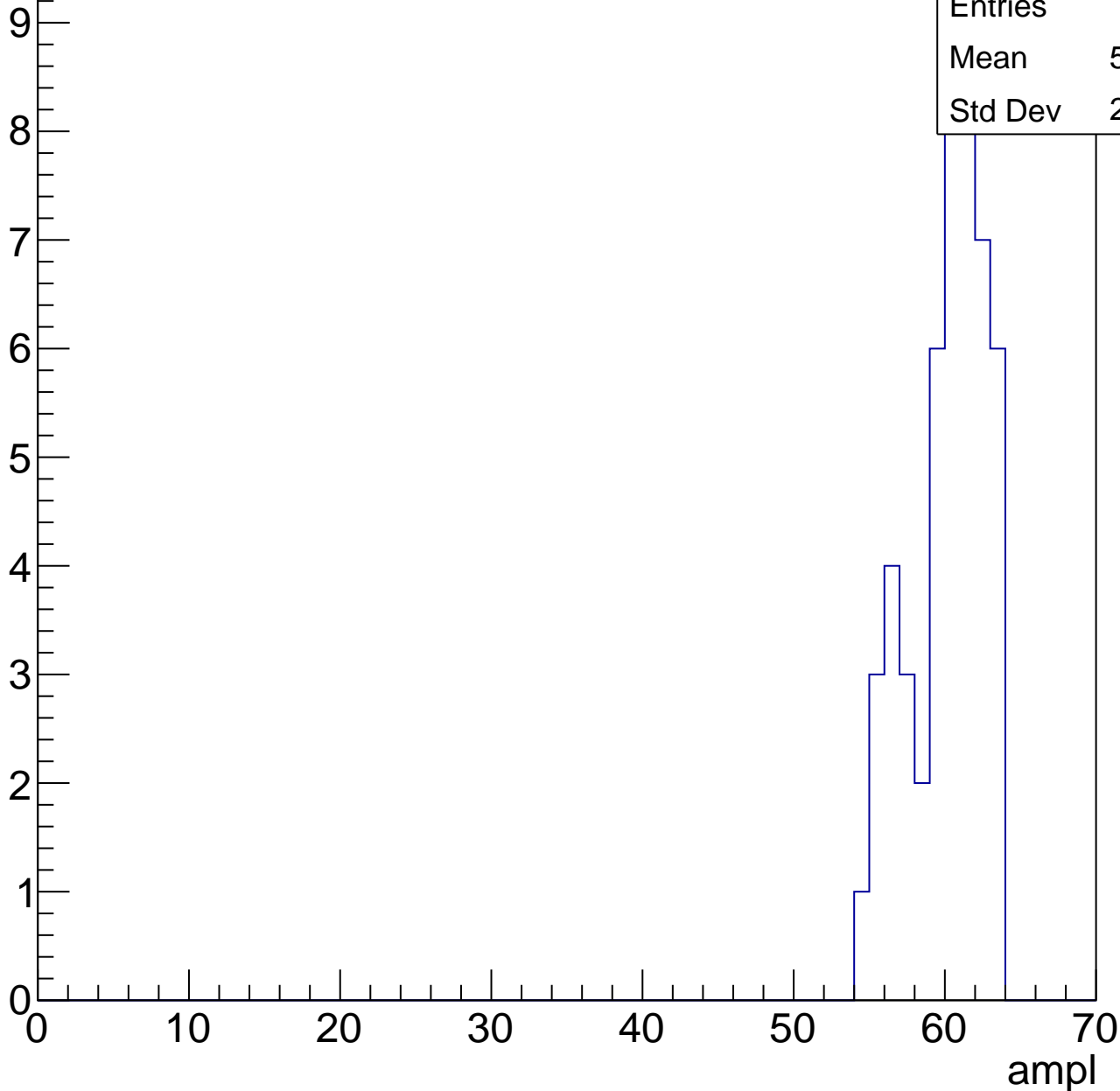


B1L101S, U4-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

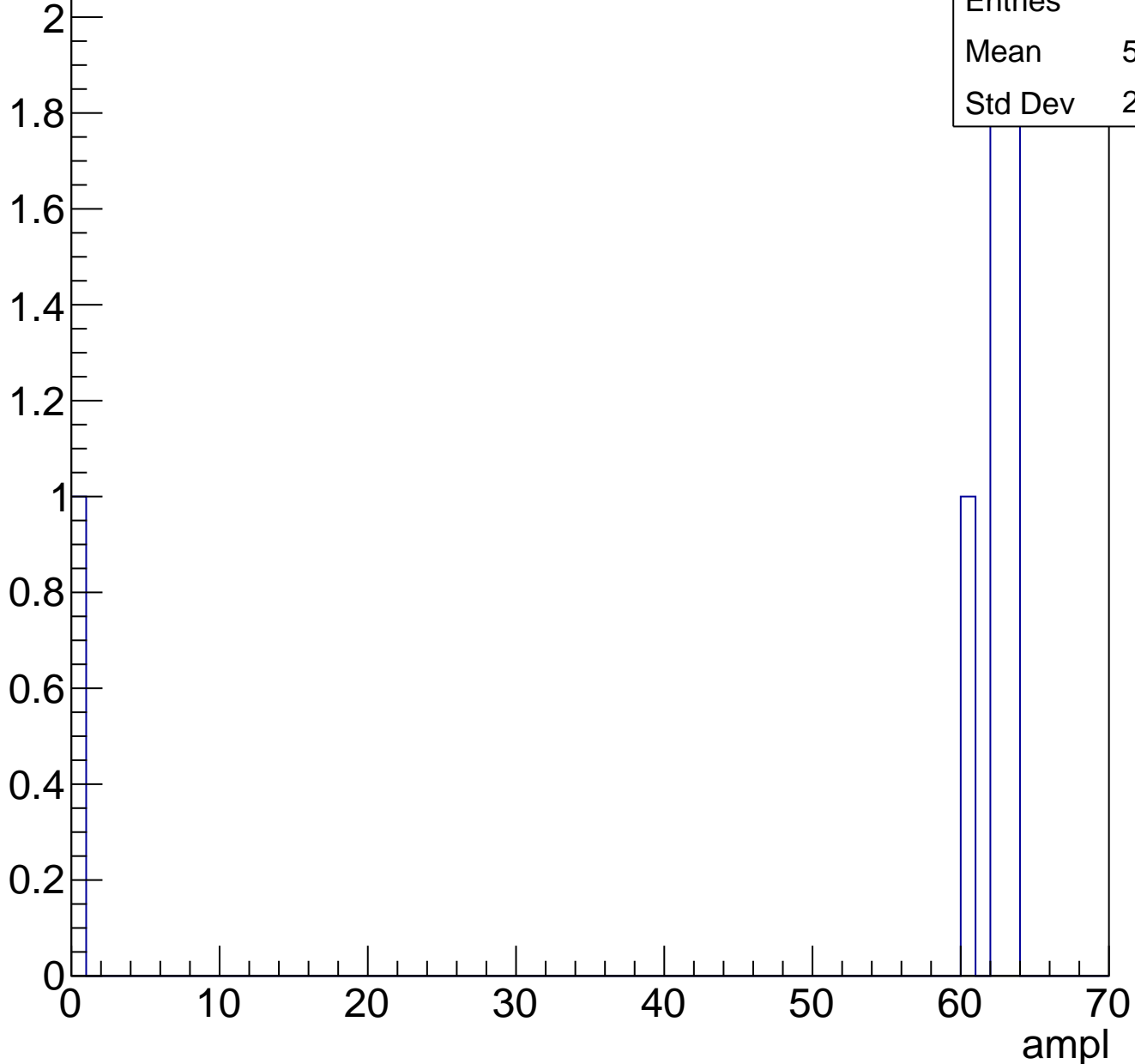
Entries	49
Mean	59.69
Std Dev	2.484



B1L101S, U4-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch46, adc0

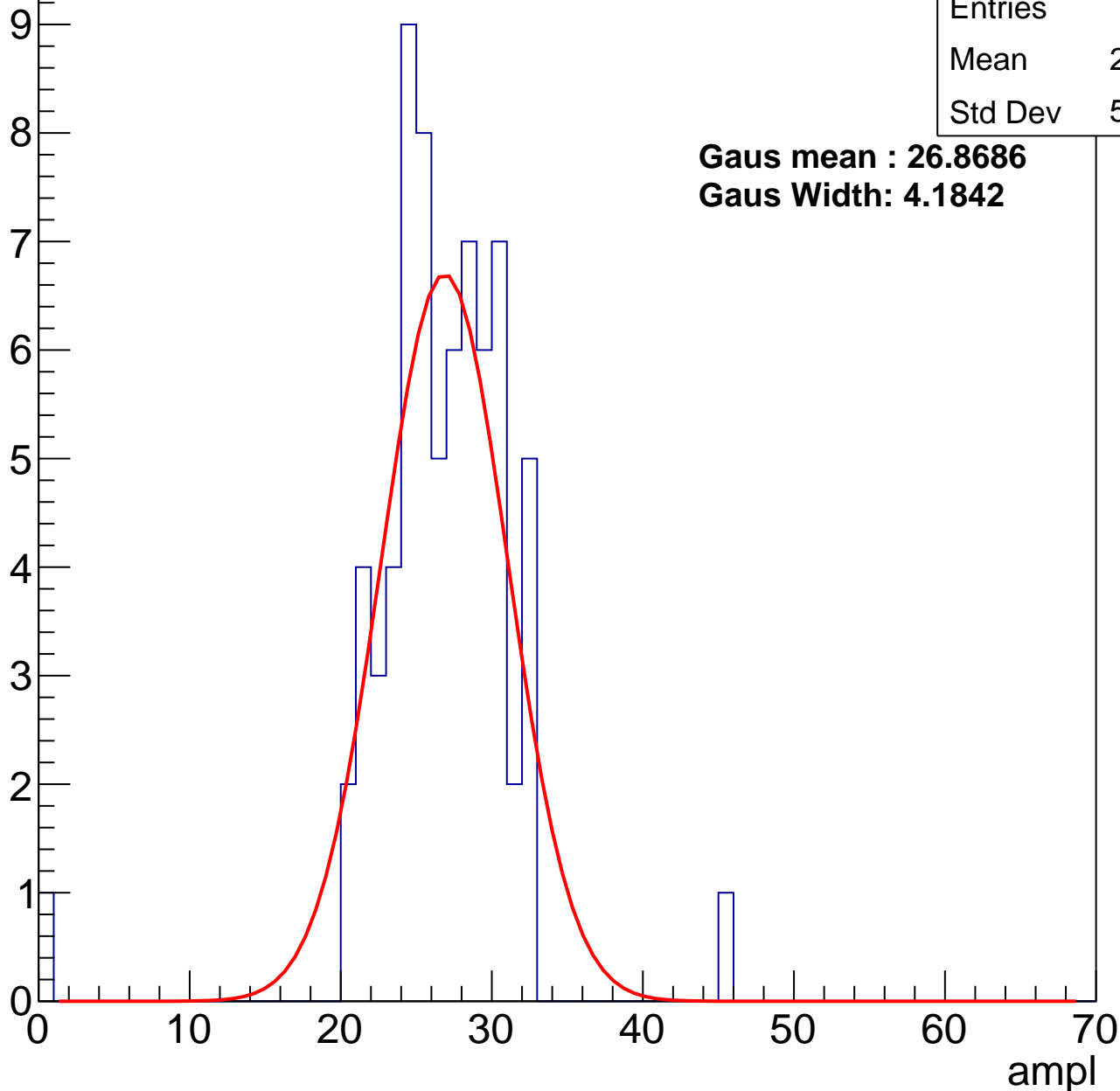
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	26.24
Std Dev	5.018

Gaus mean : 26.8686

Gaus Width: 4.1842



B1L101S, U4-ch46, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	33.93
Std Dev	3.772

Gaus mean : 33.9428

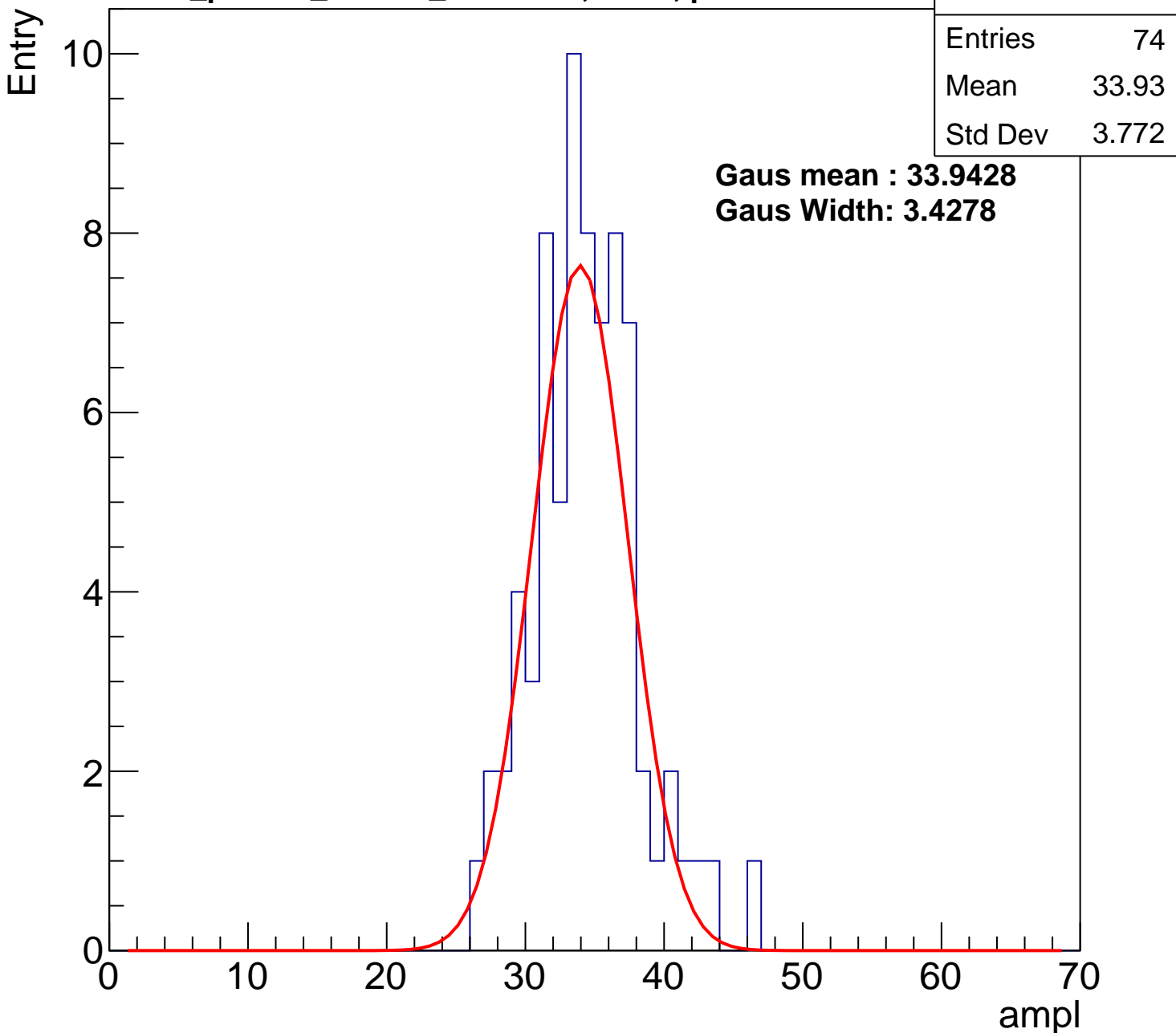
Gaus Width: 3.4278

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch46, adc2

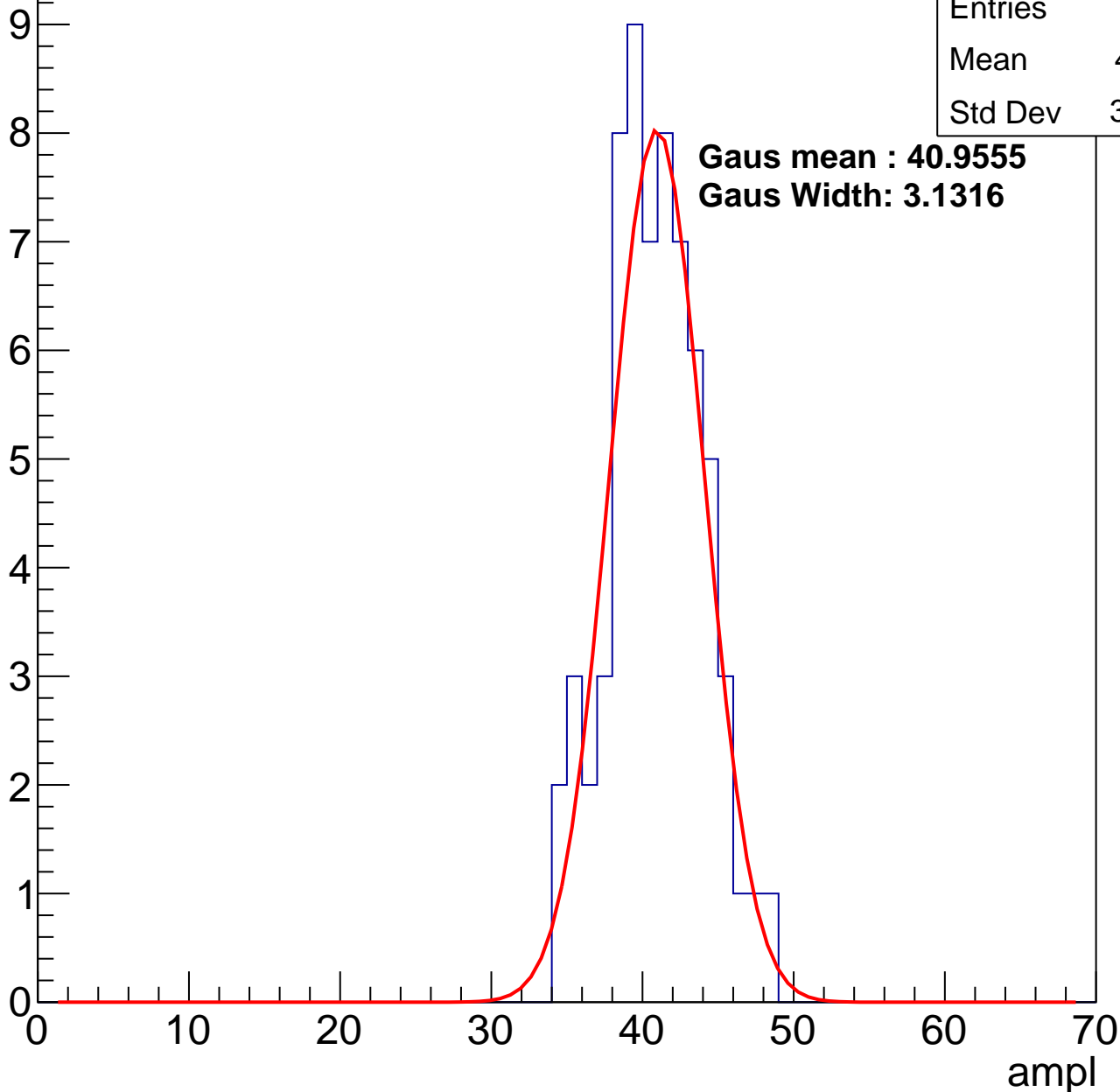
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	40.41
Std Dev	3.089

Gaus mean : 40.9555

Gaus Width: 3.1316

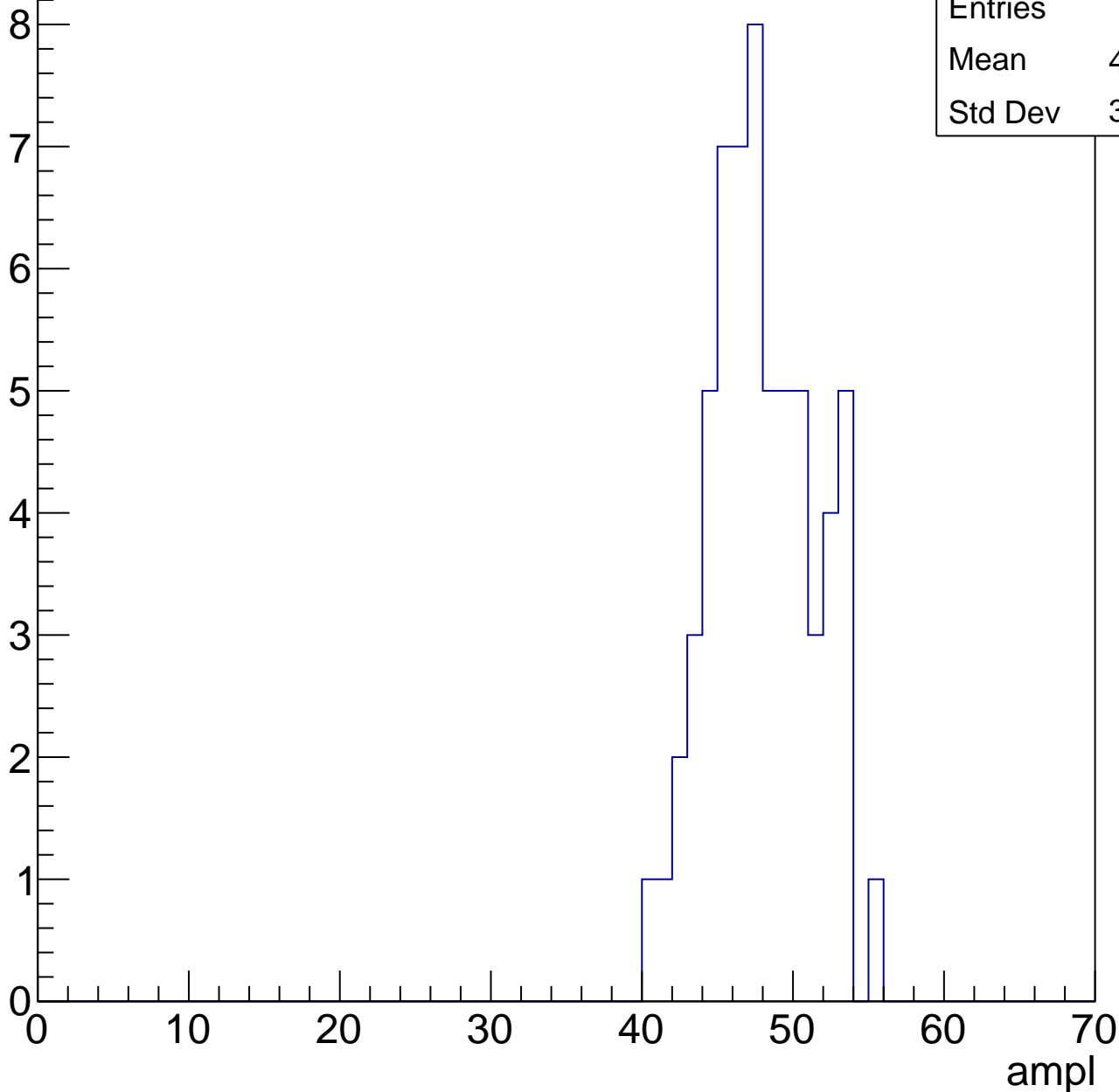


B1L101S, U4-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

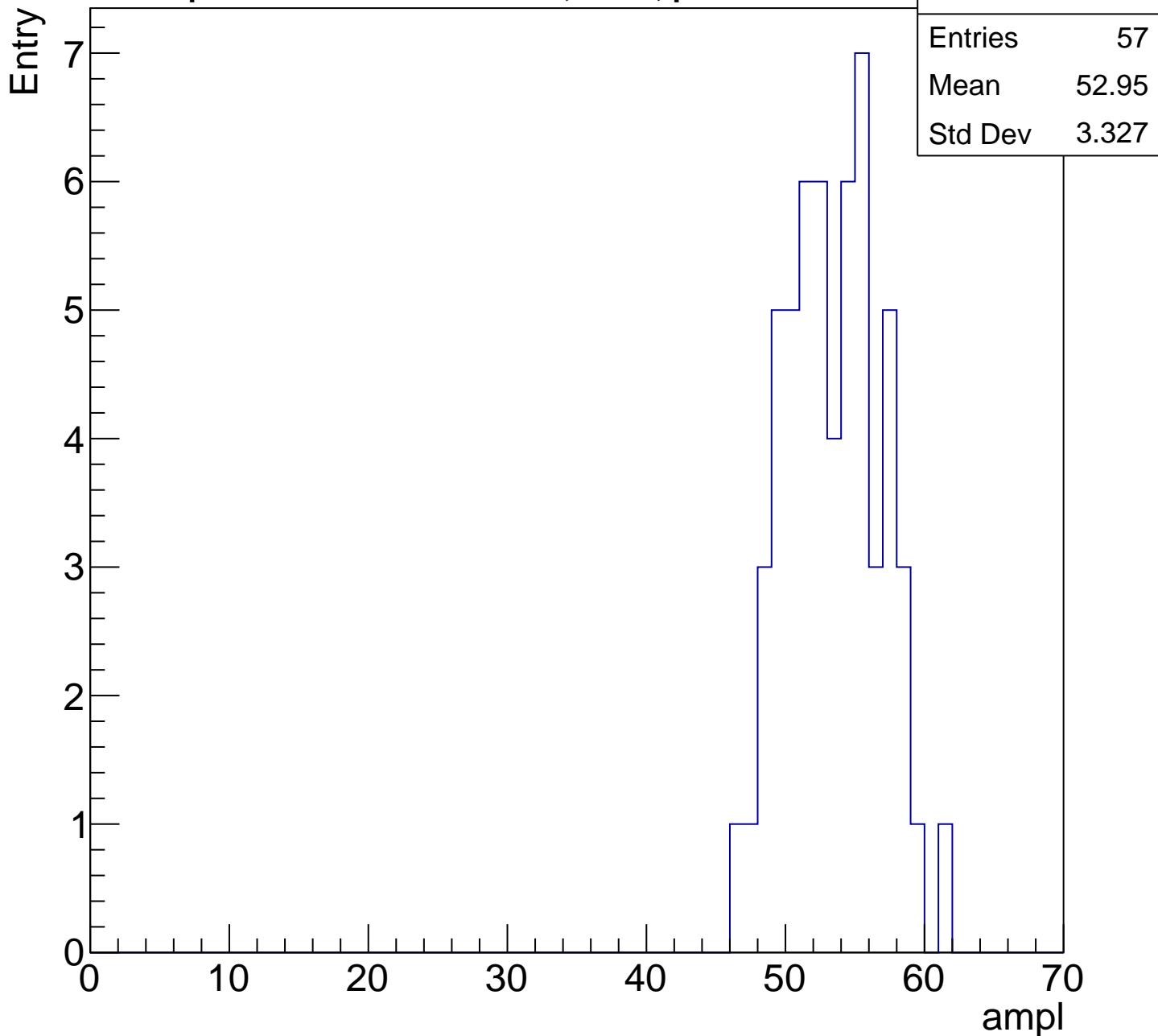
Entry

Entries	62
Mean	47.47
Std Dev	3.387



B1L101S, U4-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch46, adc5

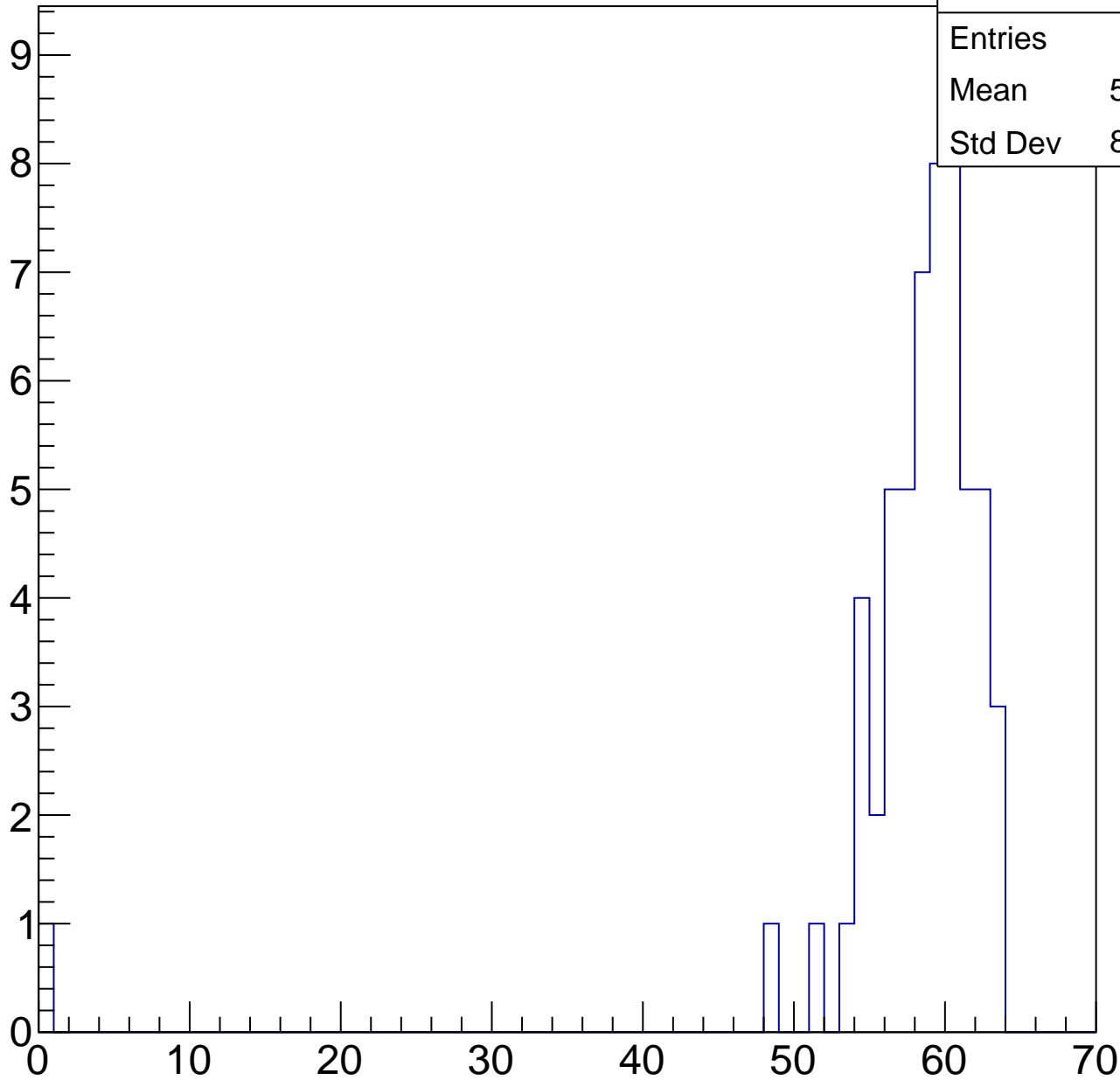
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	57.28
Std Dev	8.229

ampl

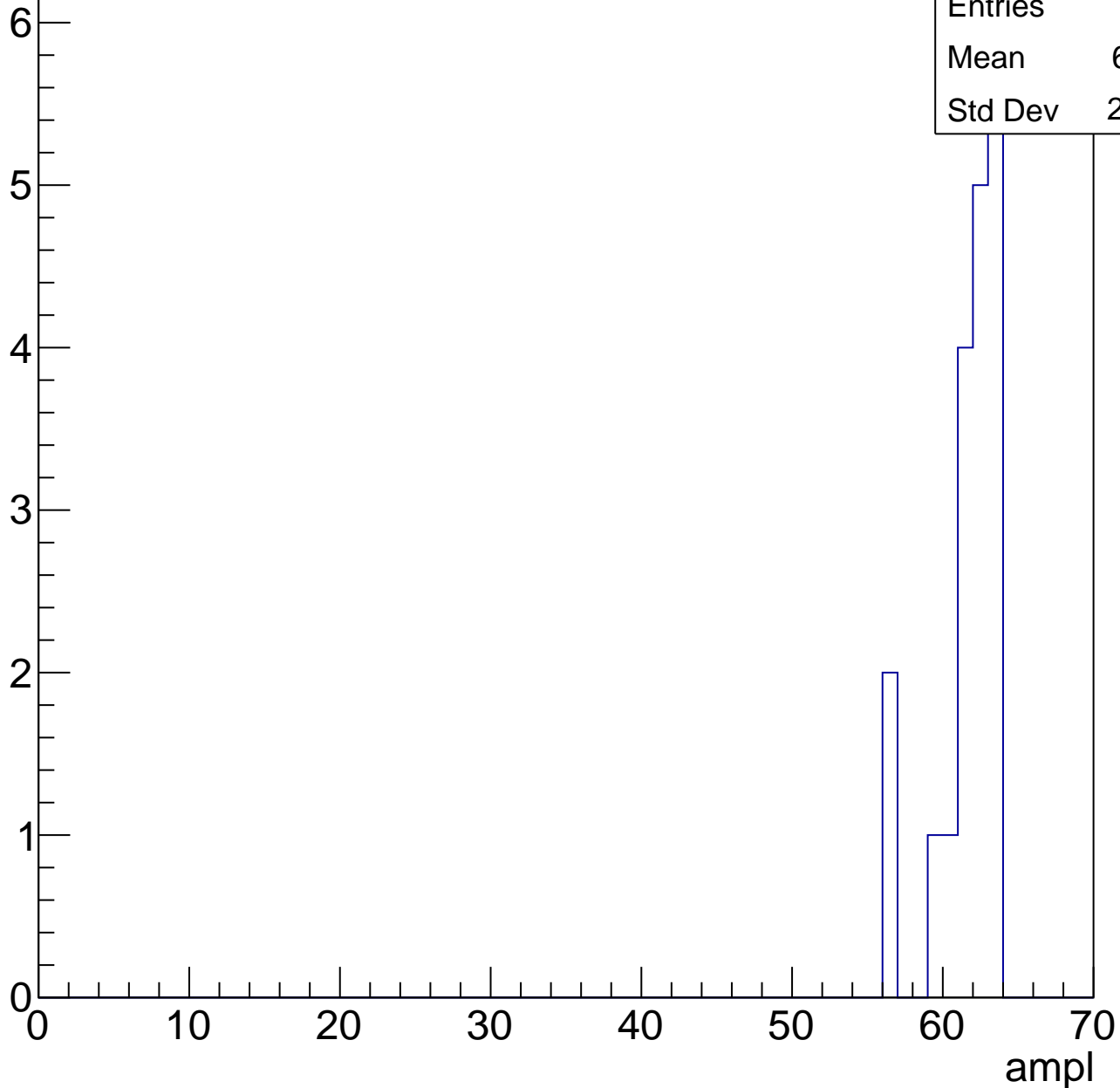


B1L101S, U4-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.21
Std Dev	2.092



B1L101S, U4-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U4-ch47, adc0

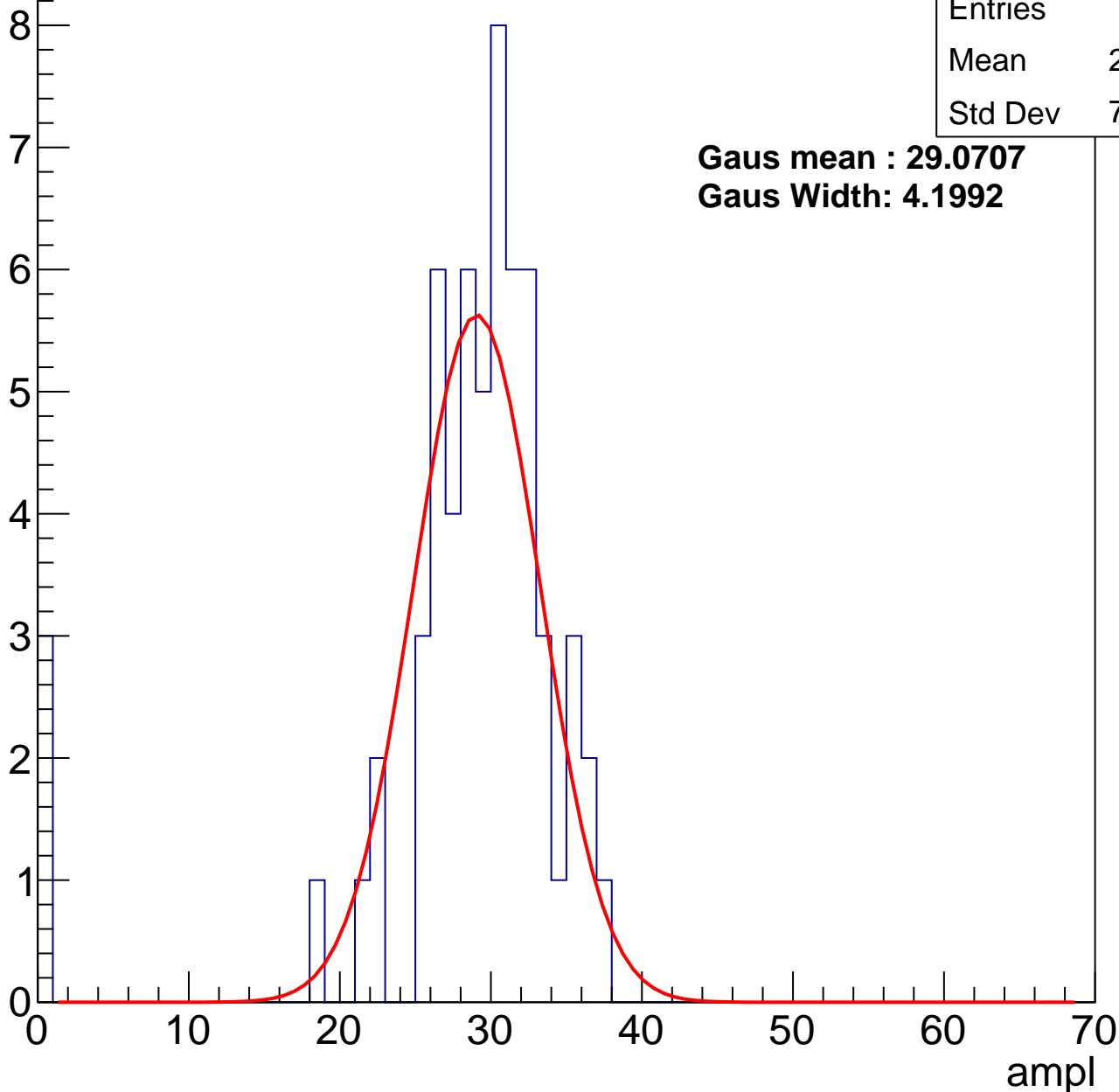
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	27.87
Std Dev	7.334

Gaus mean : 29.0707

Gaus Width: 4.1992



B1L101S, U4-ch47, adc1

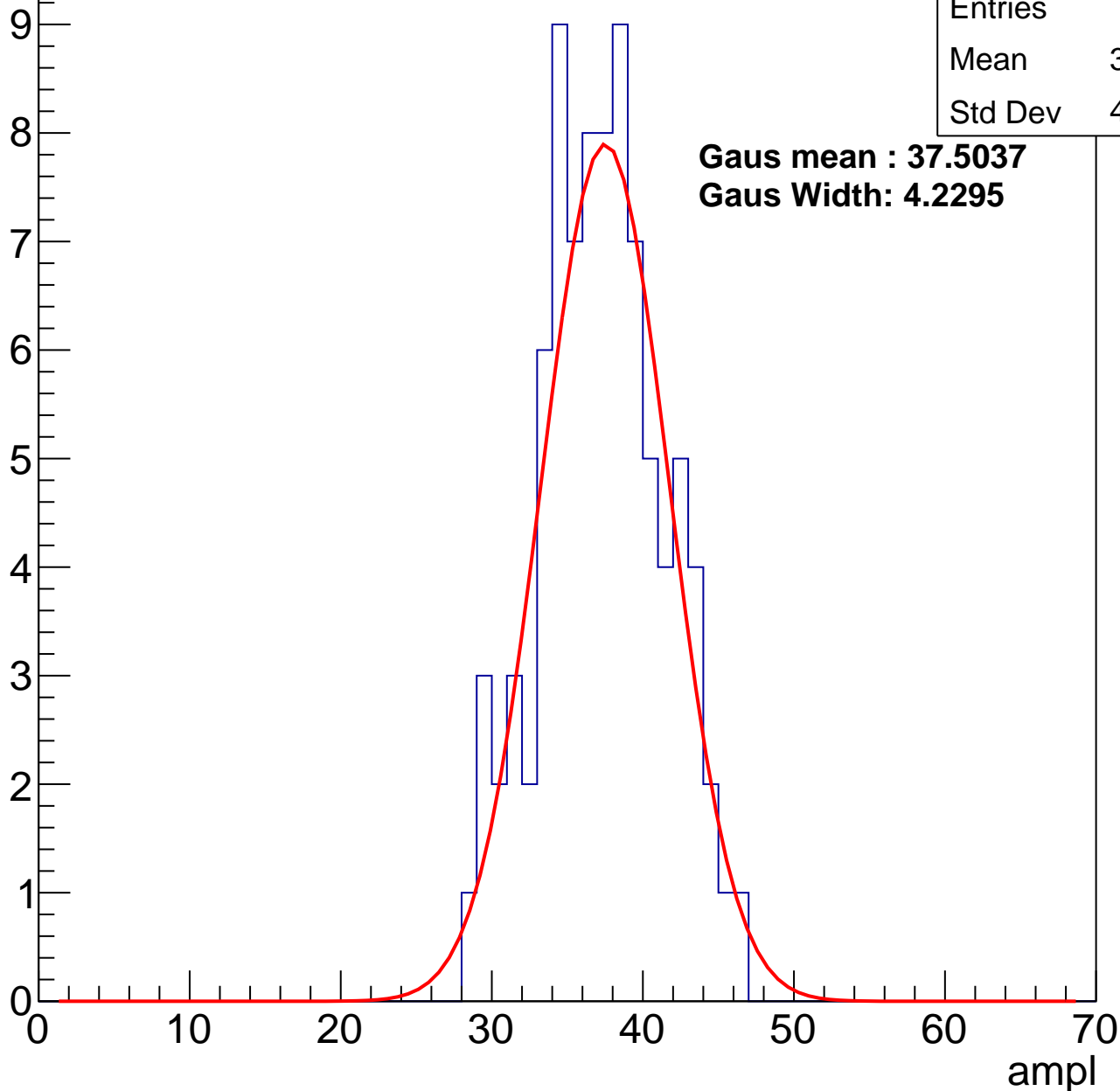
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	36.84
Std Dev	4.008

Gaus mean : 37.5037

Gaus Width: 4.2295



B1L101S, U4-ch47, adc2

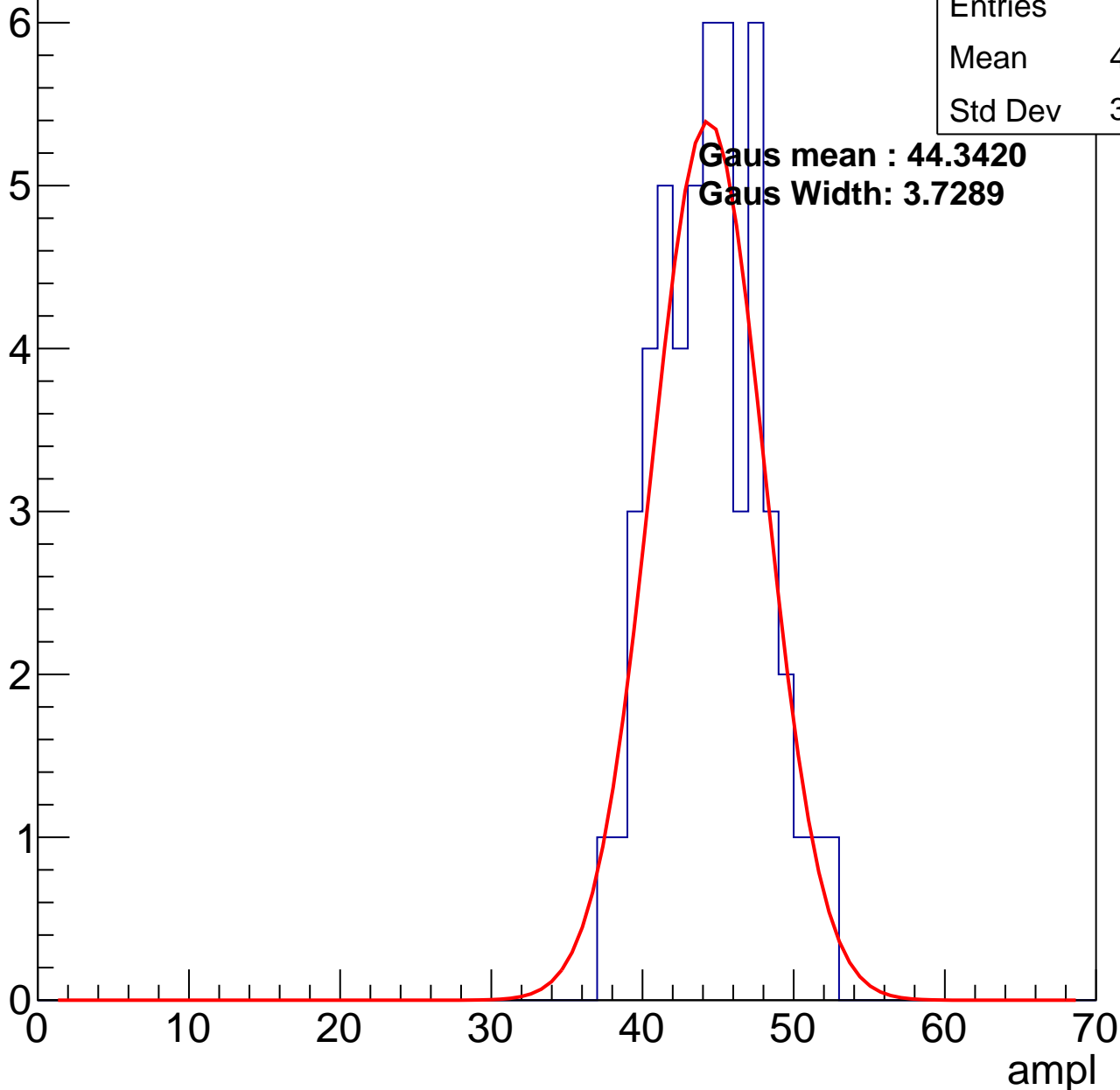
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.02
Std Dev	3.422

Gaus mean : 44.3420

Gaus Width: 3.7289

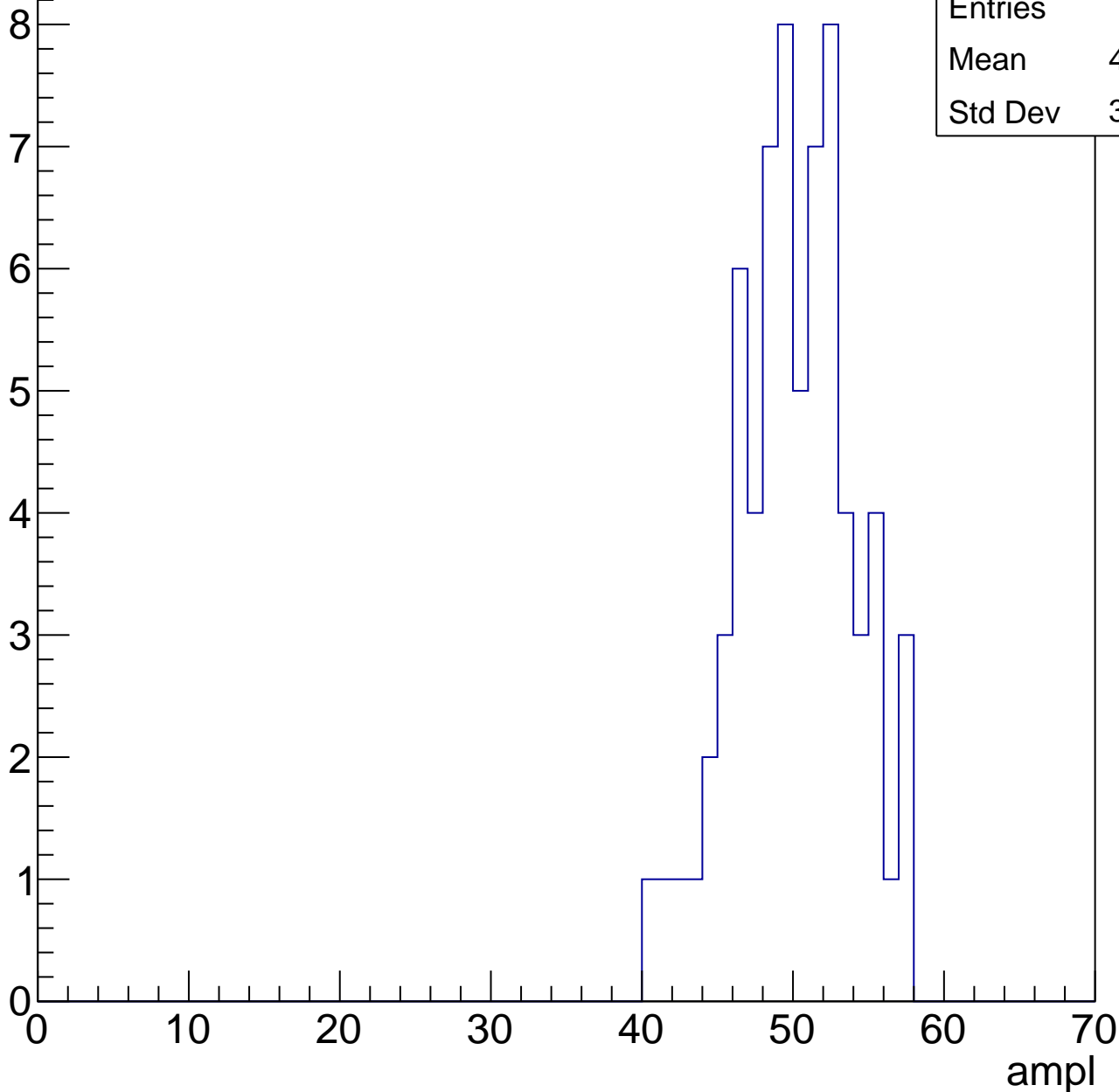


B1L101S, U4-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.64
Std Dev	3.818

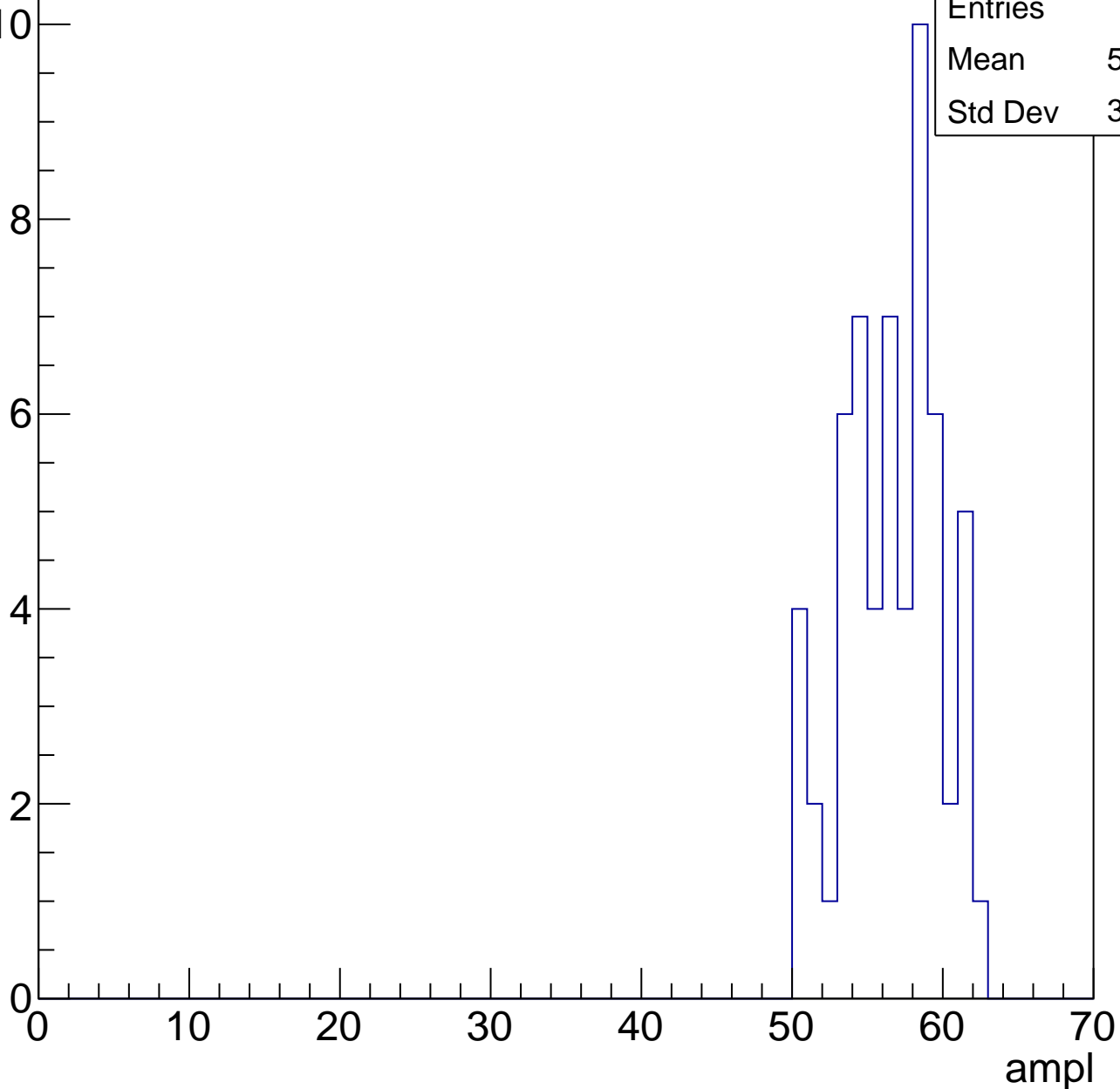


B1L101S, U4-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.12
Std Dev	3.152



B1L101S, U4-ch47, adc5

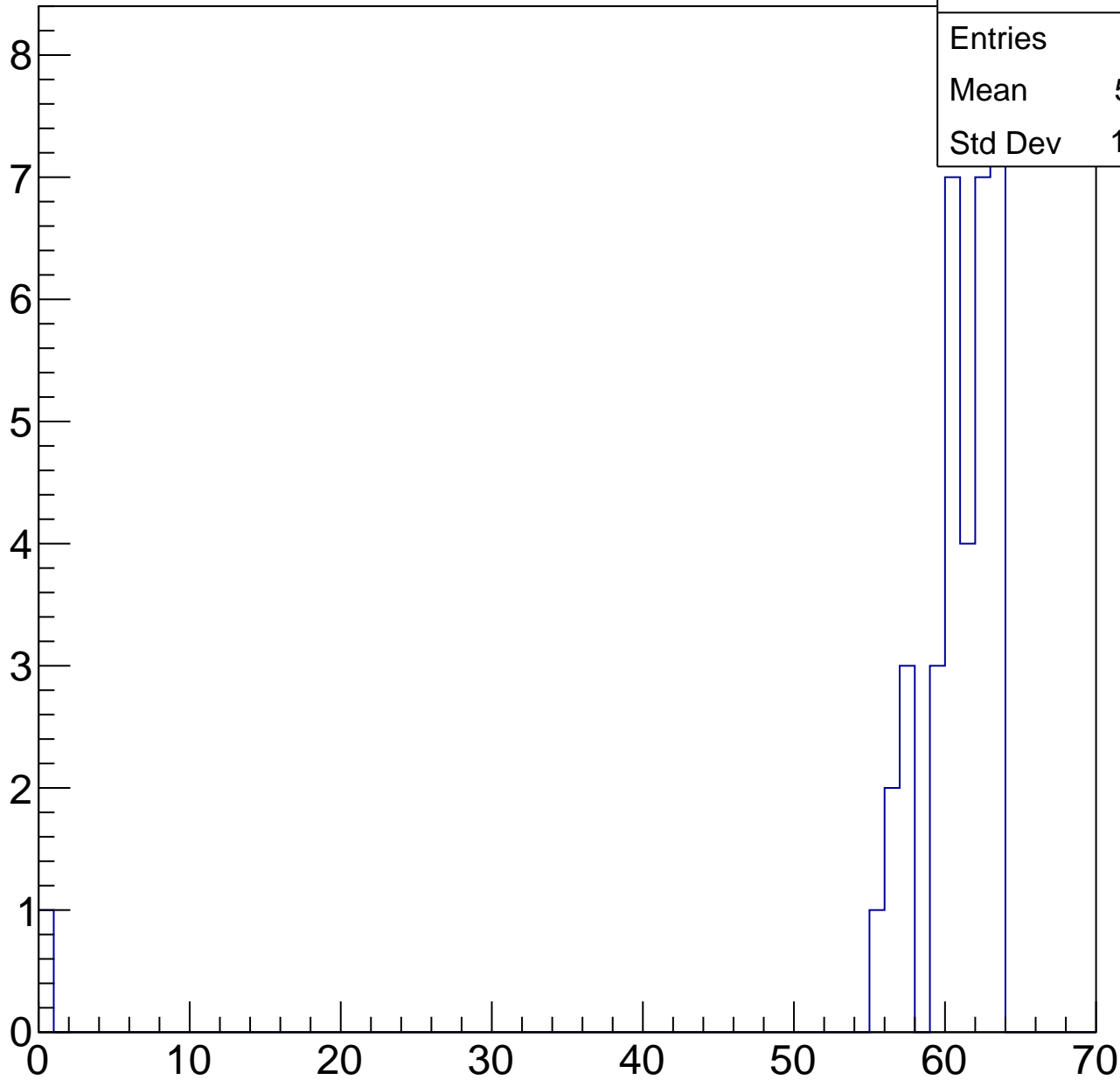
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	58.81
Std Dev	10.19

ampl



B1L101S, U4-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

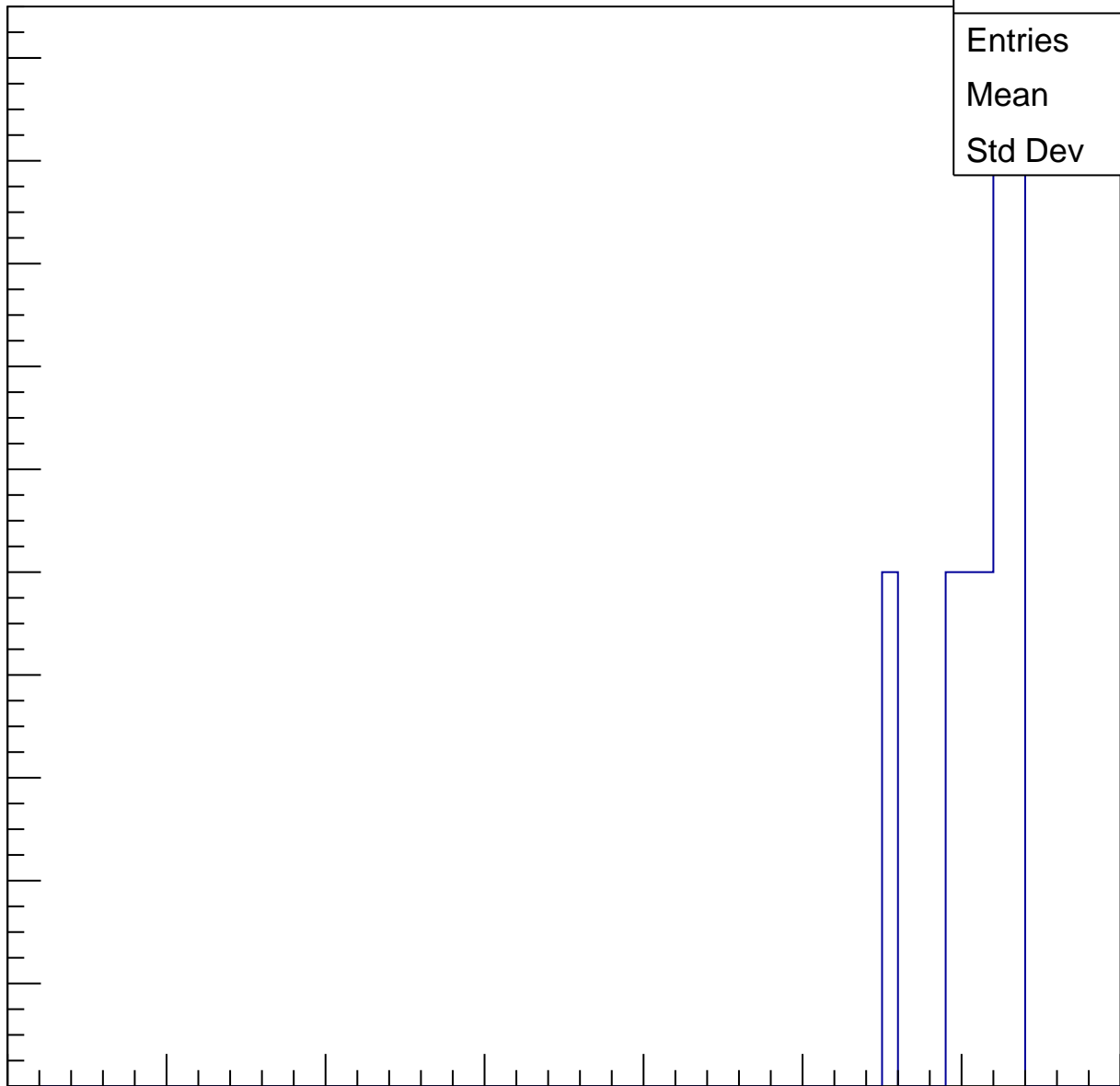
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.62
Std Dev	2.497

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch48, adc0

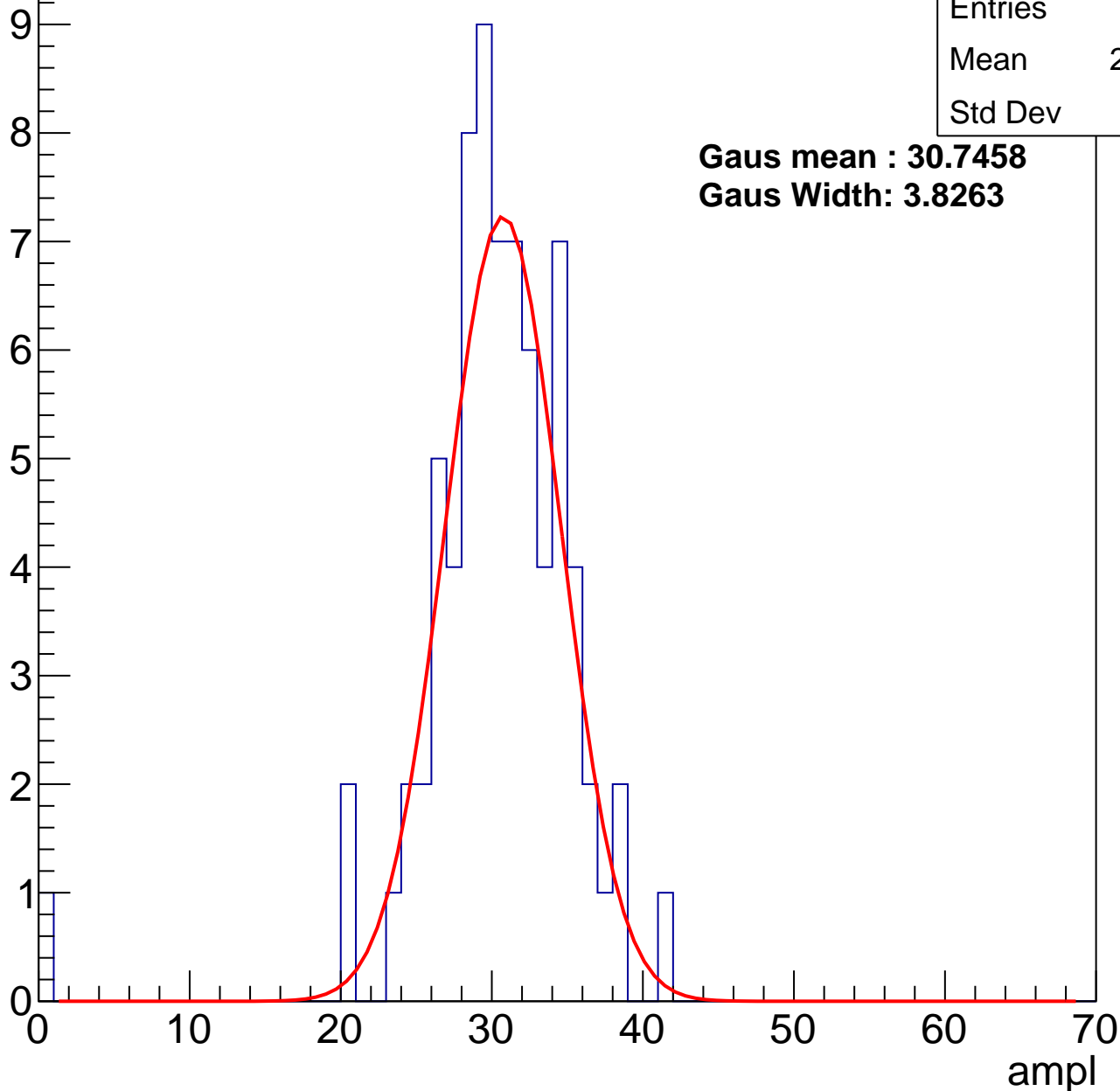
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.85
Std Dev	5.26

Gaus mean : 30.7458

Gaus Width: 3.8263



B1L101S, U4-ch48, adc1

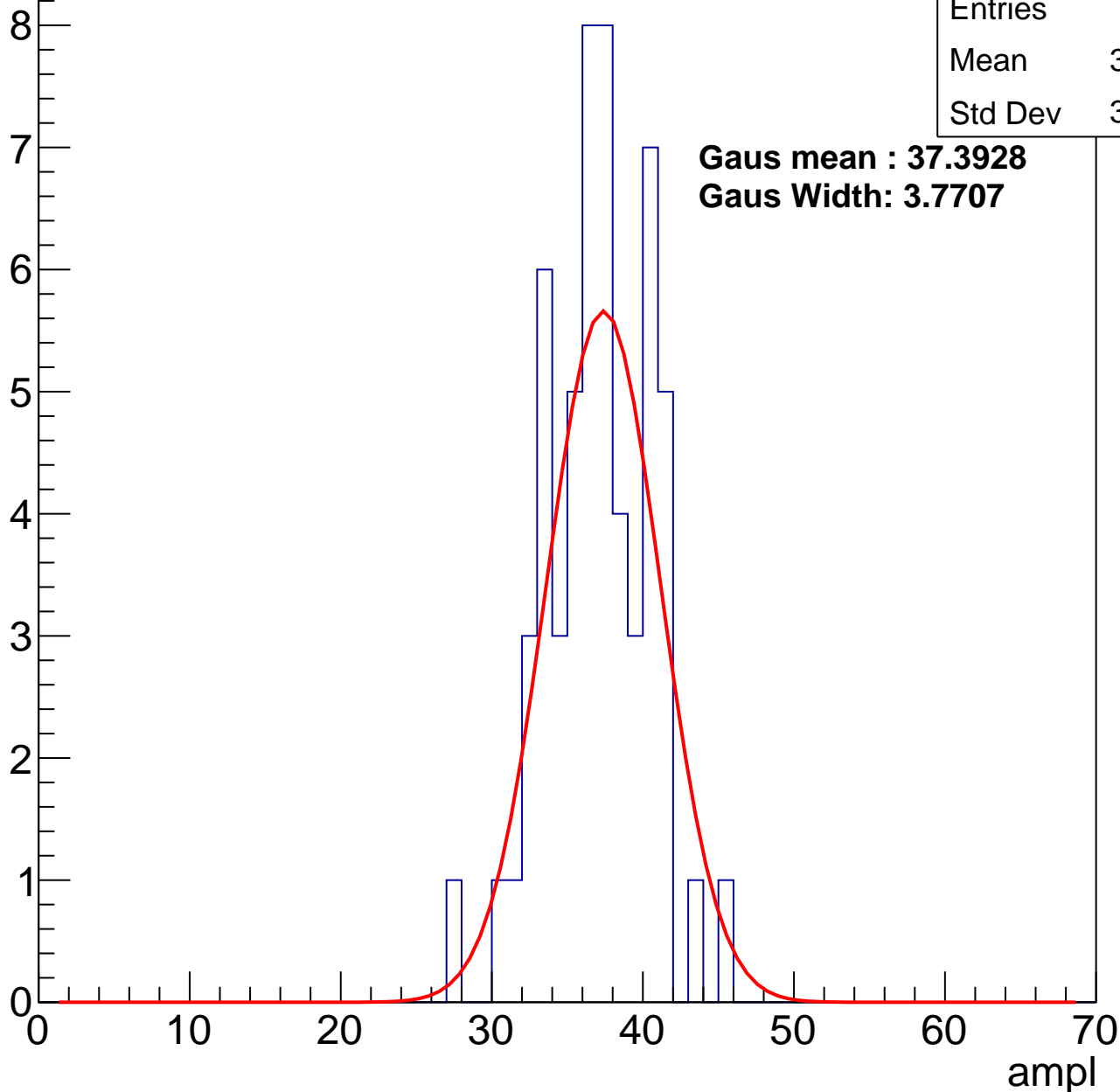
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.58
Std Dev	3.403

Gaus mean : 37.3928

Gaus Width: 3.7707



B1L101S, U4-ch48, adc2

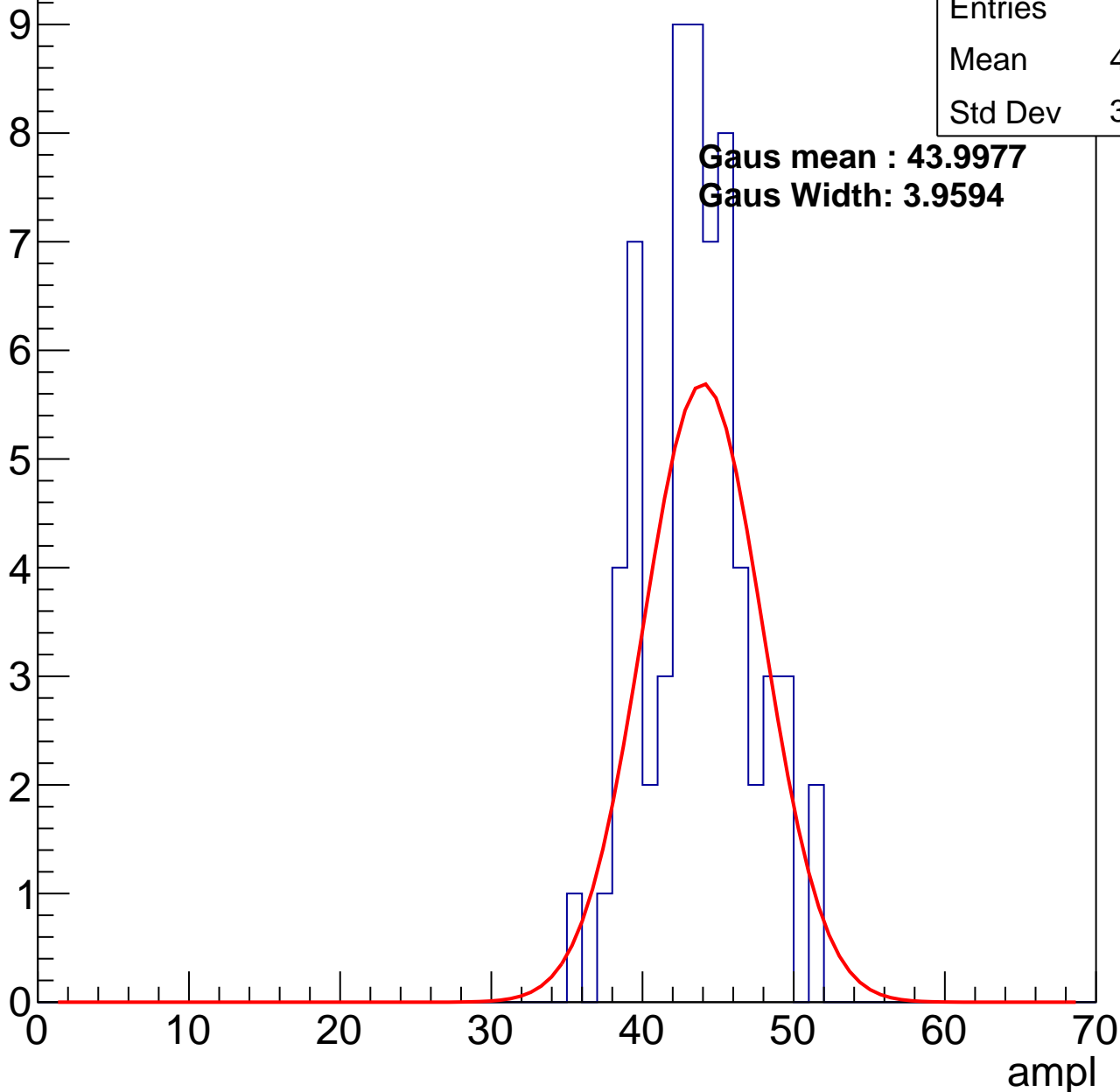
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.14
Std Dev	3.437

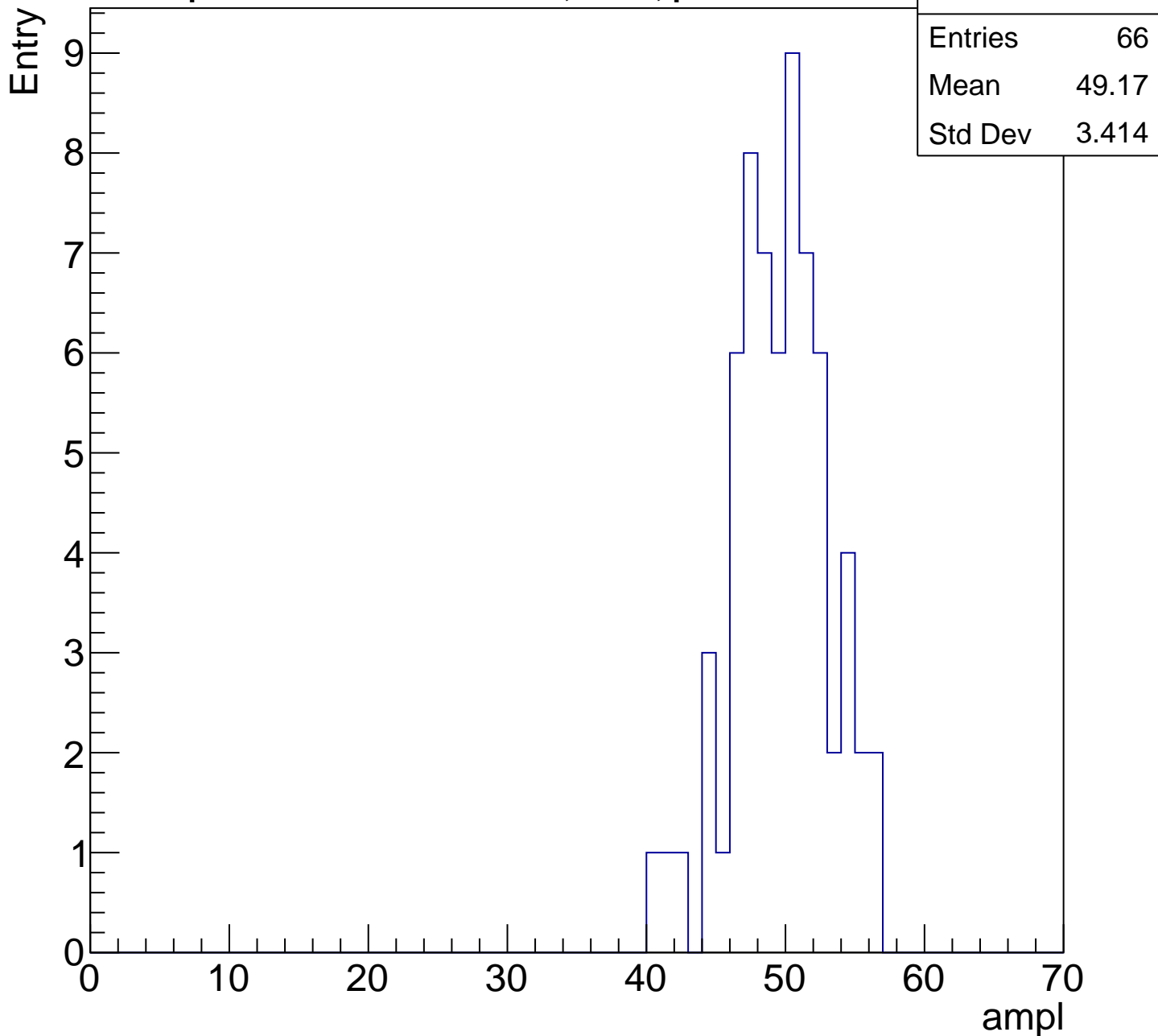
Gaus mean : 43.9977

Gaus Width: 3.9594



B1L101S, U4-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

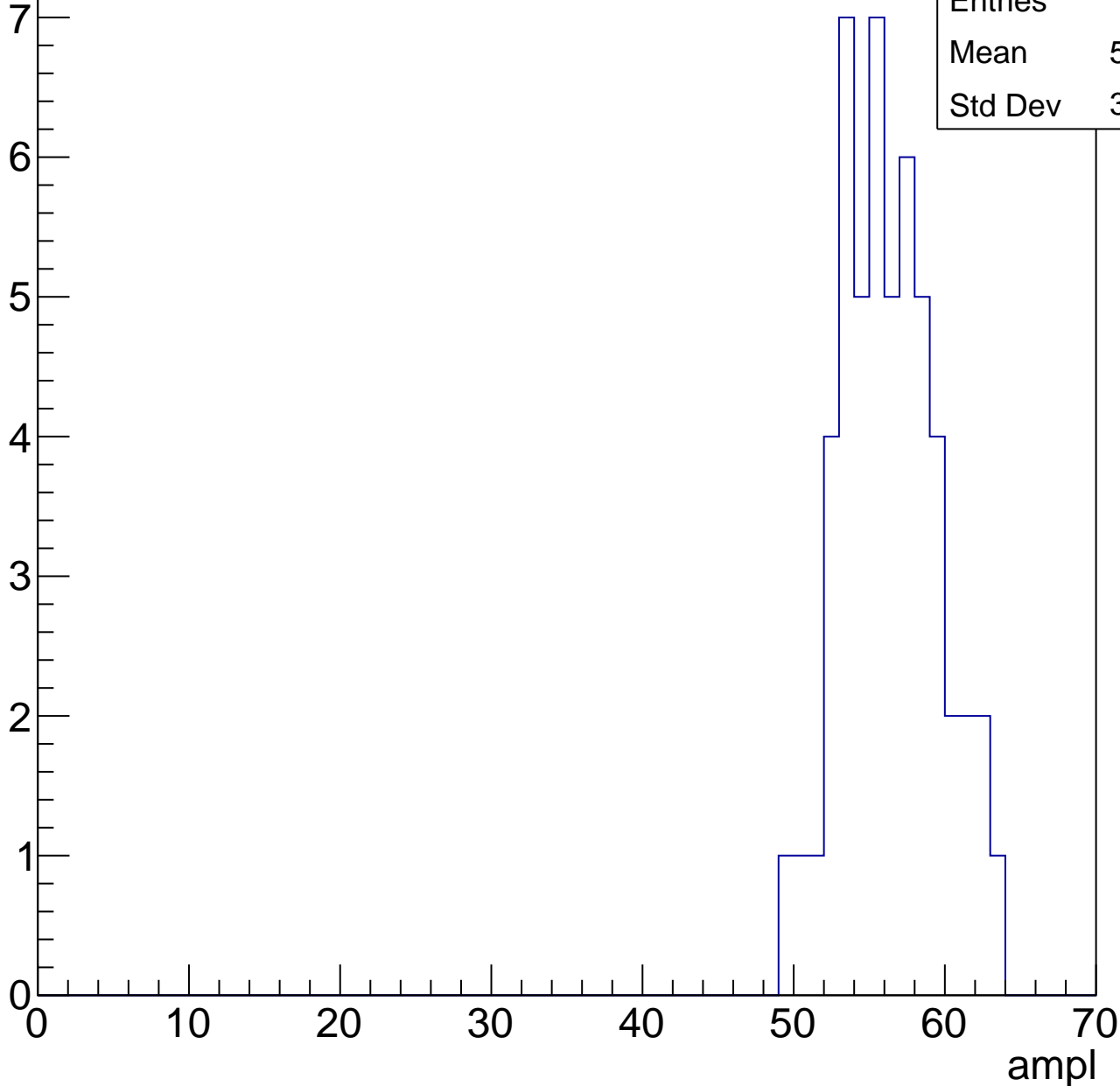


B1L101S, U4-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

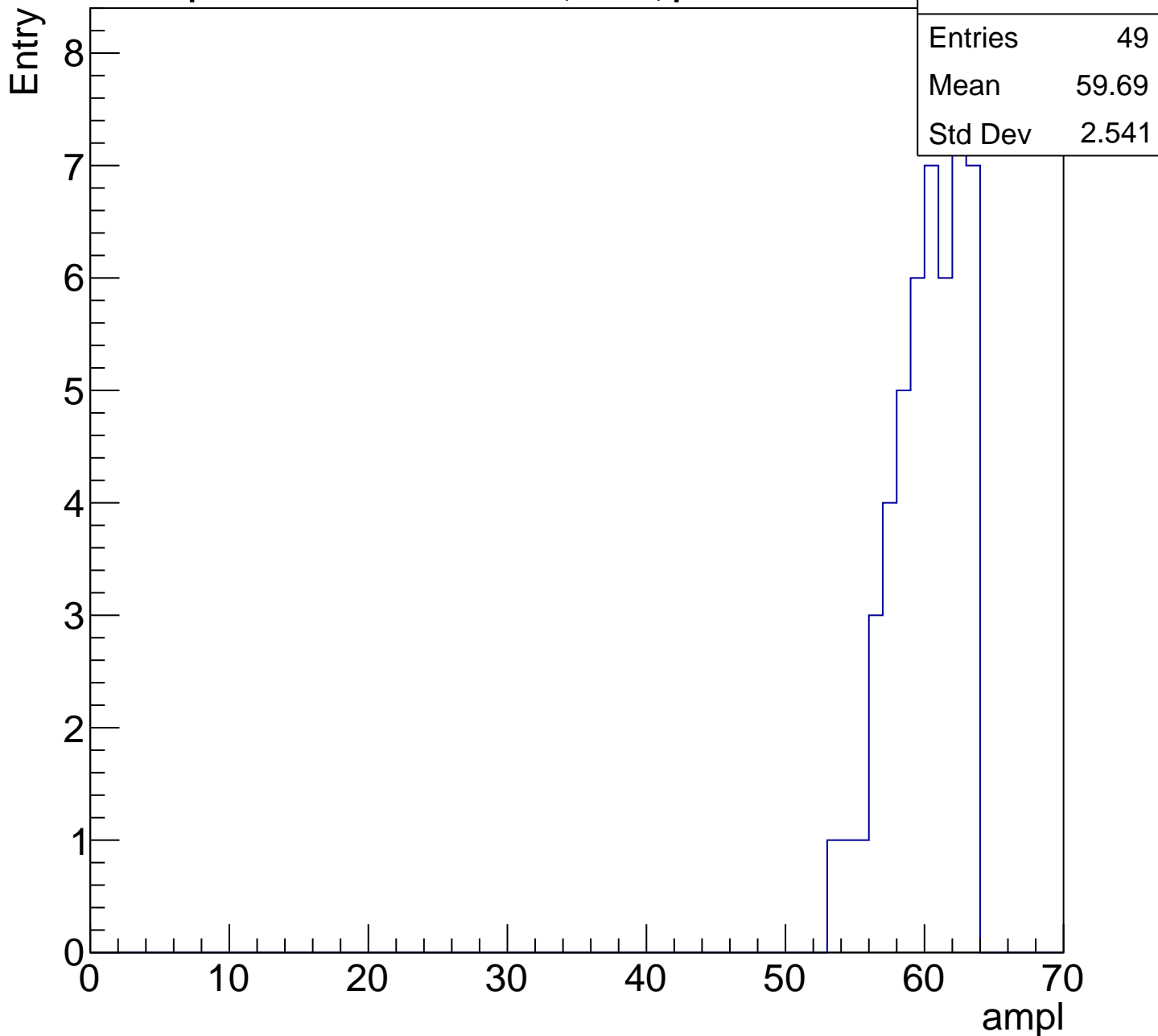
Entry

Entries	53
Mean	55.87
Std Dev	3.157



B1L101S, U4-ch48, adc5

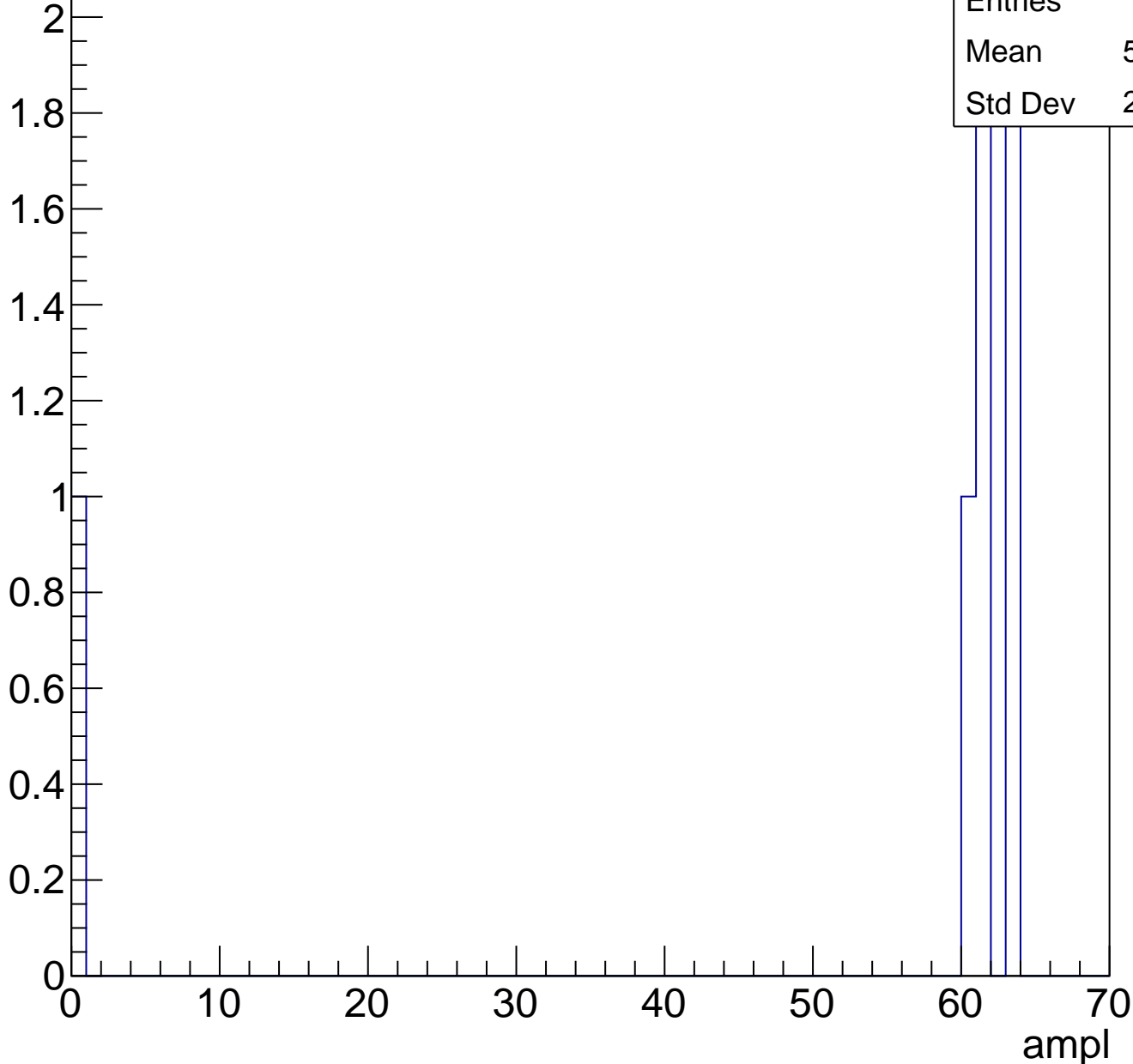
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U4-ch49, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	95
Mean	28.45
Std Dev	5.763

Gaus mean : 29.5656

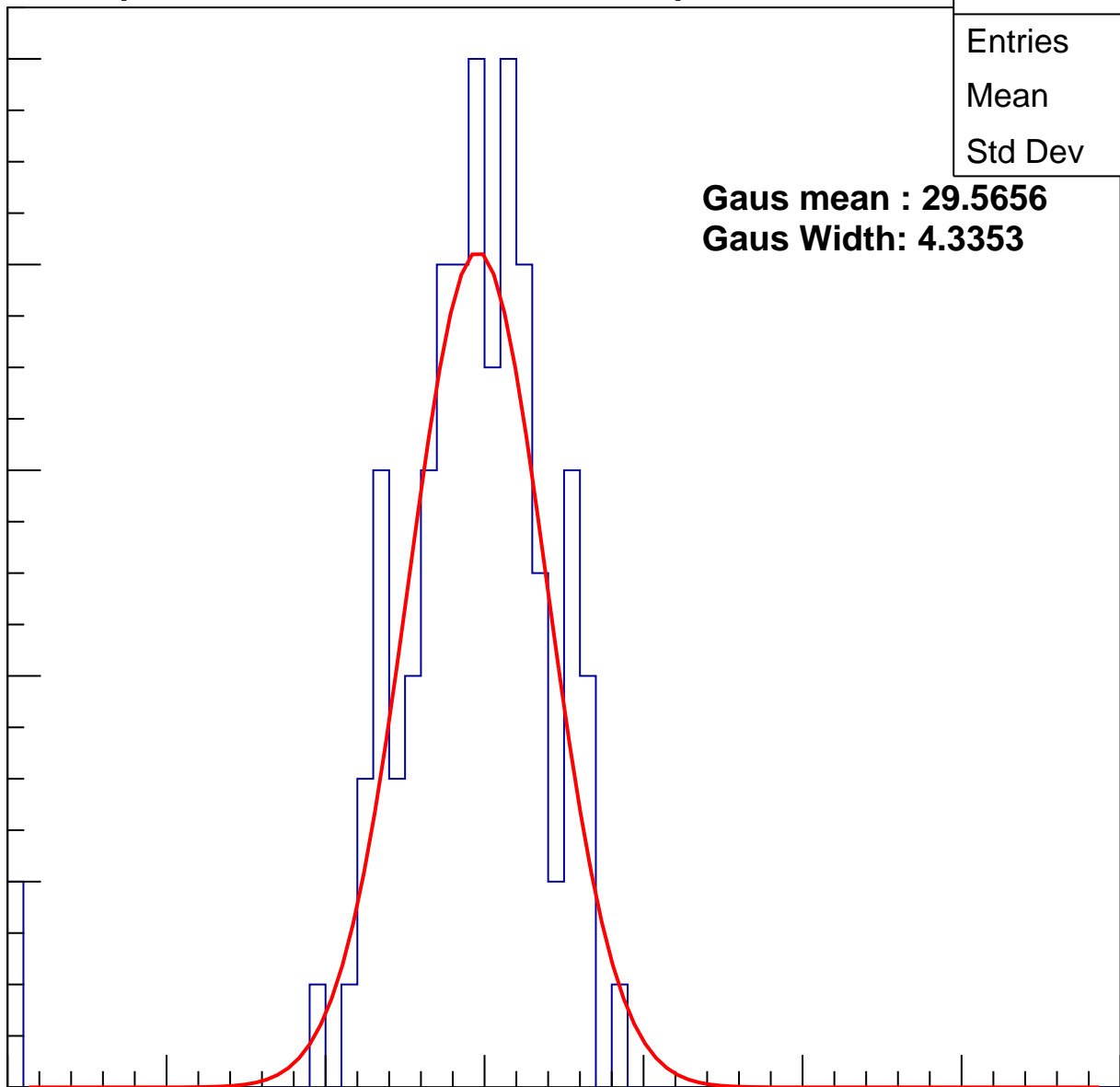
Gaus Width: 4.3353

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch49, adc1

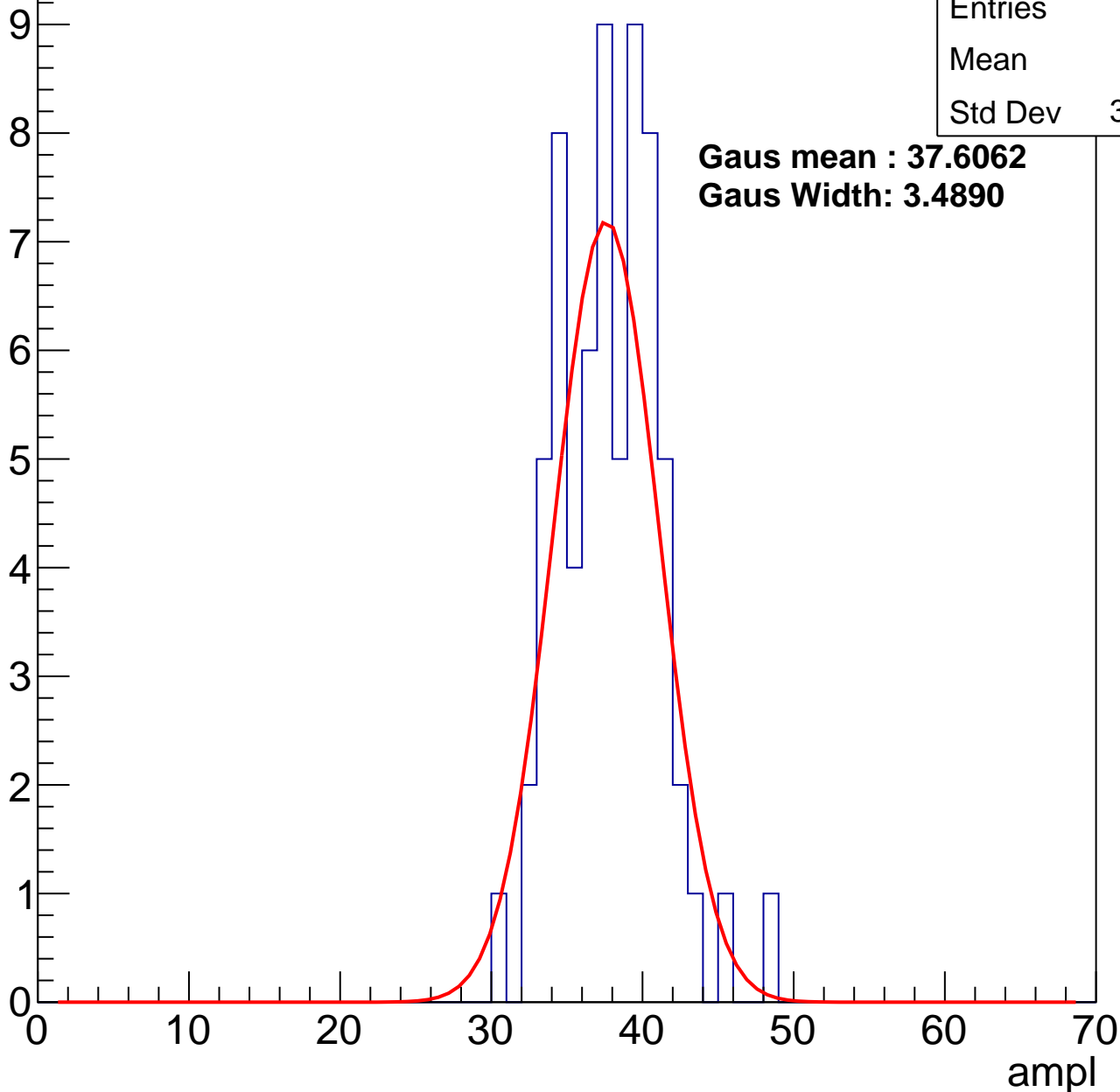
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.4
Std Dev	3.292

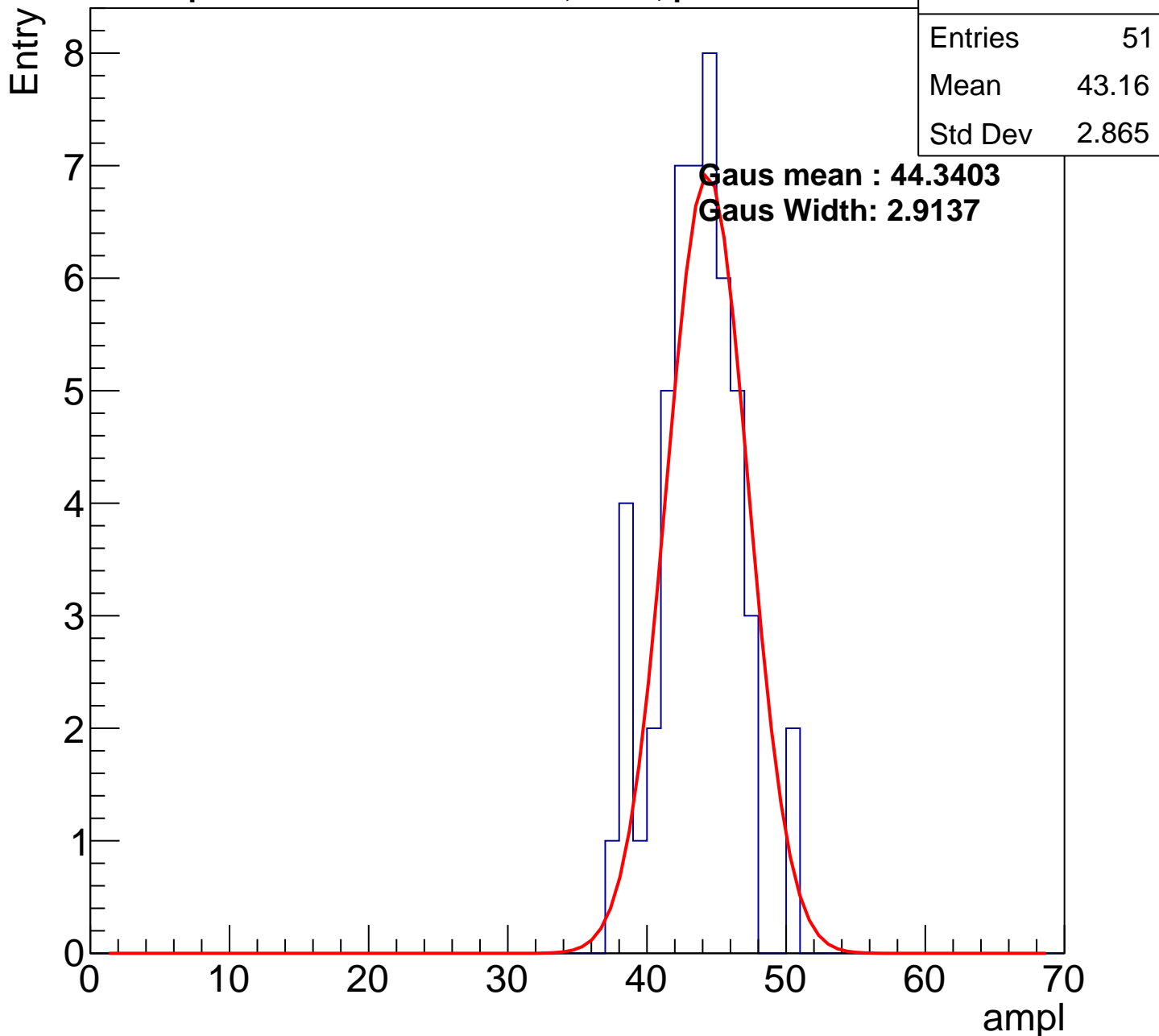
Gaus mean : 37.6062

Gaus Width: 3.4890



B1L101S, U4-ch49, adc2

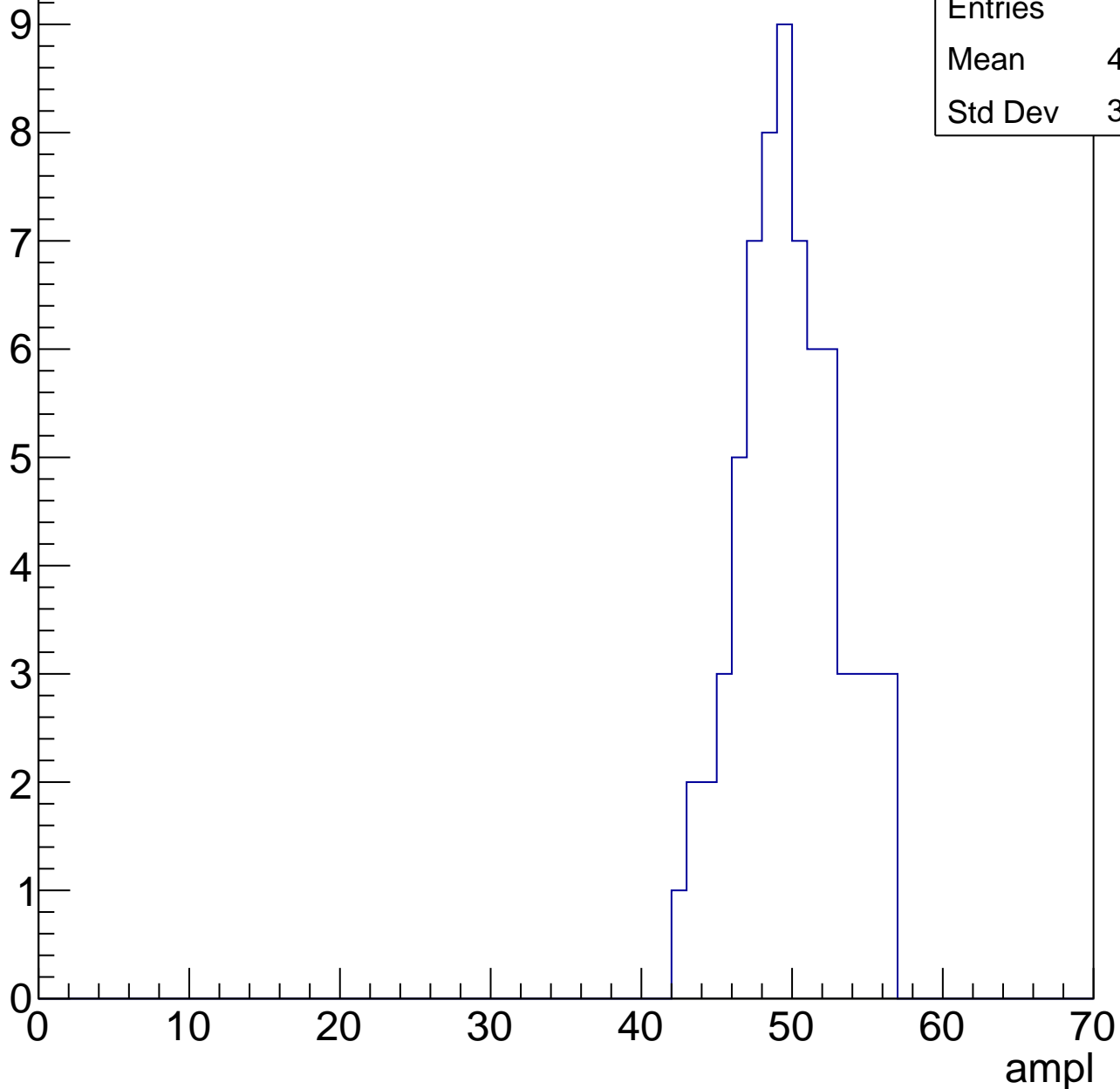
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

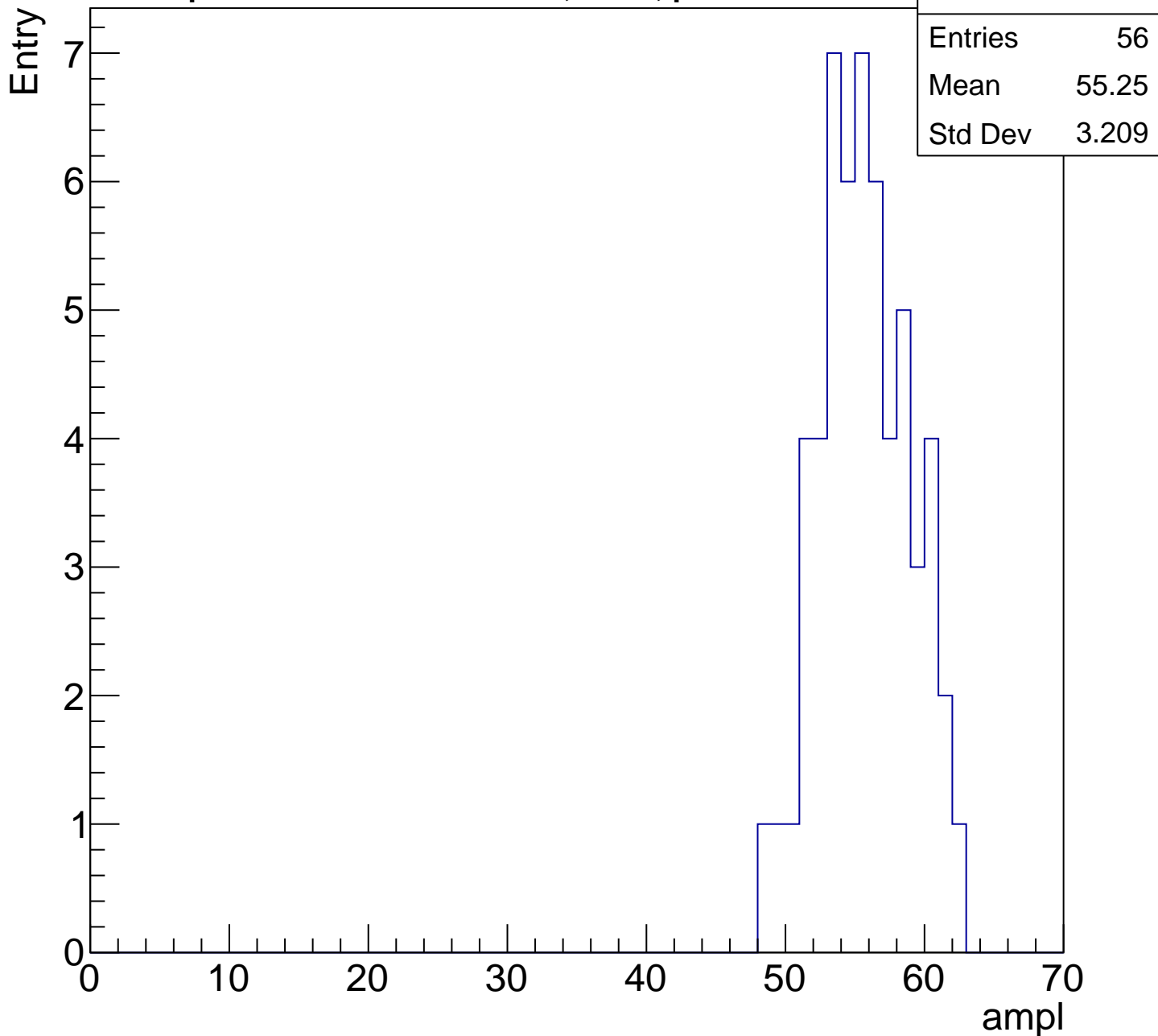
Entry



Entries	68
Mean	49.37
Std Dev	3.329

B1L101S, U4-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

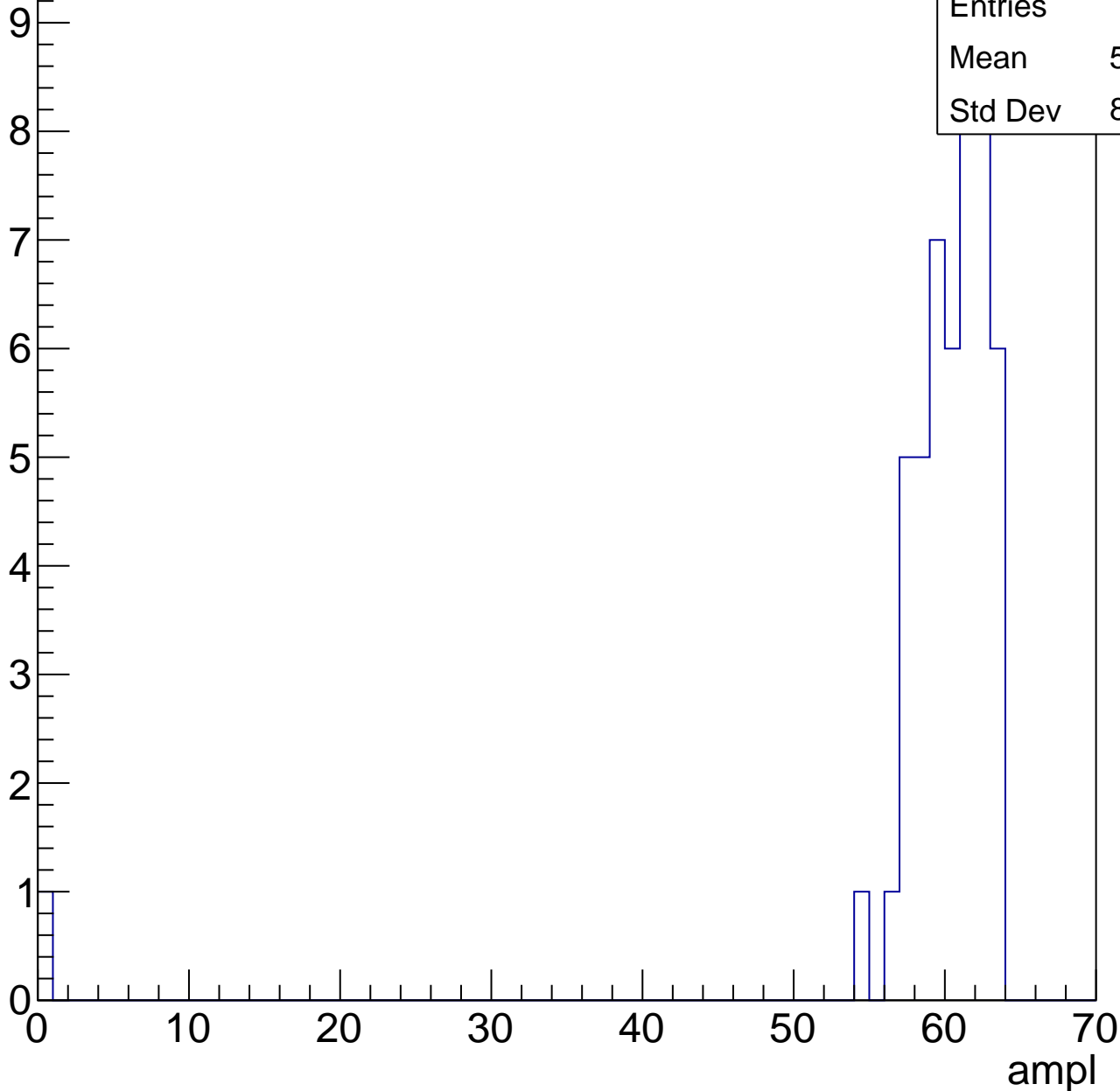


B1L101S, U4-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

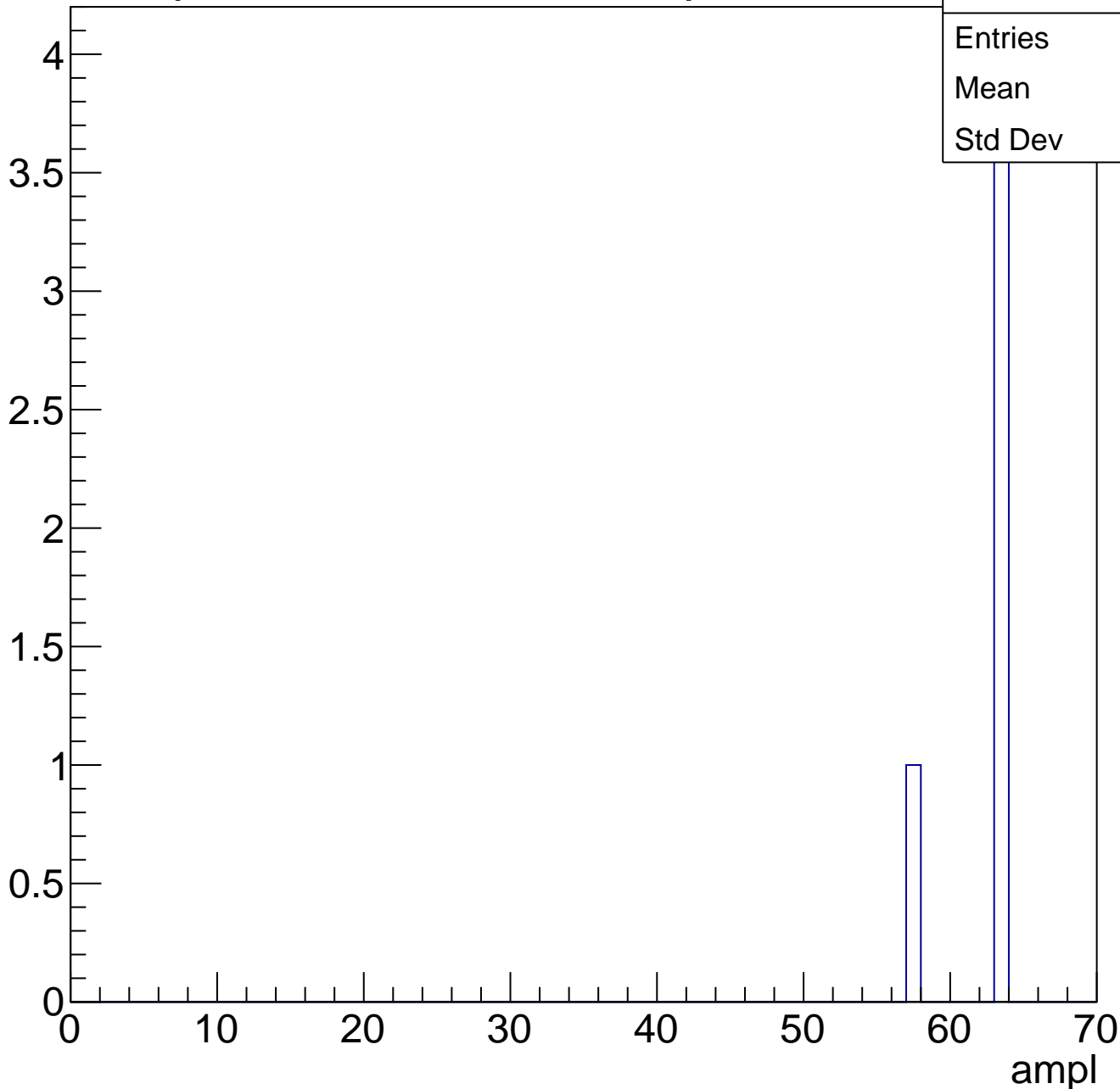
Entries	49
Mean	58.82
Std Dev	8.752



B1L101S, U4-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch50, adc0

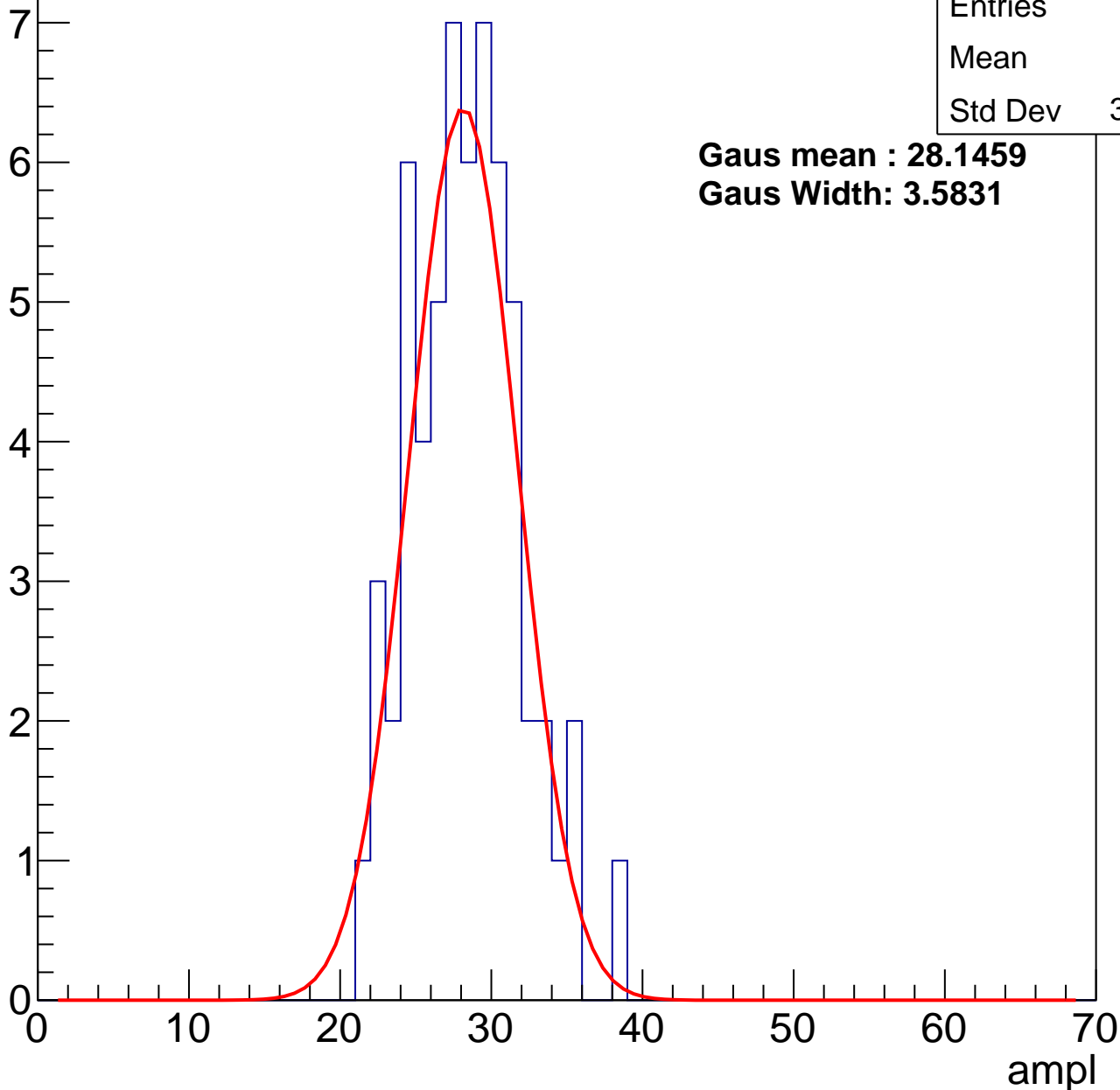
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	27.9
Std Dev	3.553

Gaus mean : 28.1459

Gaus Width: 3.5831



B1L101S, U4-ch50, adc1

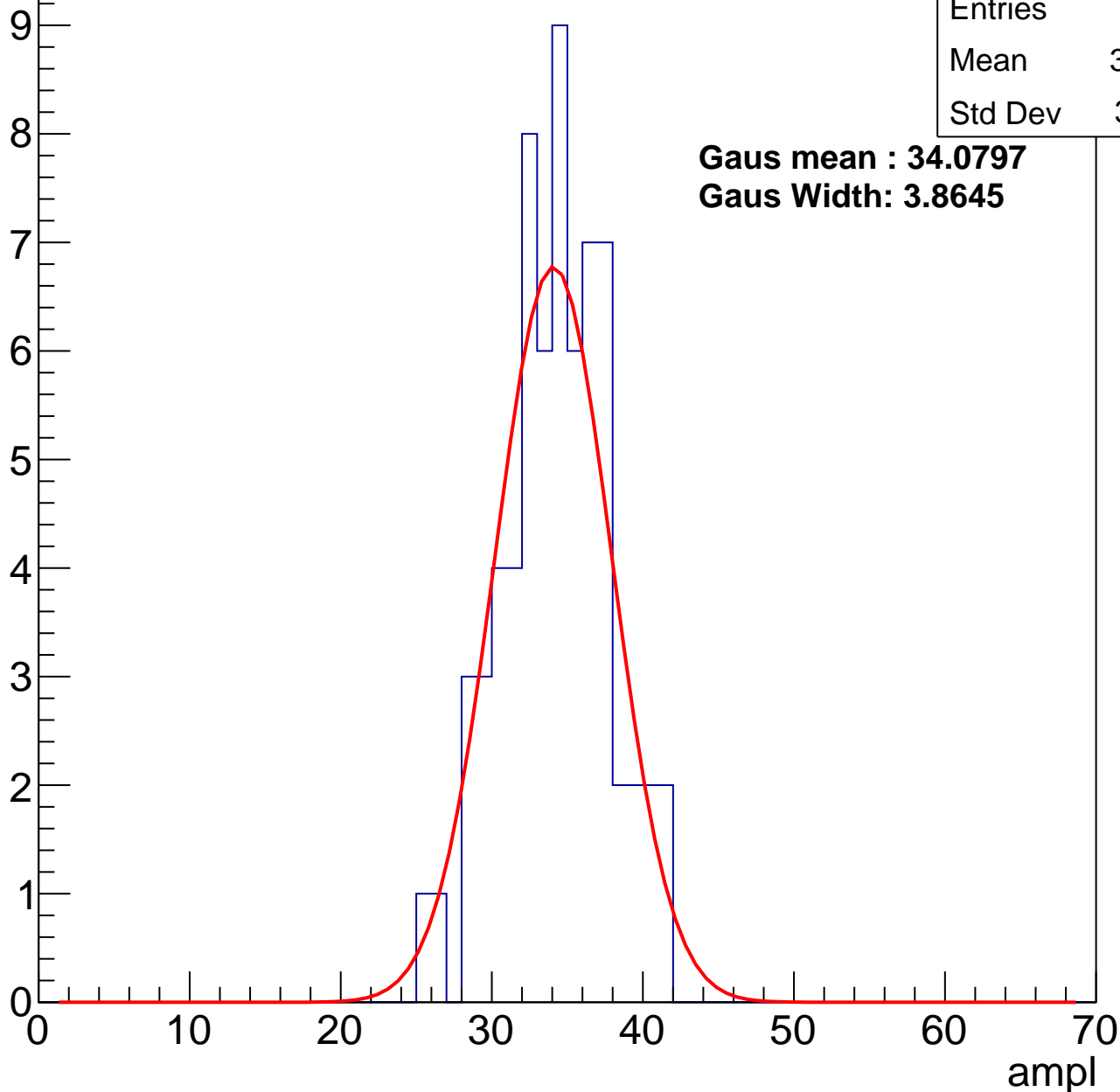
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	33.78
Std Dev	3.481

Gaus mean : 34.0797

Gaus Width: 3.8645



B1L101S, U4-ch50, adc2

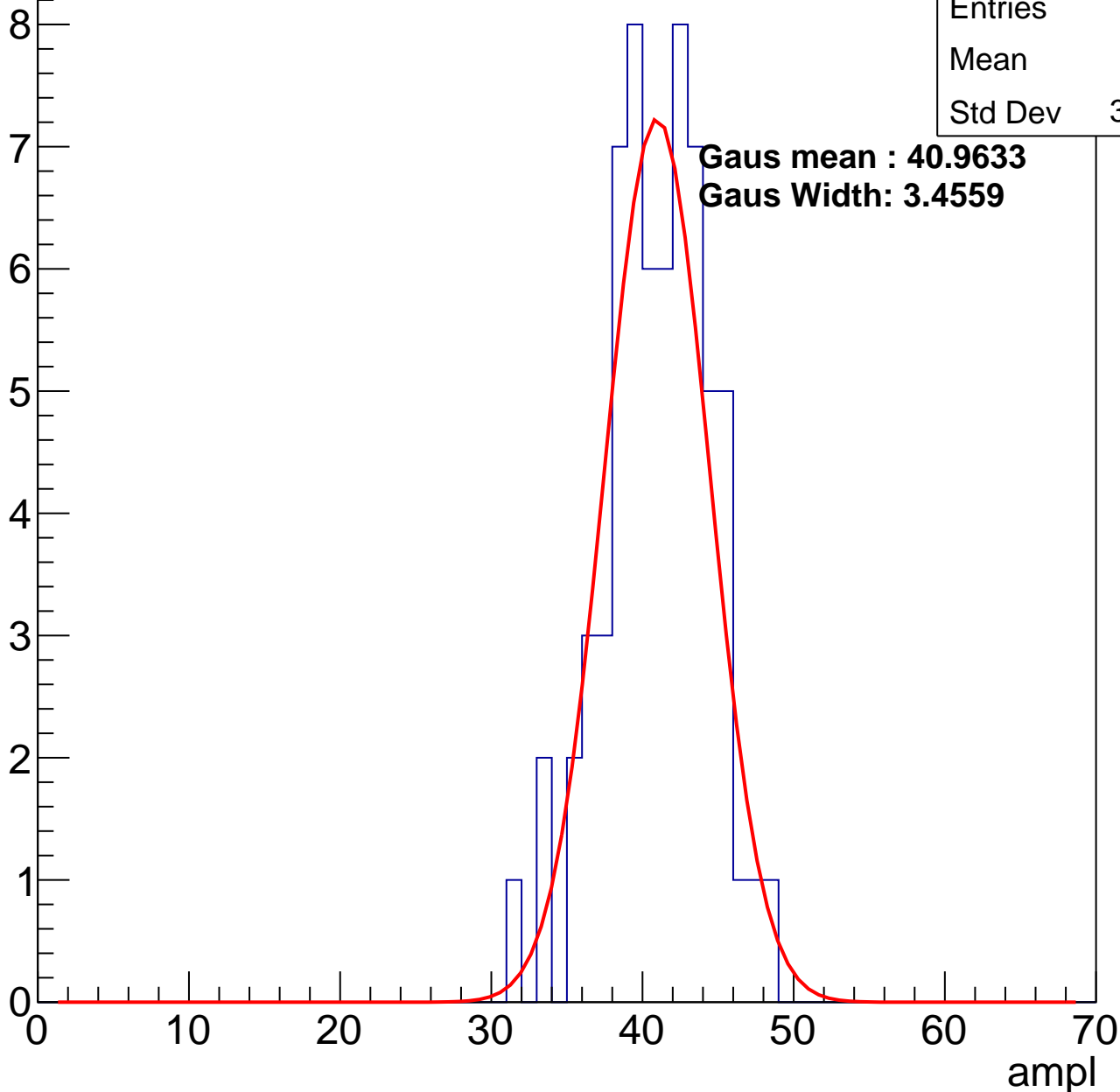
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	40.5
Std Dev	3.434

Gaus mean : 40.9633

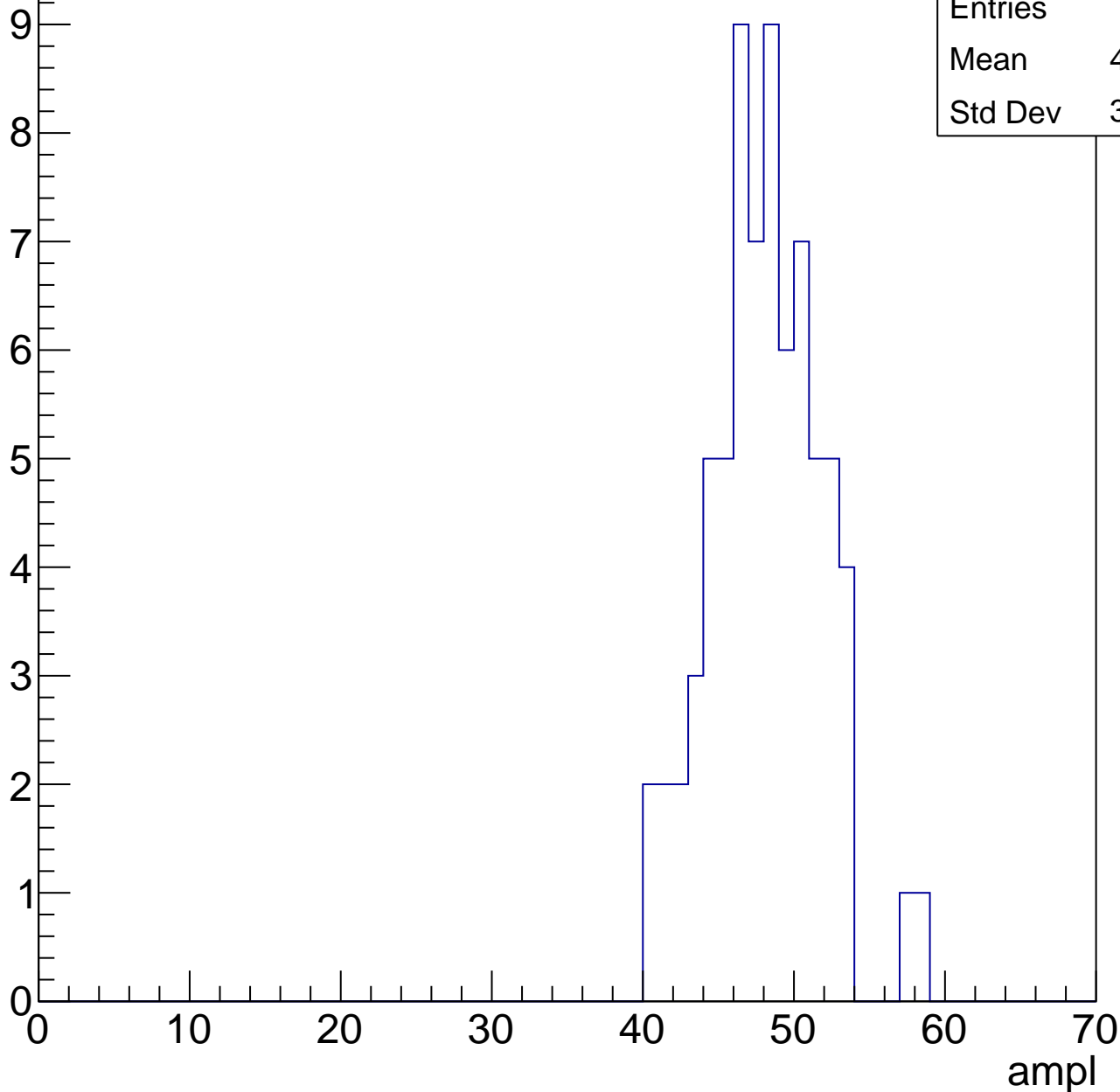
Gaus Width: 3.4559



B1L101S, U4-ch50, adc3

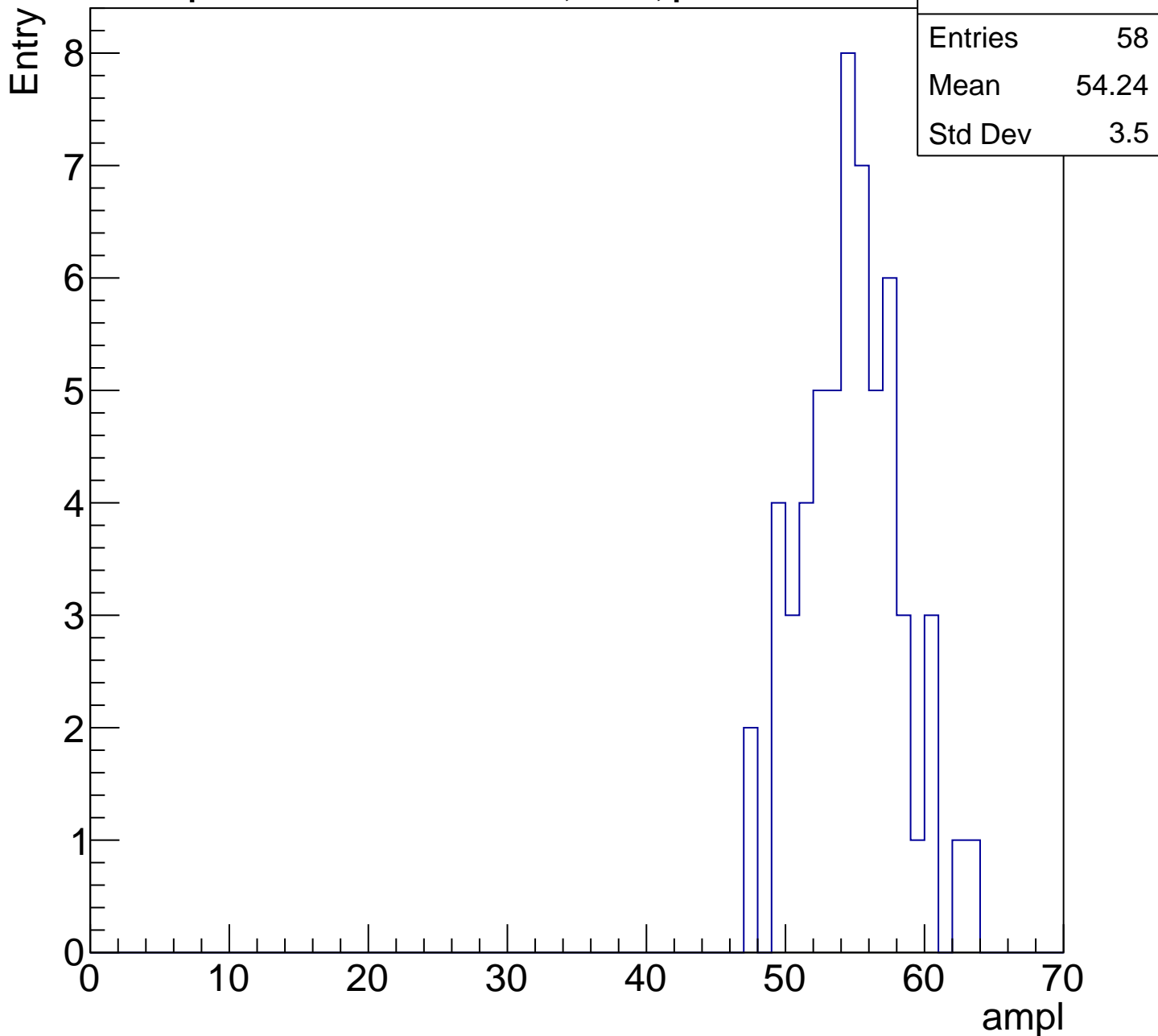
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

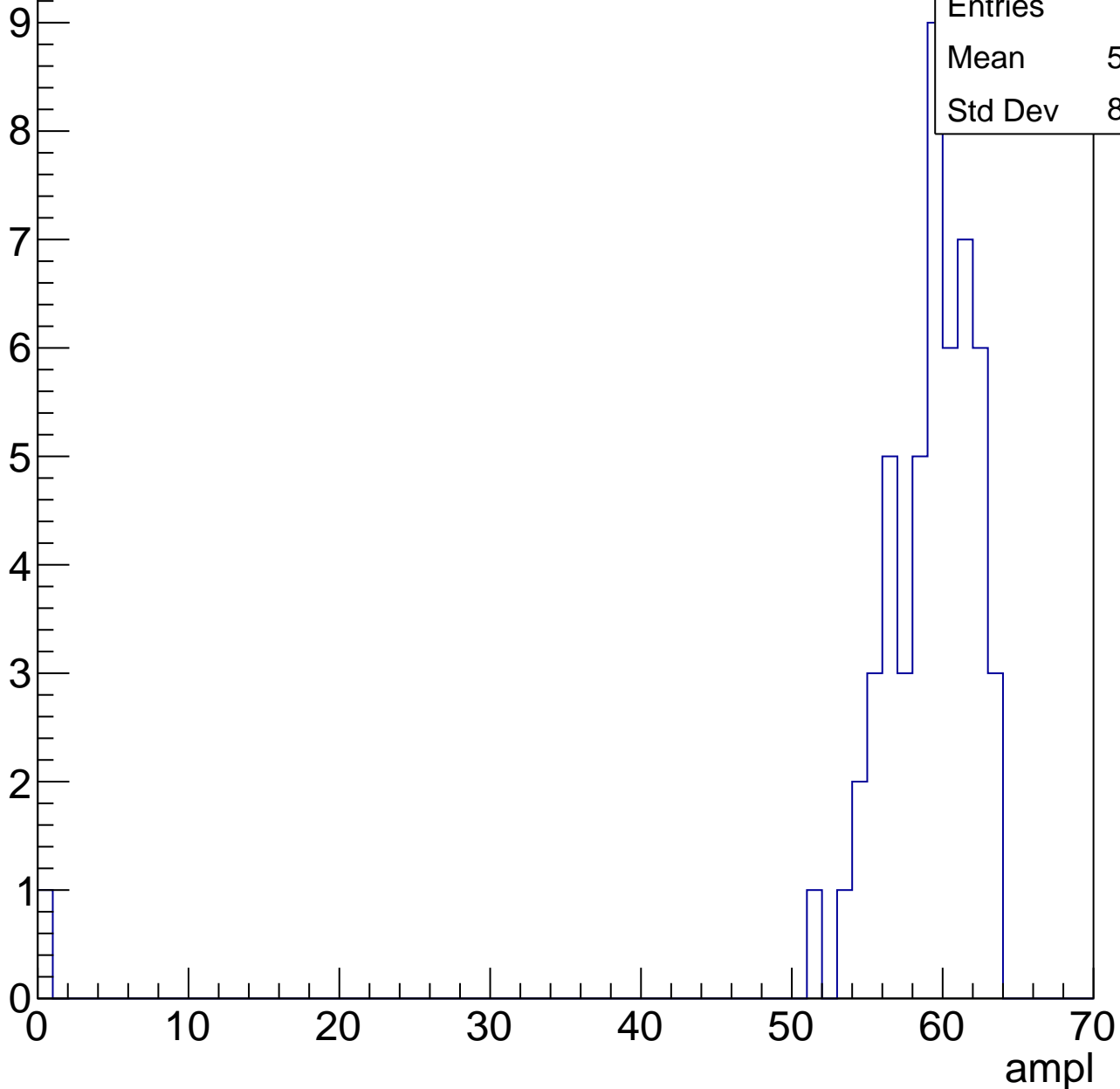


B1L101S, U4-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.63
Std Dev	8.526

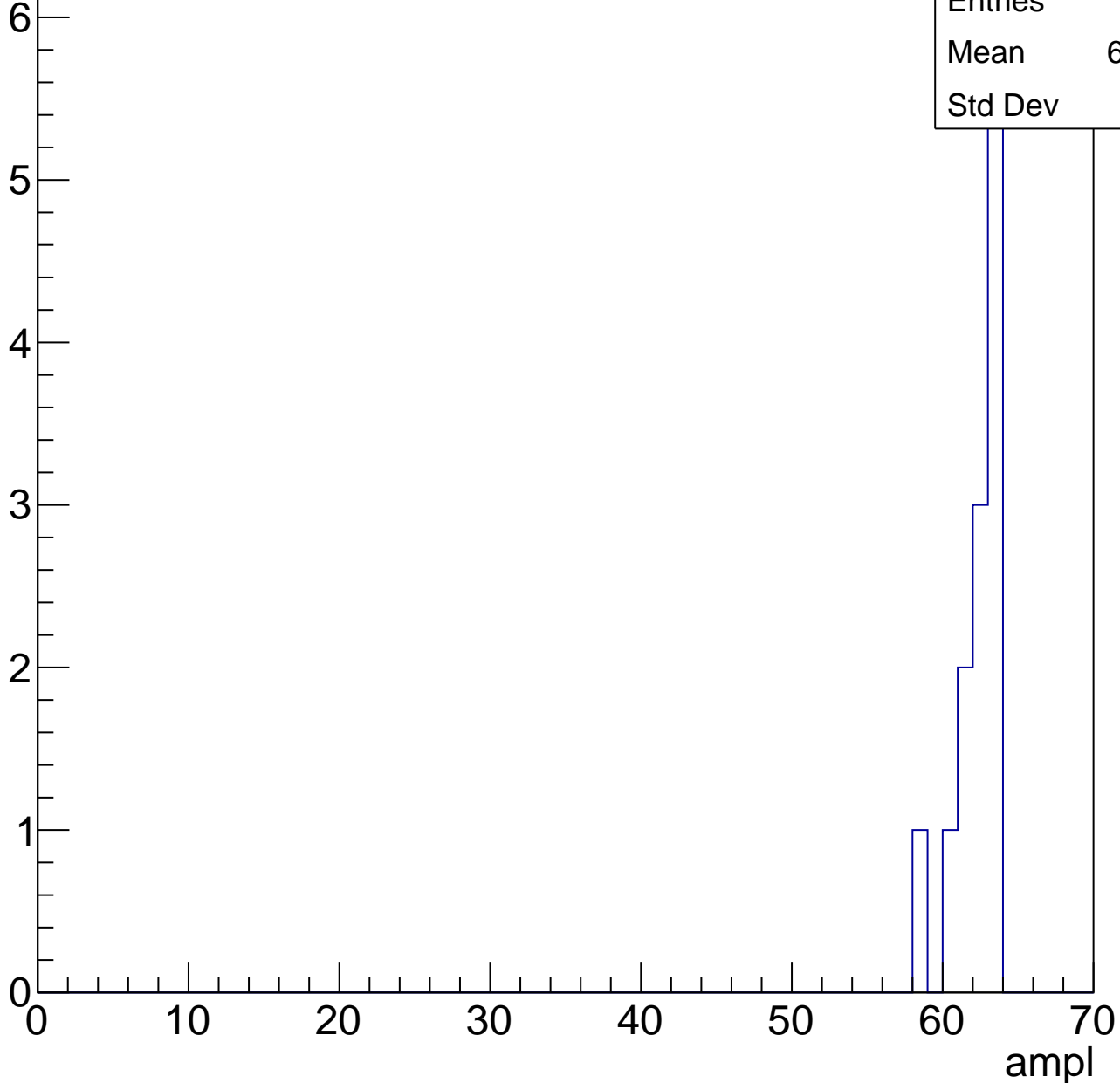


B1L101S, U4-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	61.85
Std Dev	1.46



B1L101S, U4-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch51, adc0

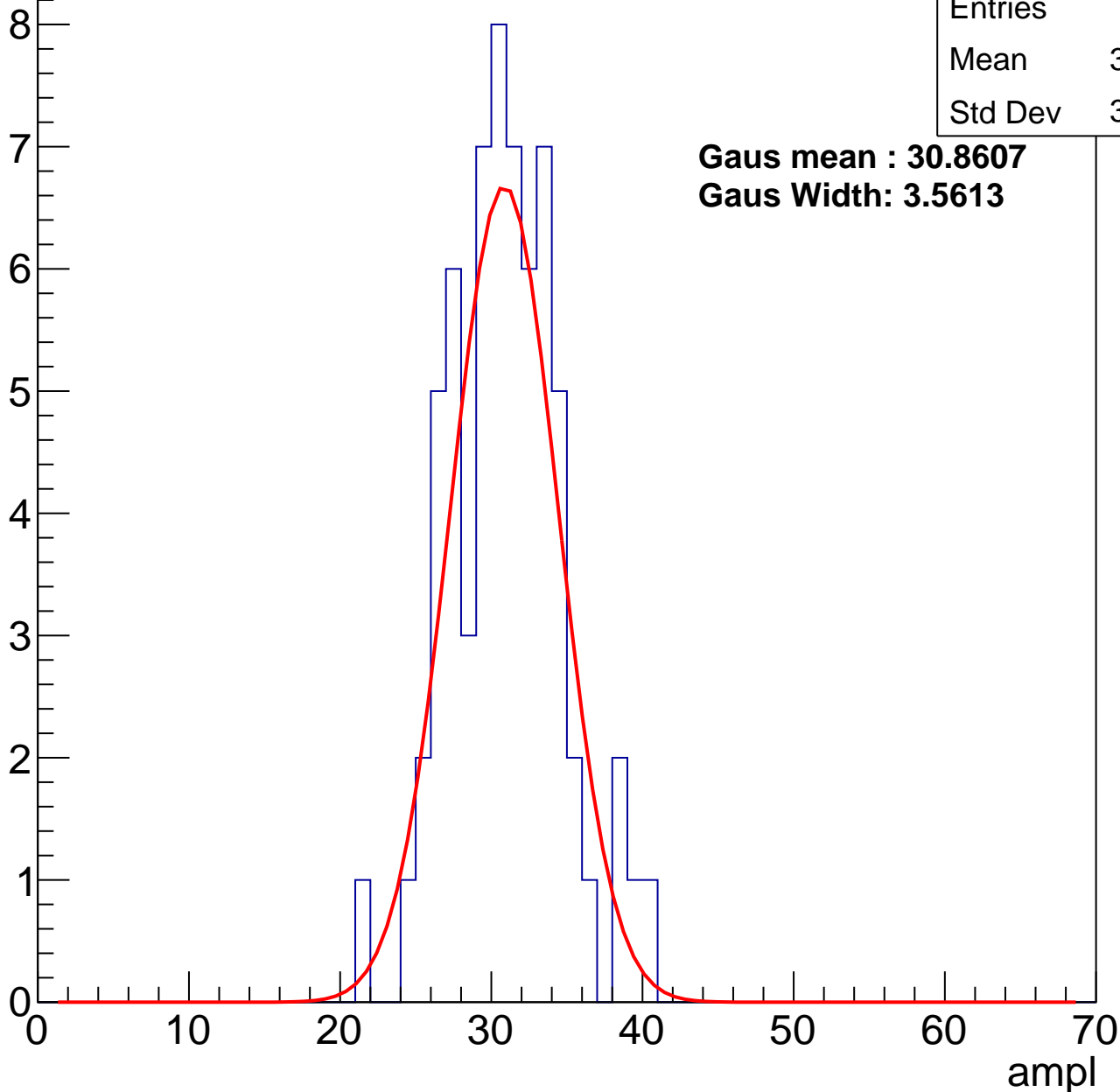
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.54
Std Dev	3.659

Gaus mean : 30.8607

Gaus Width: 3.5613



B1L101S, U4-ch51, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.08
Std Dev	3.607

Gaus mean : 37.5250

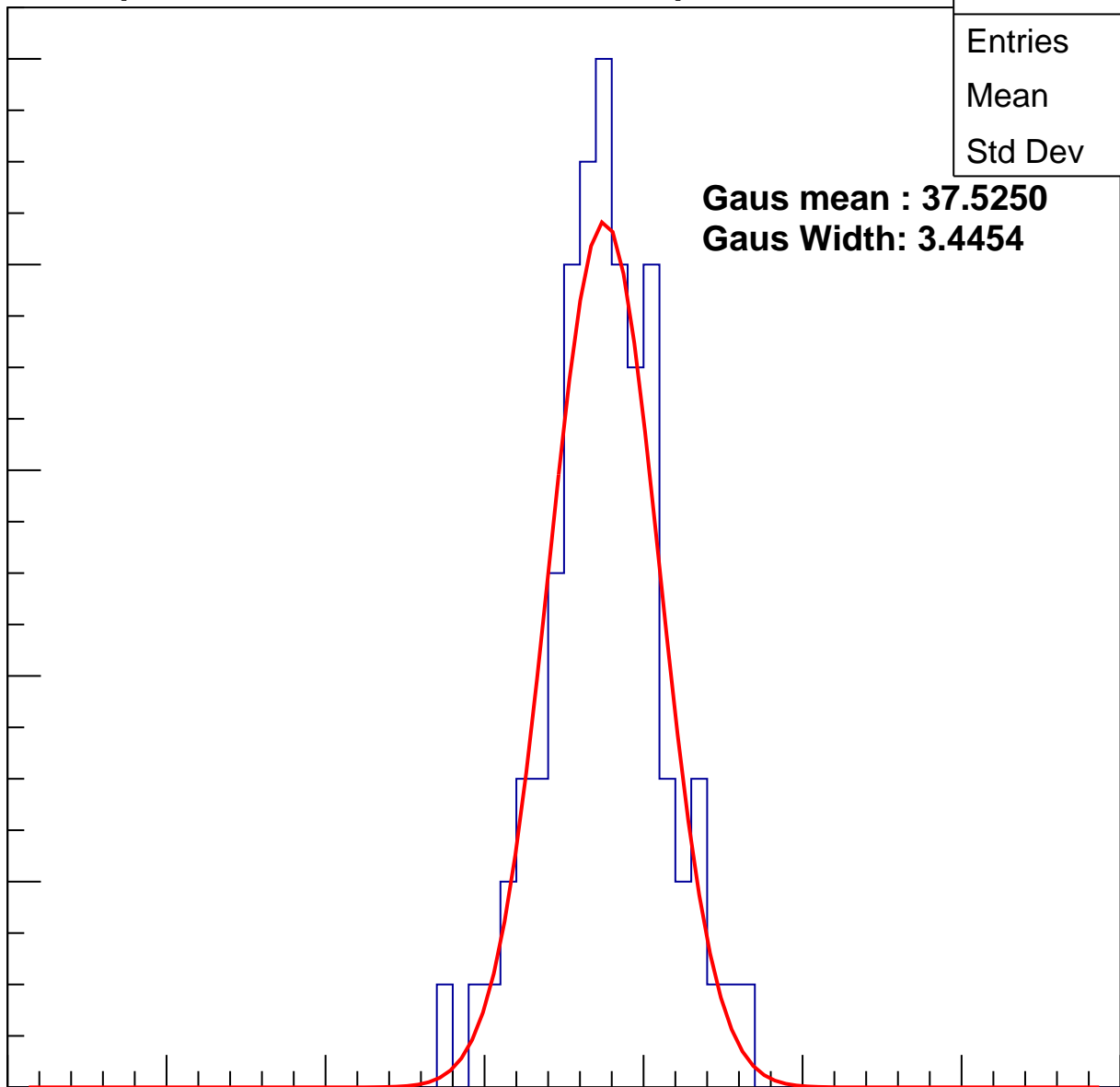
Gaus Width: 3.4454

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch51, adc2

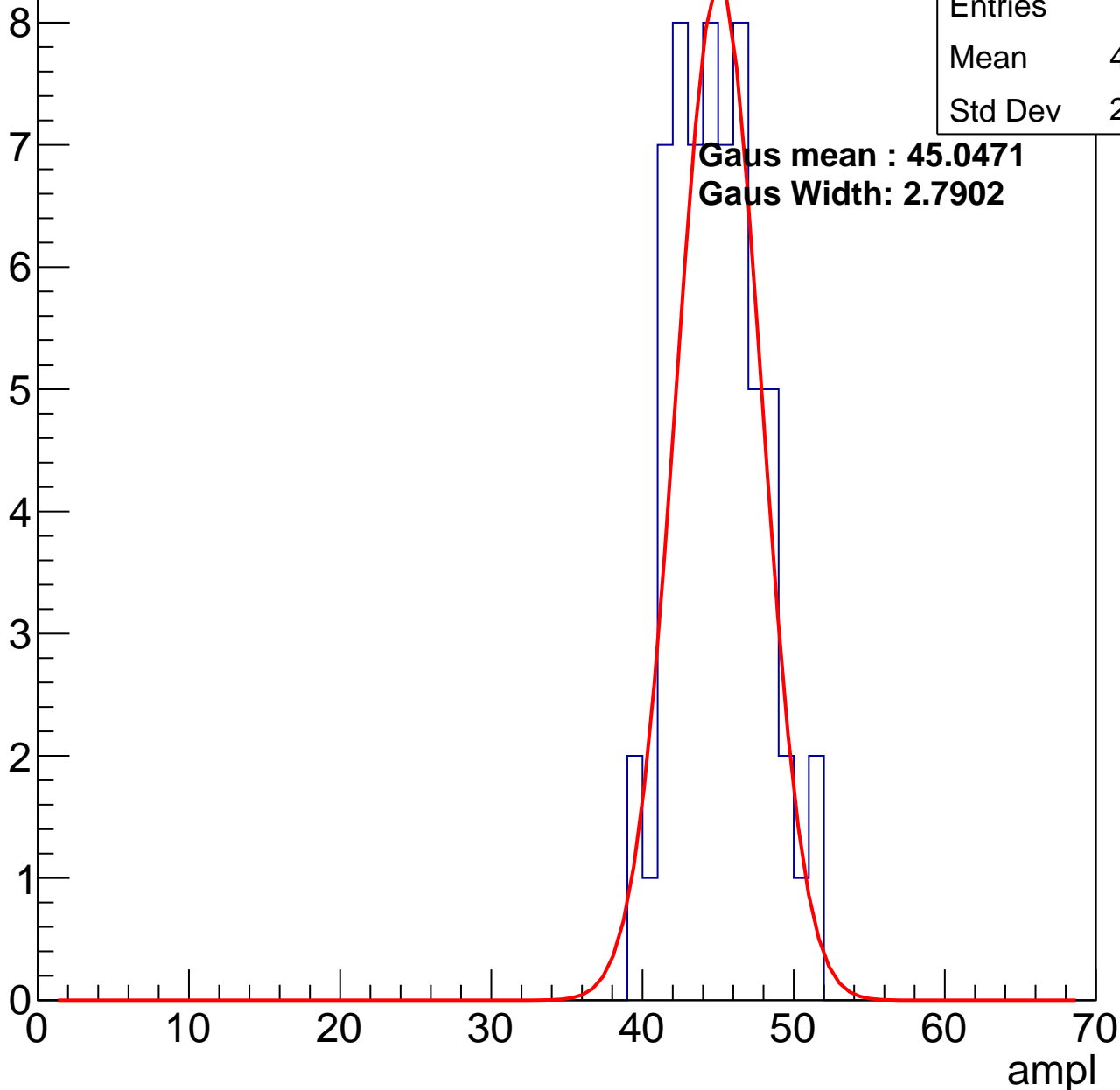
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.48
Std Dev	2.822

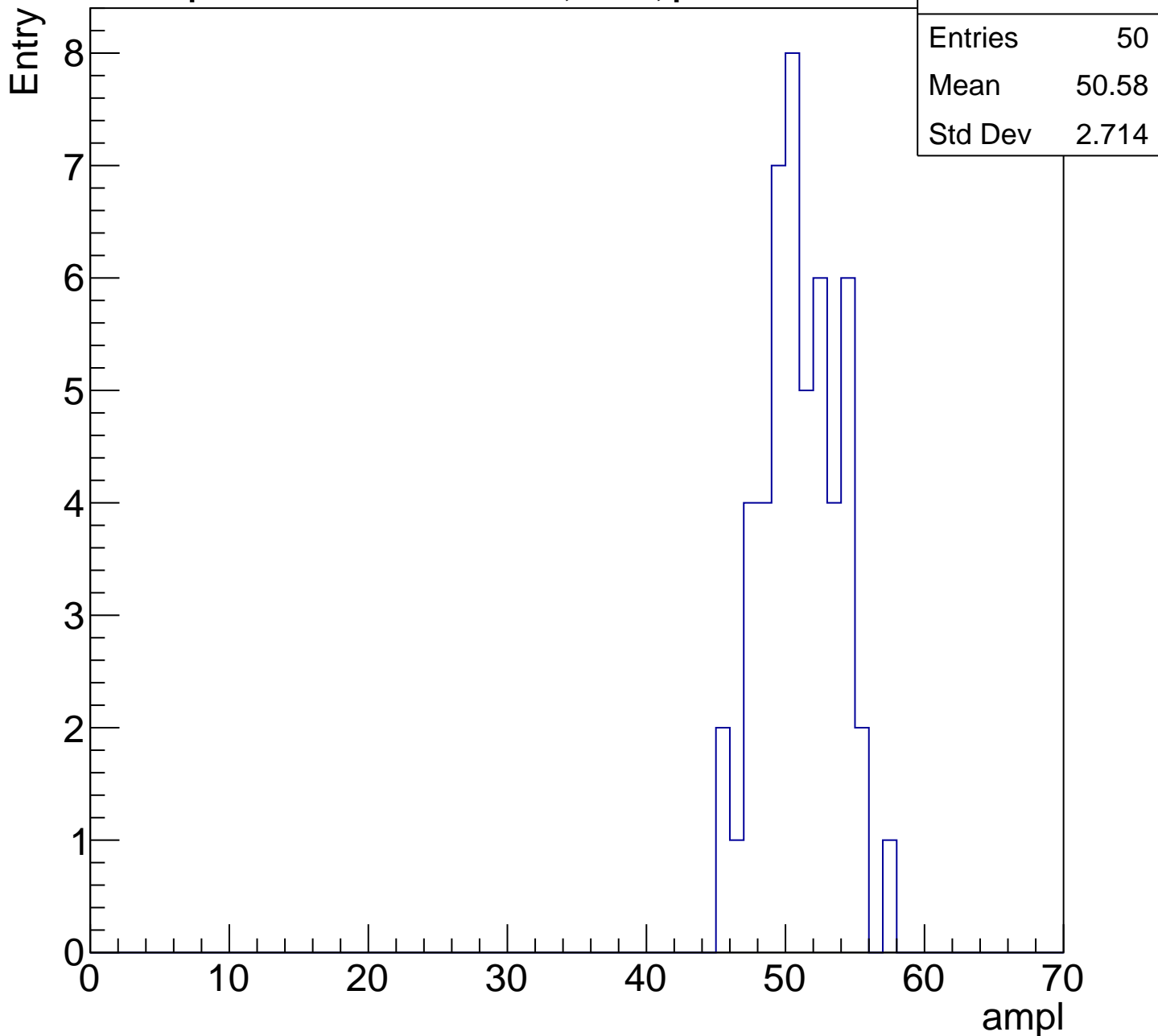
Gaus mean : 45.0471

Gaus Width: 2.7902



B1L101S, U4-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

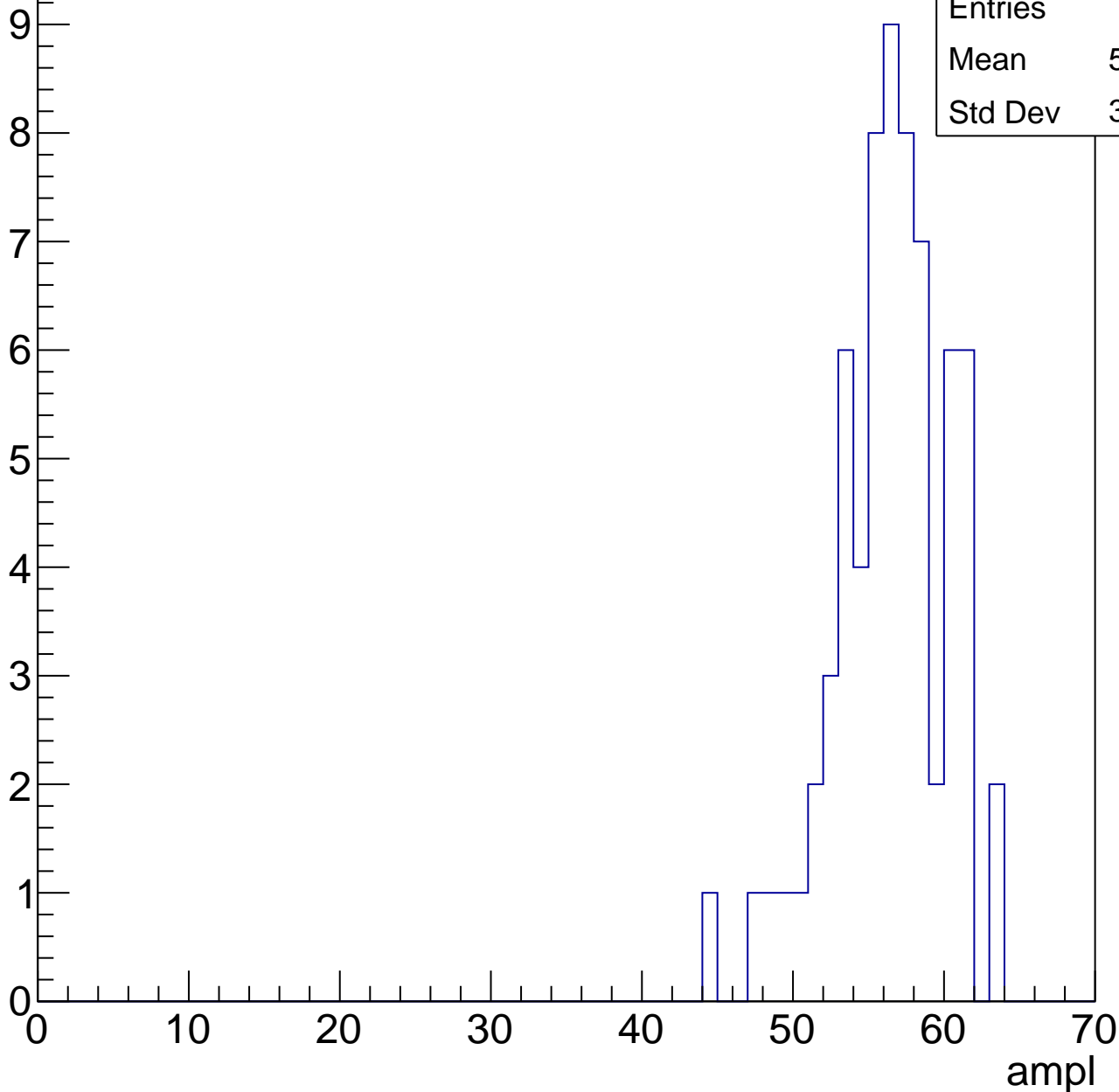


B1L101S, U4-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	55.97
Std Dev	3.734



B1L101S, U4-ch51, adc5

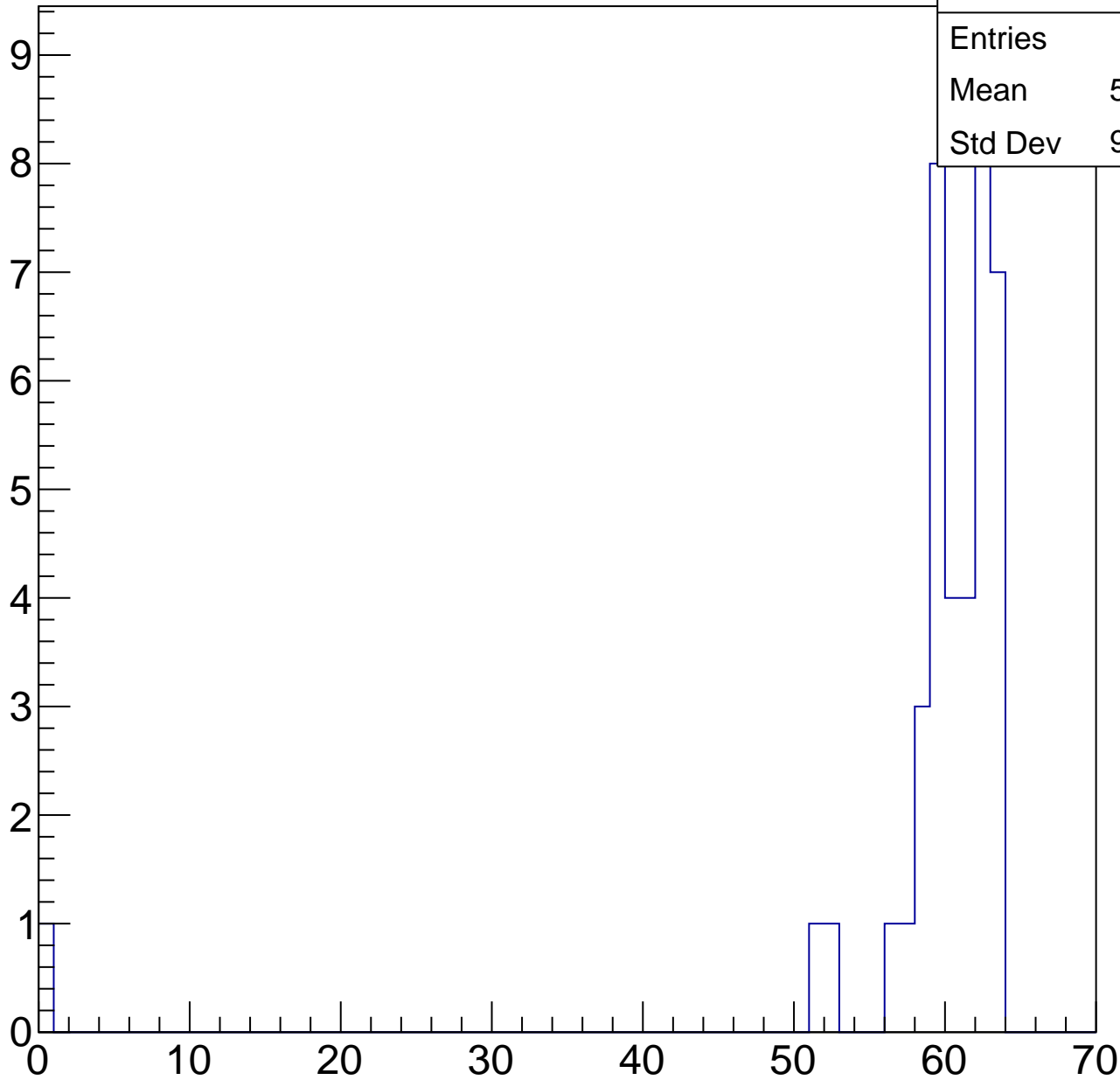
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.62
Std Dev	9.769

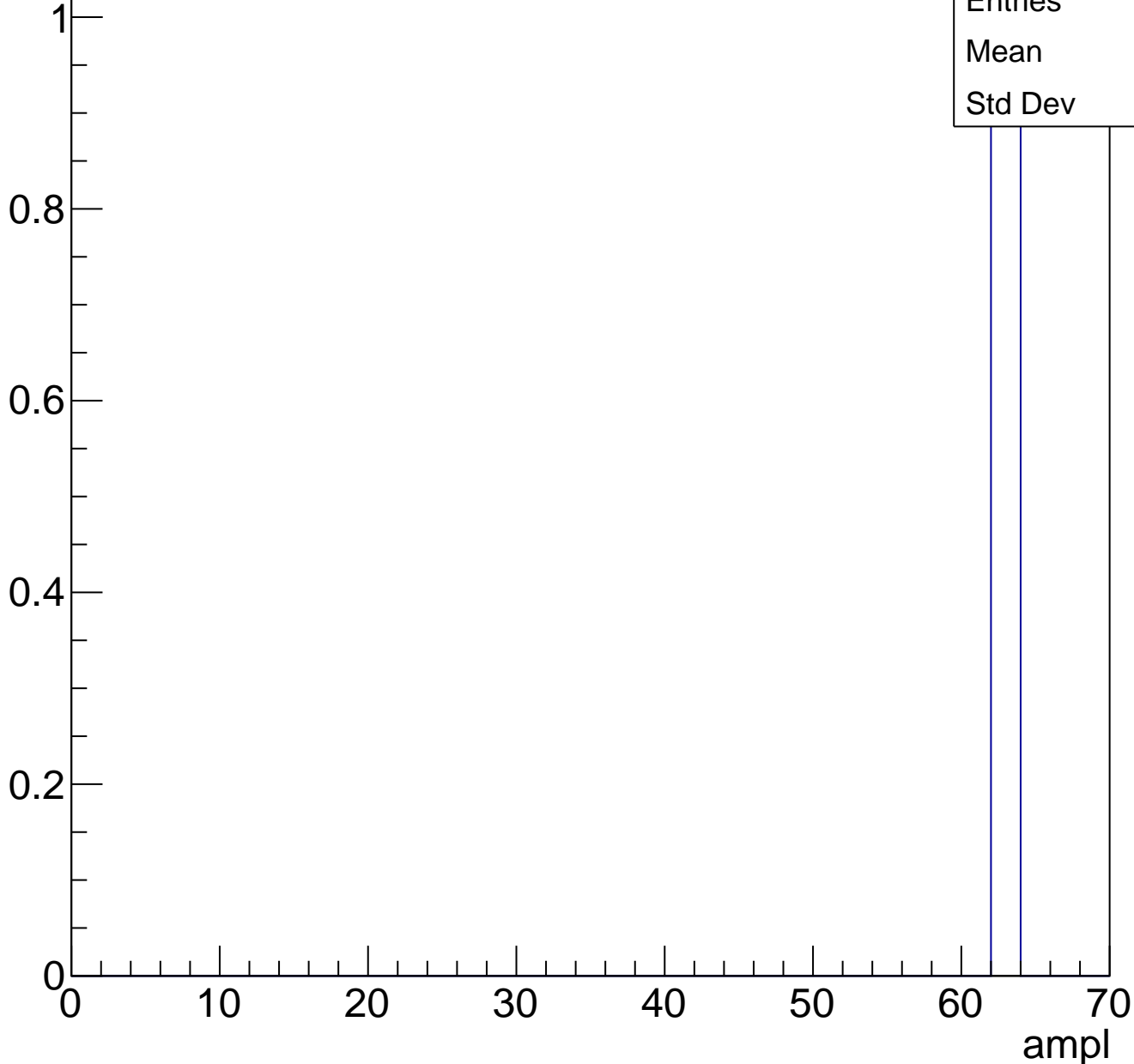
ampl



B1L101S, U4-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch52, adc0

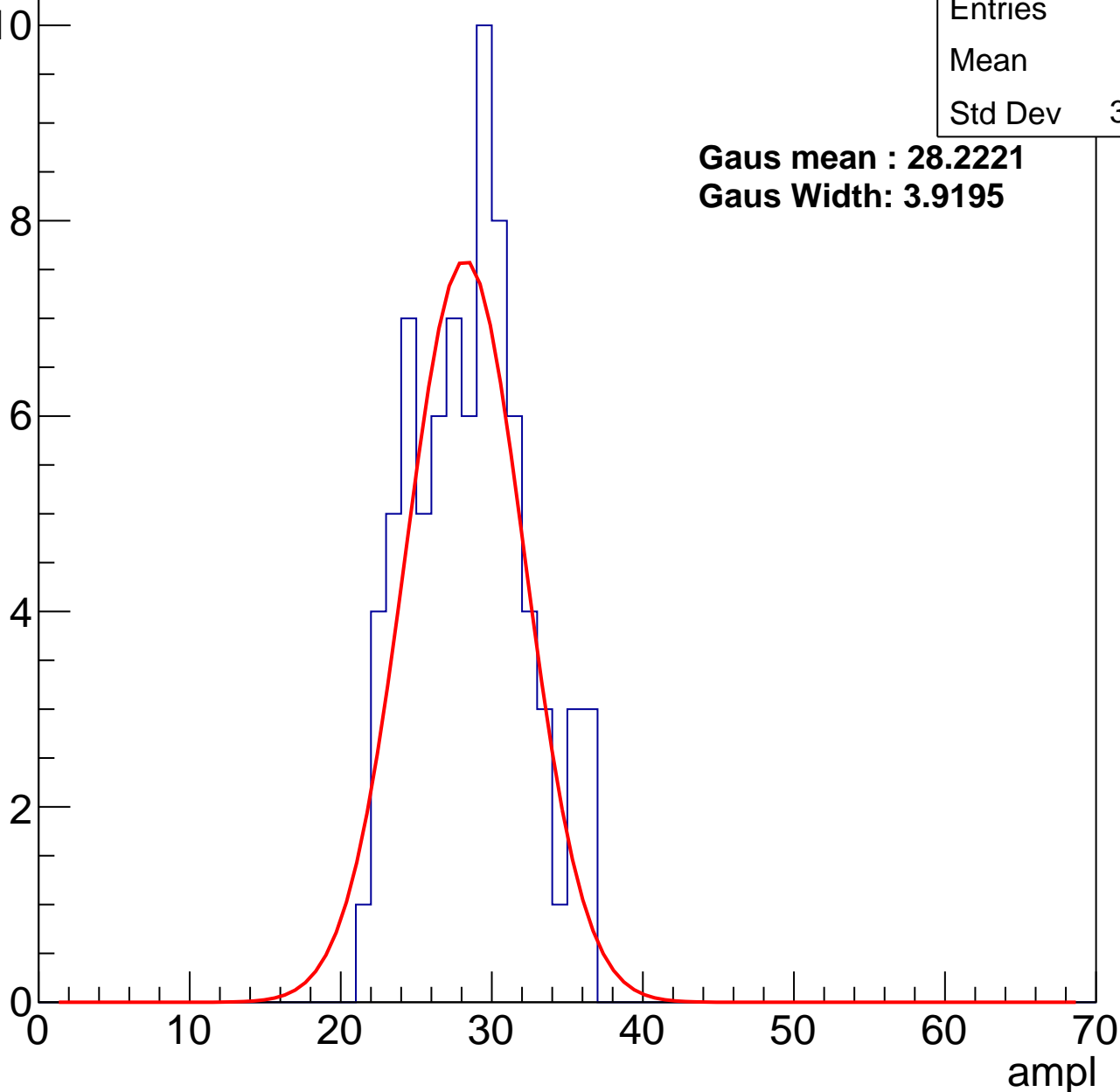
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.1
Std Dev	3.757

Gaus mean : 28.2221

Gaus Width: 3.9195



B1L101S, U4-ch52, adc1

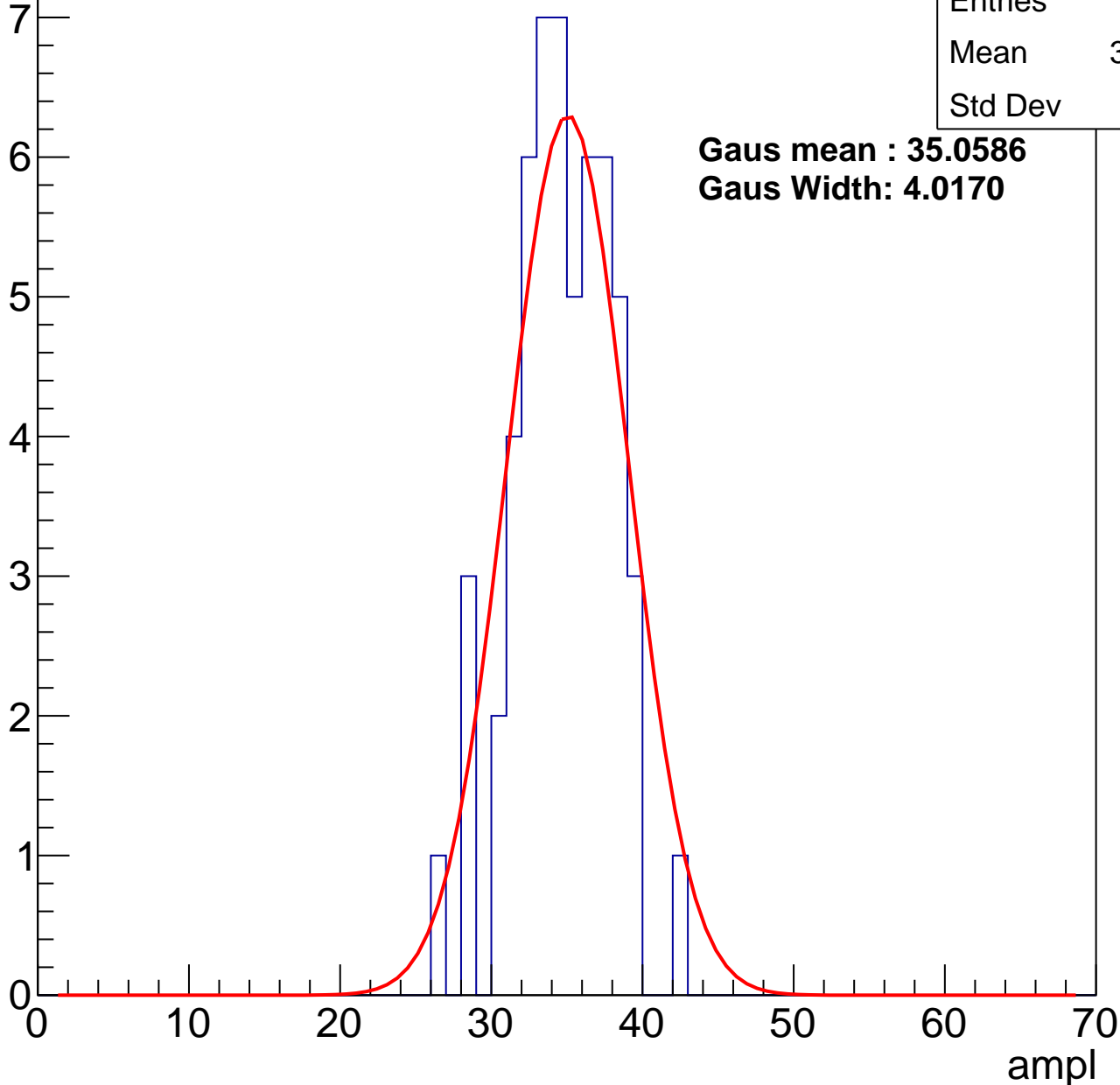
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	34.23
Std Dev	3.19

Gaus mean : 35.0586

Gaus Width: 4.0170



B1L101S, U4-ch52, adc2

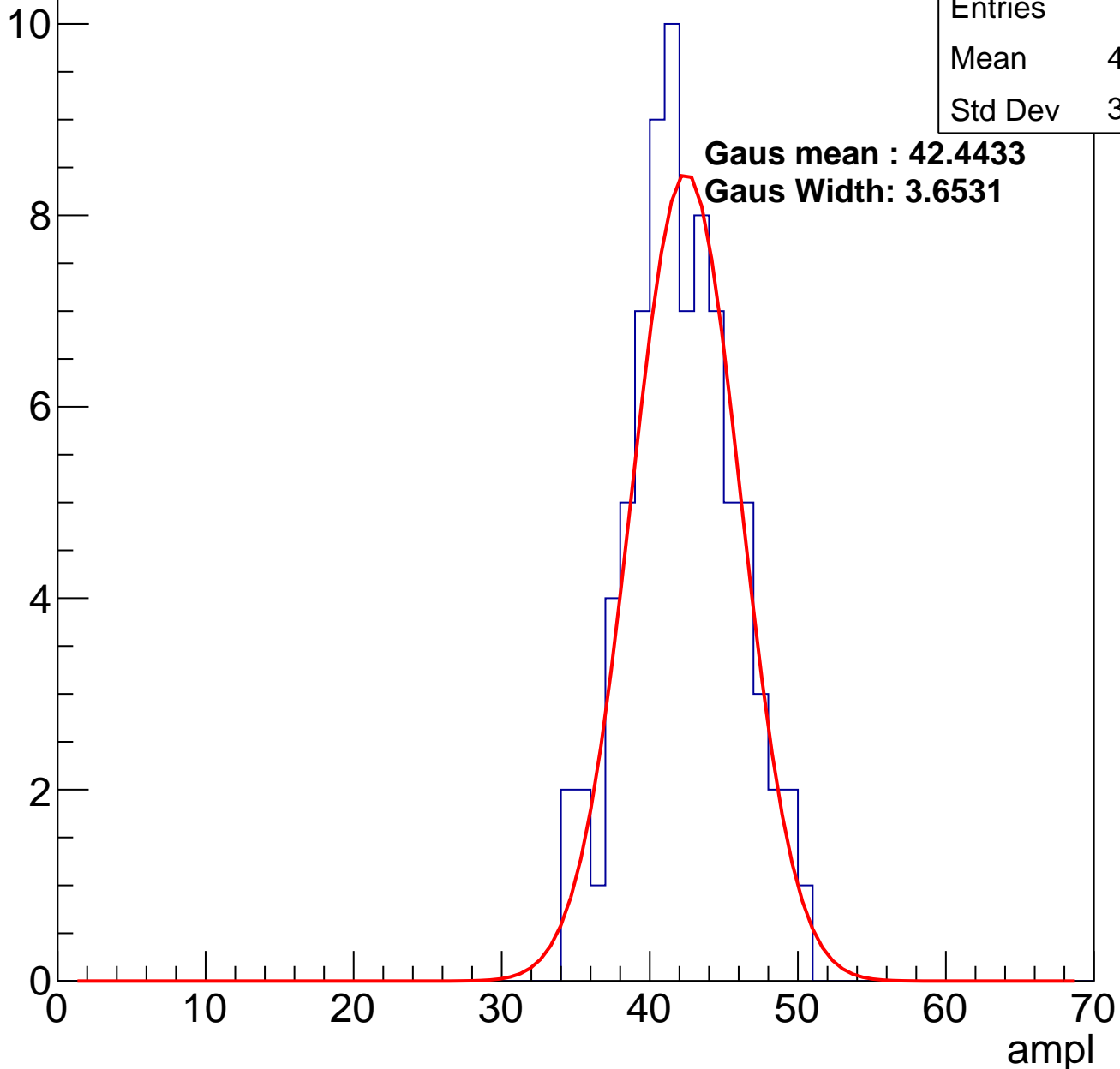
calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	41.76
Std Dev	3.565

Gaus mean : 42.4433

Gaus Width: 3.6531

Entry

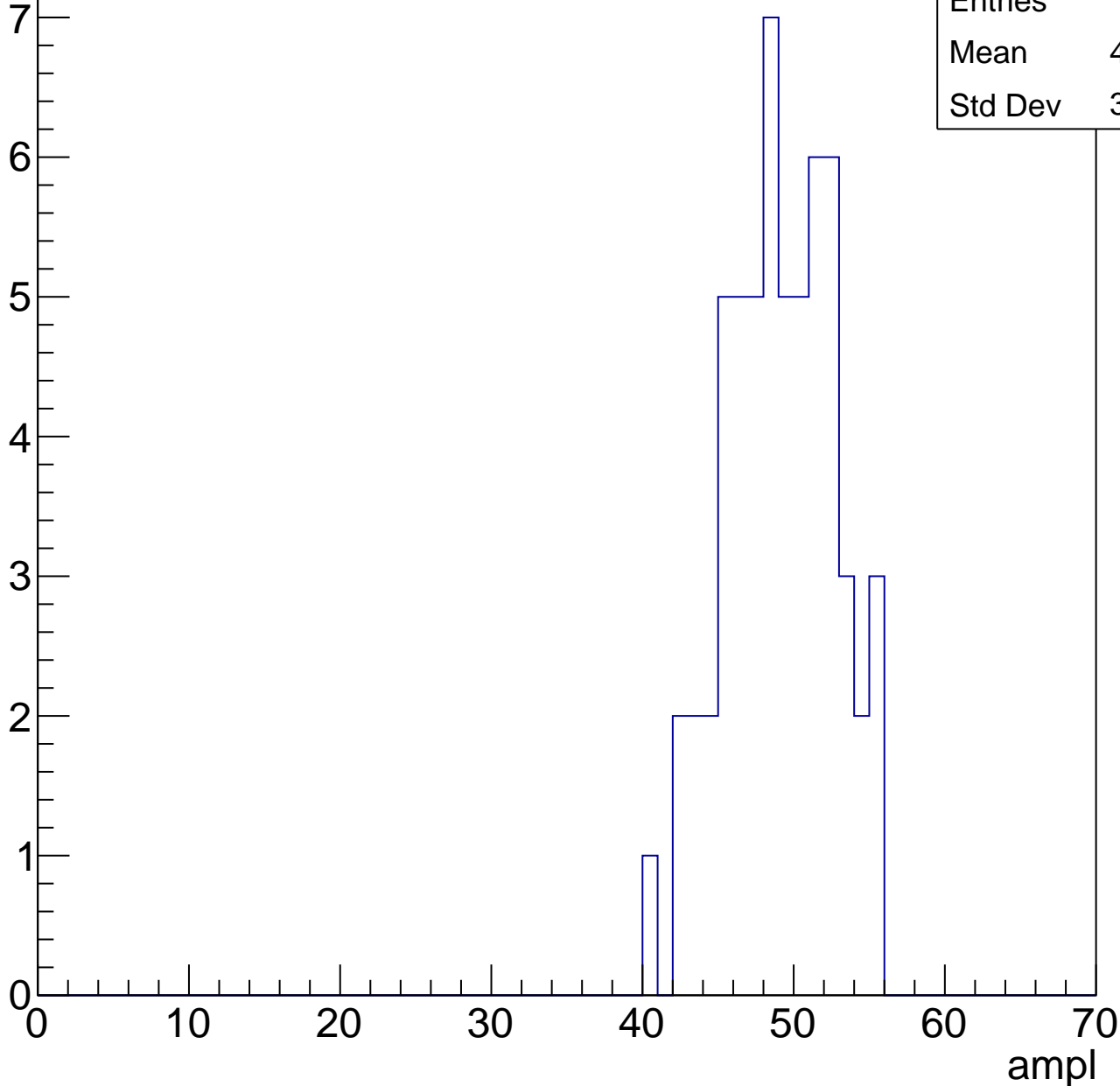


B1L101S, U4-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

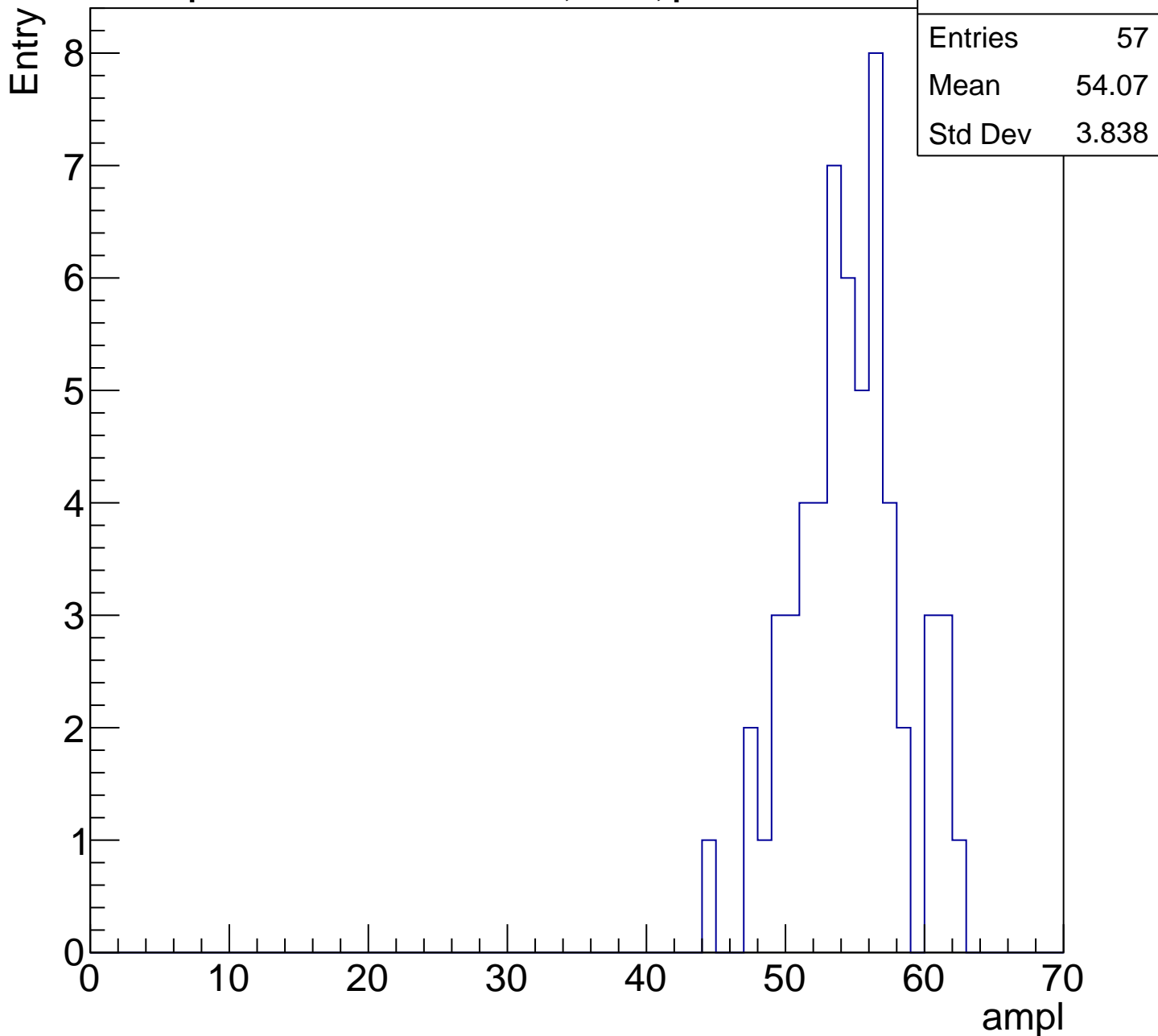
Entry

Entries	59
Mean	48.63
Std Dev	3.527



B1L101S, U4-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

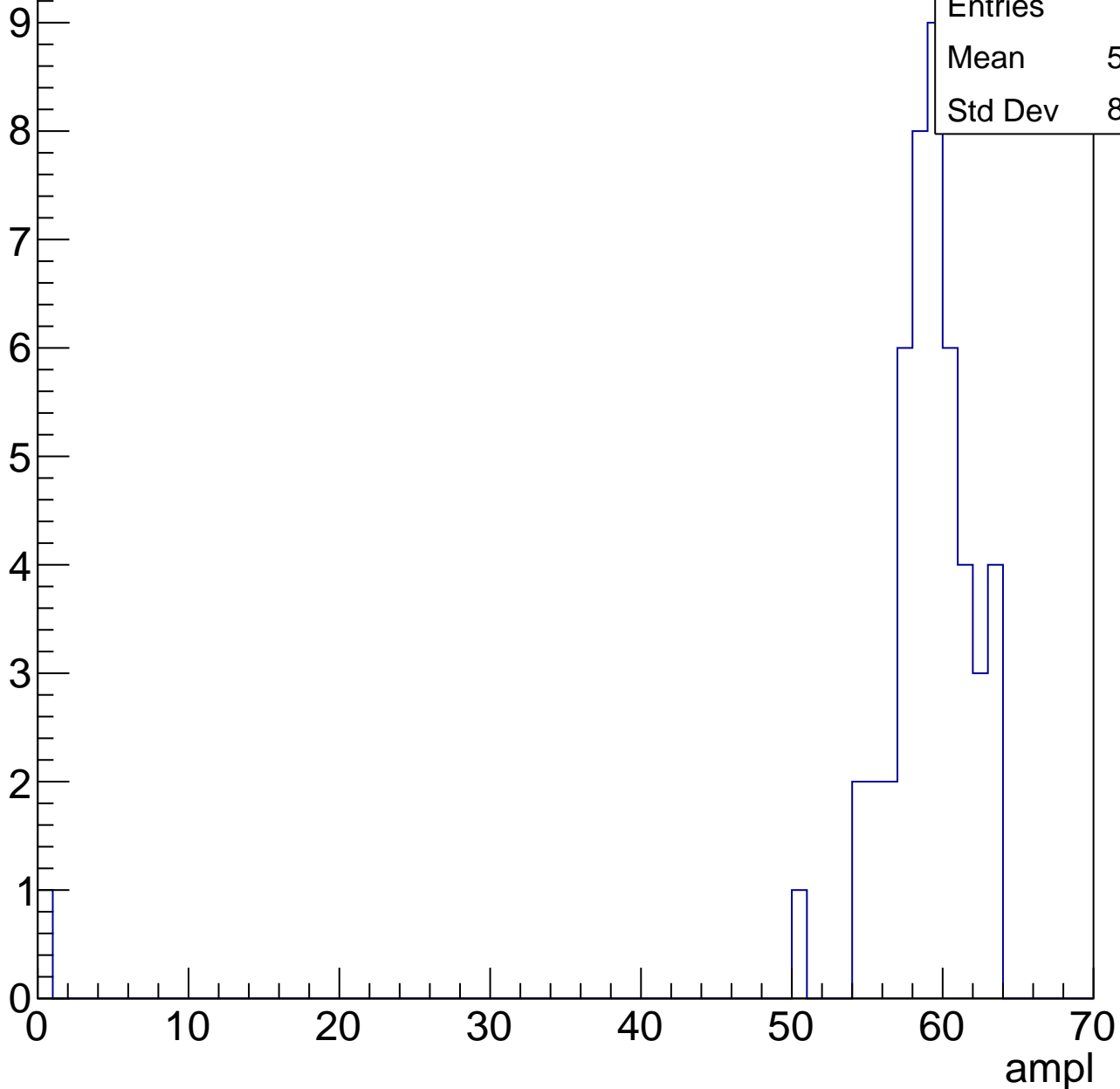


B1L101S, U4-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.48
Std Dev	8.775

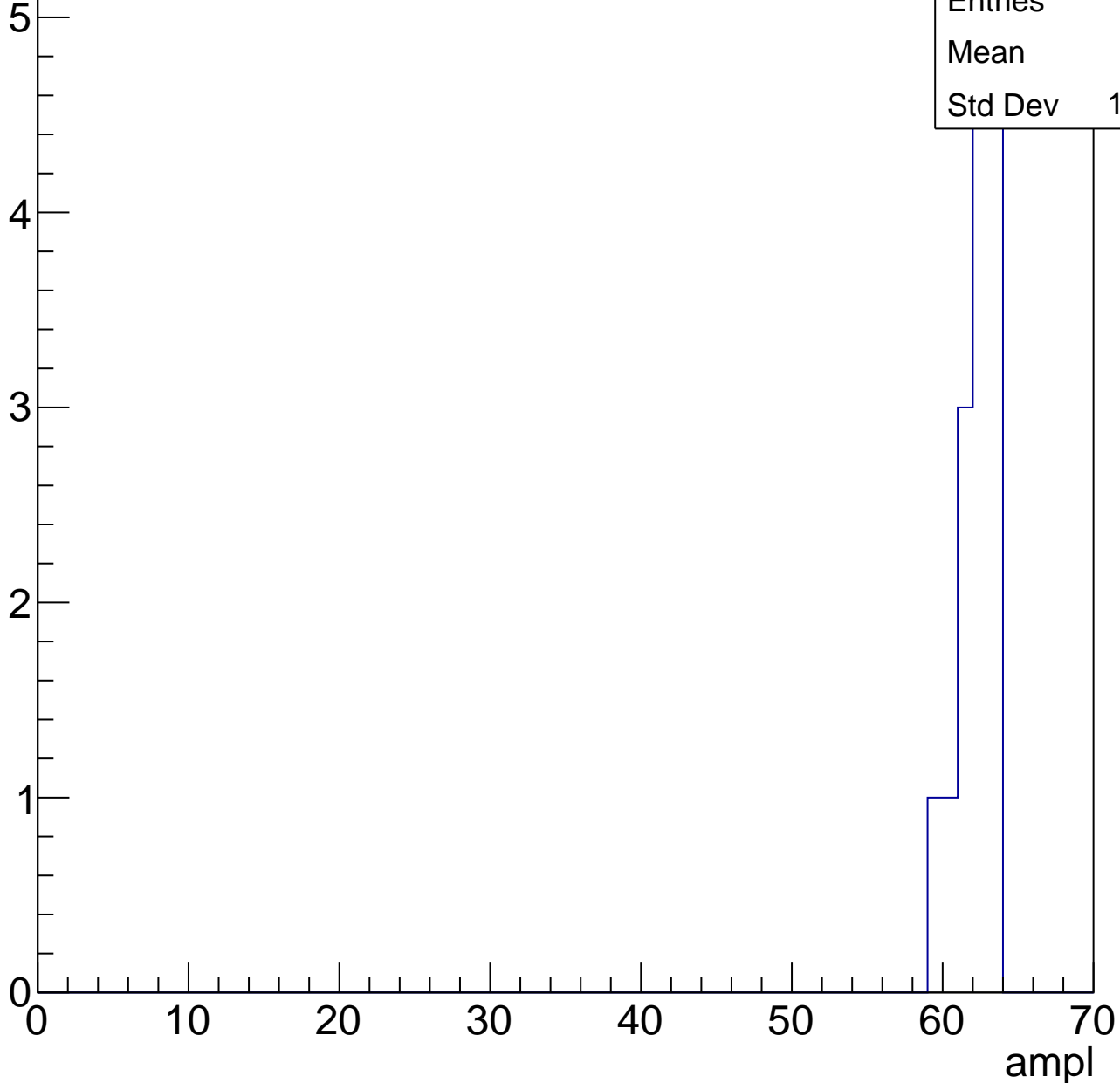


B1L101S, U4-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

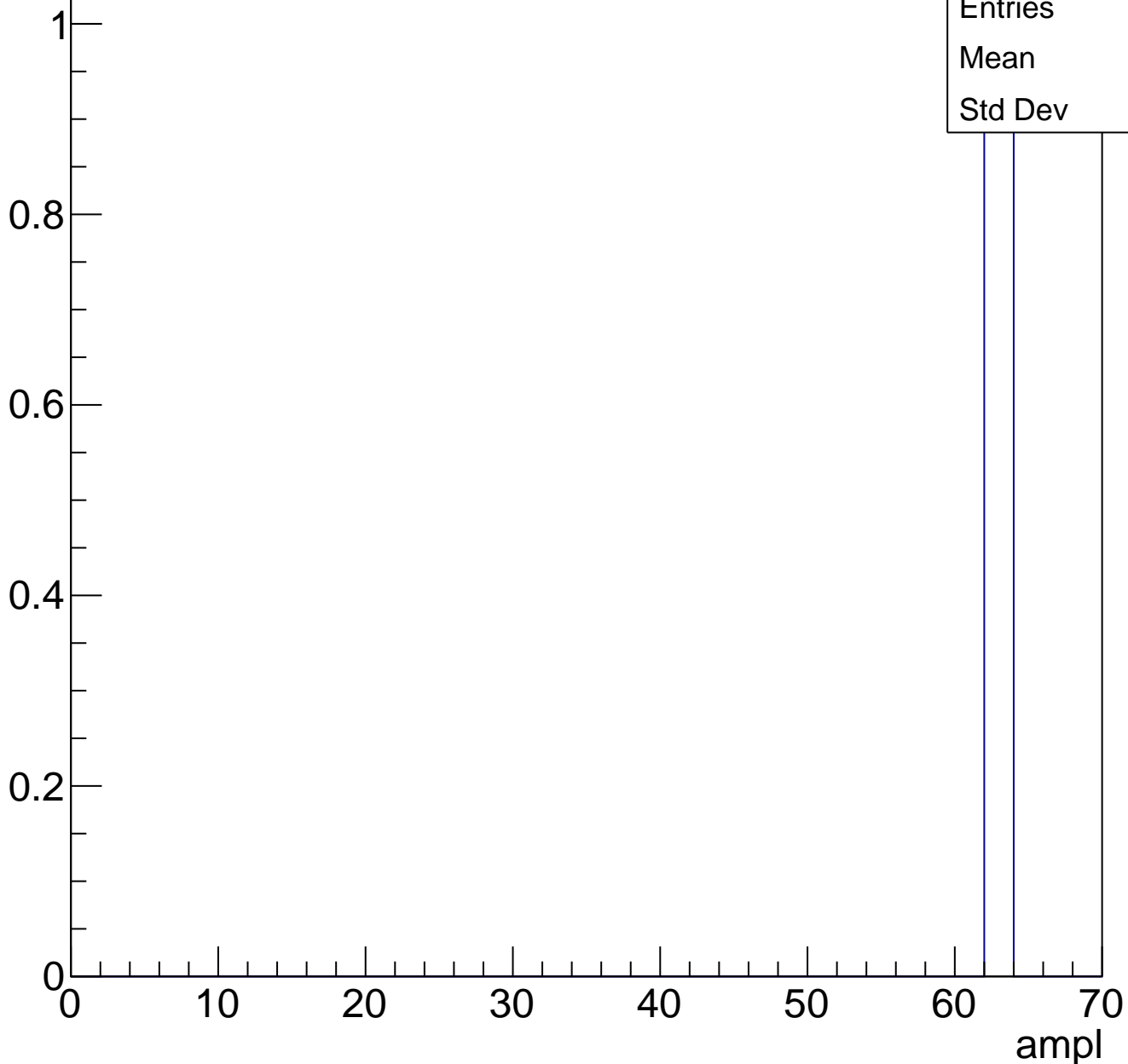
Entries	15
Mean	61.8
Std Dev	1.166



B1L101S, U4-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch53, adc0

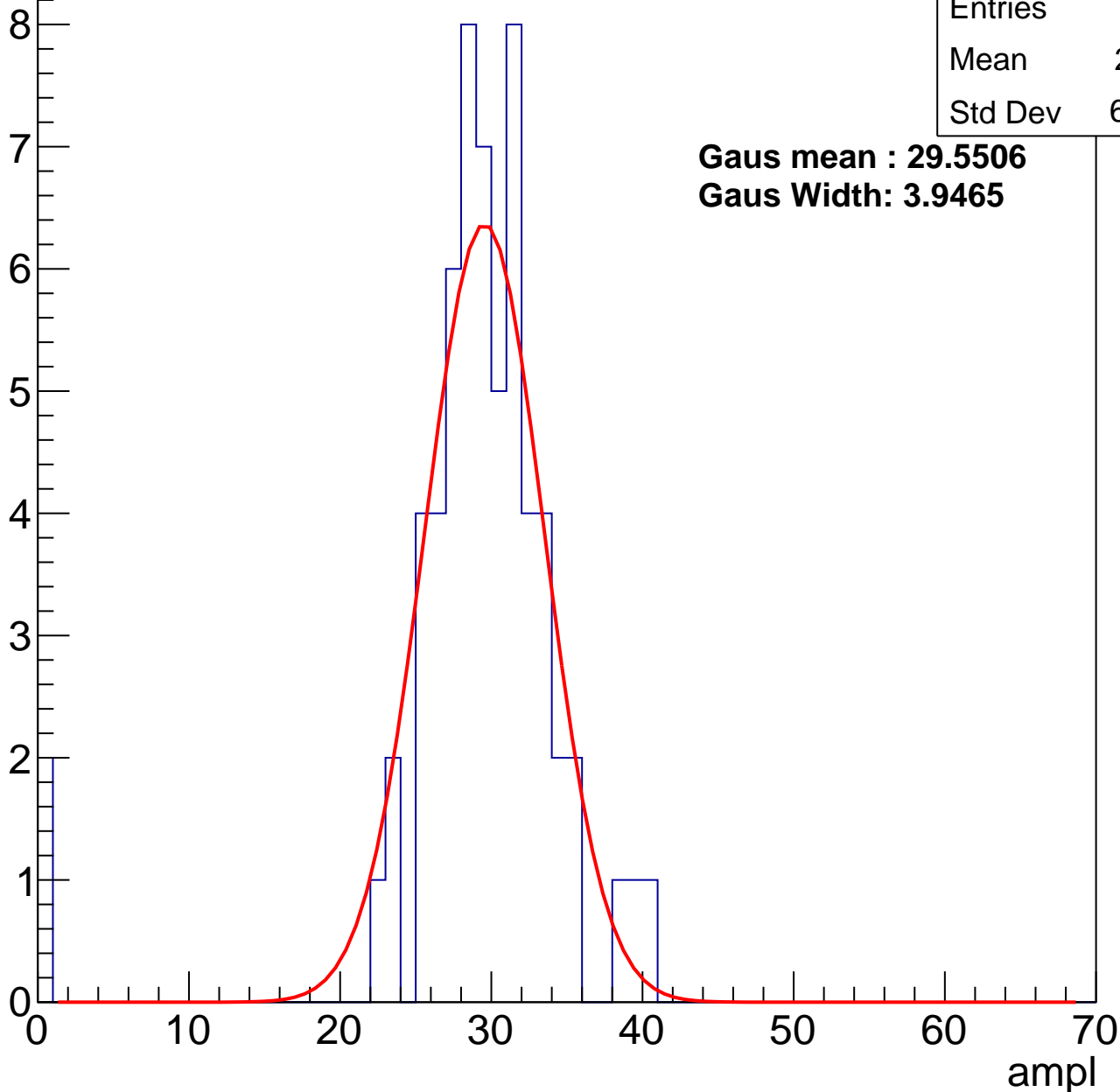
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.61
Std Dev	6.333

Gaus mean : 29.5506

Gaus Width: 3.9465



B1L101S, U4-ch53, adc1

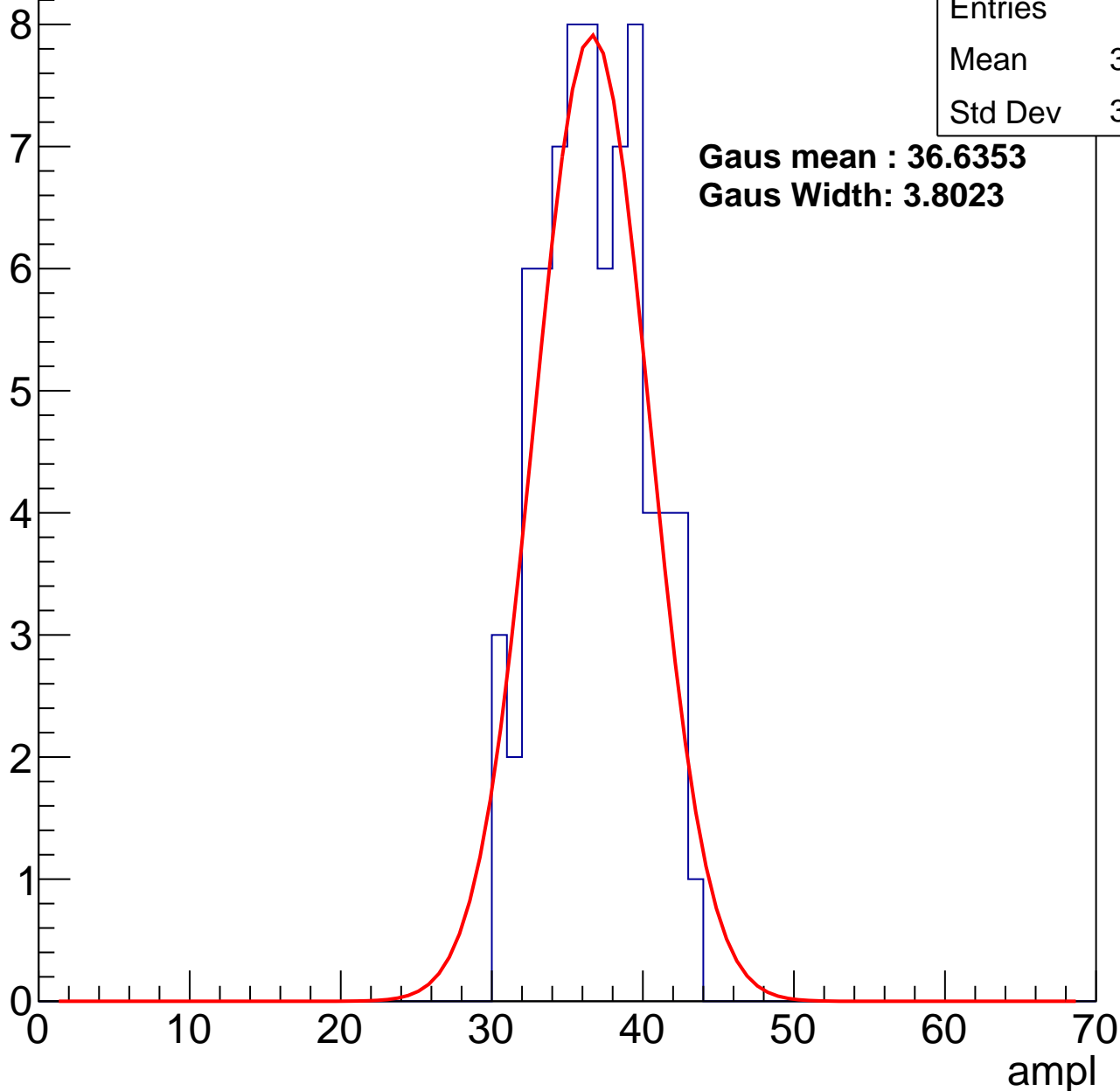
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.26
Std Dev	3.292

Gaus mean : 36.6353

Gaus Width: 3.8023



B1L101S, U4-ch53, adc2

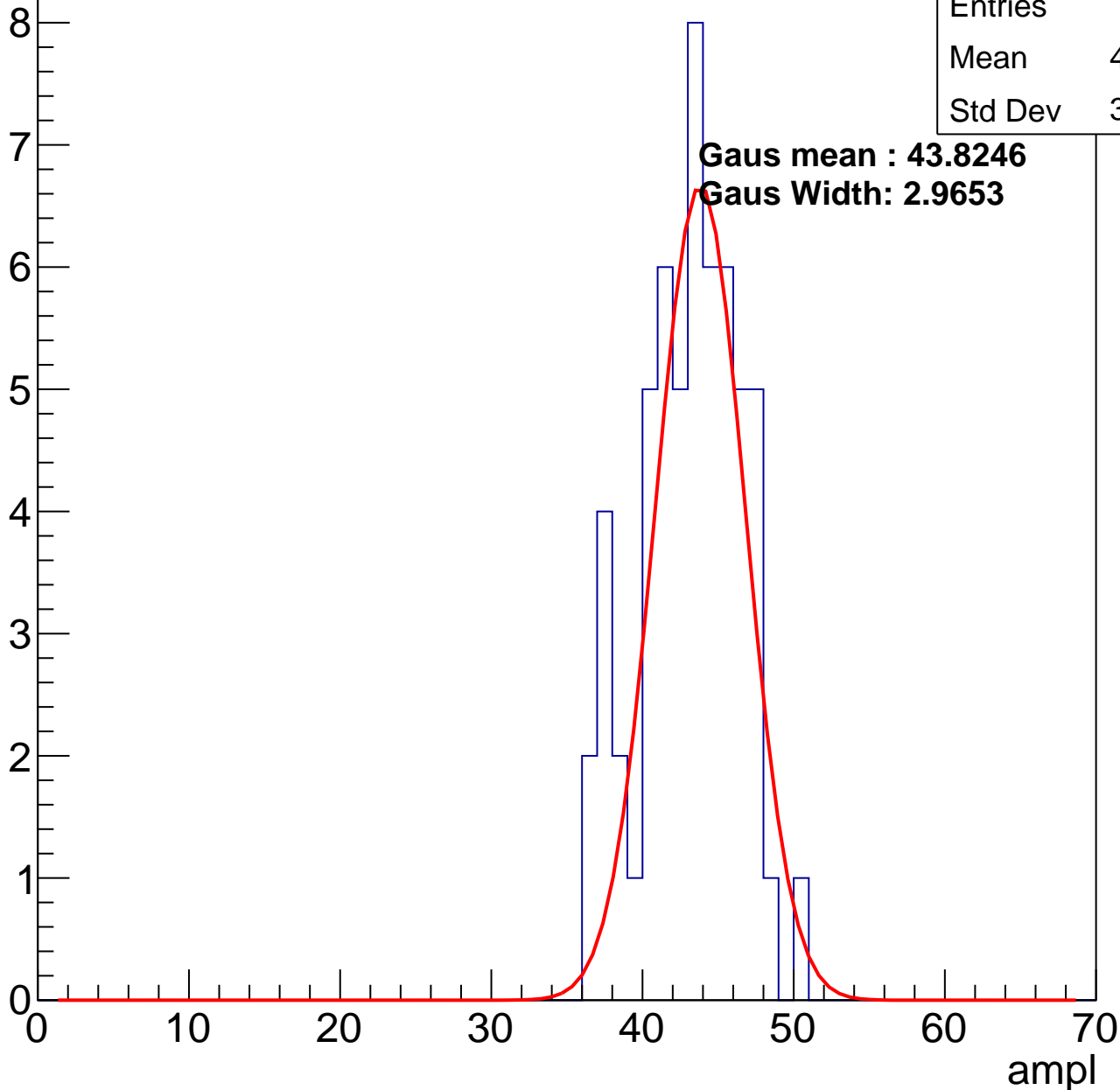
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.67
Std Dev	3.257

Gaus mean : 43.8246

Gaus Width: 2.9653

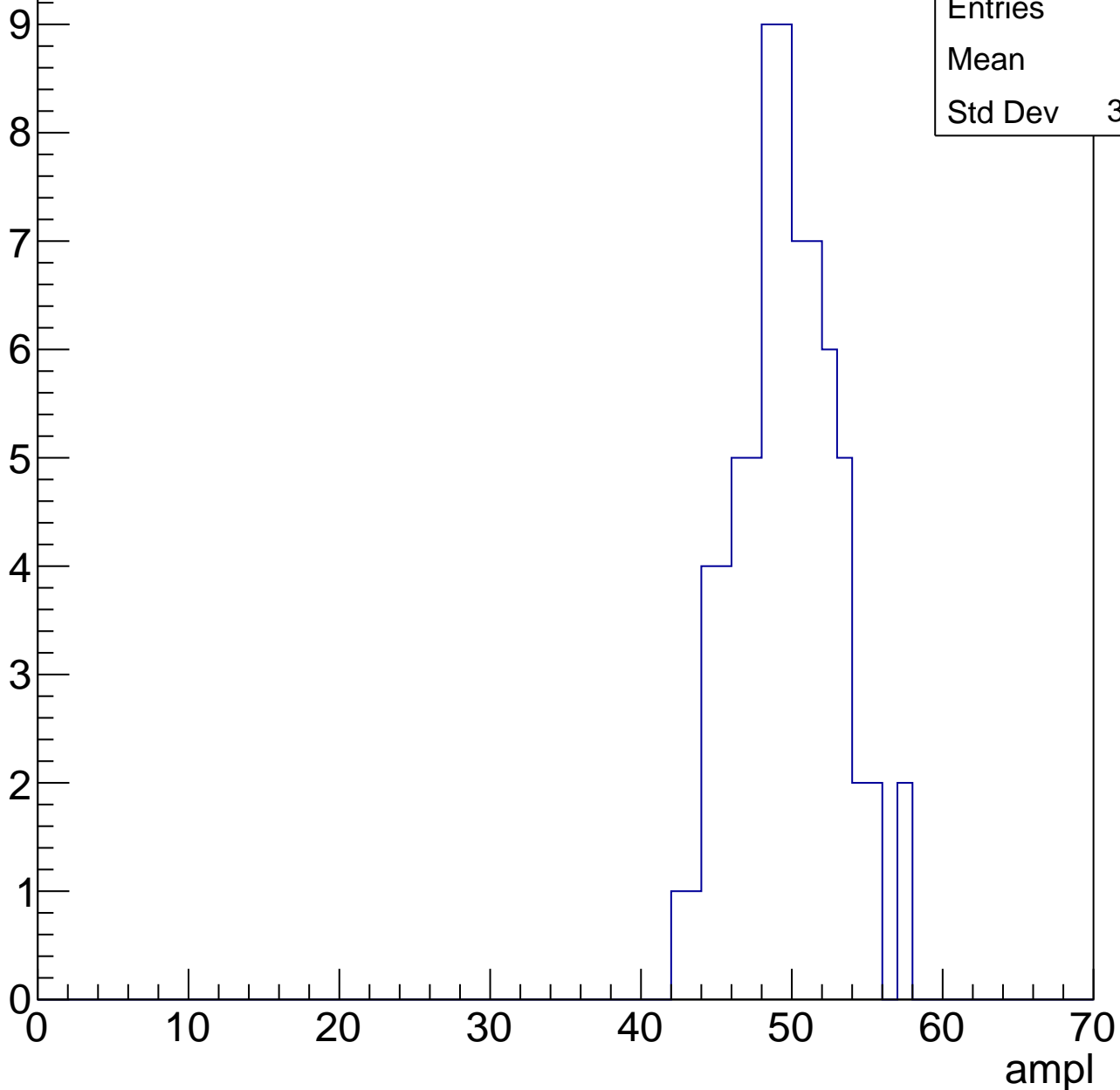


B1L101S, U4-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

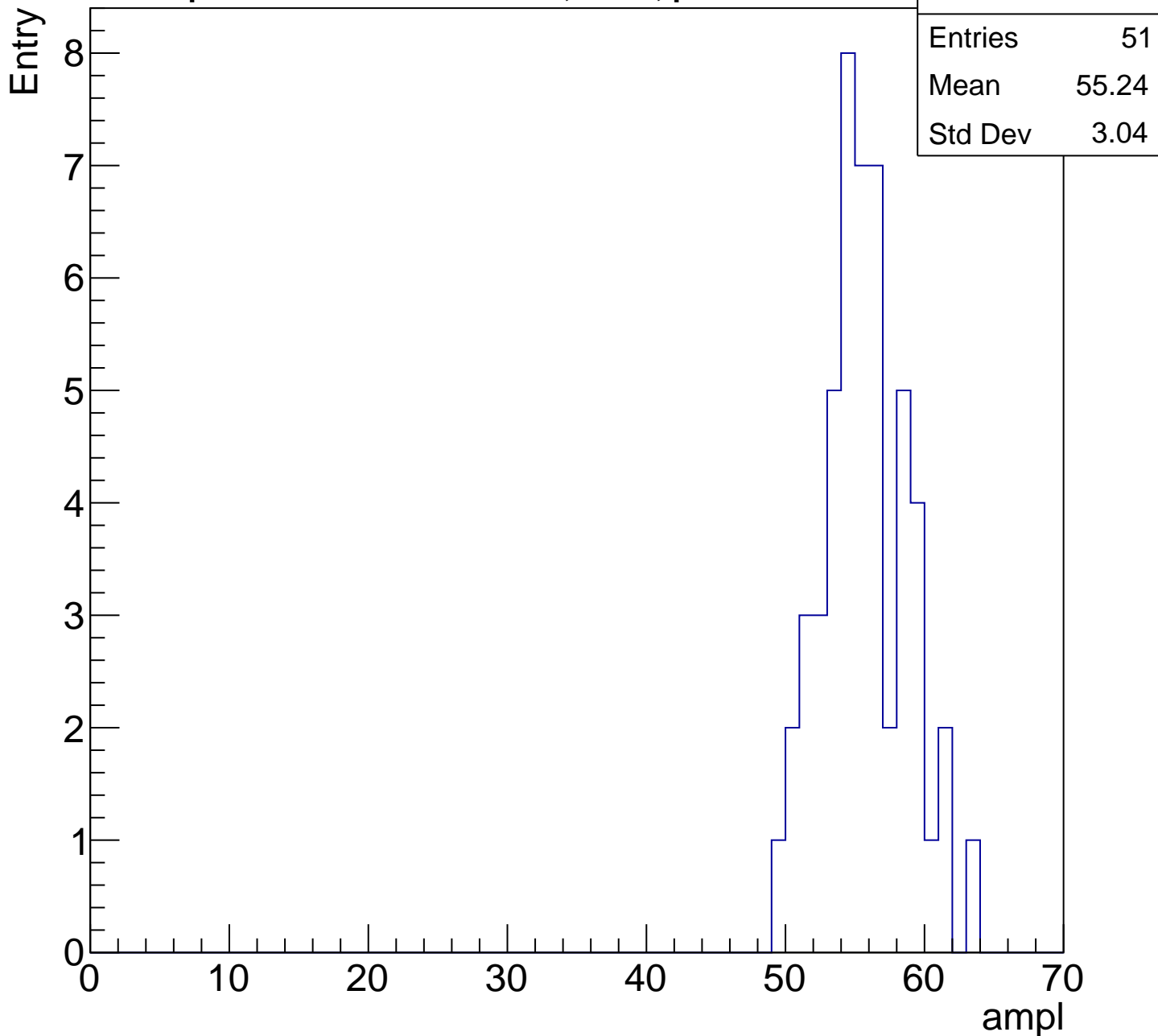
Entry

Entries	69
Mean	49.2
Std Dev	3.273



B1L101S, U4-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch53, adc5

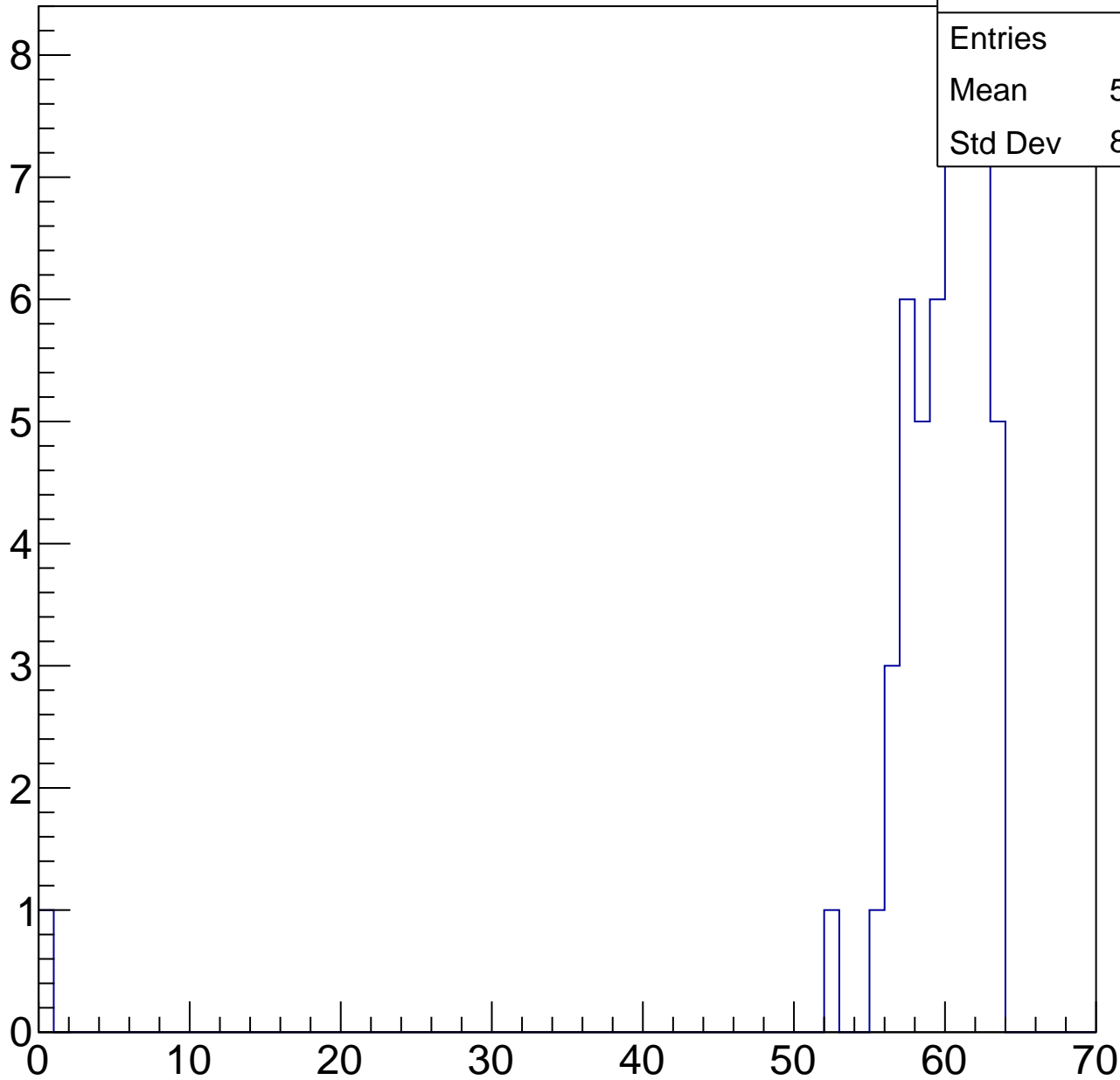
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.46
Std Dev	8.525

ampl



B1L101S, U4-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch54, adc0

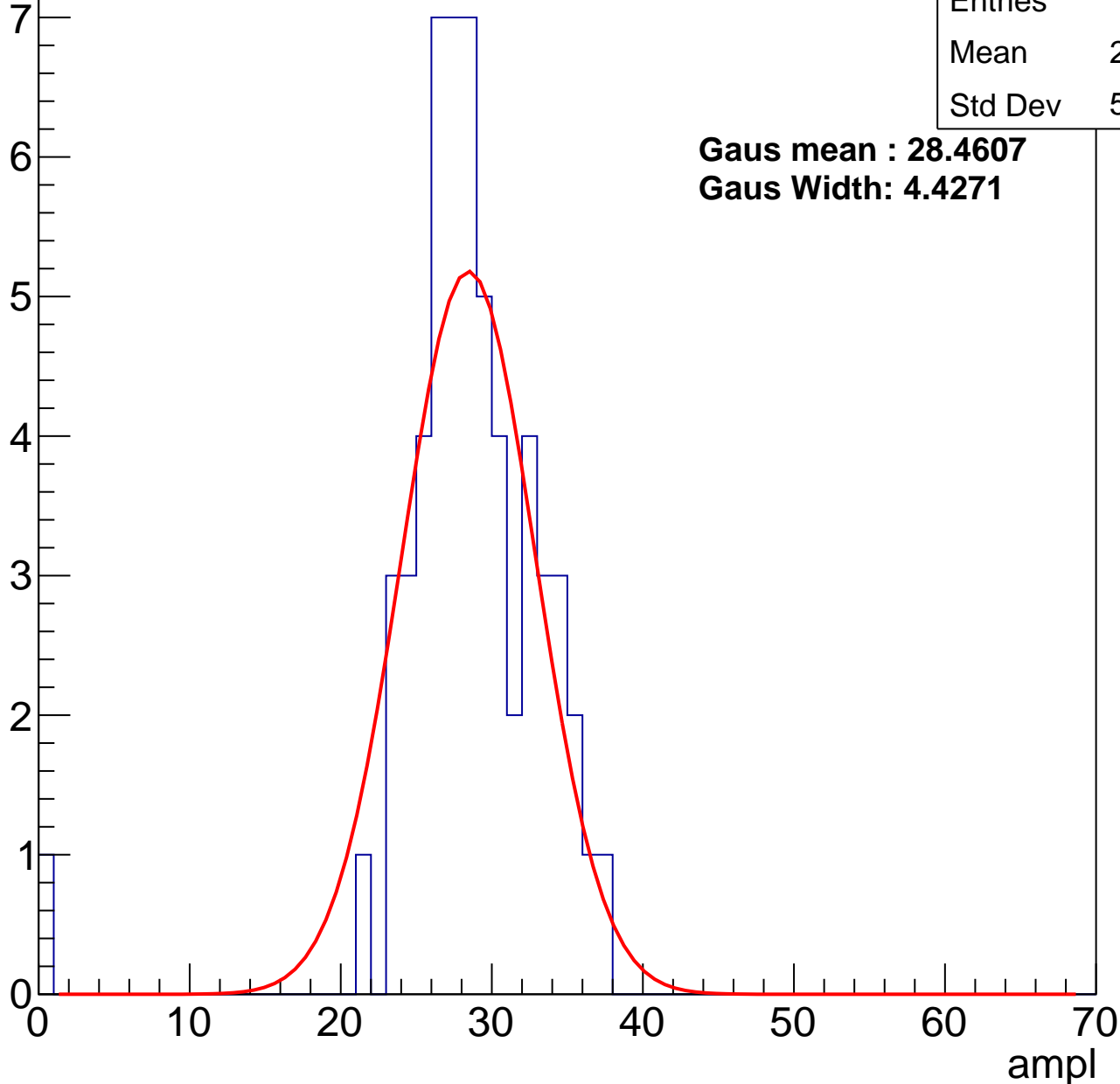
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	28.07
Std Dev	5.179

Gaus mean : 28.4607

Gaus Width: 4.4271



B1L101S, U4-ch54, adc1

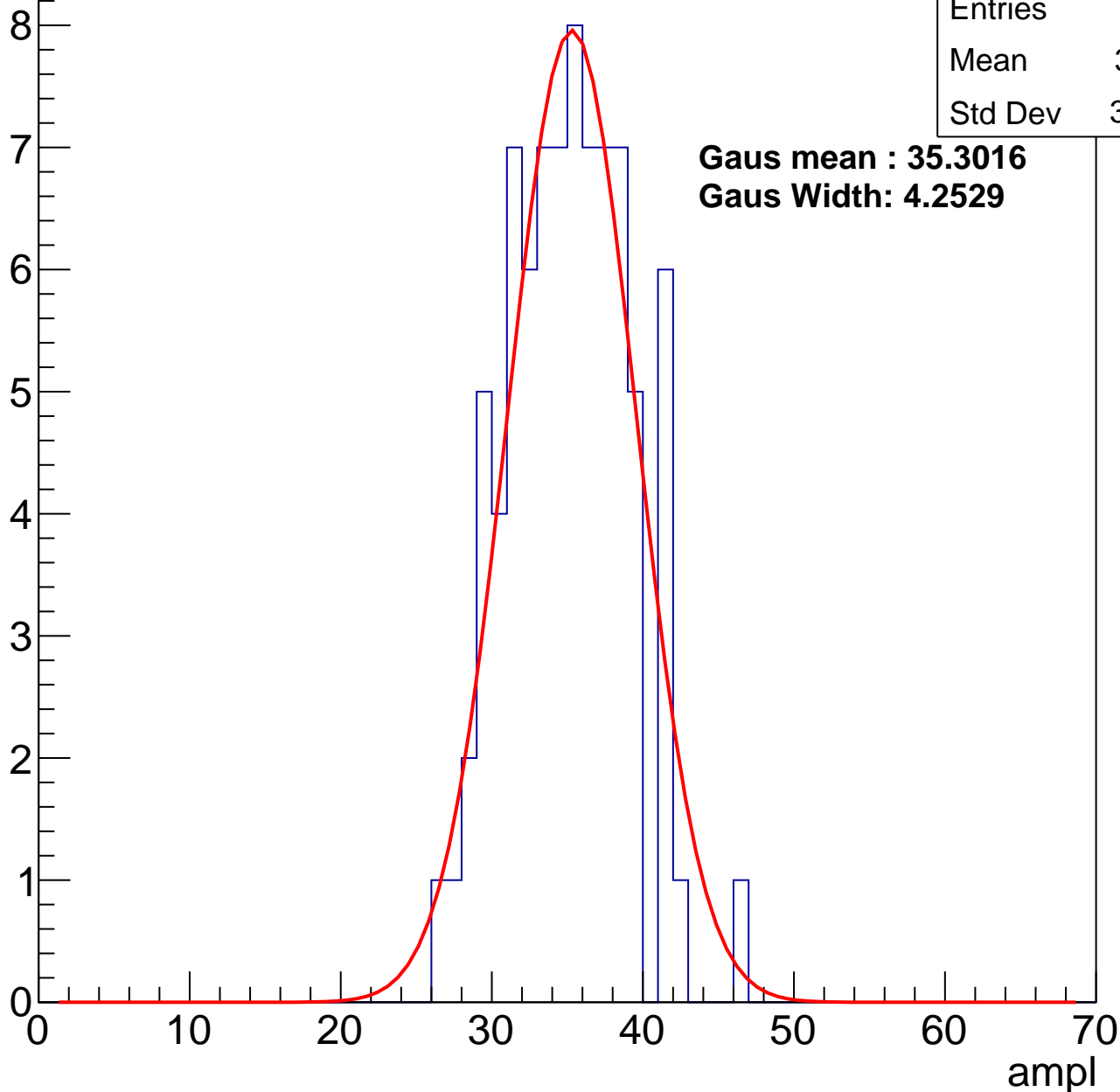
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	34.61
Std Dev	3.944

Gaus mean : 35.3016

Gaus Width: 4.2529



B1L101S, U4-ch54, adc2

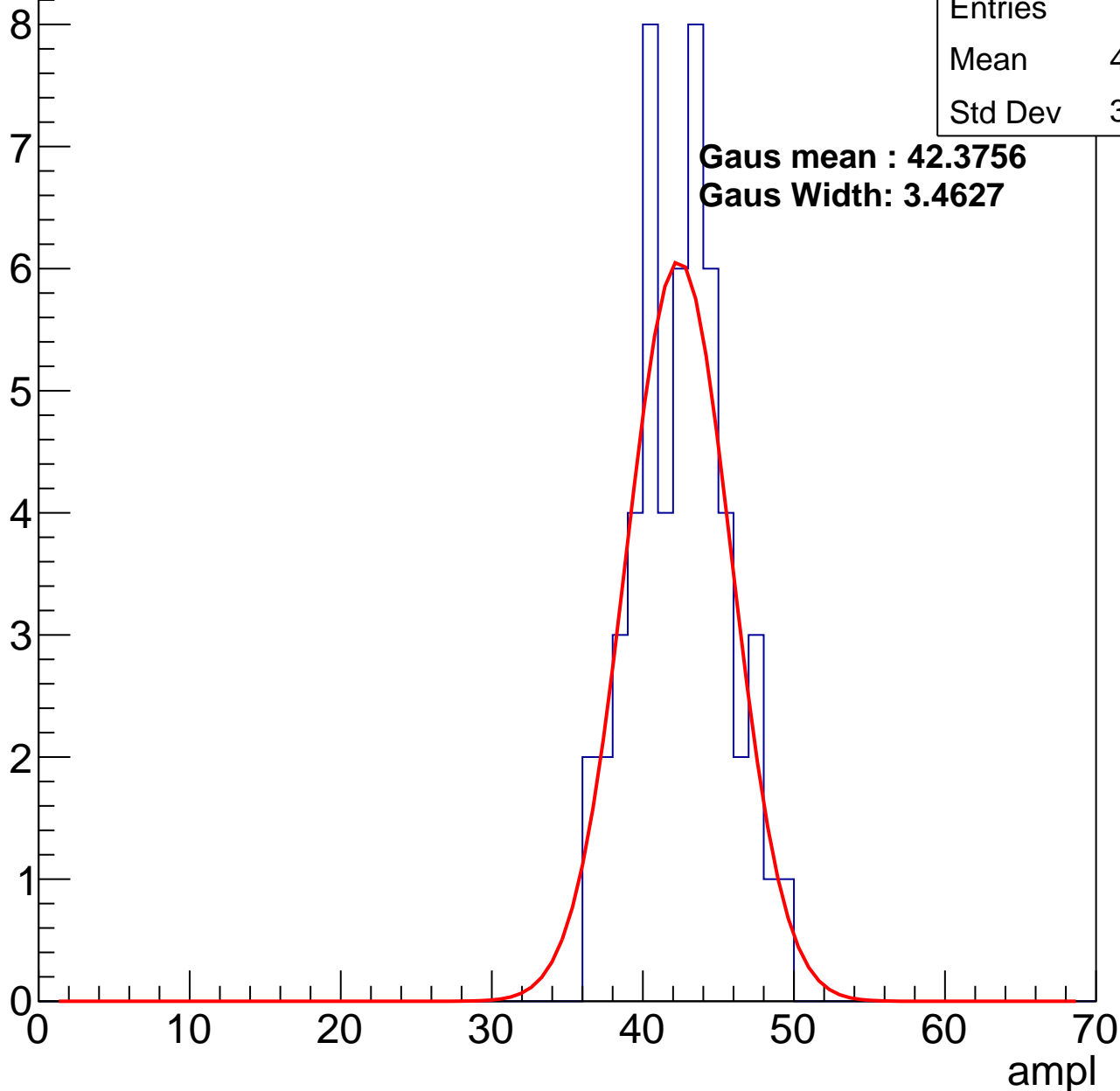
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	42.04
Std Dev	3.049

Gaus mean : 42.3756

Gaus Width: 3.4627

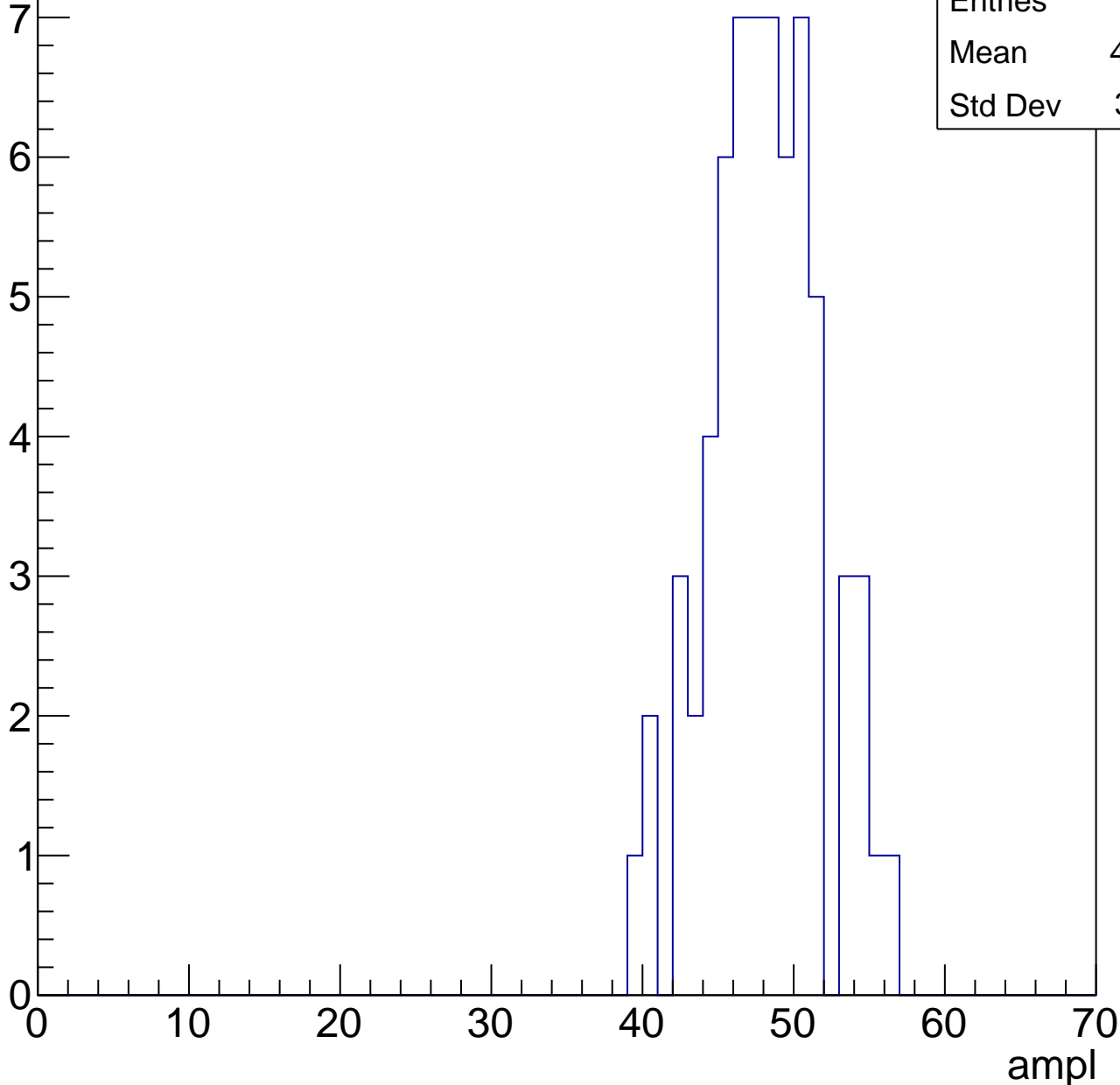


B1L101S, U4-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

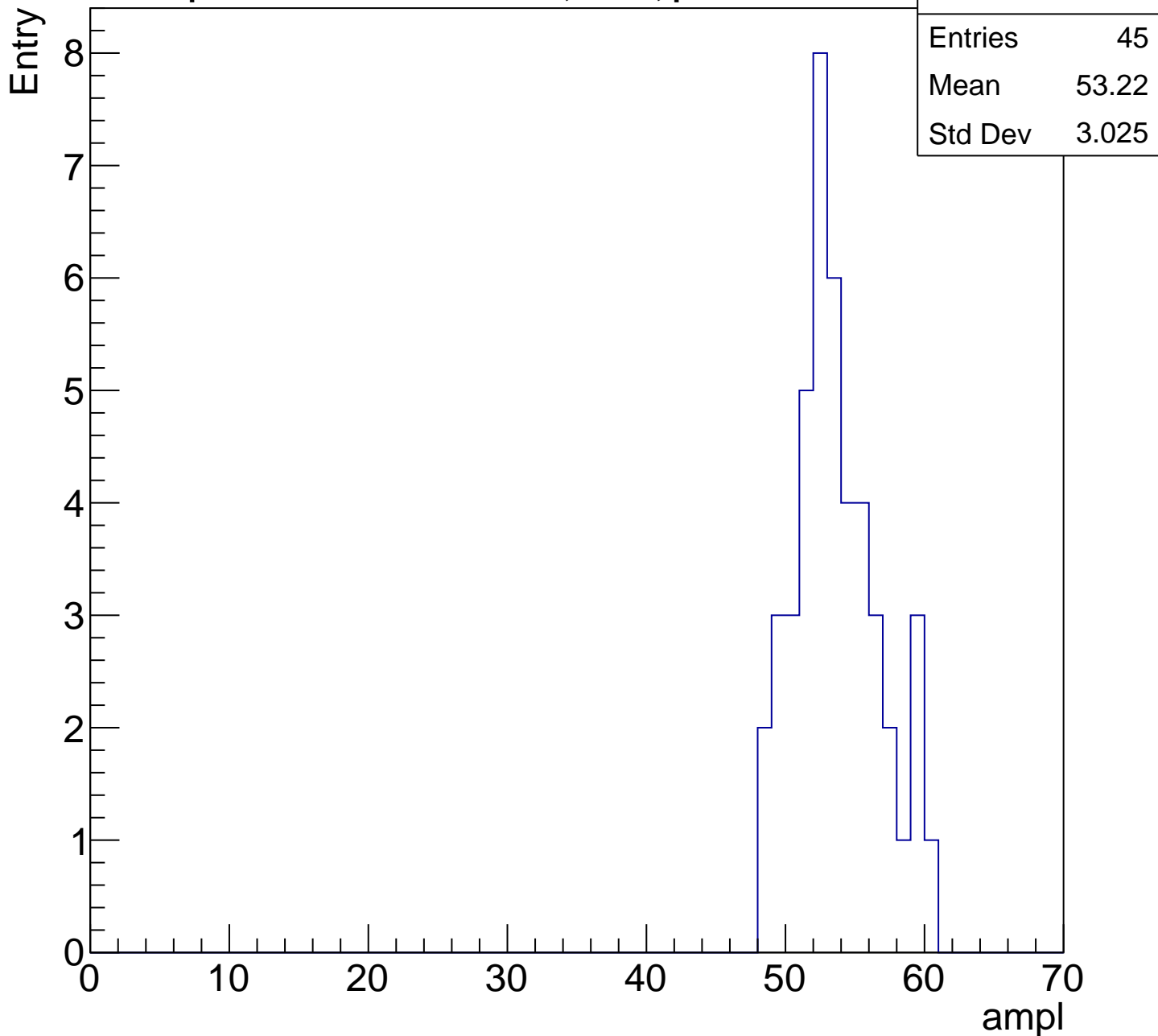
Entry

Entries	65
Mean	47.62
Std Dev	3.691



B1L101S, U4-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch54, adc5

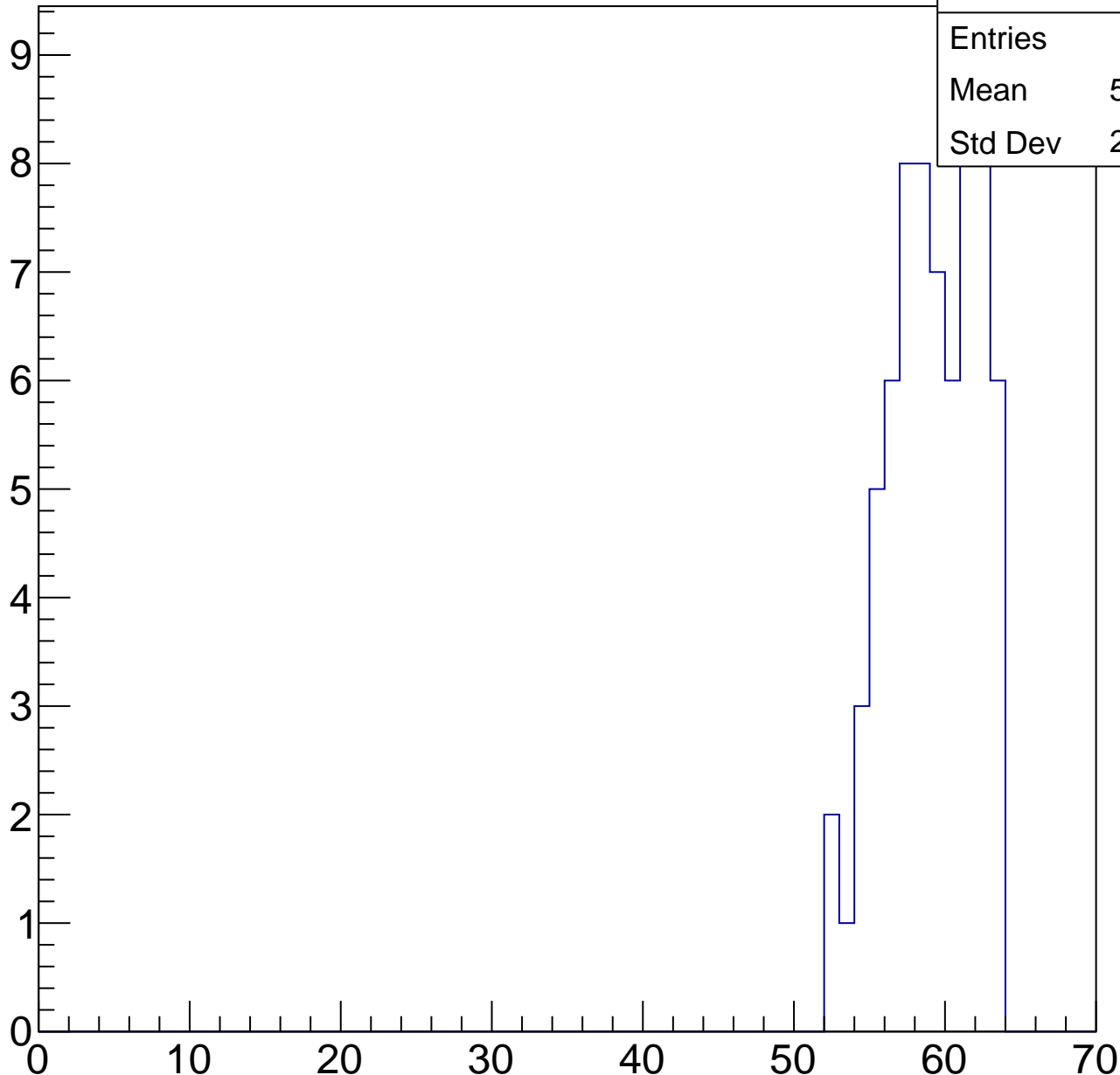
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	58.64
Std Dev	2.909

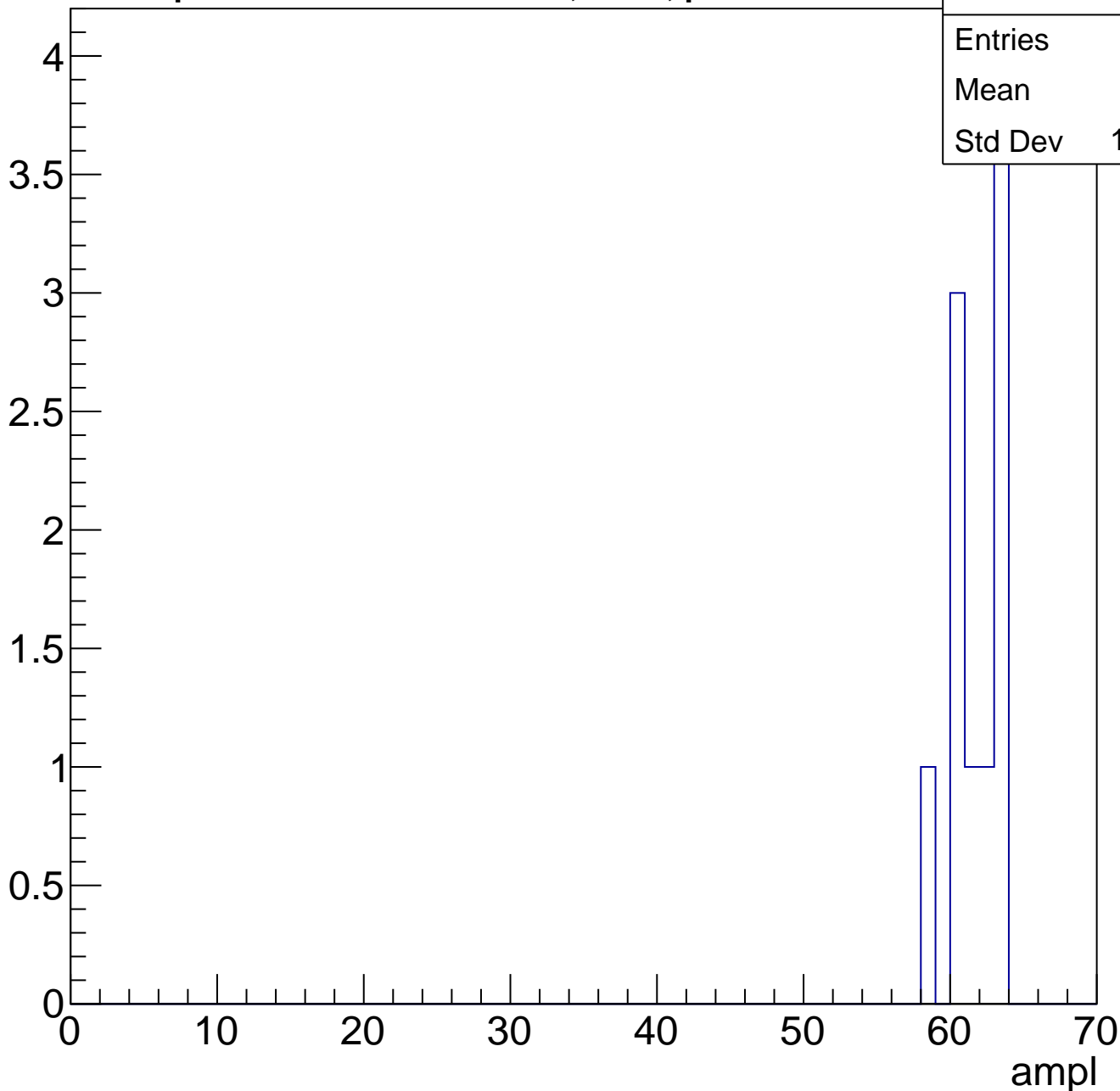
ampl



B1L101S, U4-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

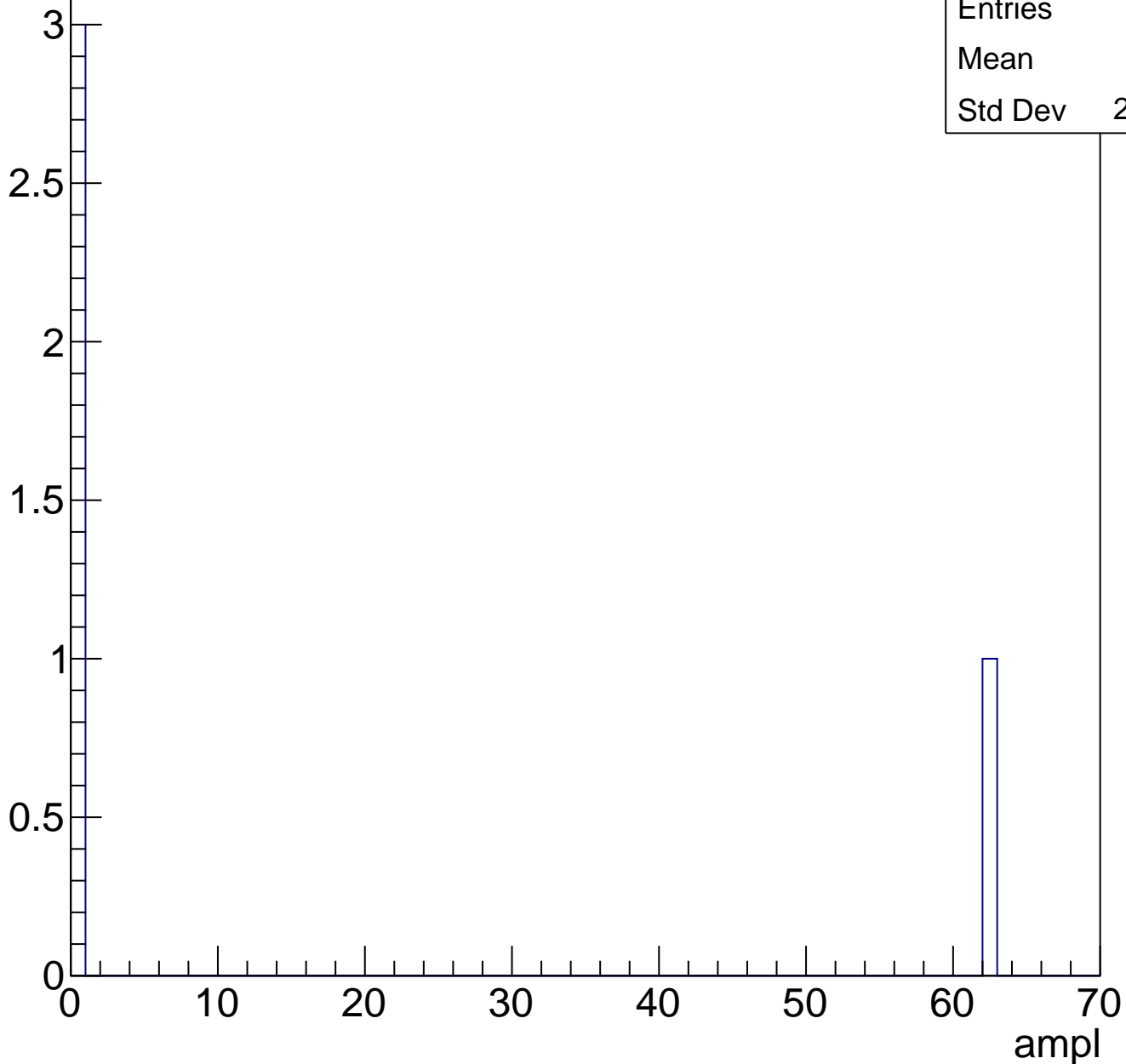
Entry



B1L101S, U4-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch55, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	27.16
Std Dev	6.416

Gaus mean : 28.0762

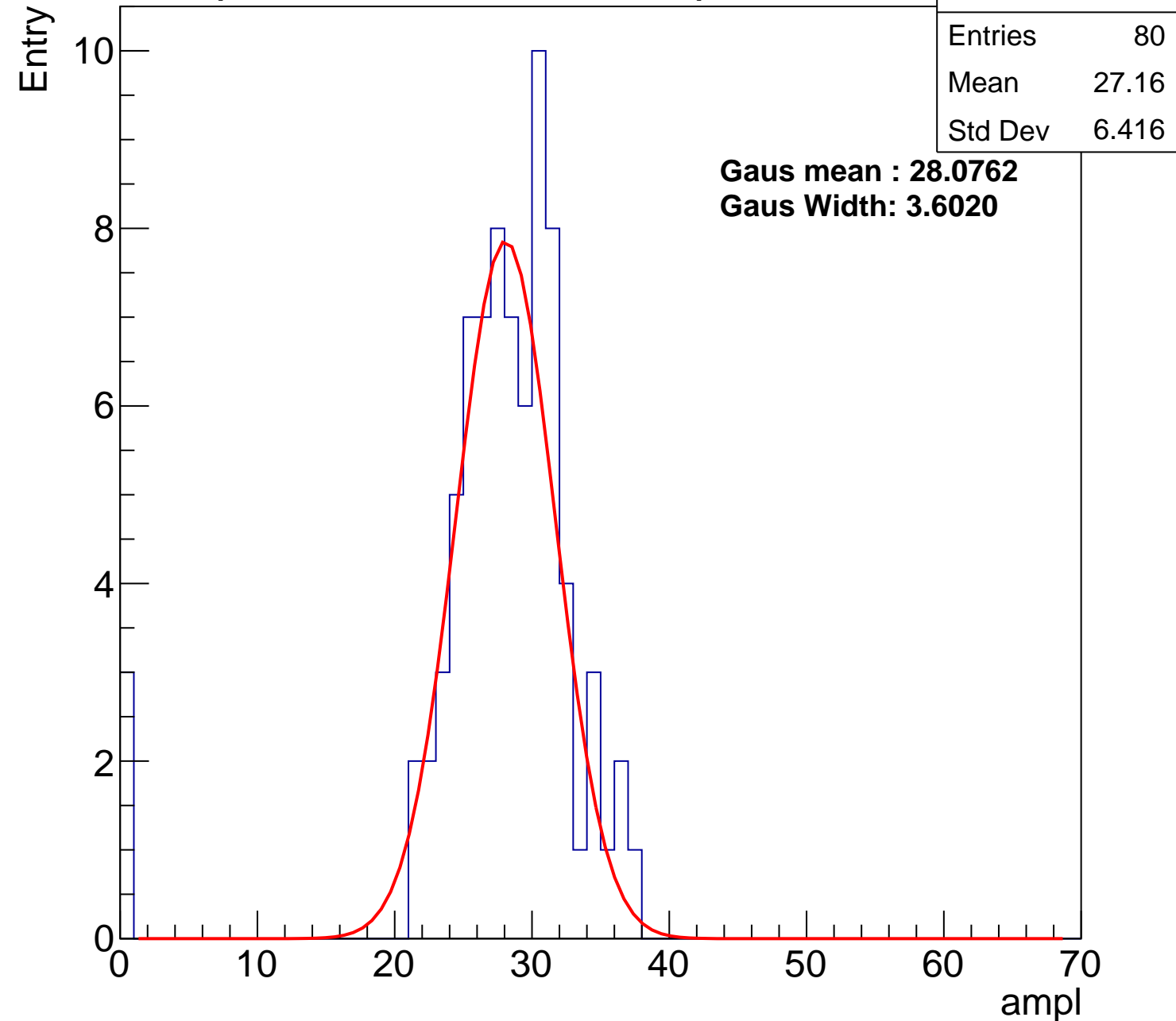
Gaus Width: 3.6020

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch55, adc1

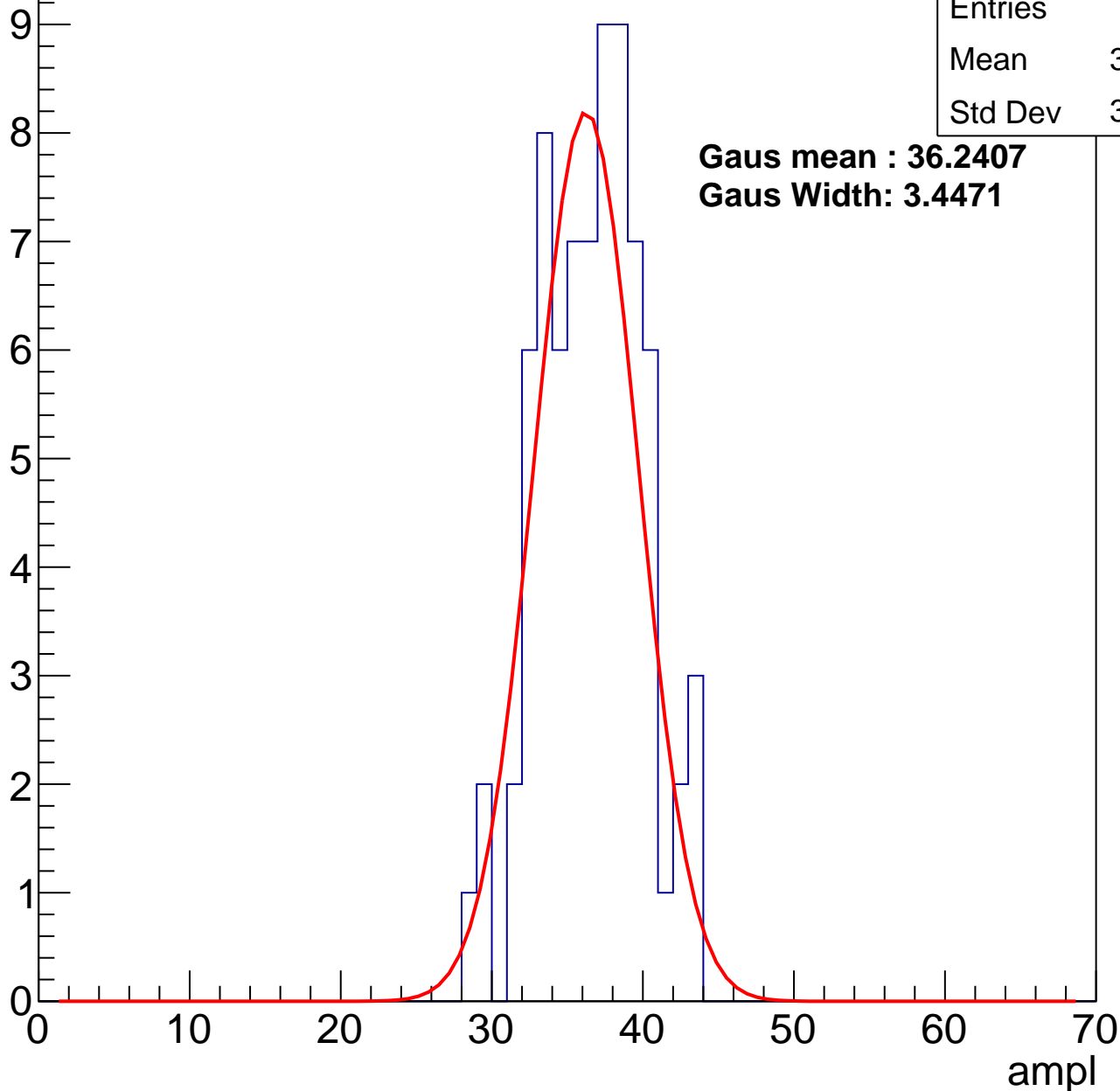
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.14
Std Dev	3.359

Gaus mean : 36.2407

Gaus Width: 3.4471



B1L101S, U4-ch55, adc2

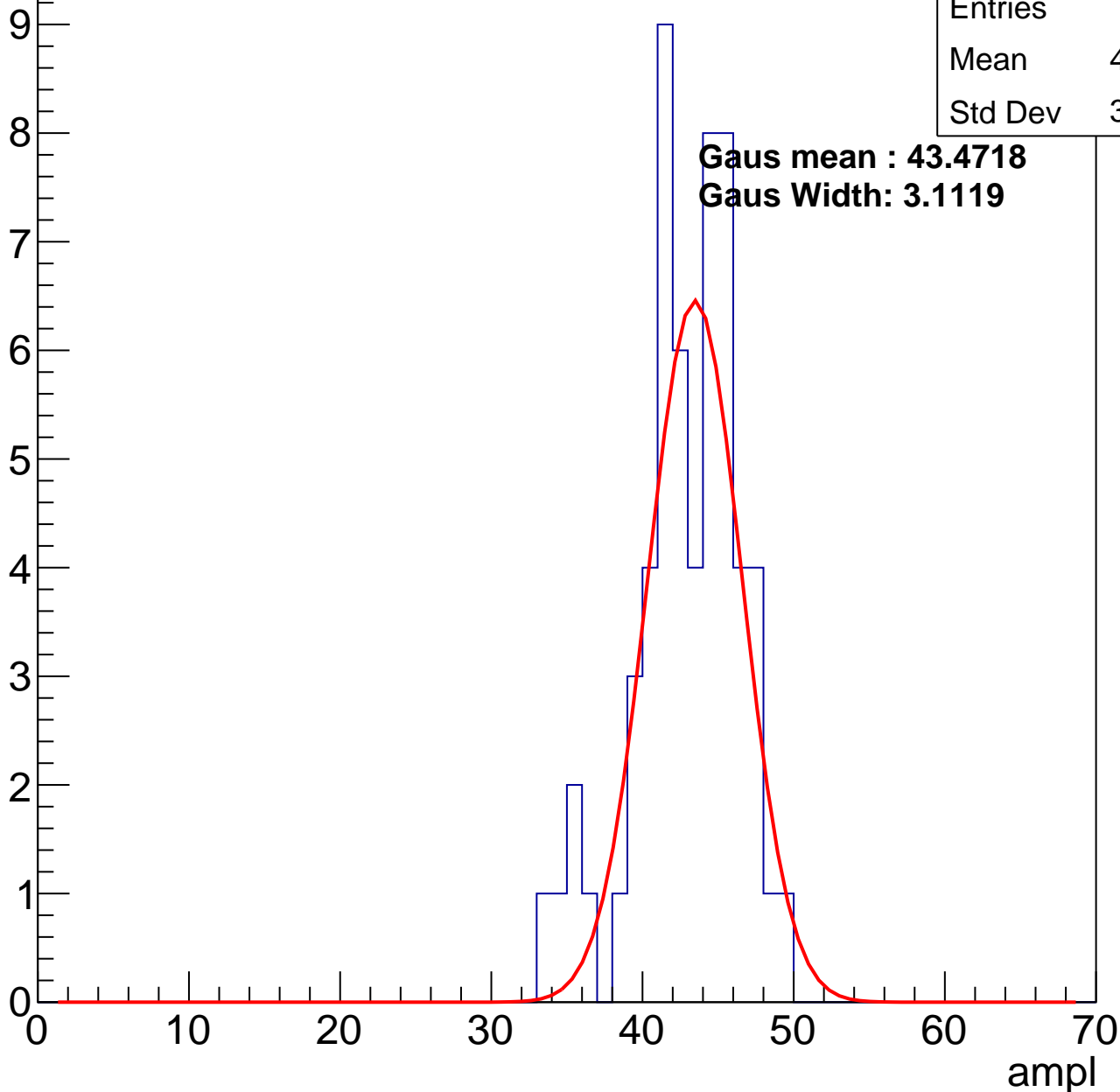
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.45
Std Dev	3.455

Gaus mean : 43.4718

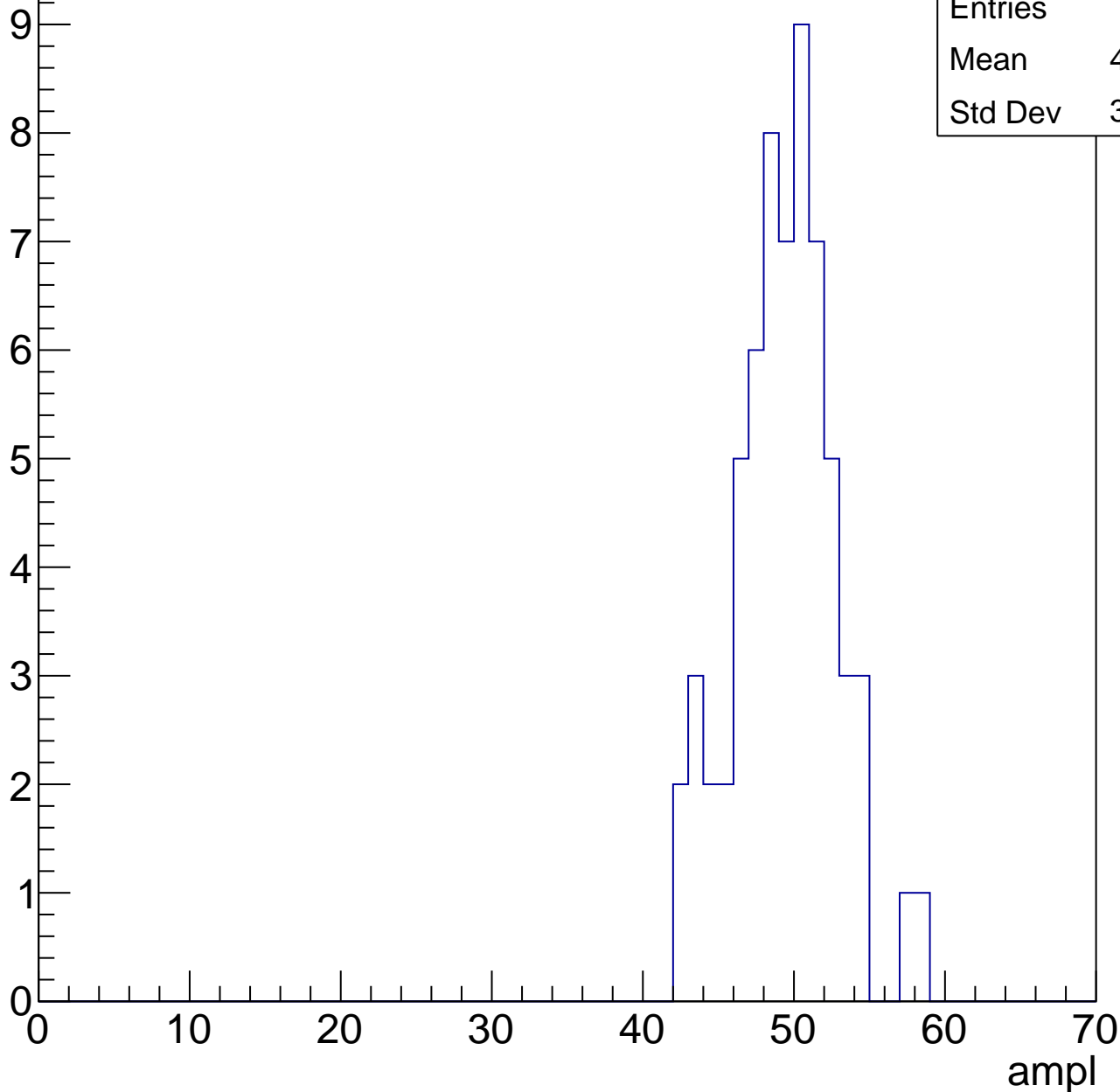
Gaus Width: 3.1119



B1L101S, U4-ch55, adc3

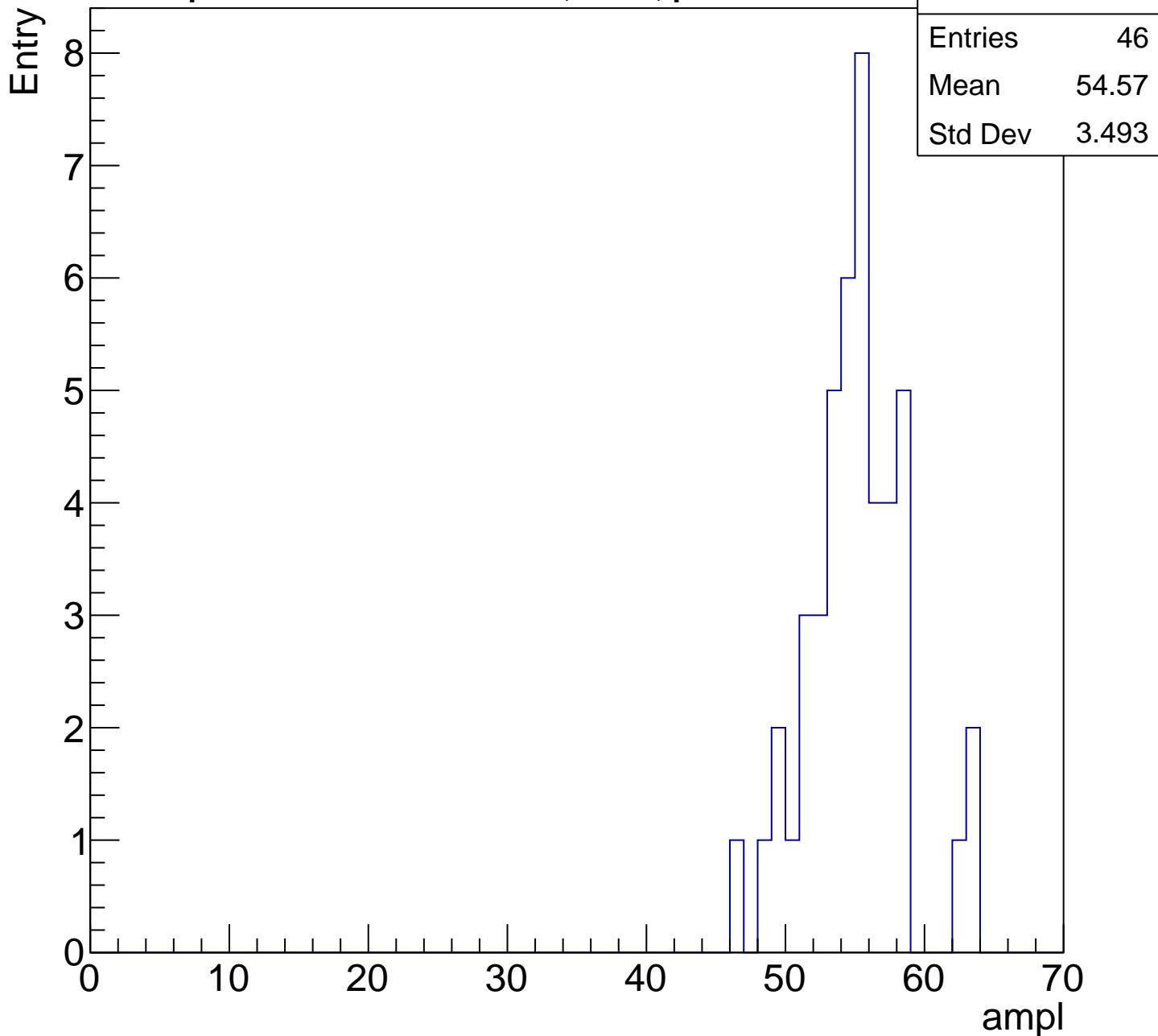
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

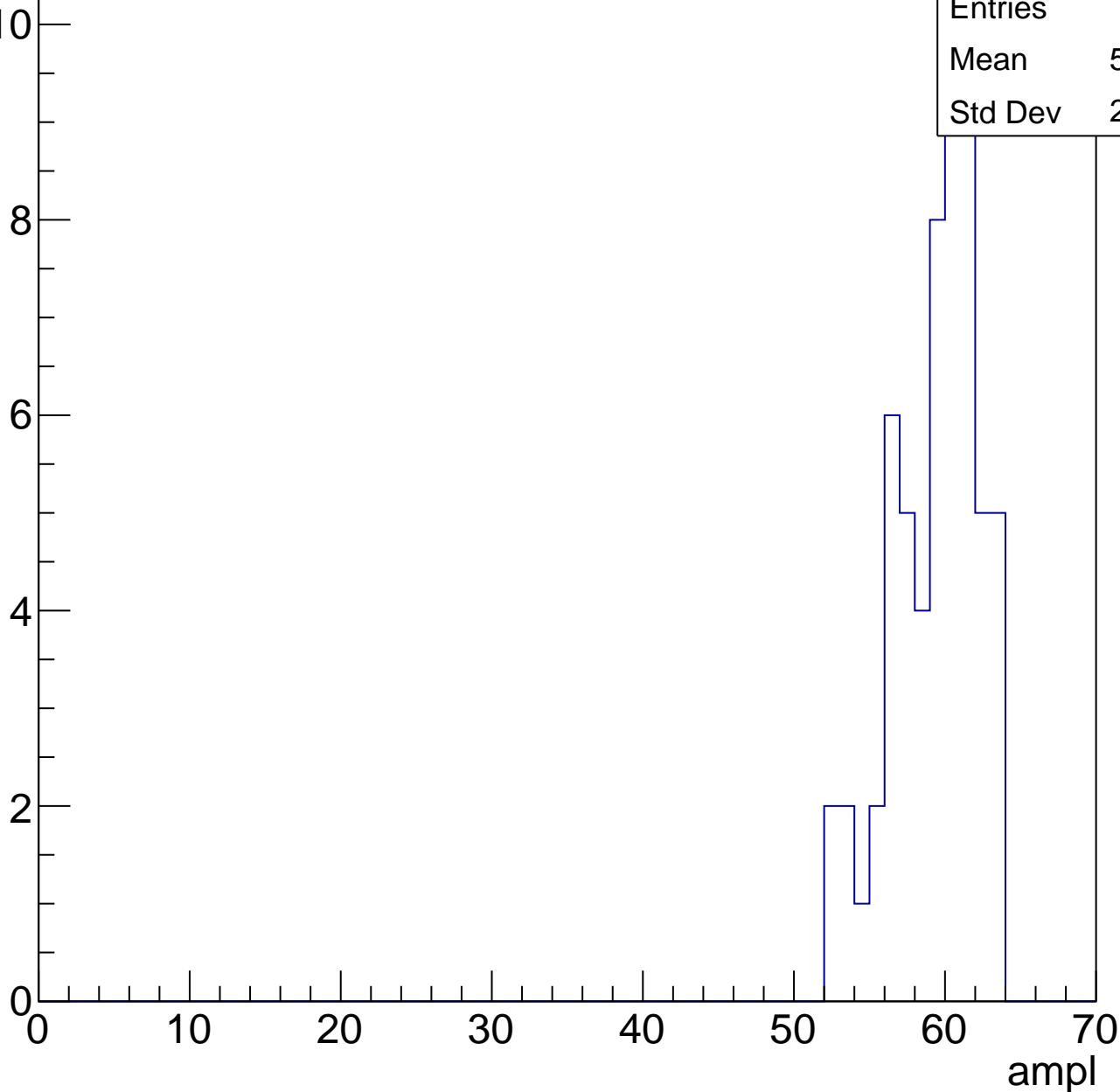


B1L101S, U4-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

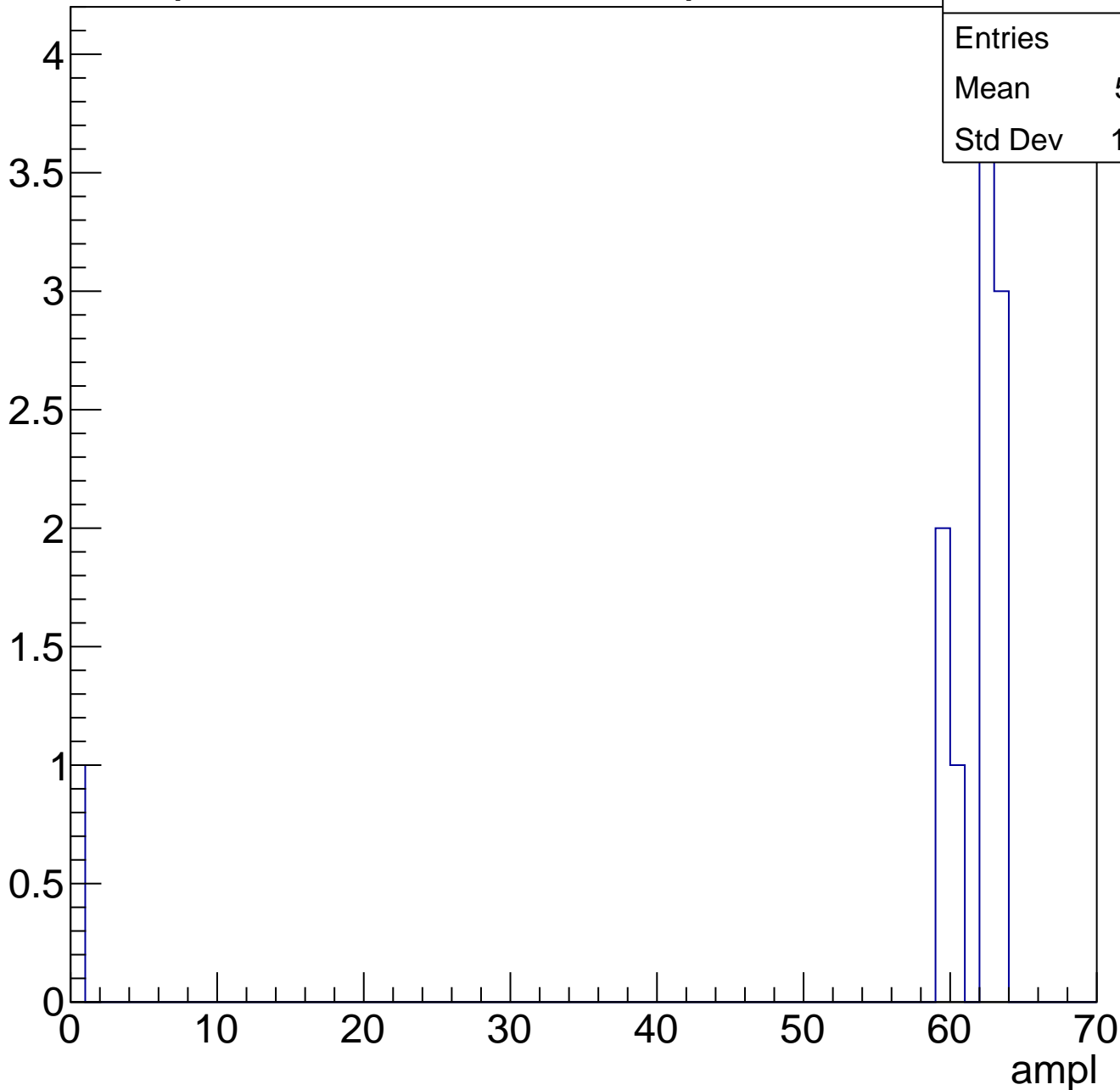
Entries	59
Mean	58.88
Std Dev	2.847



B1L101S, U4-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L101S, U4-ch56, adc0

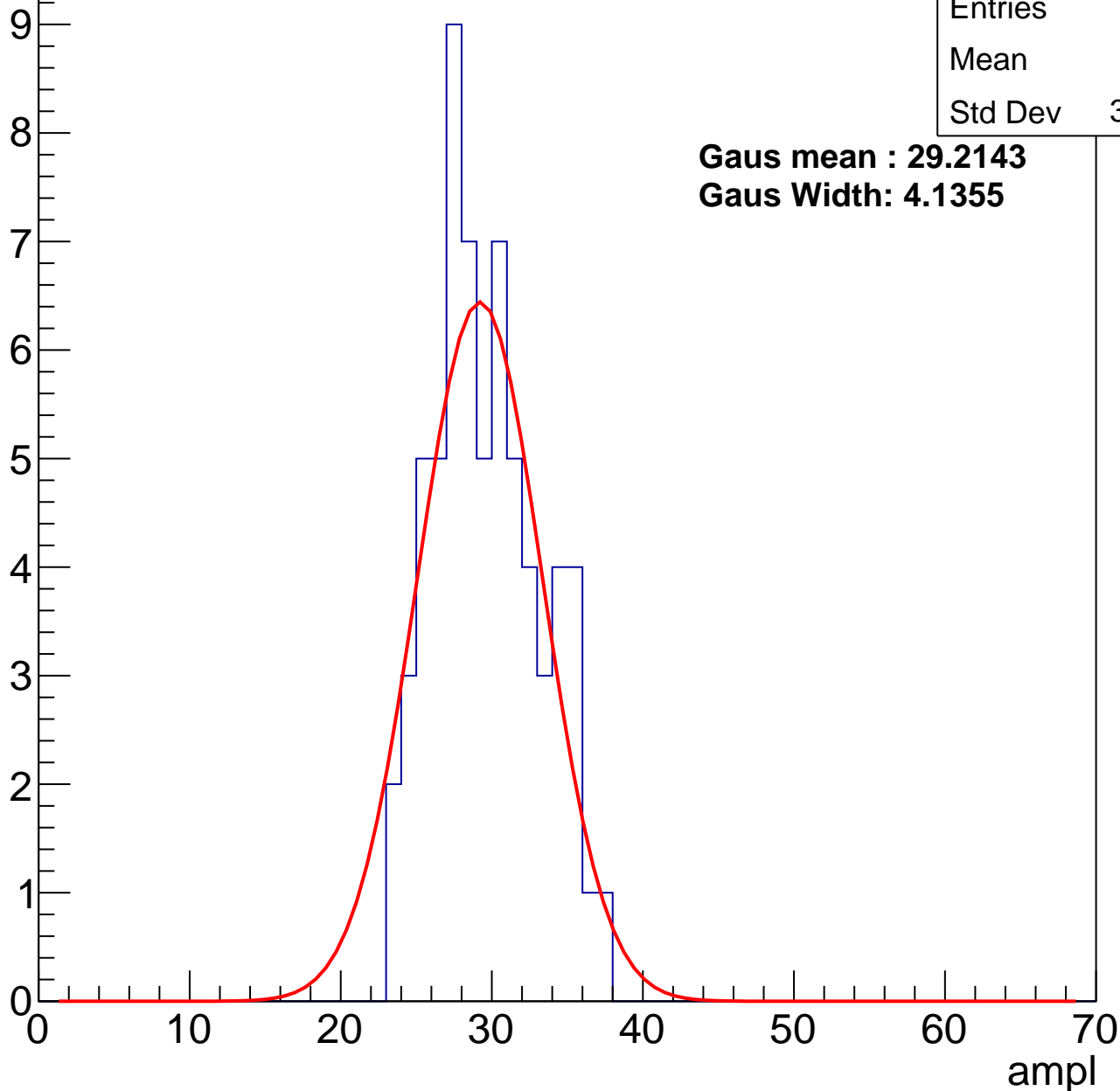
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.2
Std Dev	3.465

Gaus mean : 29.2143

Gaus Width: 4.1355



B1L101S, U4-ch56, adc1

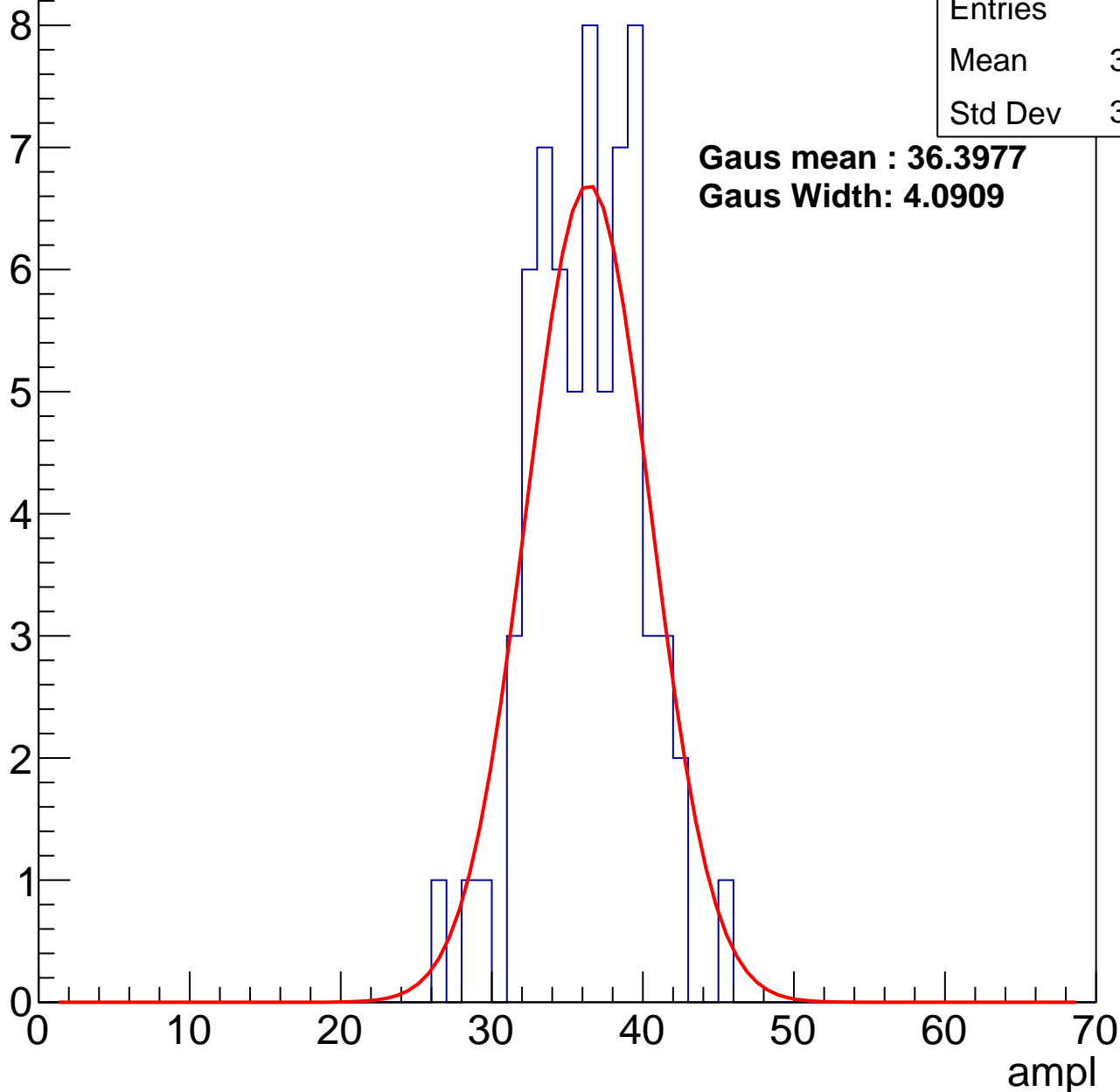
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.84
Std Dev	3.573

Gaus mean : 36.3977

Gaus Width: 4.0909



B1L101S, U4-ch56, adc2

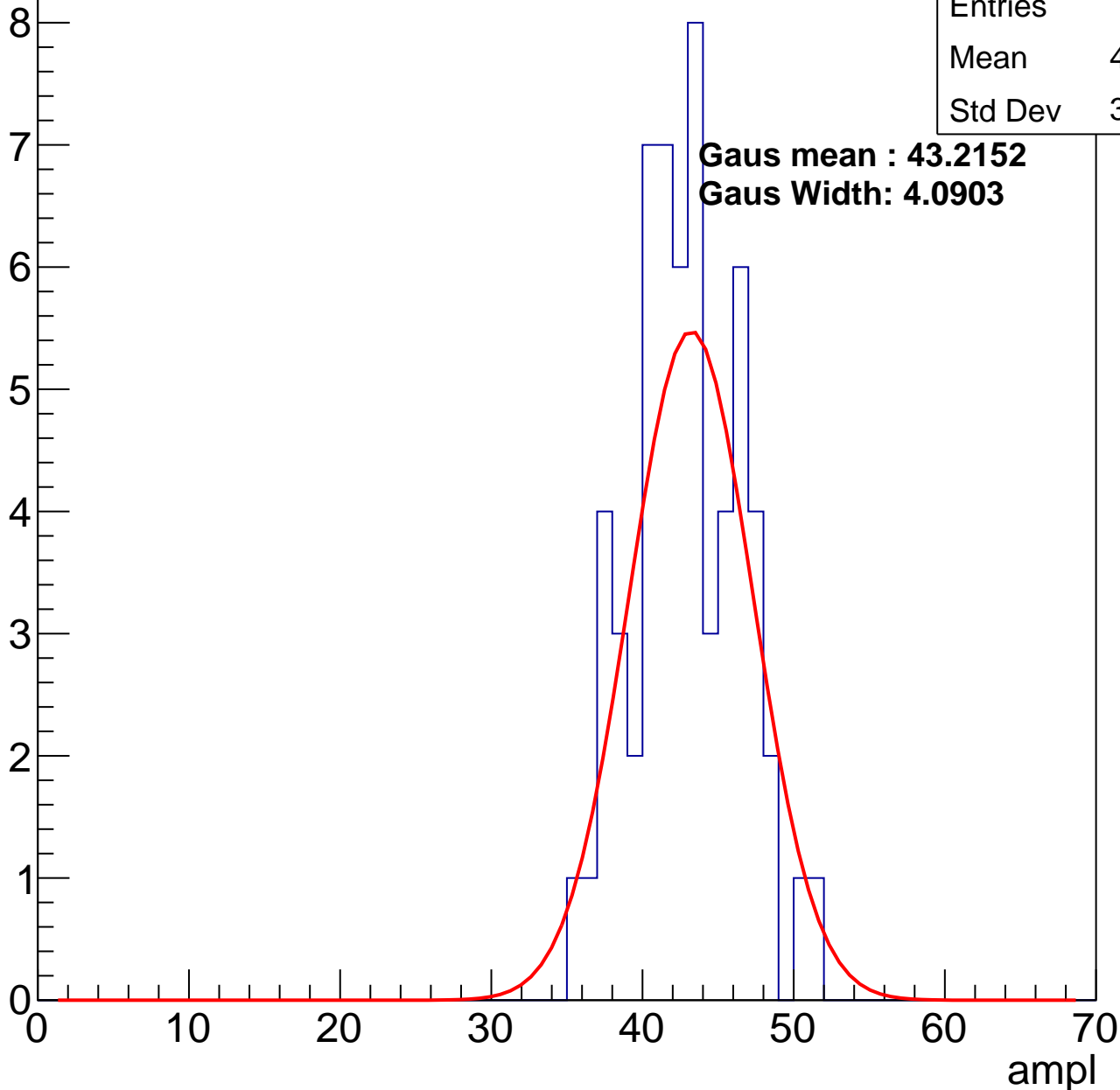
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.45
Std Dev	3.519

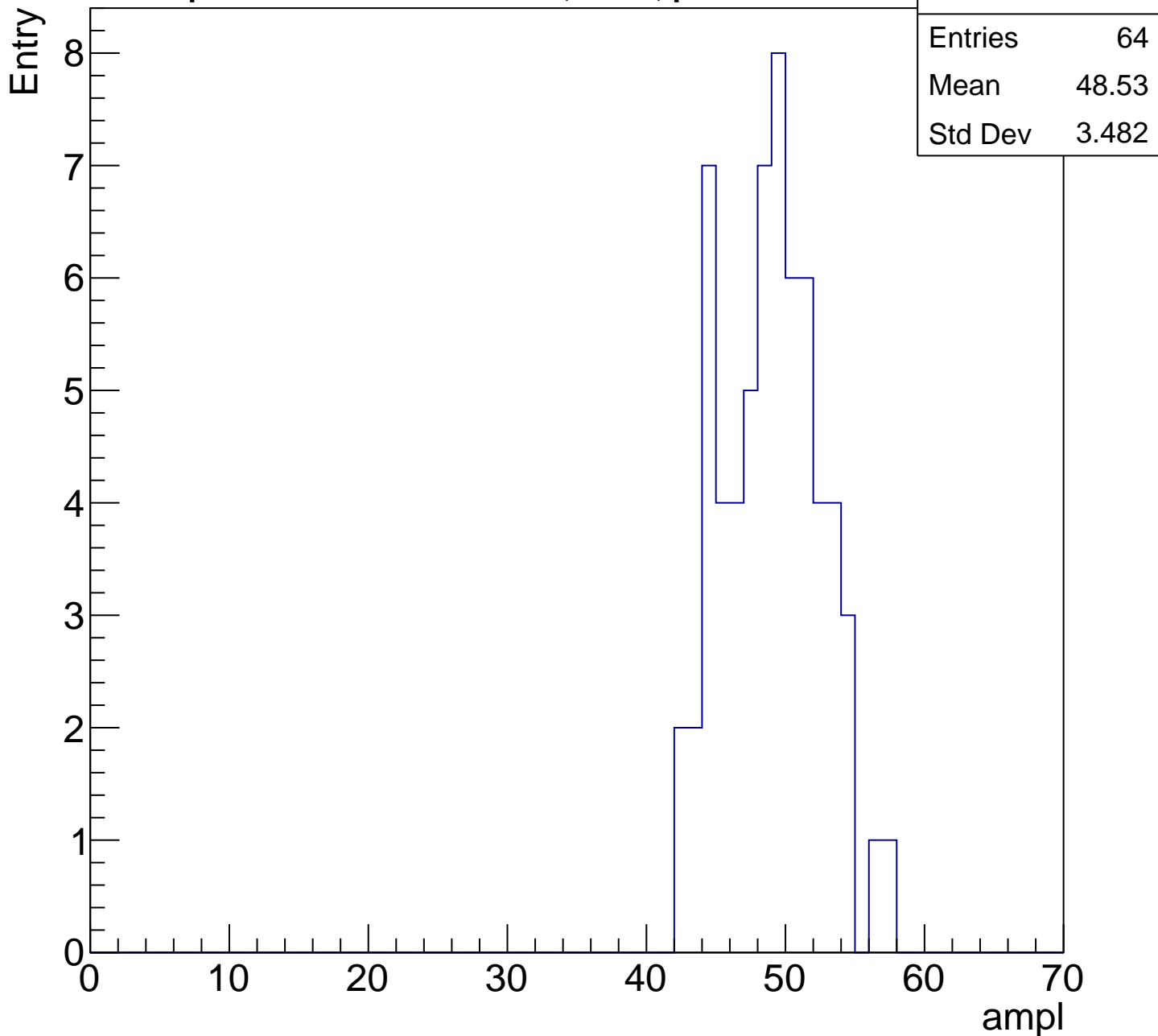
Gaus mean : 43.2152

Gaus Width: 4.0903



B1L101S, U4-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

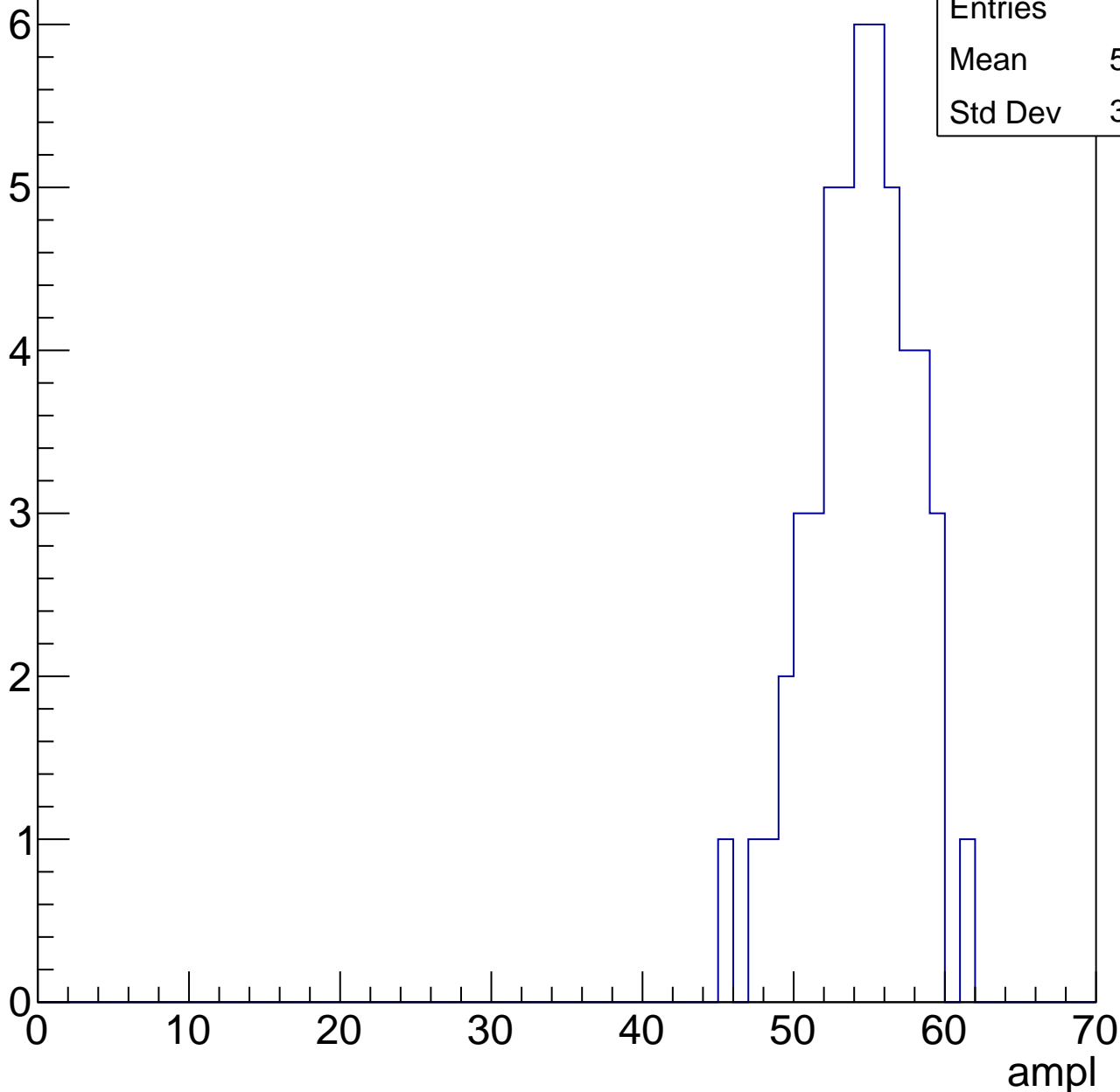


B1L101S, U4-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	53.96
Std Dev	3.364



B1L101S, U4-ch56, adc5

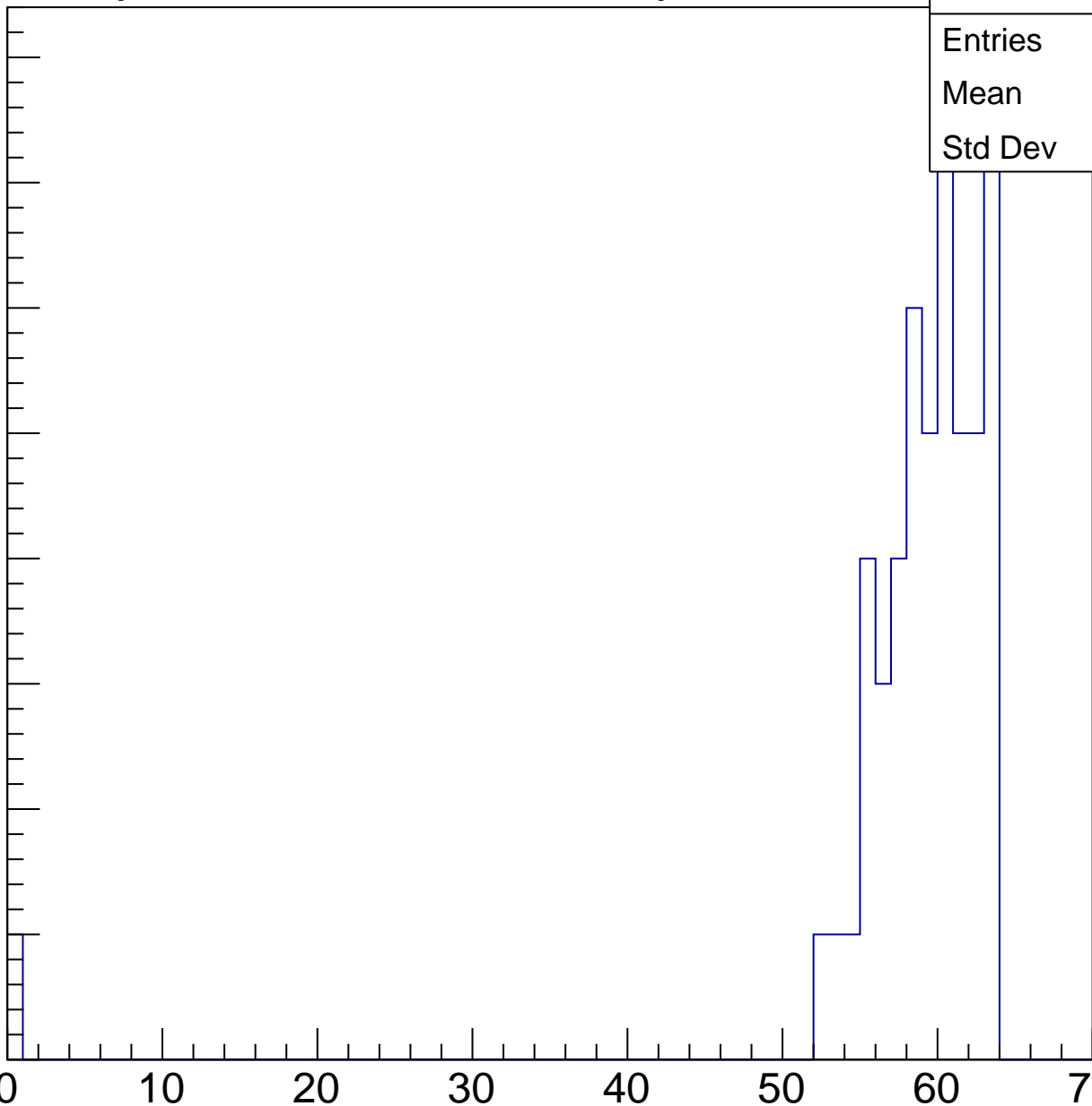
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.02
Std Dev	8.61

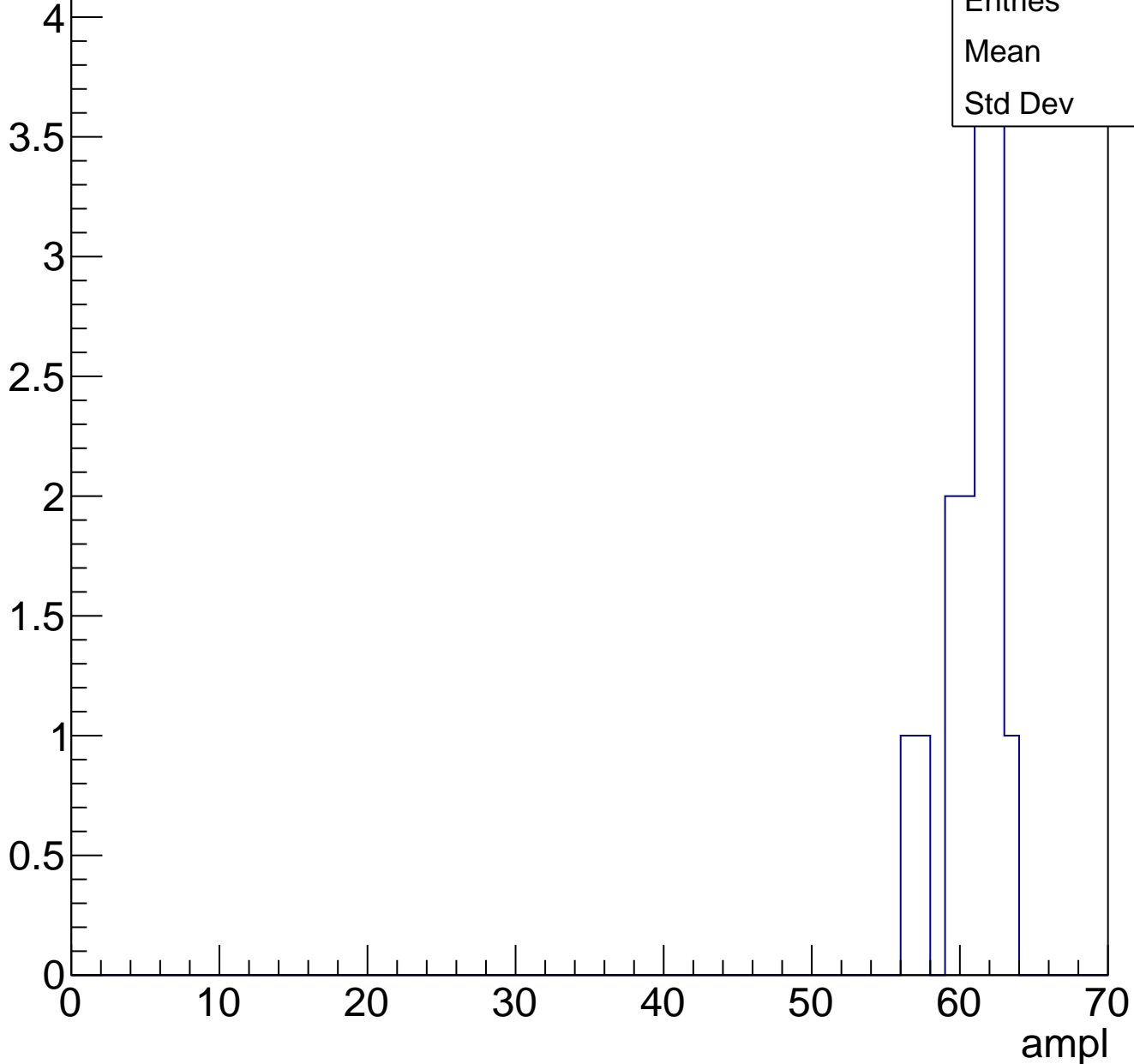
ampl



B1L101S, U4-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	49.33
Std Dev	18.62

ampl

0 10 20 30 40 50 60 70

B1L101S, U4-ch57, adc0

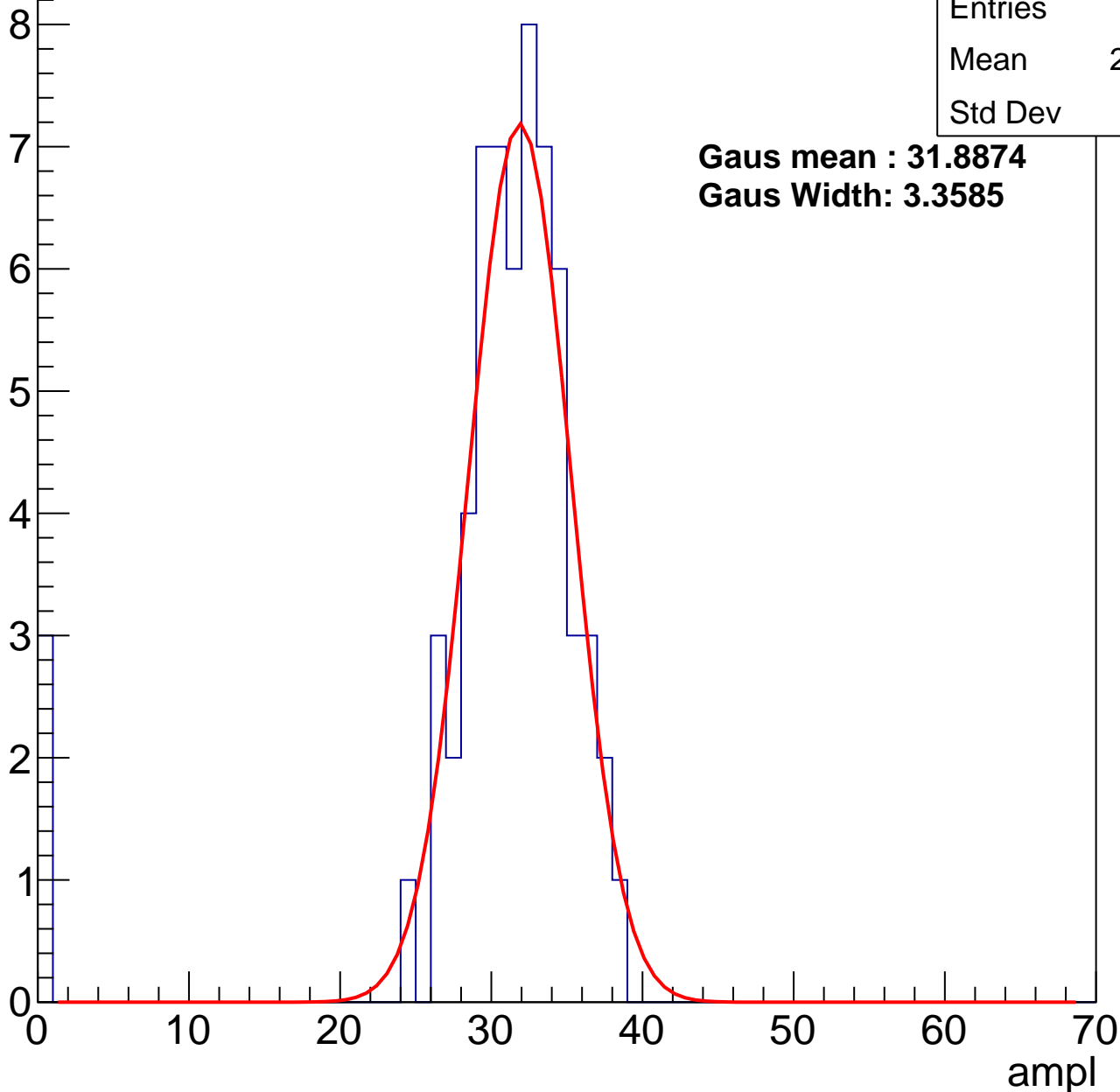
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.89
Std Dev	7.31

Gaus mean : 31.8874

Gaus Width: 3.3585



B1L101S, U4-ch57, adc1

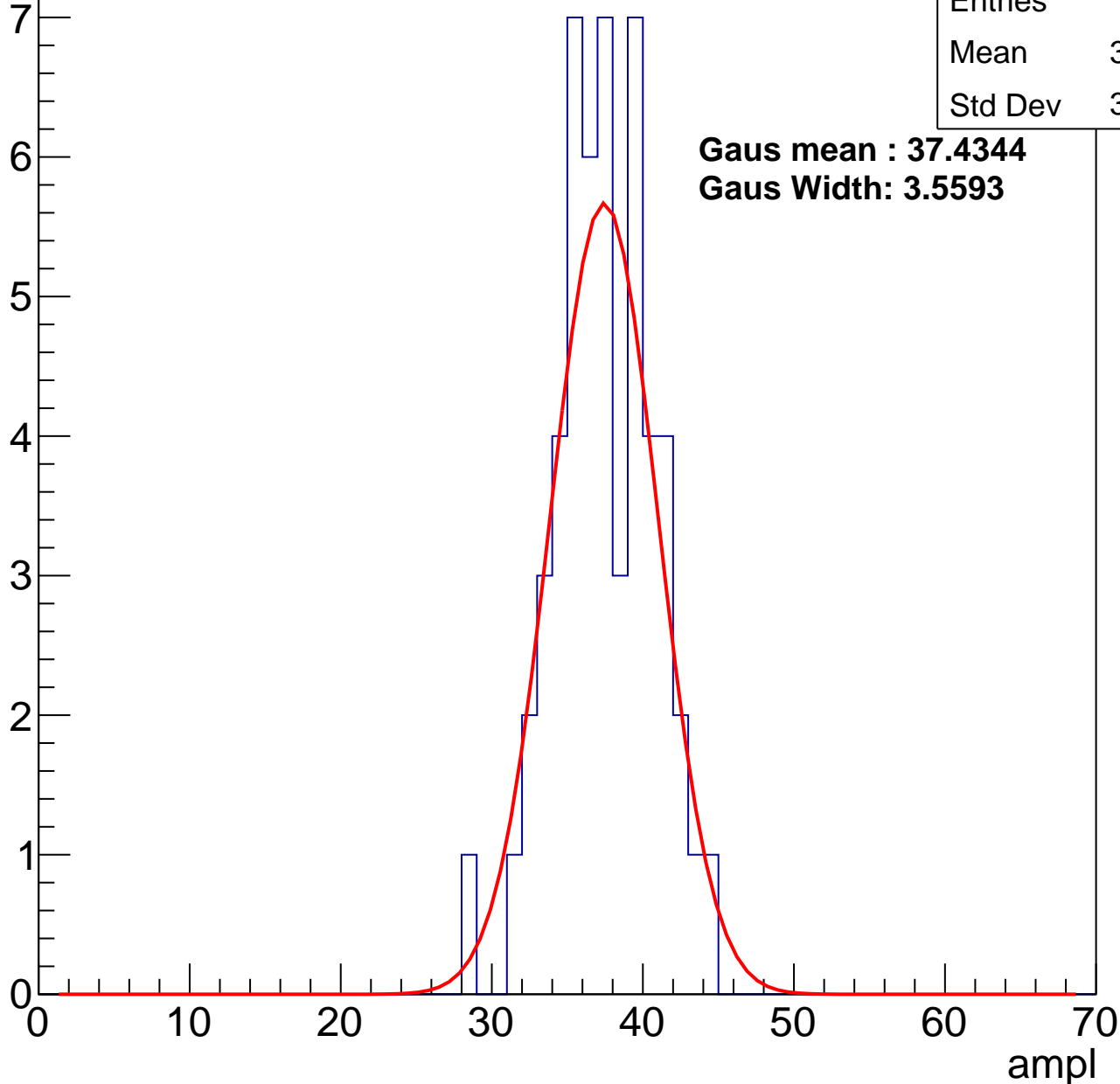
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	36.98
Std Dev	3.224

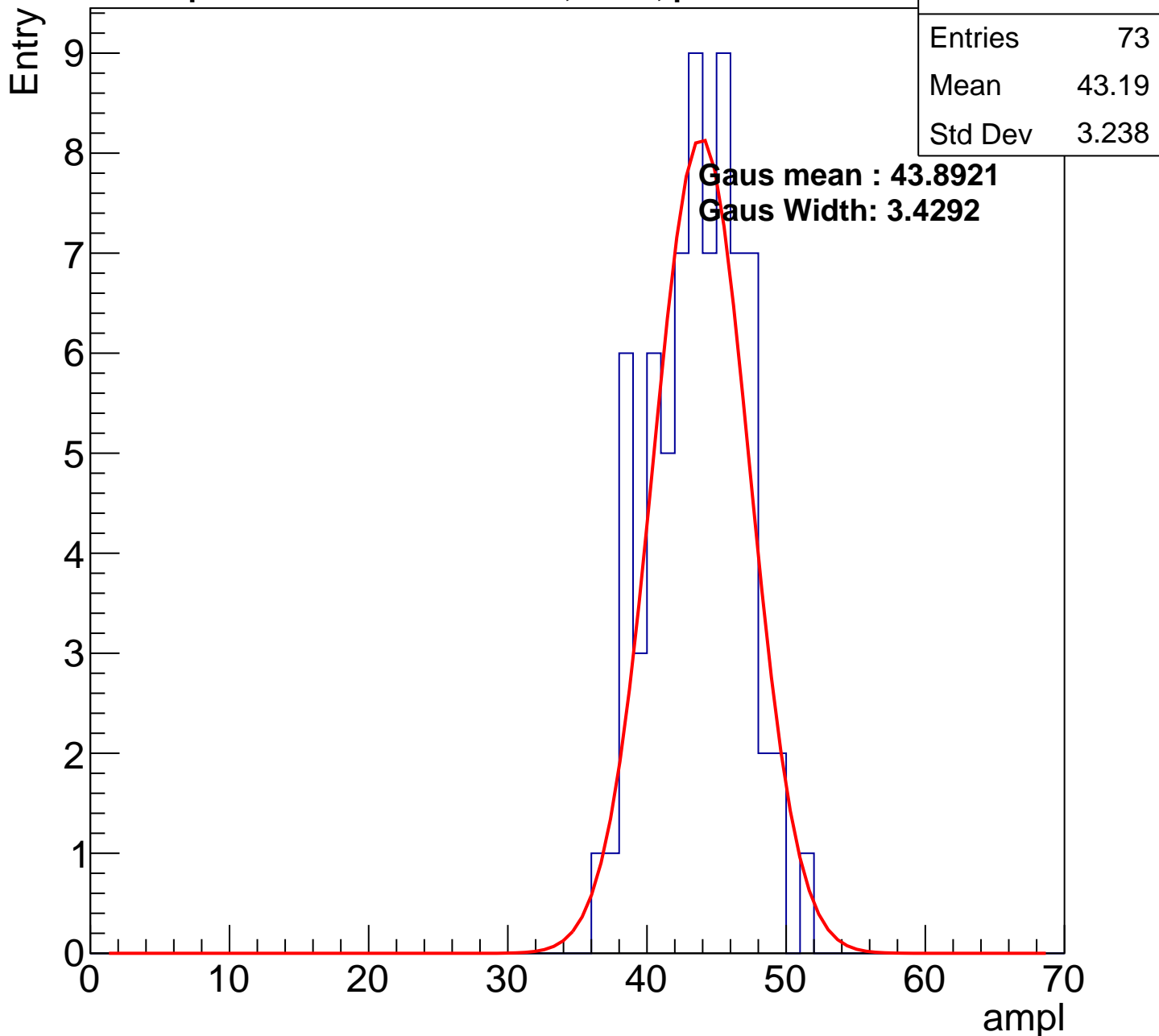
Gaus mean : 37.4344

Gaus Width: 3.5593



B1L101S, U4-ch57, adc2

calib_packv5_042523_0143.root, FC#0, port D2

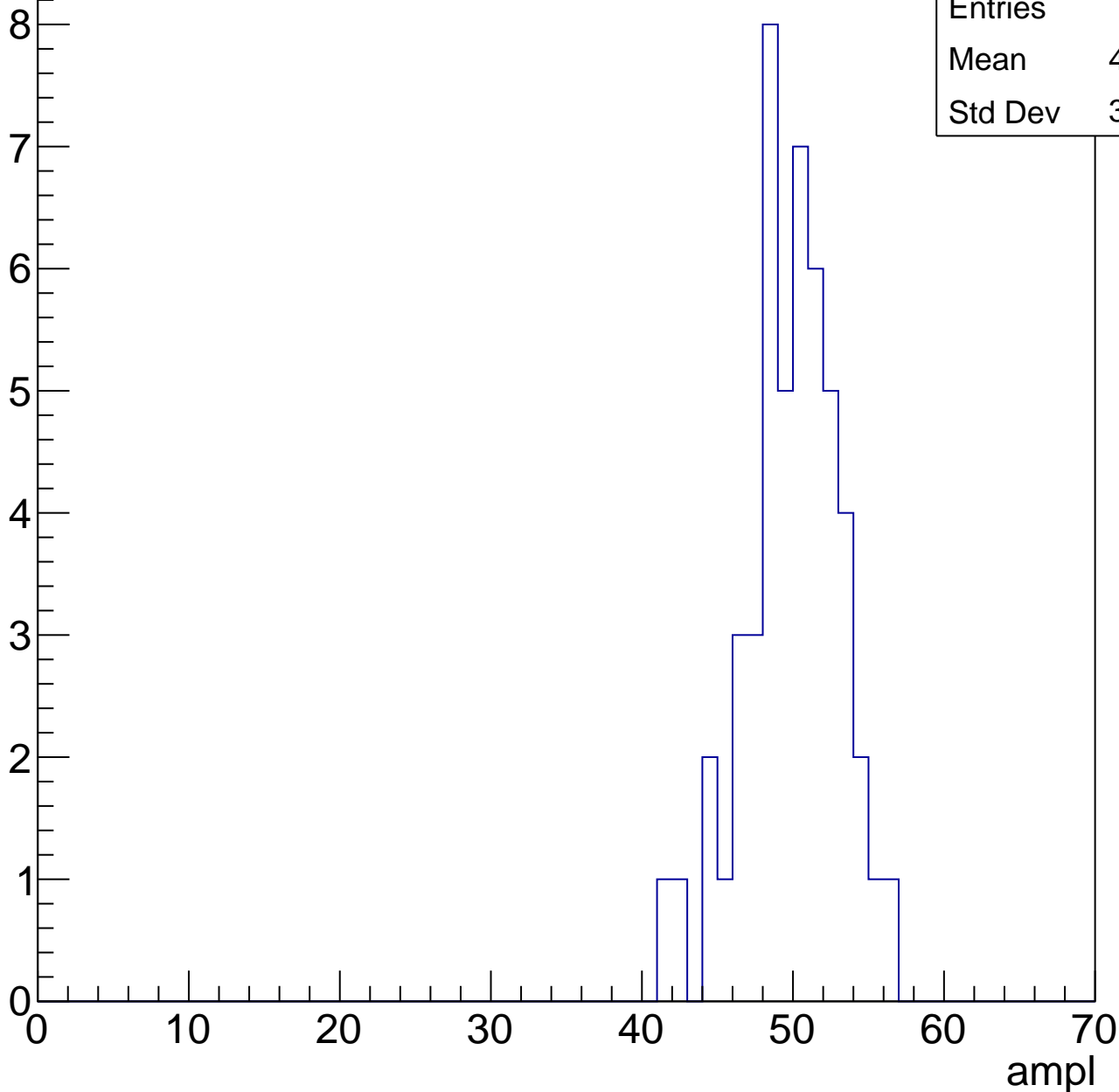


B1L101S, U4-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

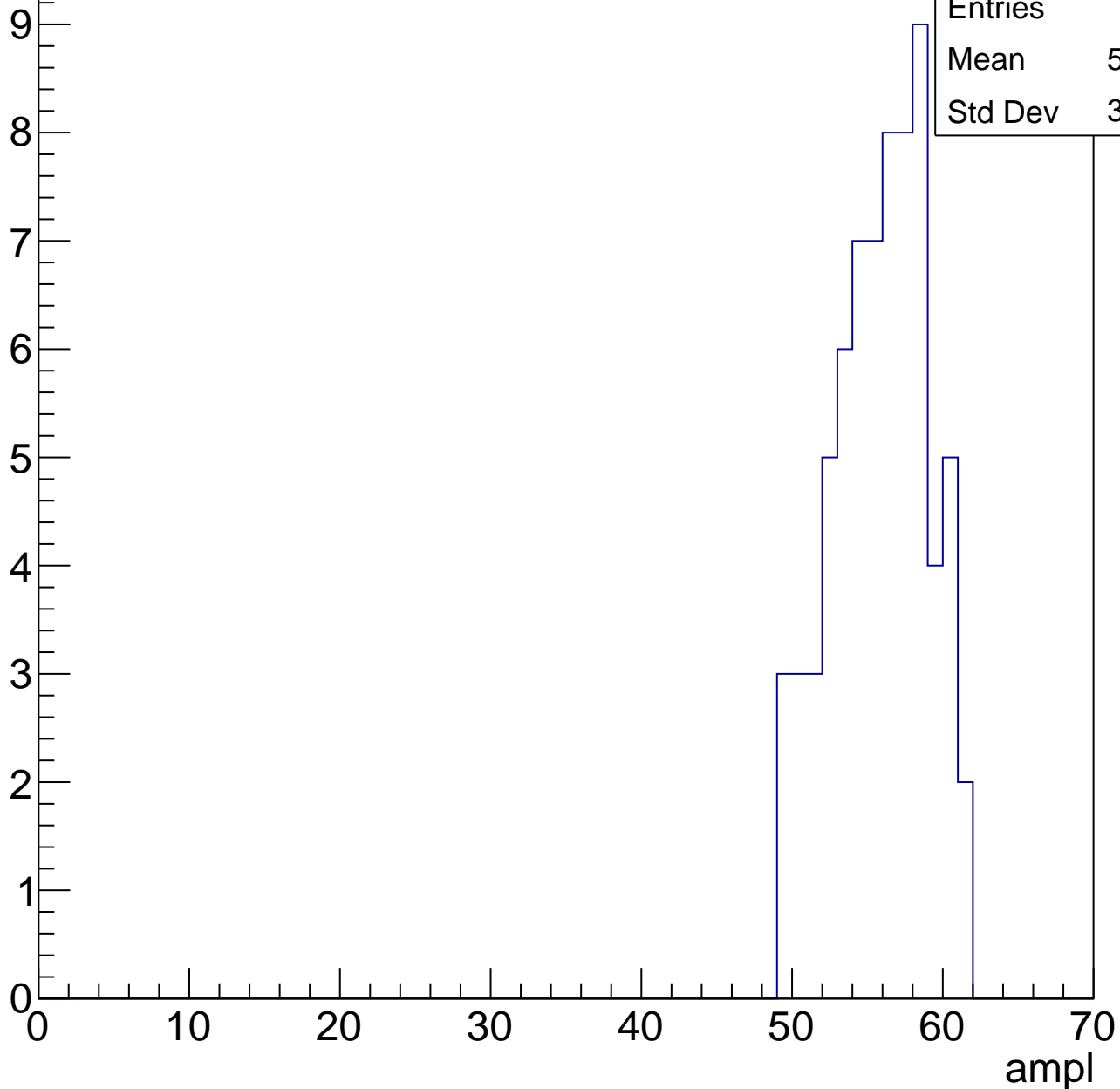
Entries	50
Mean	49.42
Std Dev	3.137



B1L101S, U4-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

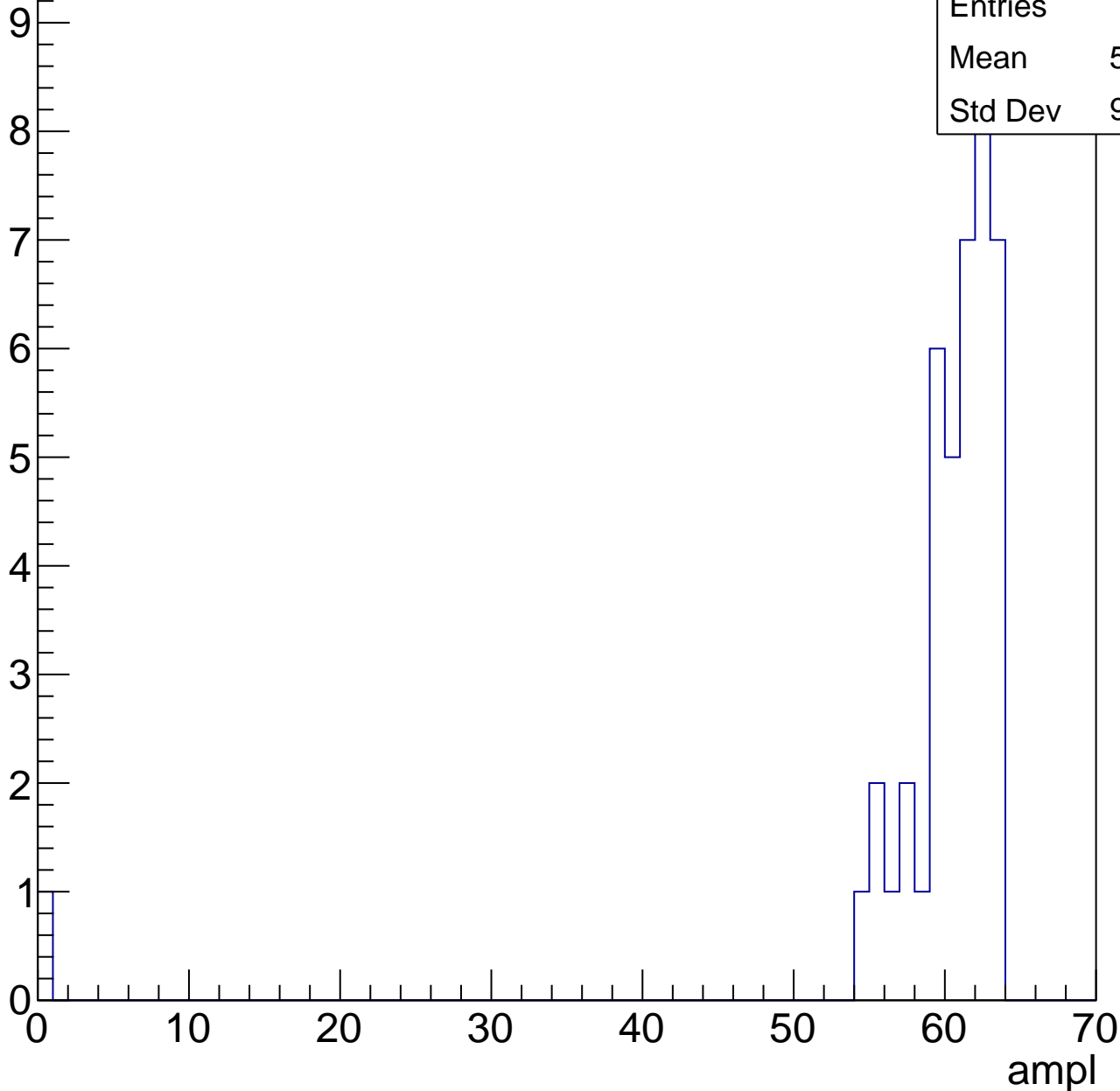


B1L101S, U4-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.86
Std Dev	9.488



B1L101S, U4-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch58, adc0

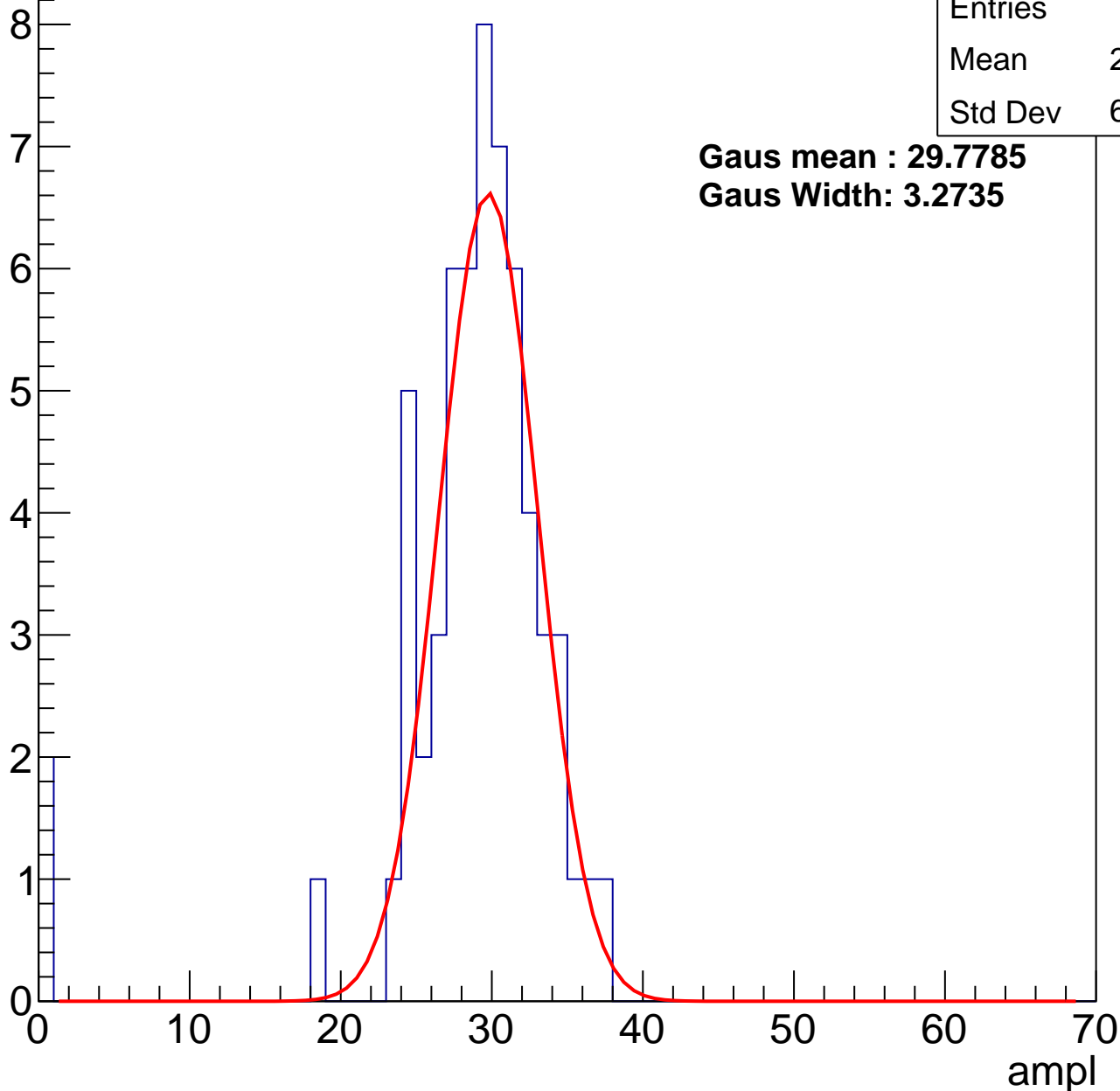
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.07
Std Dev	6.245

Gaus mean : 29.7785

Gaus Width: 3.2735



B1L101S, U4-ch58, adc1

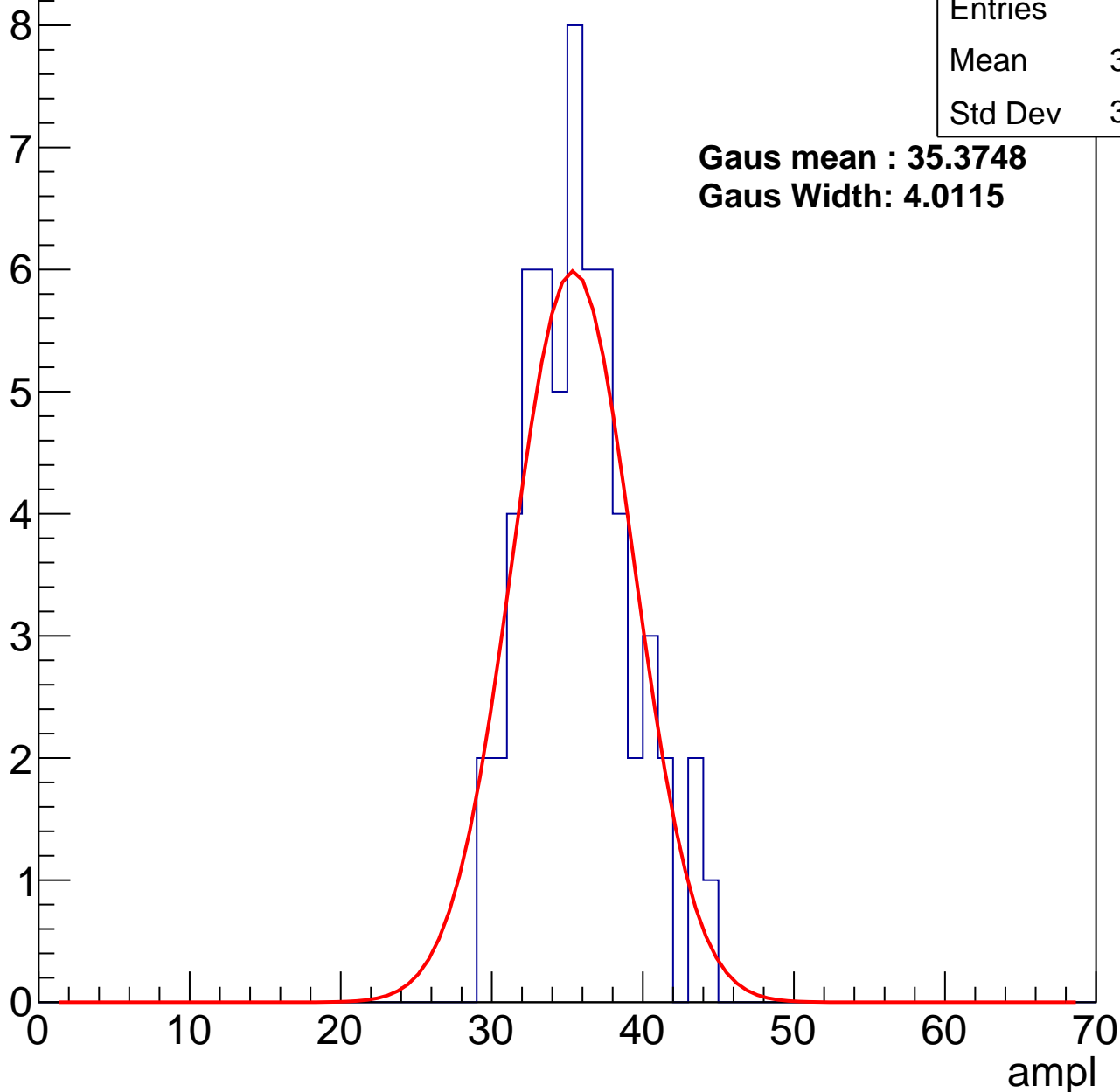
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	35.29
Std Dev	3.489

Gaus mean : 35.3748

Gaus Width: 4.0115



B1L101S, U4-ch58, adc2

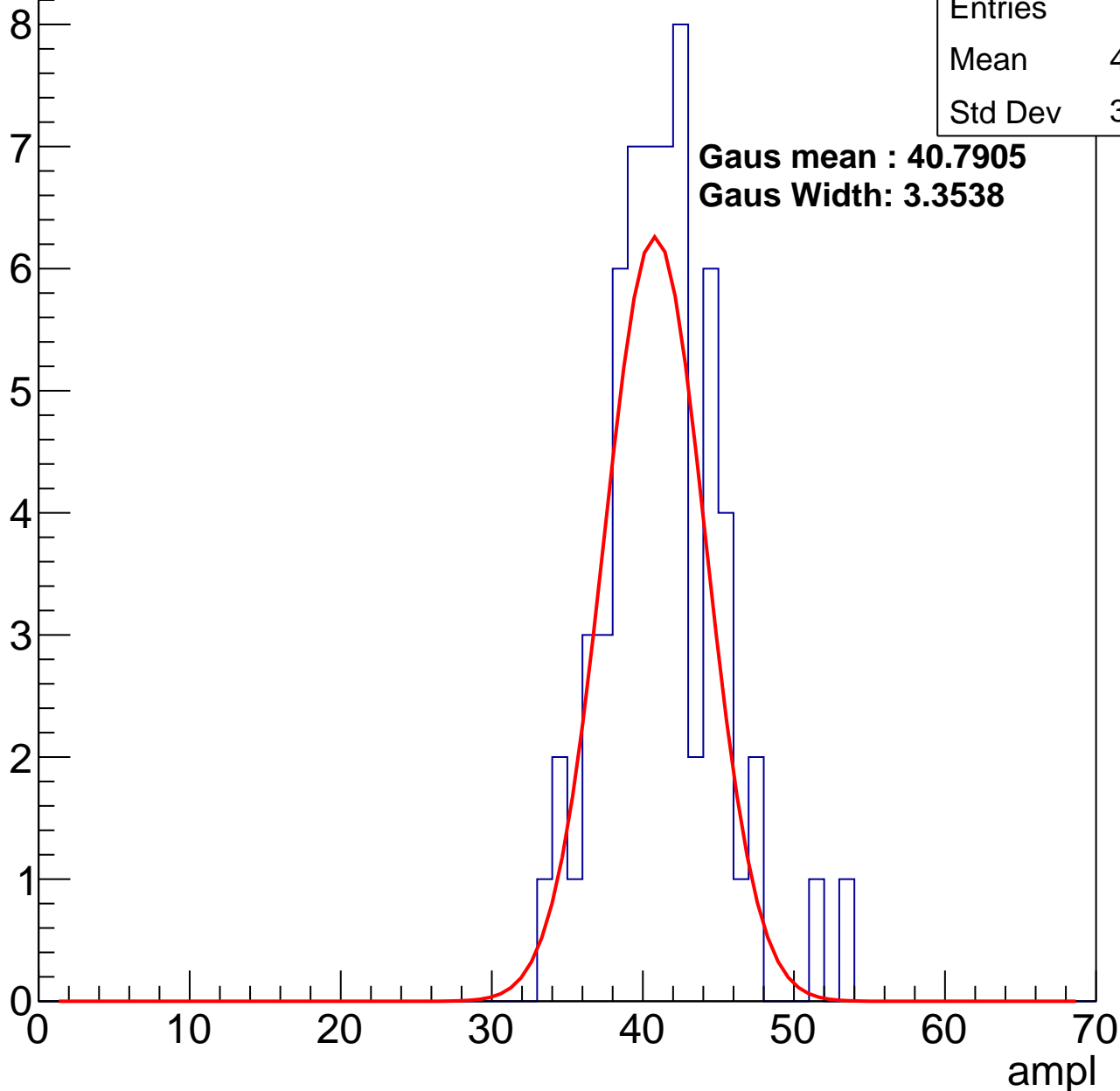
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	40.85
Std Dev	3.775

Gaus mean : 40.7905

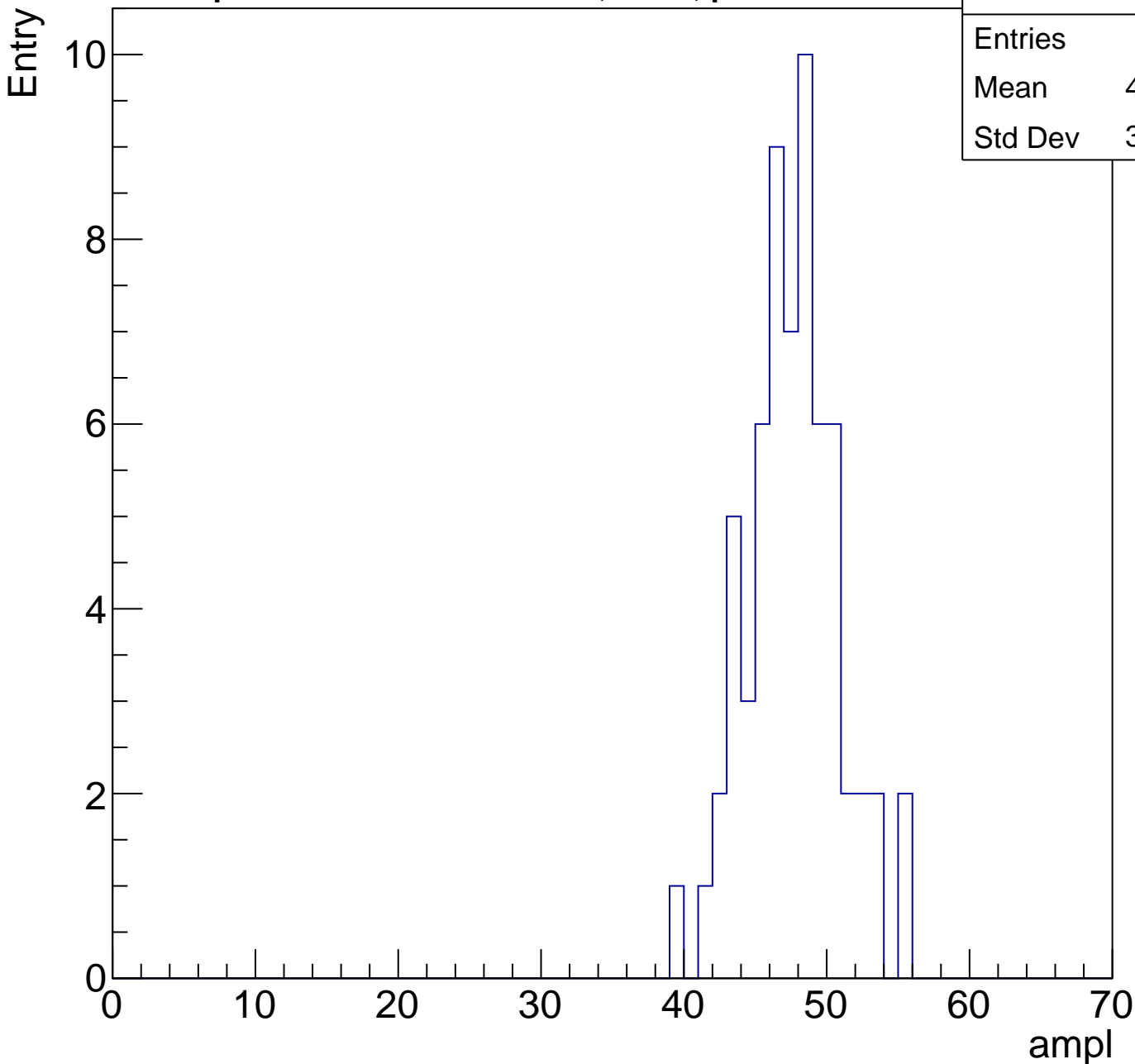
Gaus Width: 3.3538



B1L101S, U4-ch58, adc3

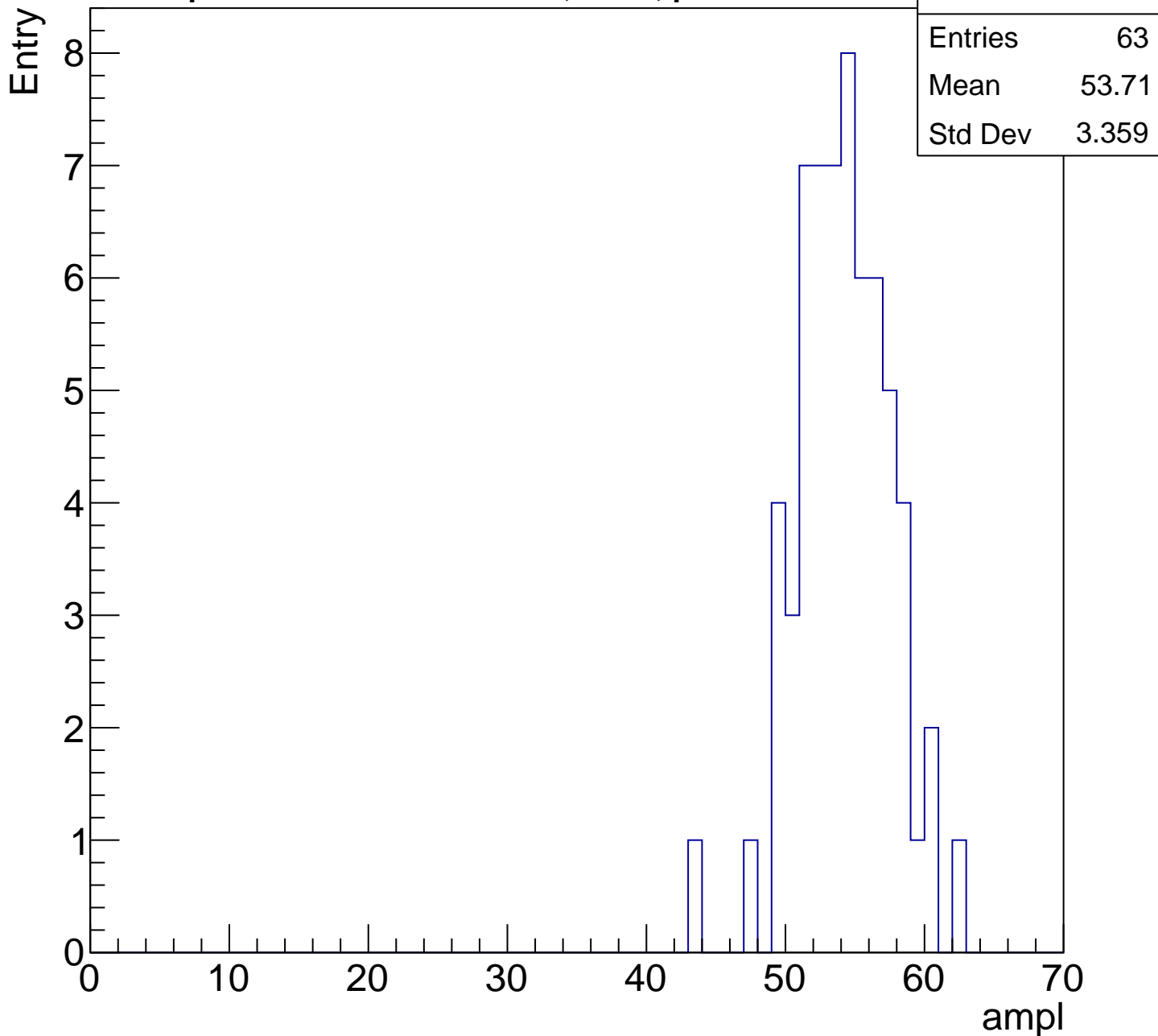
calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	47.19
Std Dev	3.206



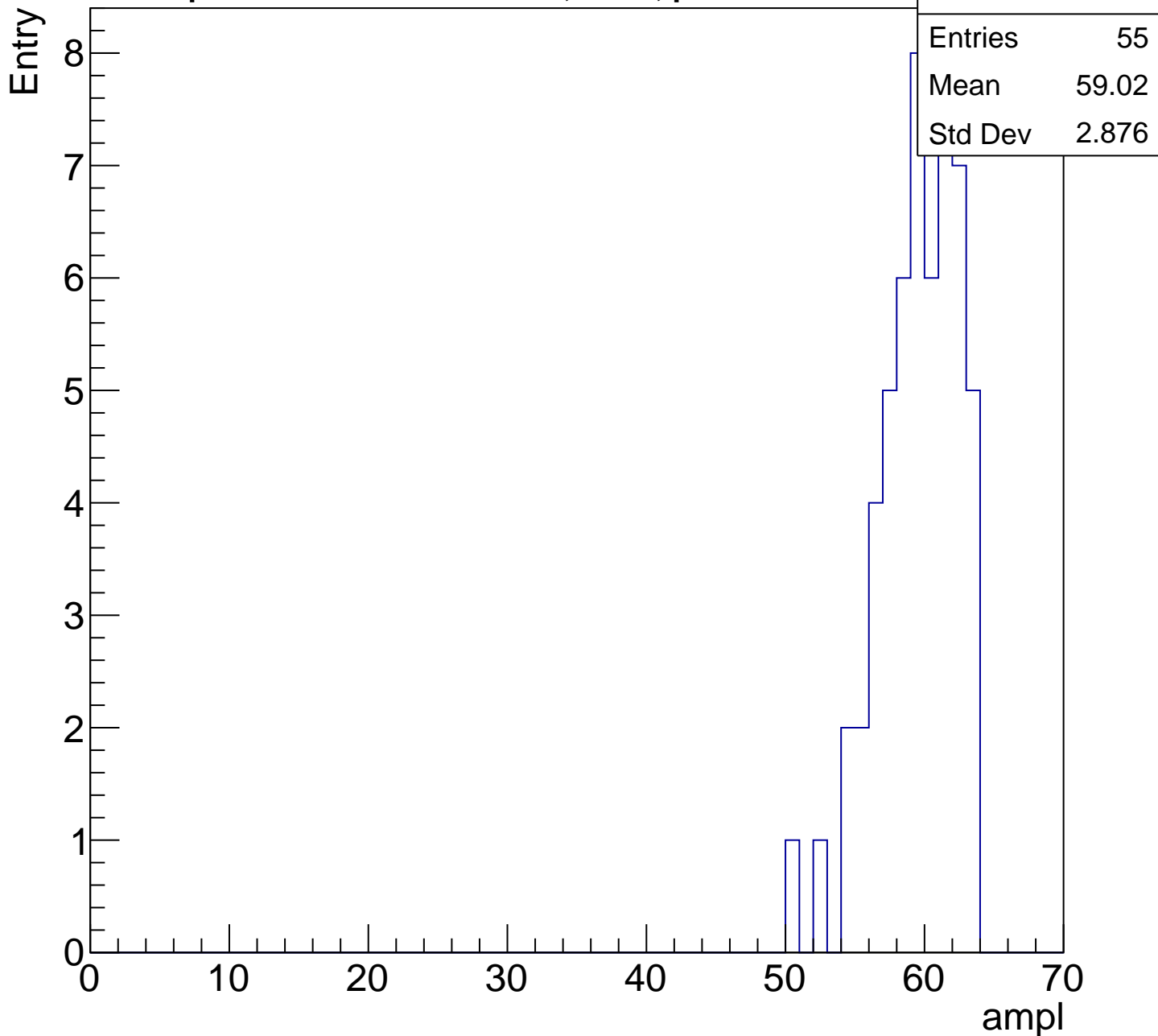
B1L101S, U4-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

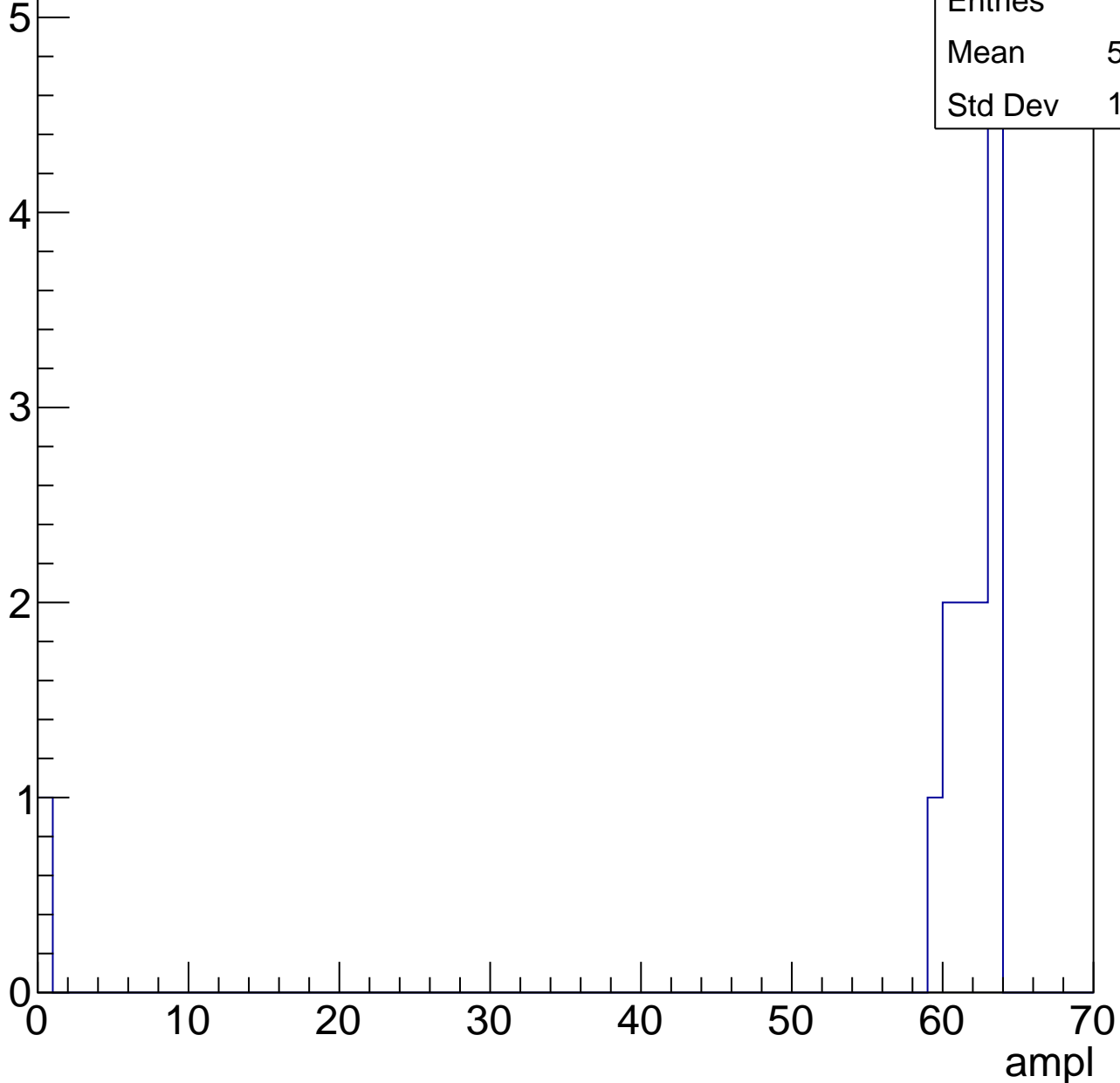


B1L101S, U4-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	56.92
Std Dev	16.49



B1L101S, U4-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch59, adc0

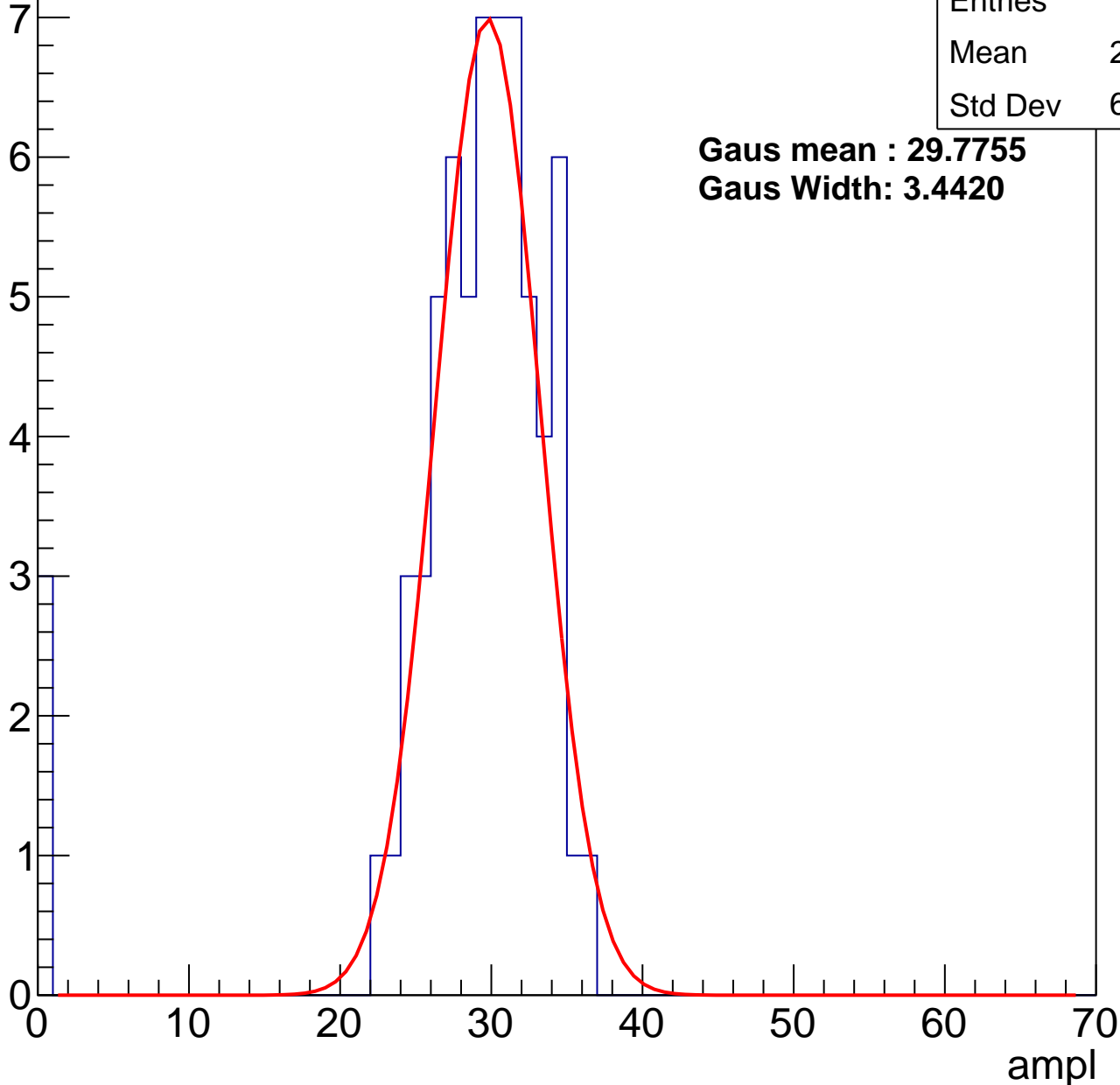
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.02
Std Dev	6.929

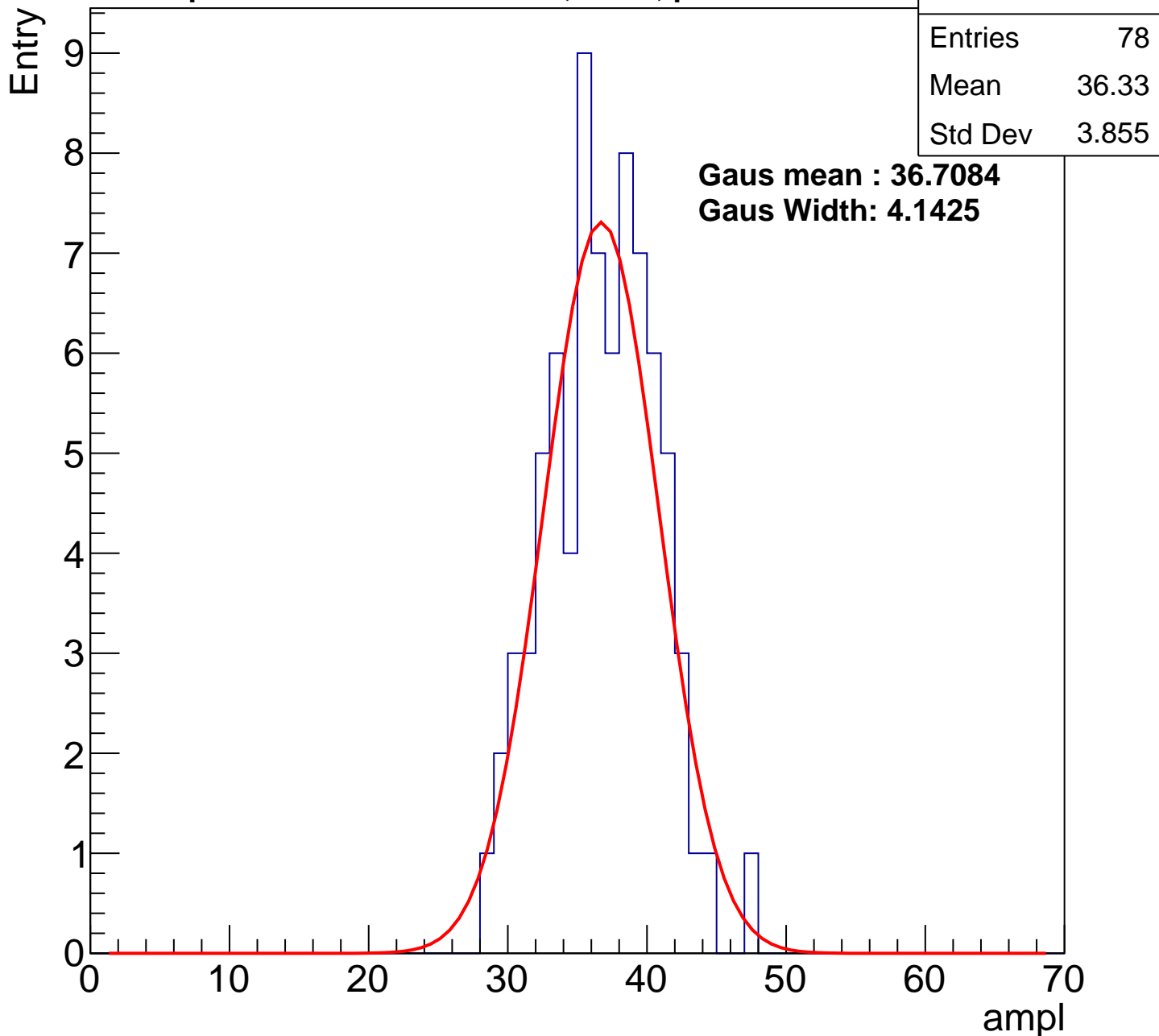
Gaus mean : 29.7755

Gaus Width: 3.4420



B1L101S, U4-ch59, adc1

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch59, adc2

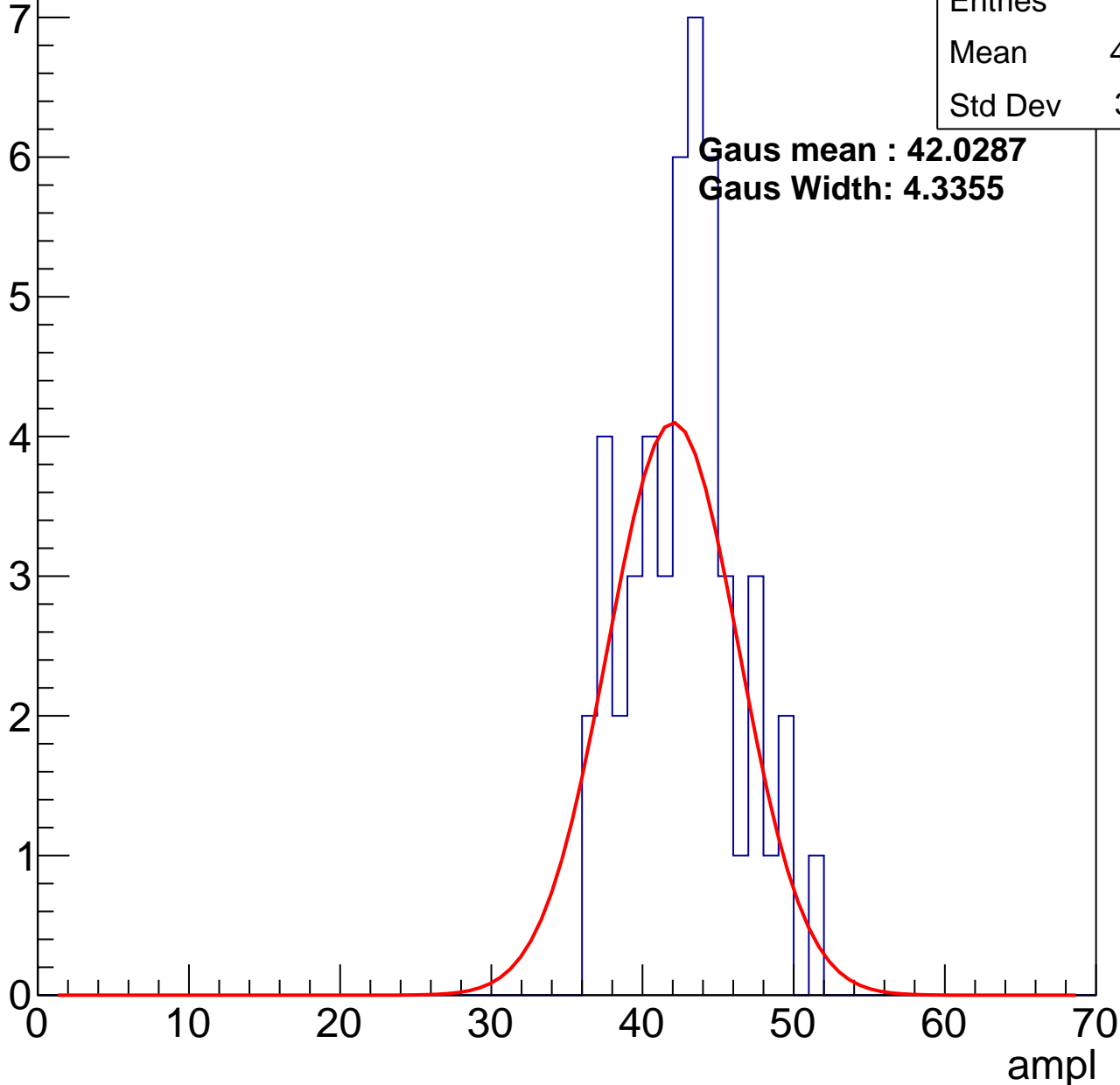
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	42.33
Std Dev	3.561

Gaus mean : 42.0287

Gaus Width: 4.3355

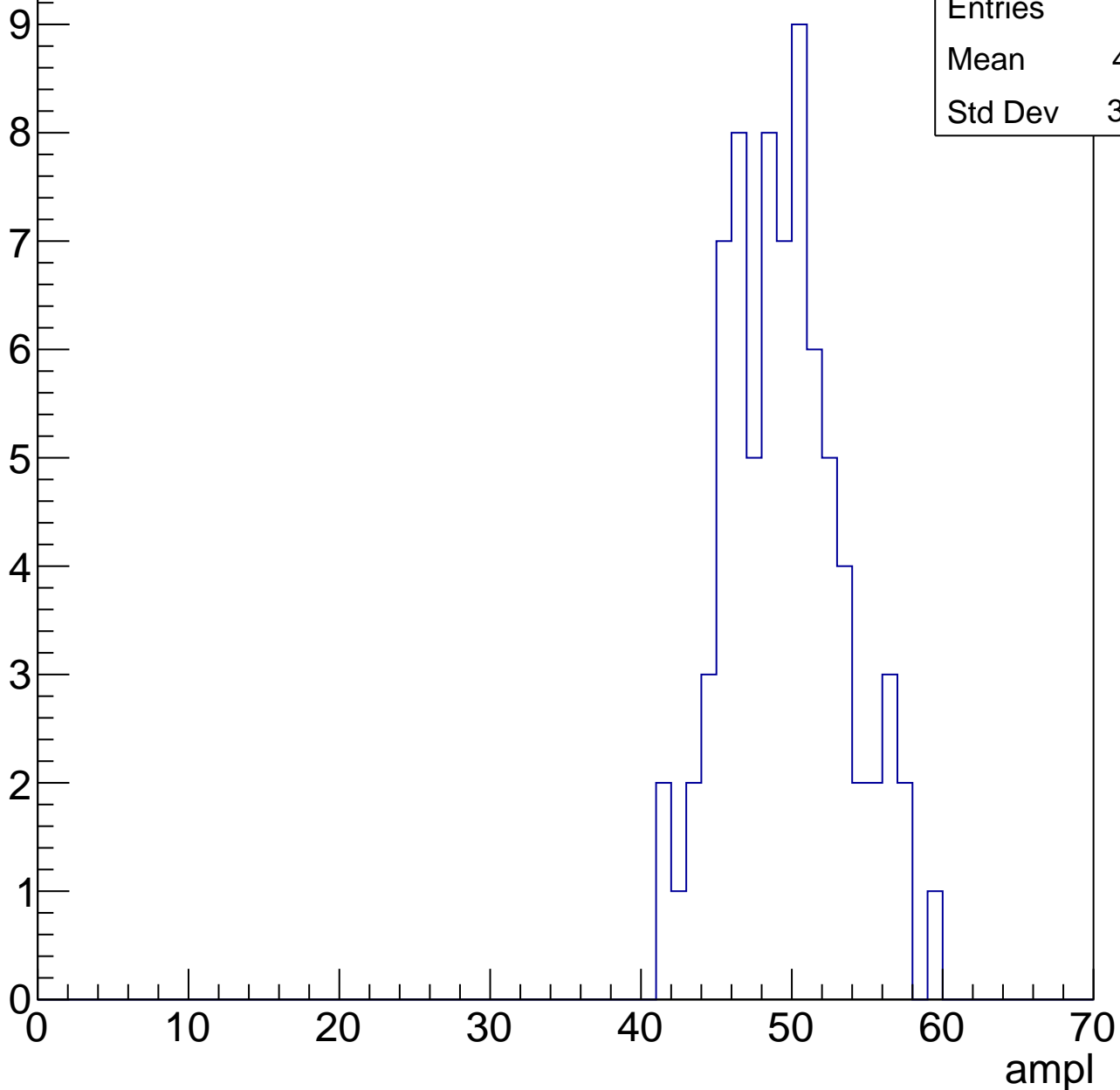


B1L101S, U4-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

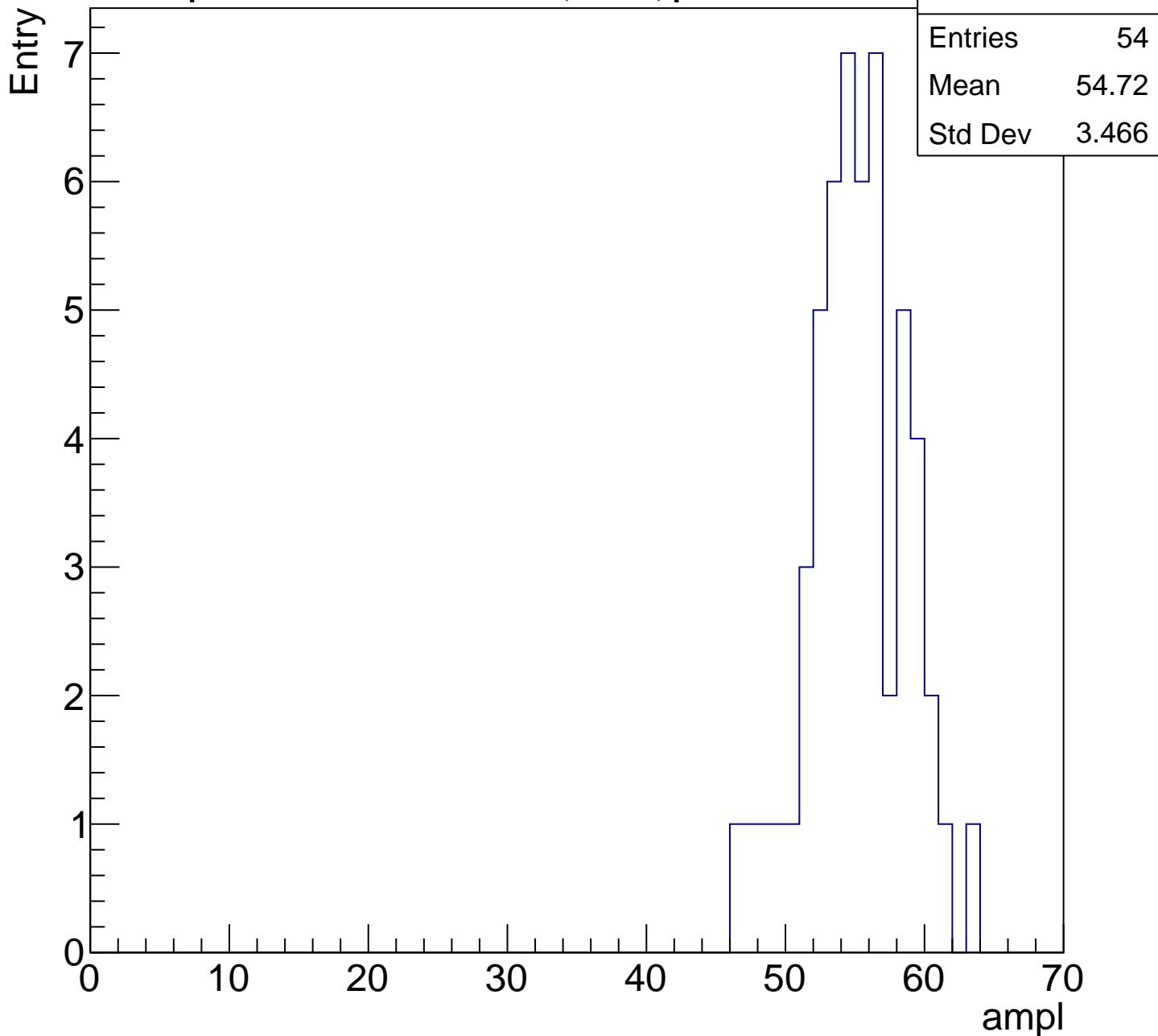
Entry

Entries	77
Mean	49.01
Std Dev	3.906



B1L101S, U4-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch59, adc5

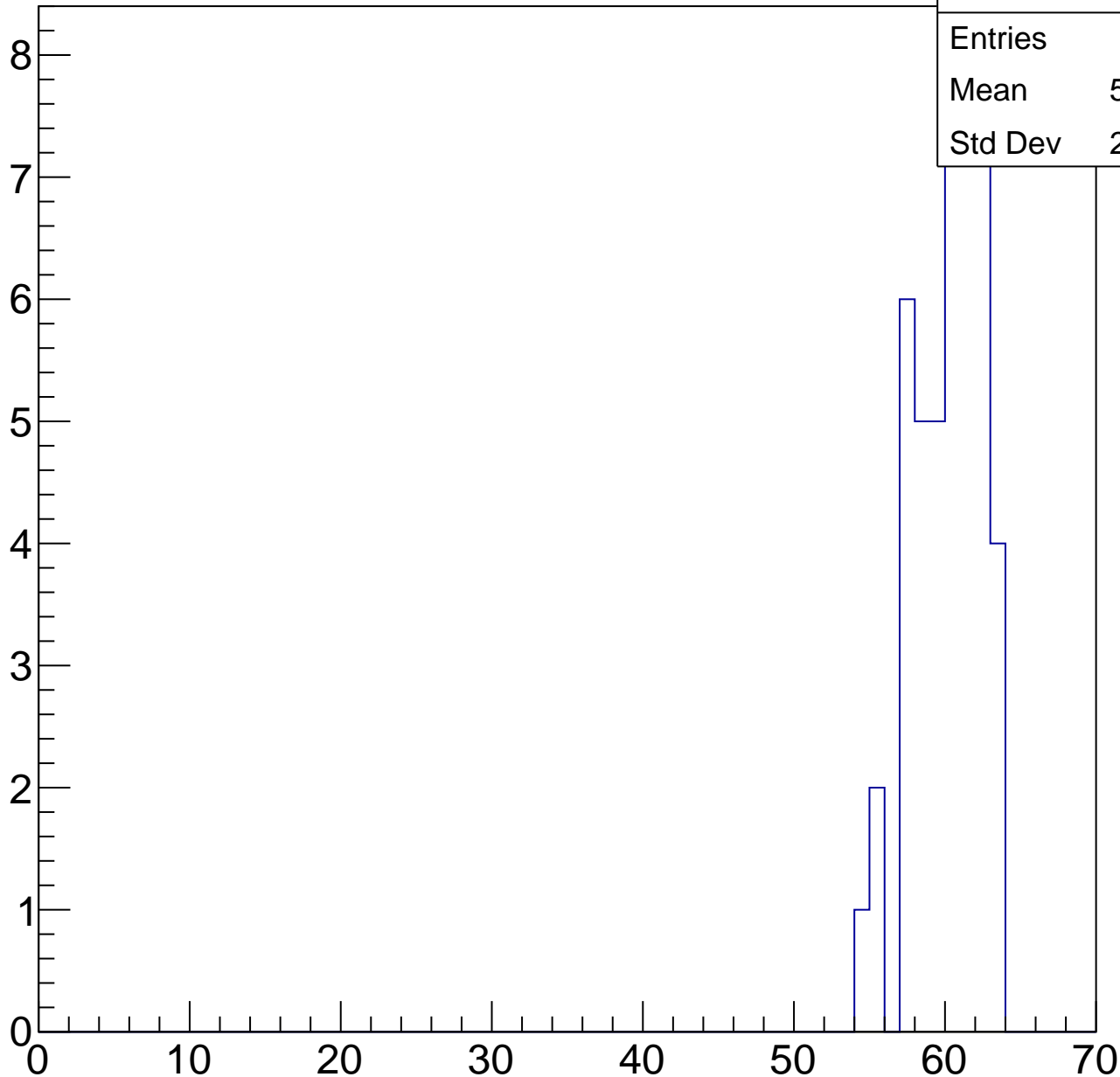
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.72
Std Dev	2.247

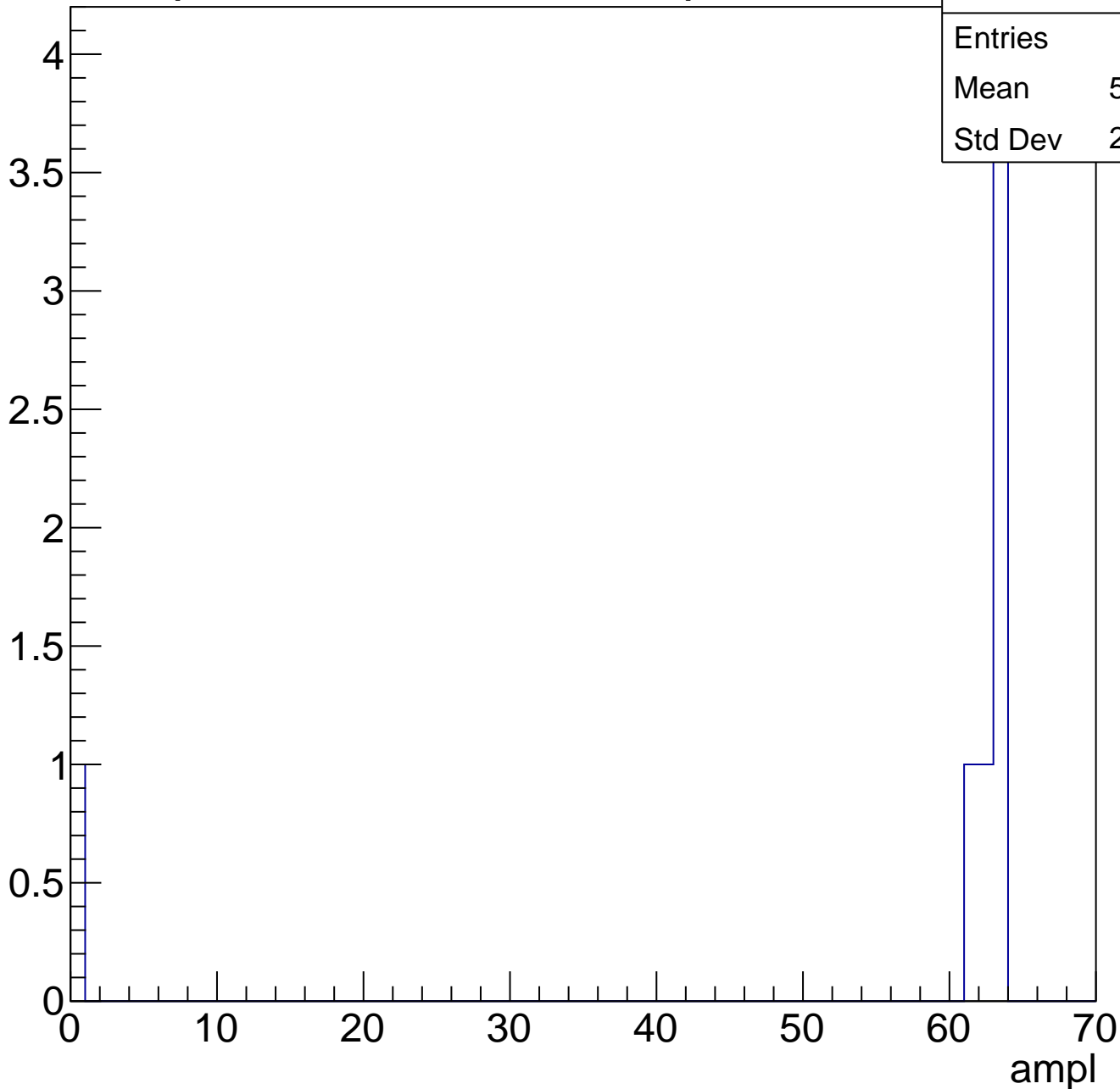
ampl



B1L101S, U4-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch60, adc0

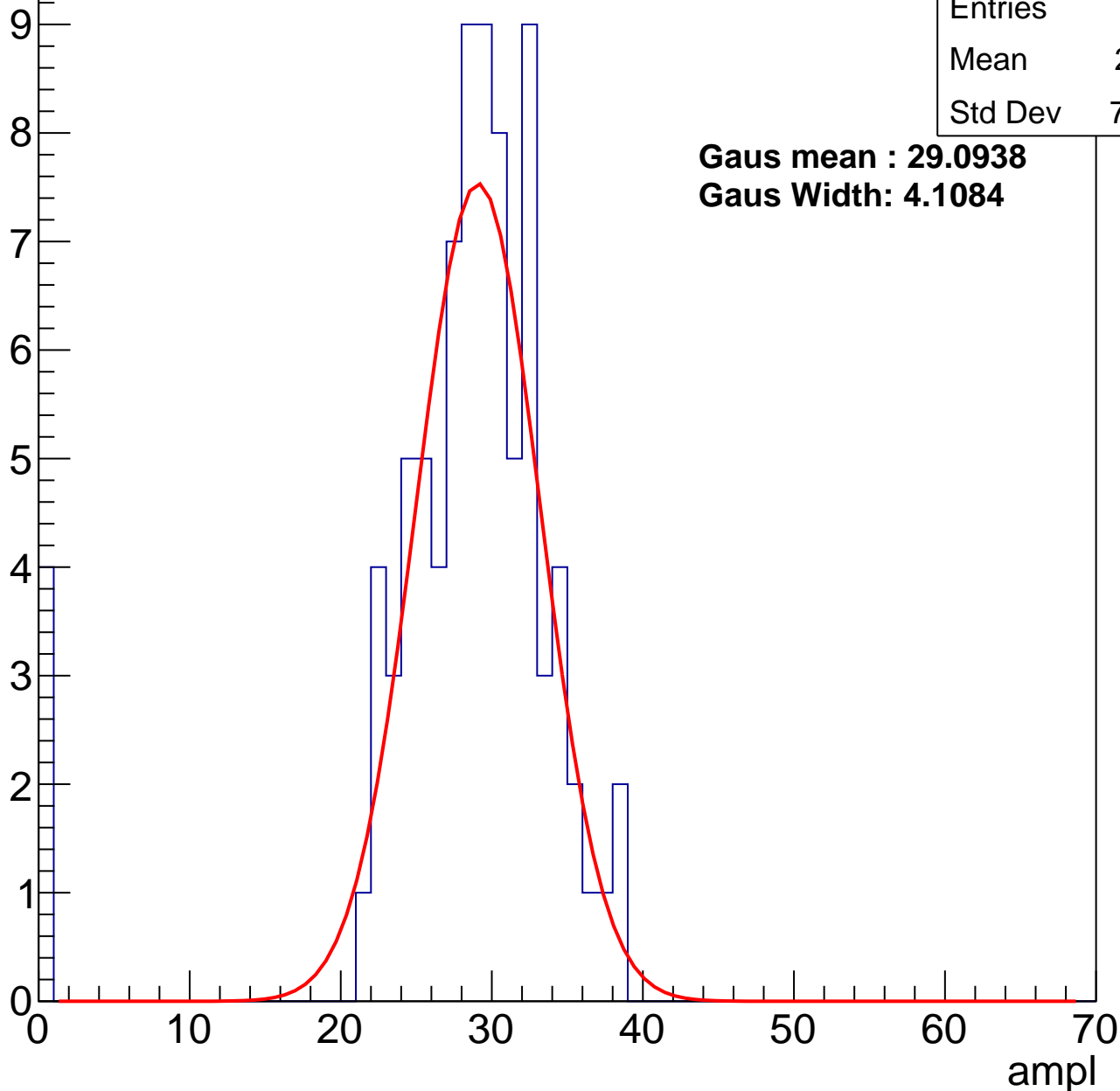
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	27.51
Std Dev	7.172

Gaus mean : 29.0938

Gaus Width: 4.1084



B1L101S, U4-ch60, adc1

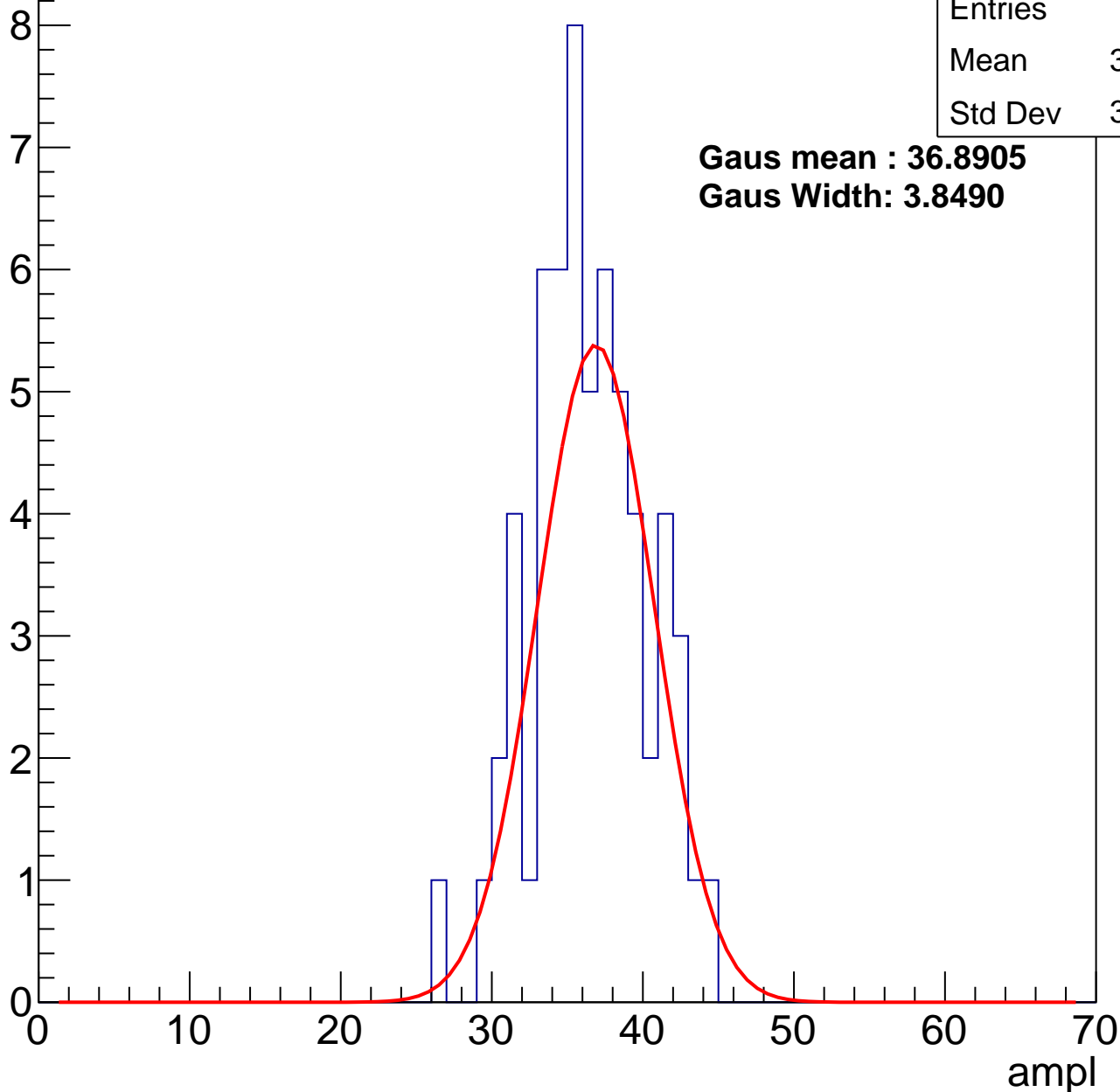
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.97
Std Dev	3.737

Gaus mean : 36.8905

Gaus Width: 3.8490



B1L101S, U4-ch60, adc2

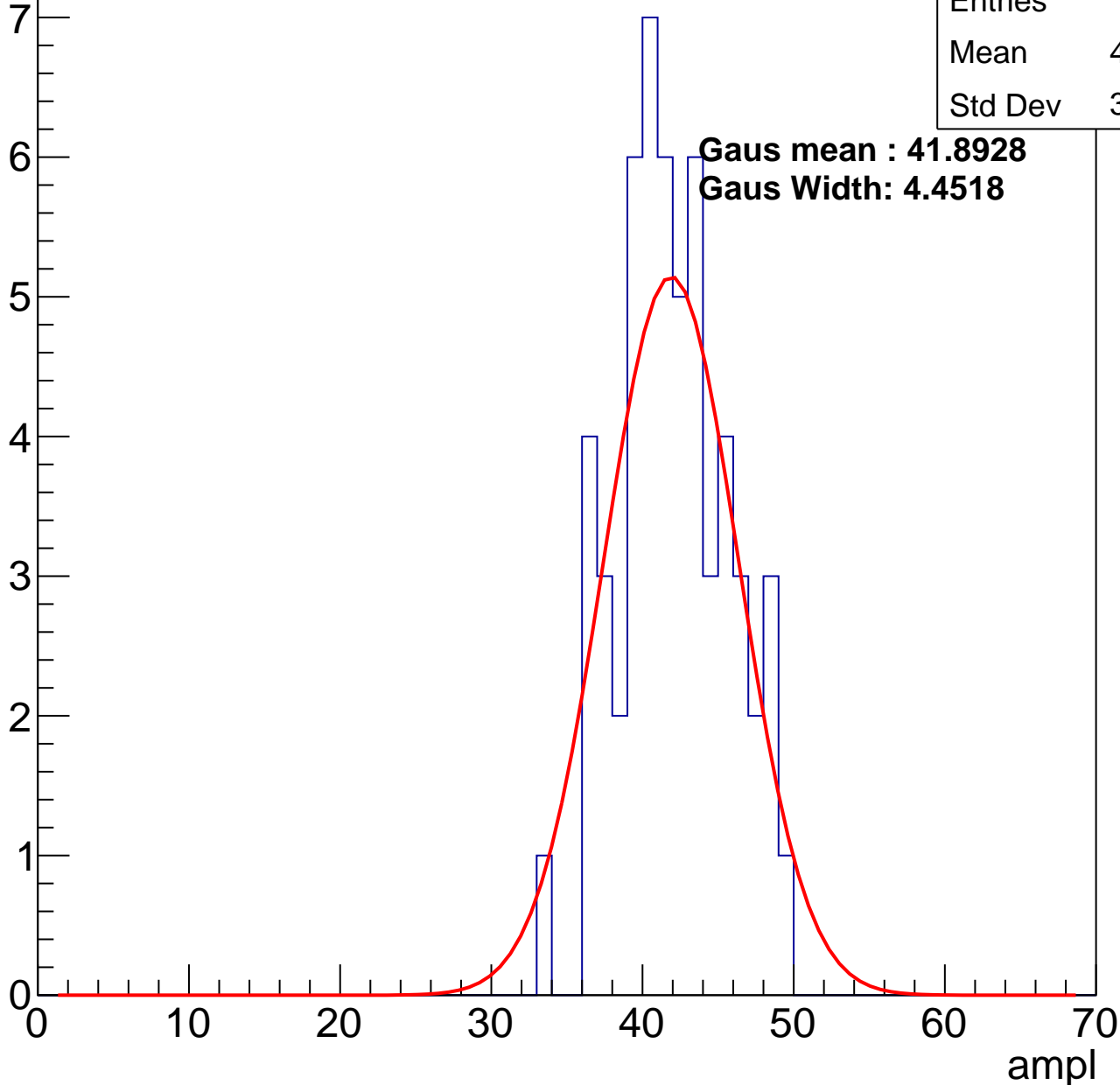
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.59
Std Dev	3.585

Gaus mean : 41.8928

Gaus Width: 4.4518

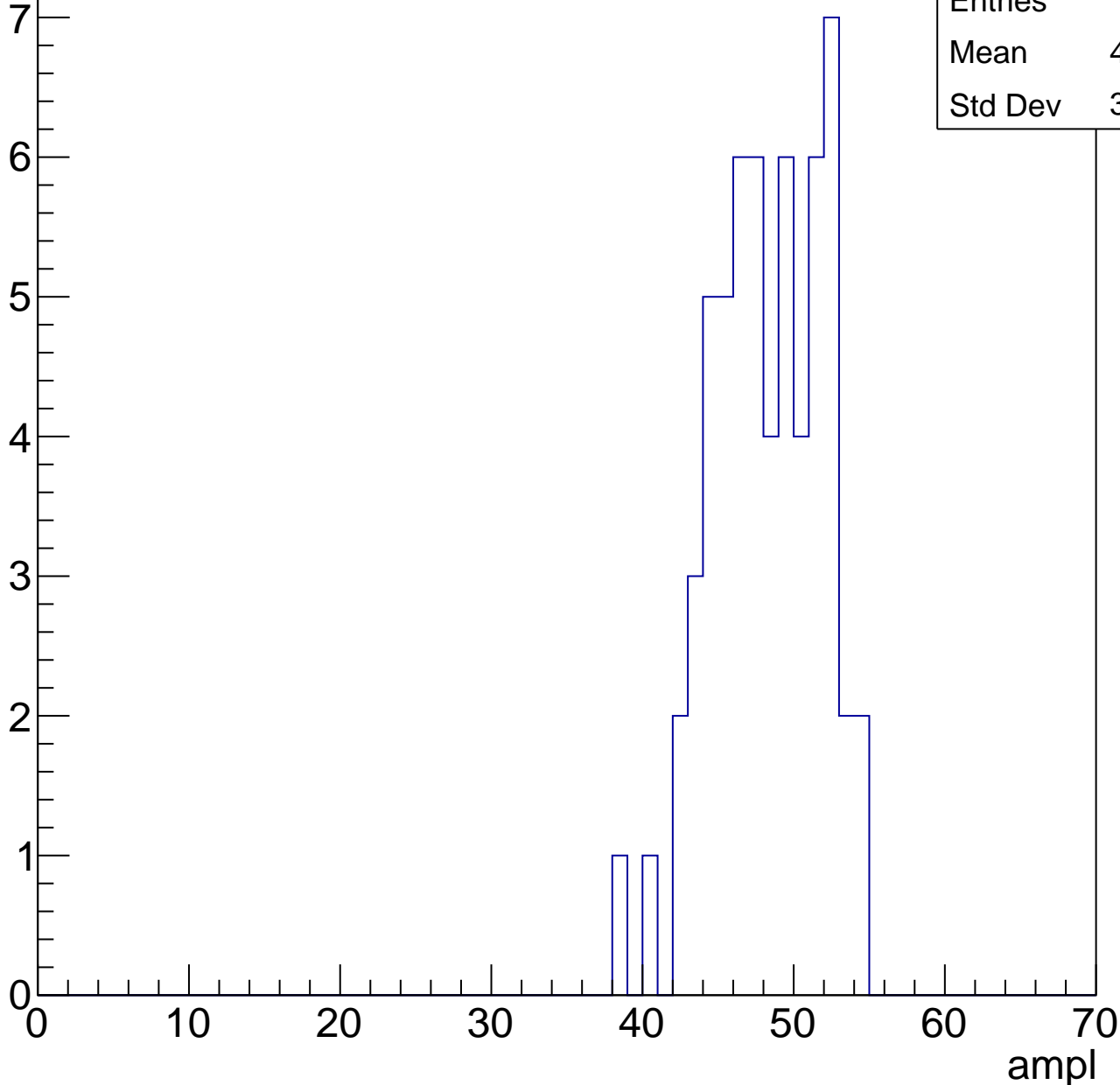


B1L101S, U4-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

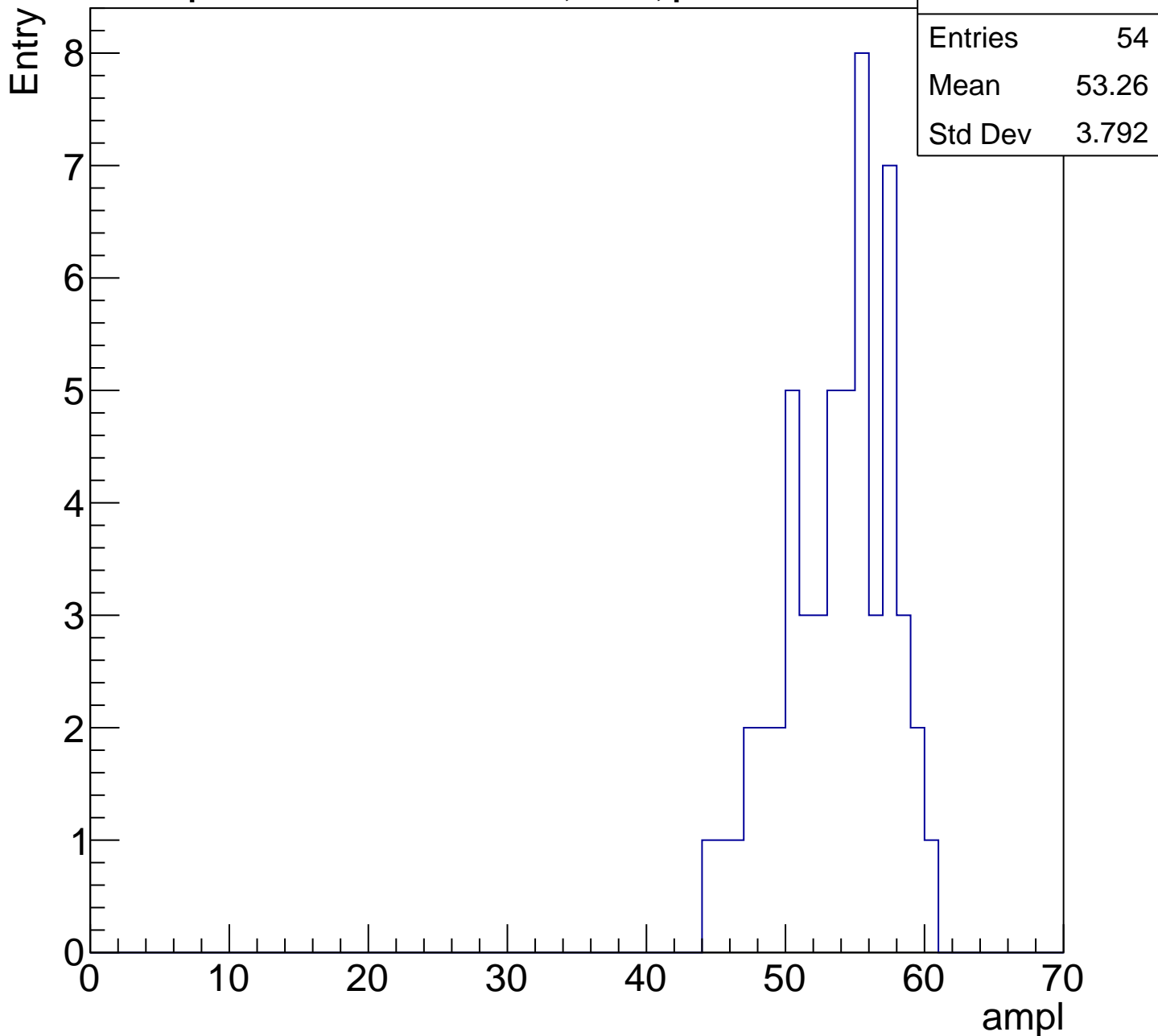
Entry

Entries	60
Mean	47.73
Std Dev	3.586



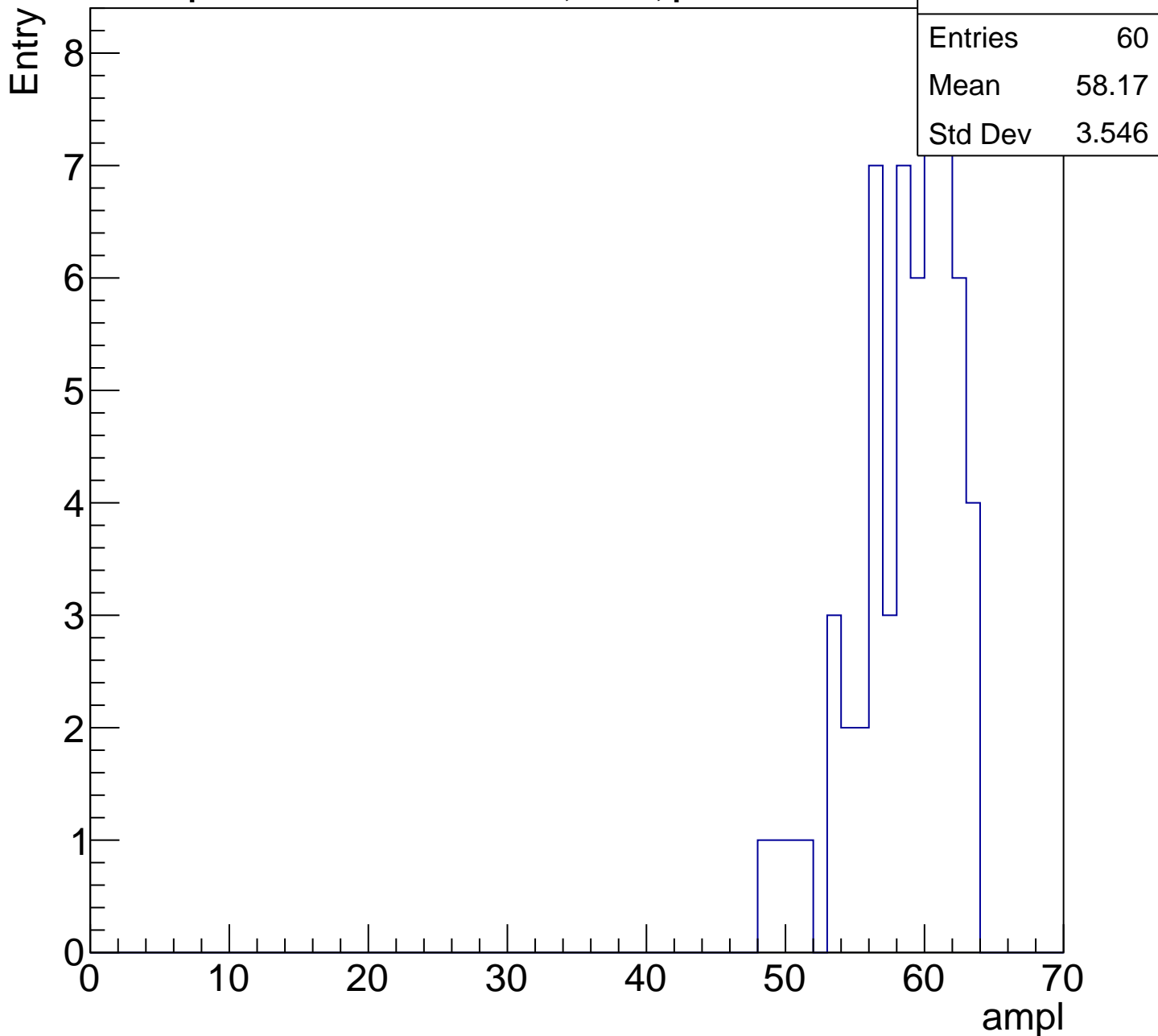
B1L101S, U4-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

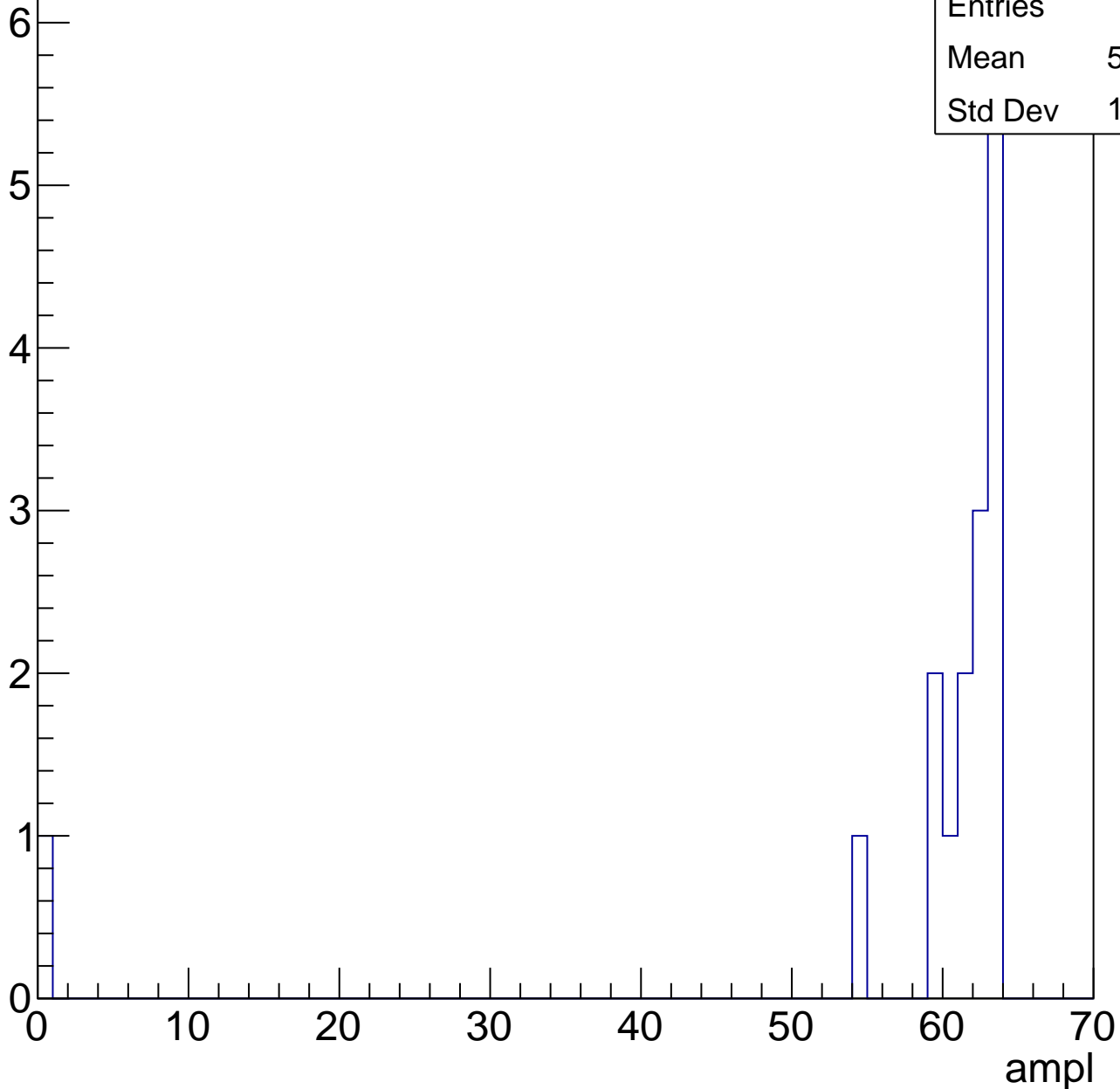


B1L101S, U4-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	57.38
Std Dev	14.99



B1L101S, U4-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch61, adc0

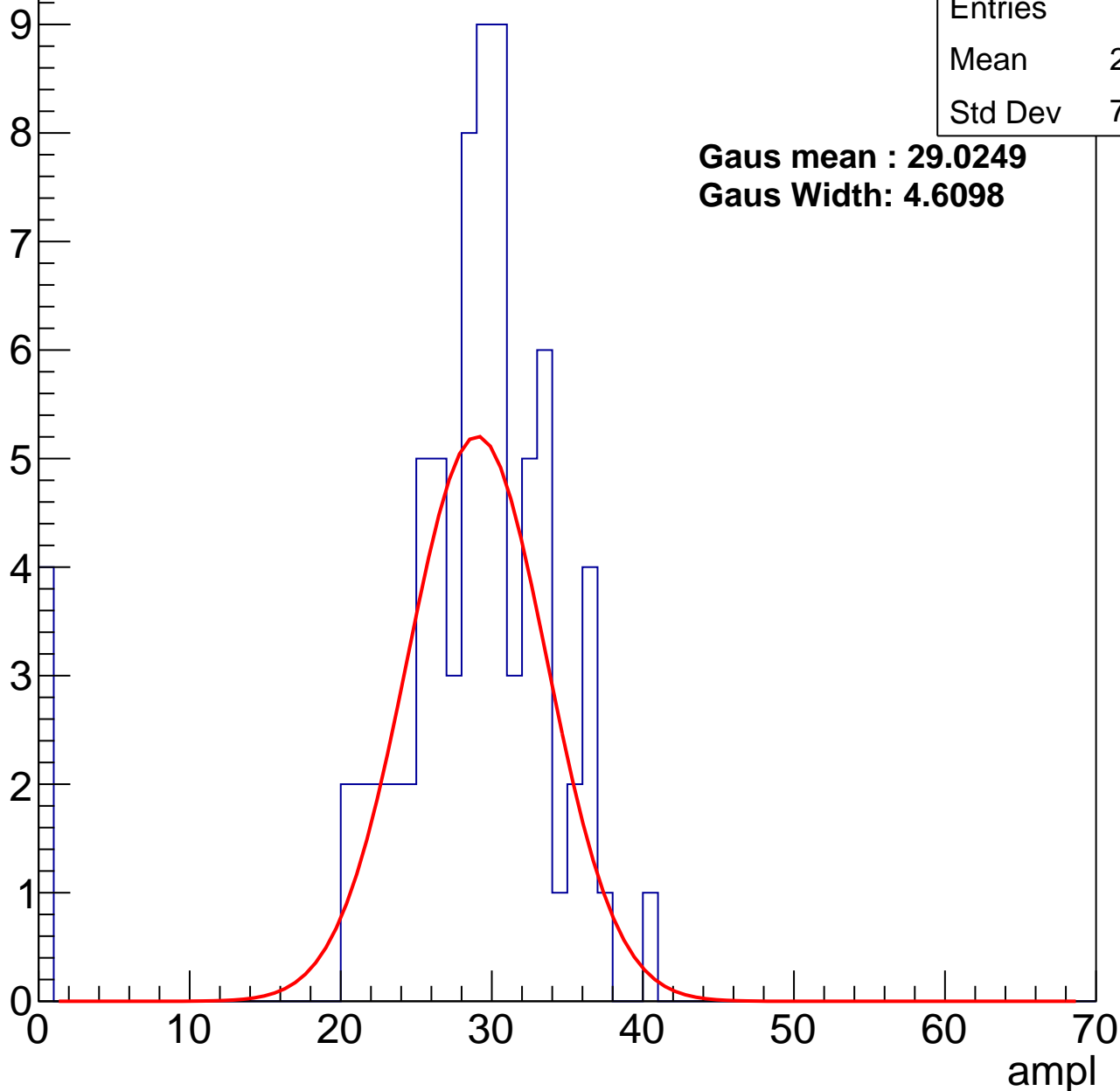
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	27.46
Std Dev	7.677

Gaus mean : 29.0249

Gaus Width: 4.6098



B1L101S, U4-ch61, adc1

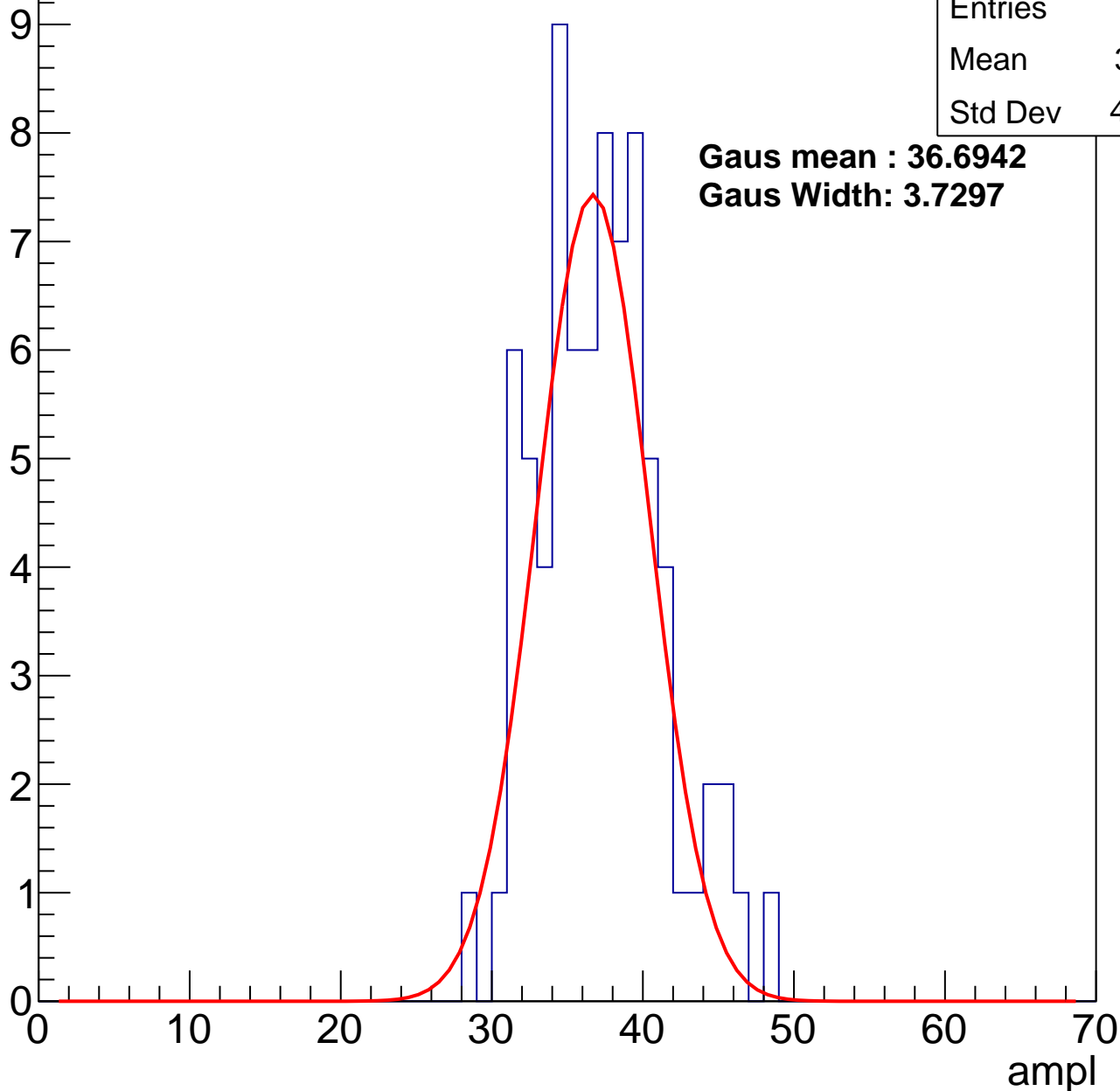
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.71
Std Dev	4.045

Gaus mean : 36.6942

Gaus Width: 3.7297



B1L101S, U4-ch61, adc2

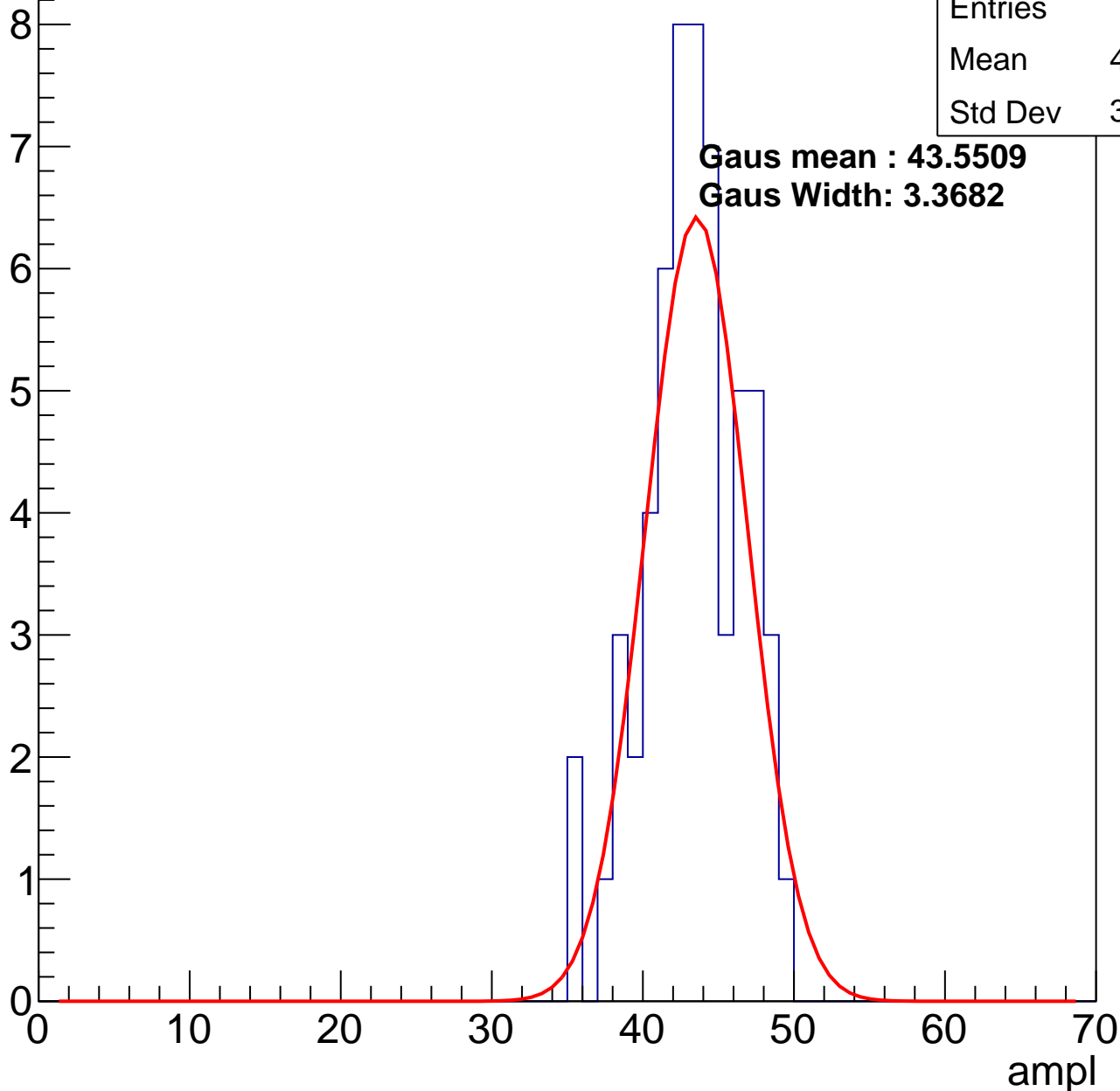
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.86
Std Dev	3.197

Gaus mean : 43.5509

Gaus Width: 3.3682

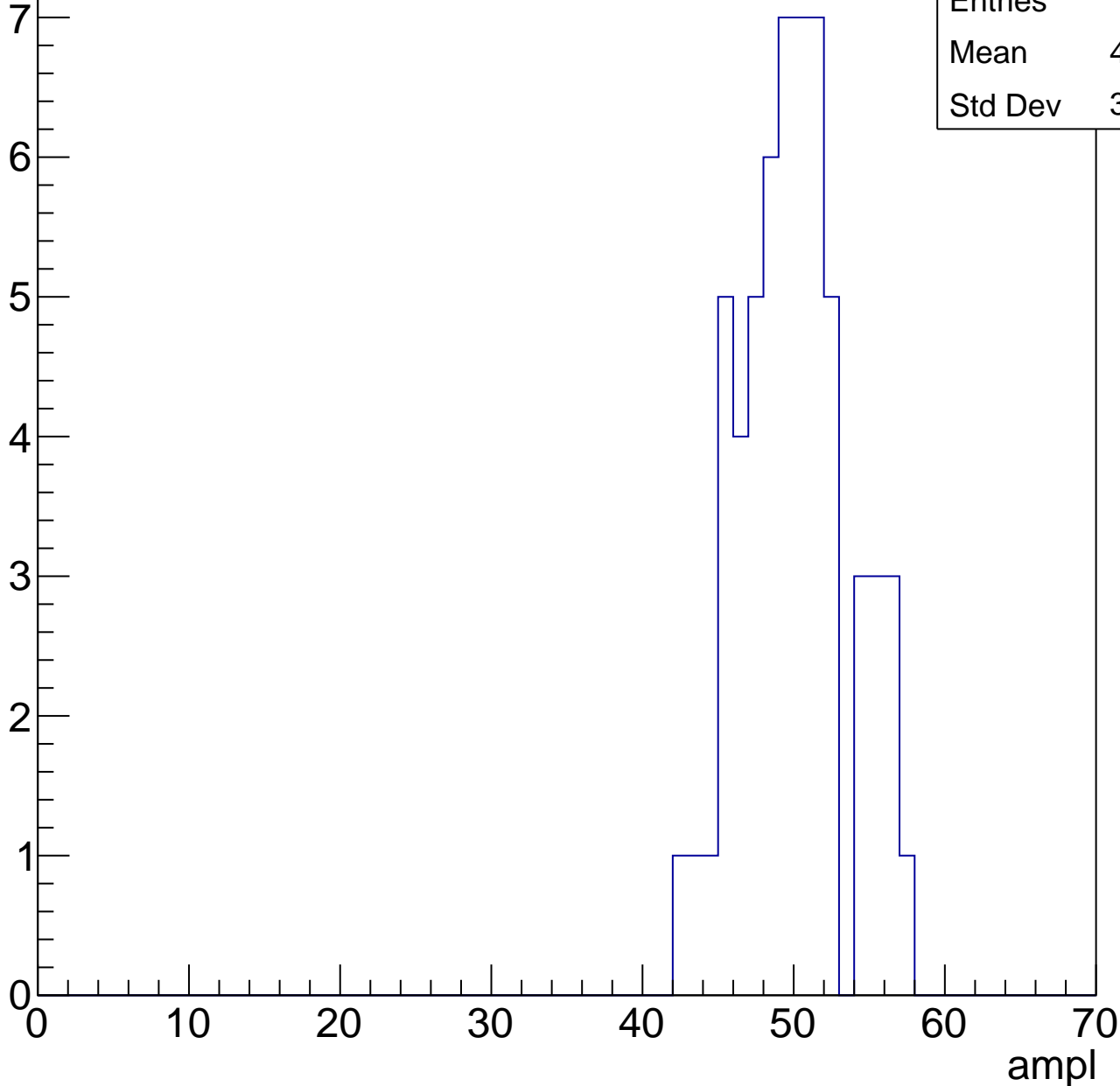


B1L101S, U4-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.54
Std Dev	3.466

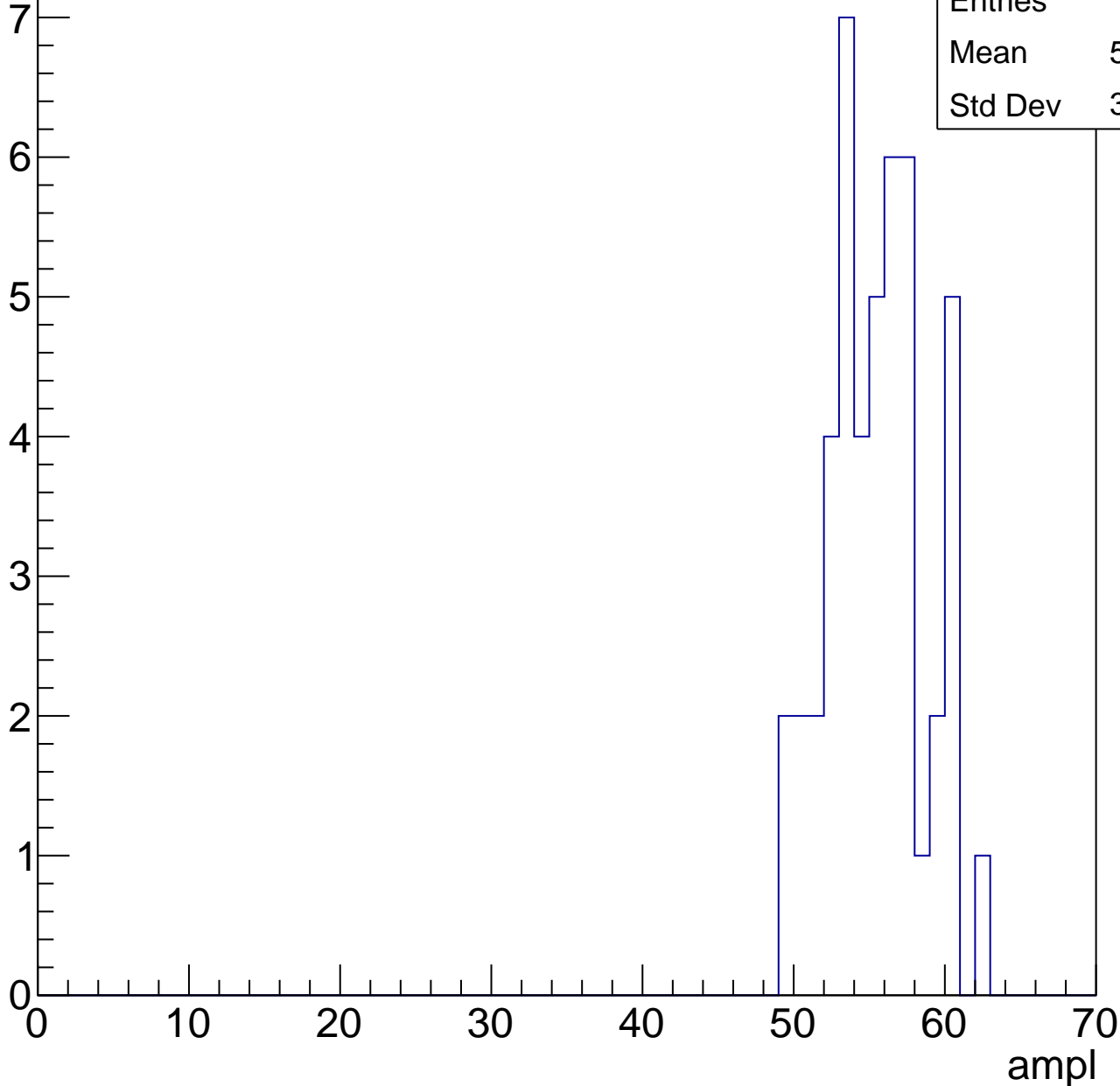


B1L101S, U4-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	55.02
Std Dev	3.152



B1L101S, U4-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

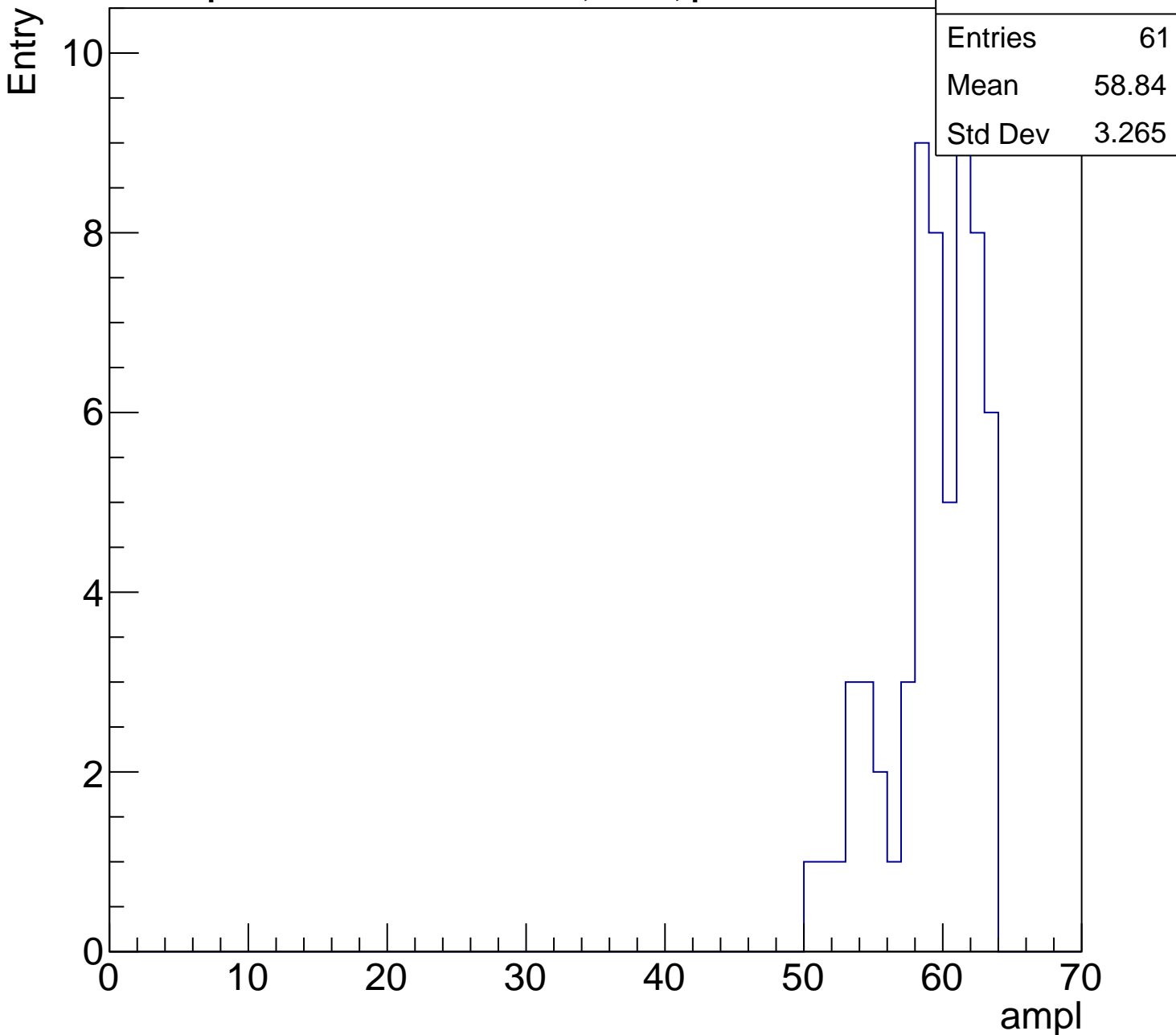
40

50

60

ampl

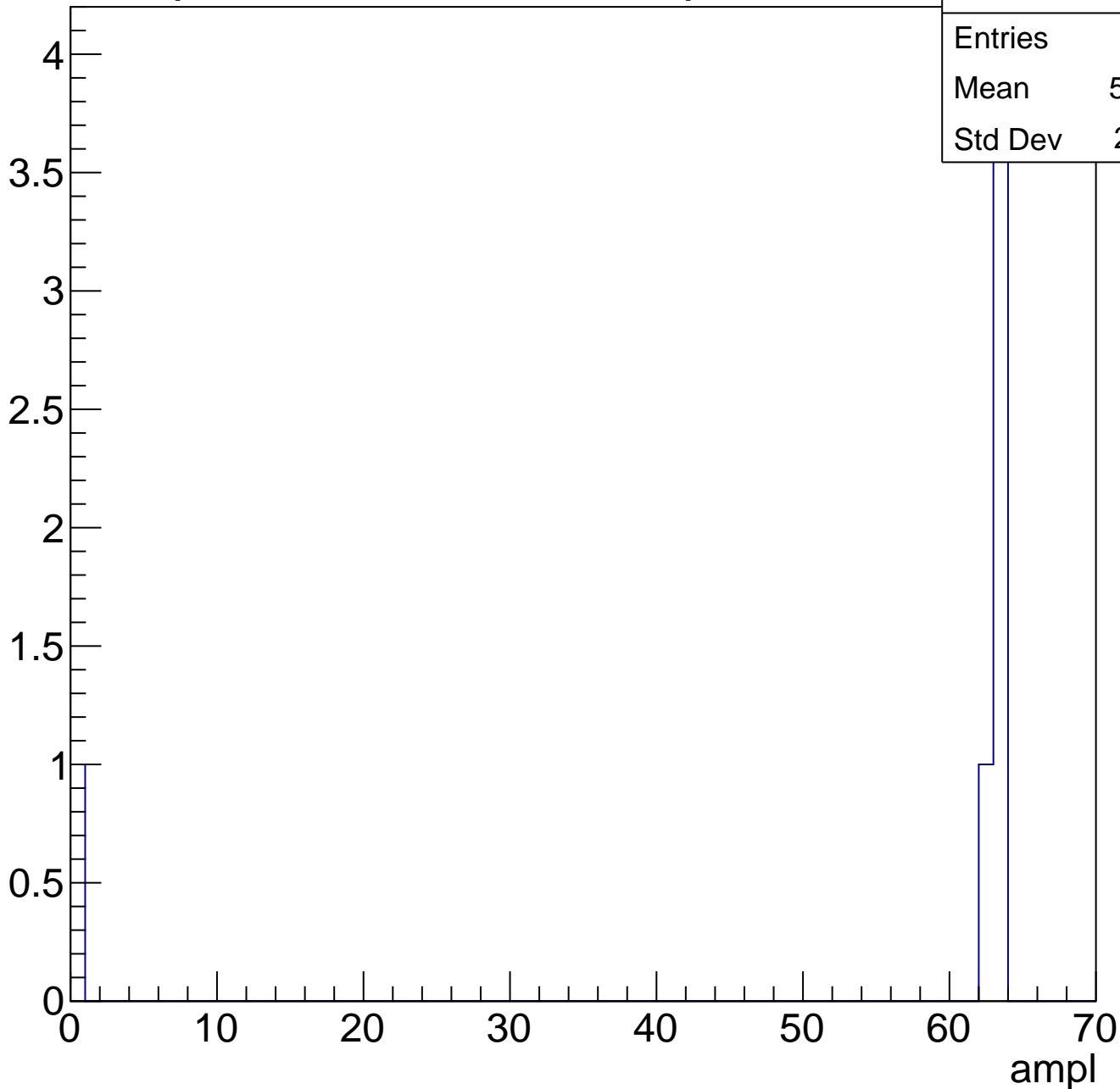
Entries	61
Mean	58.84
Std Dev	3.265



B1L101S, U4-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch62, adc0

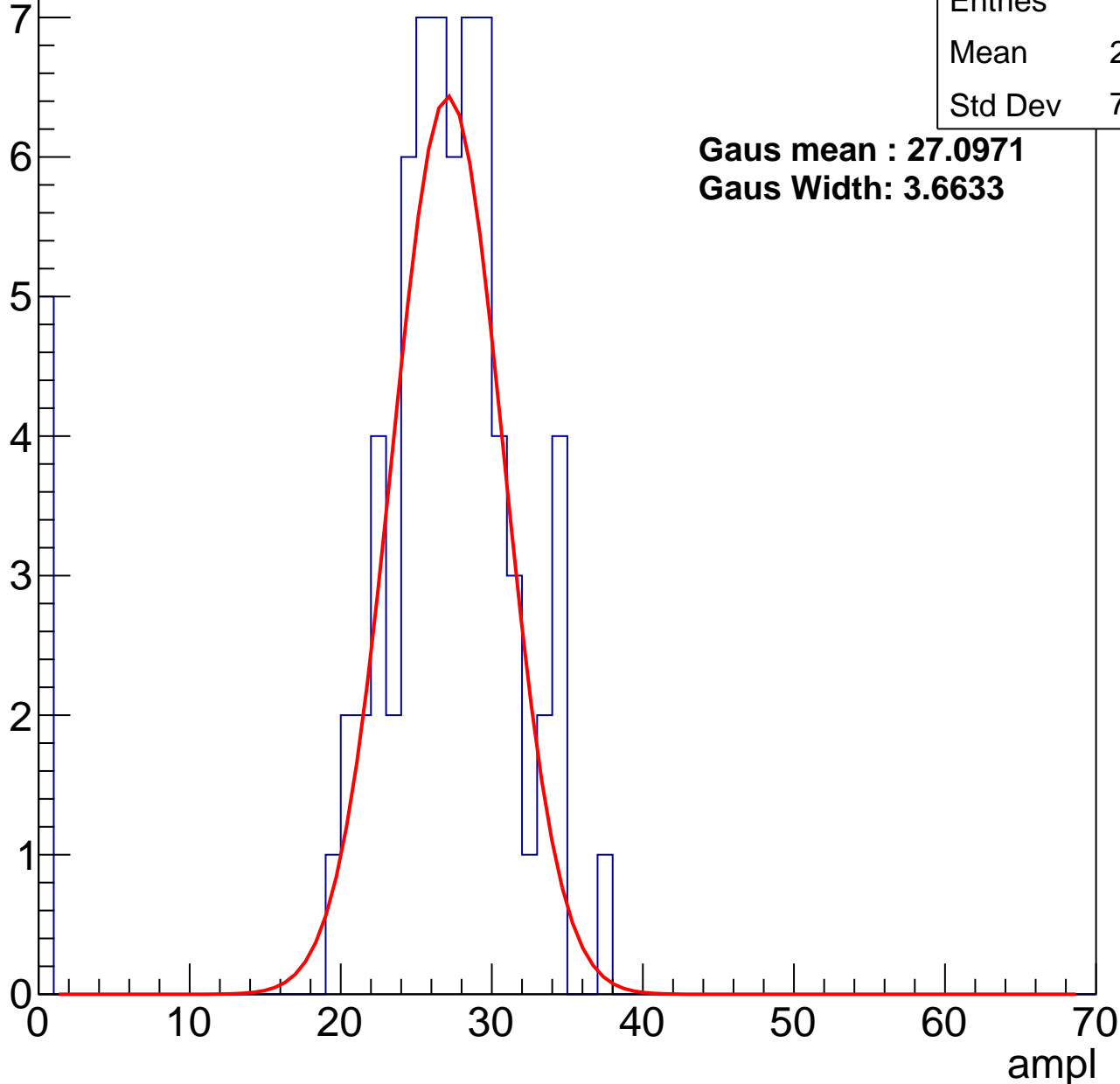
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	25.08
Std Dev	7.829

Gaus mean : 27.0971

Gaus Width: 3.6633



B1L101S, U4-ch62, adc1

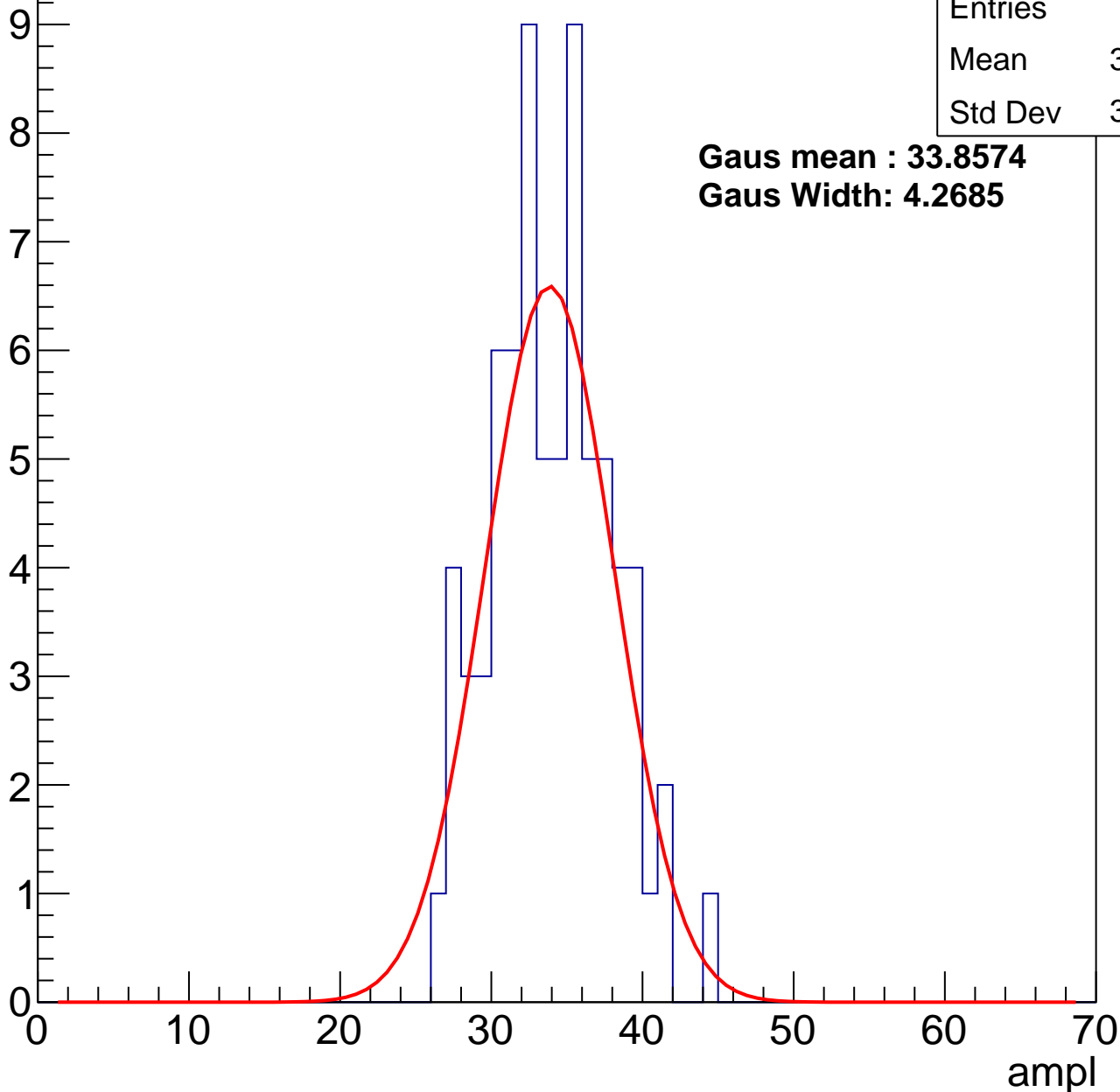
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	33.53
Std Dev	3.864

Gaus mean : 33.8574

Gaus Width: 4.2685



B1L101S, U4-ch62, adc2

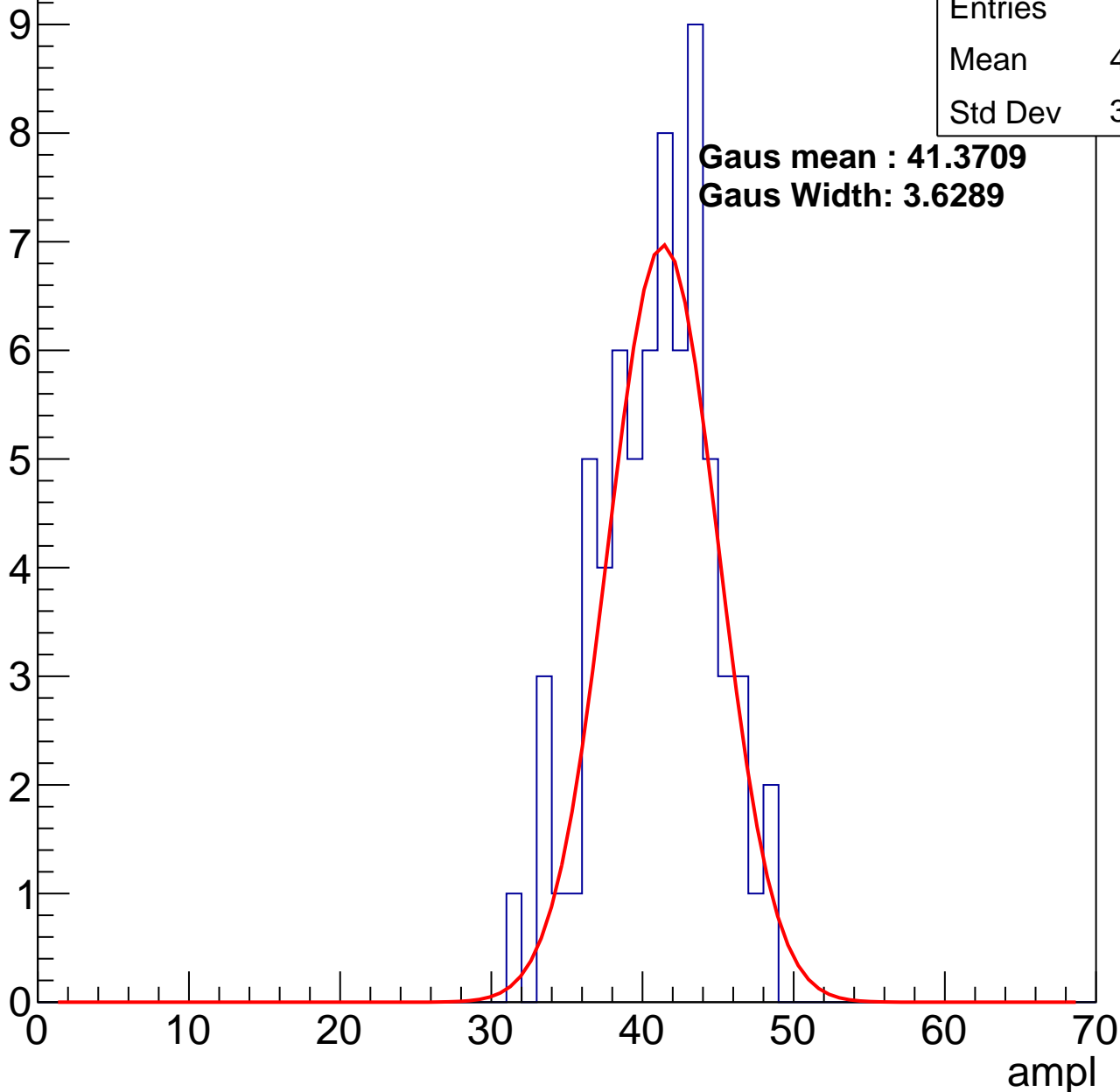
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	40.48
Std Dev	3.752

Gaus mean : 41.3709

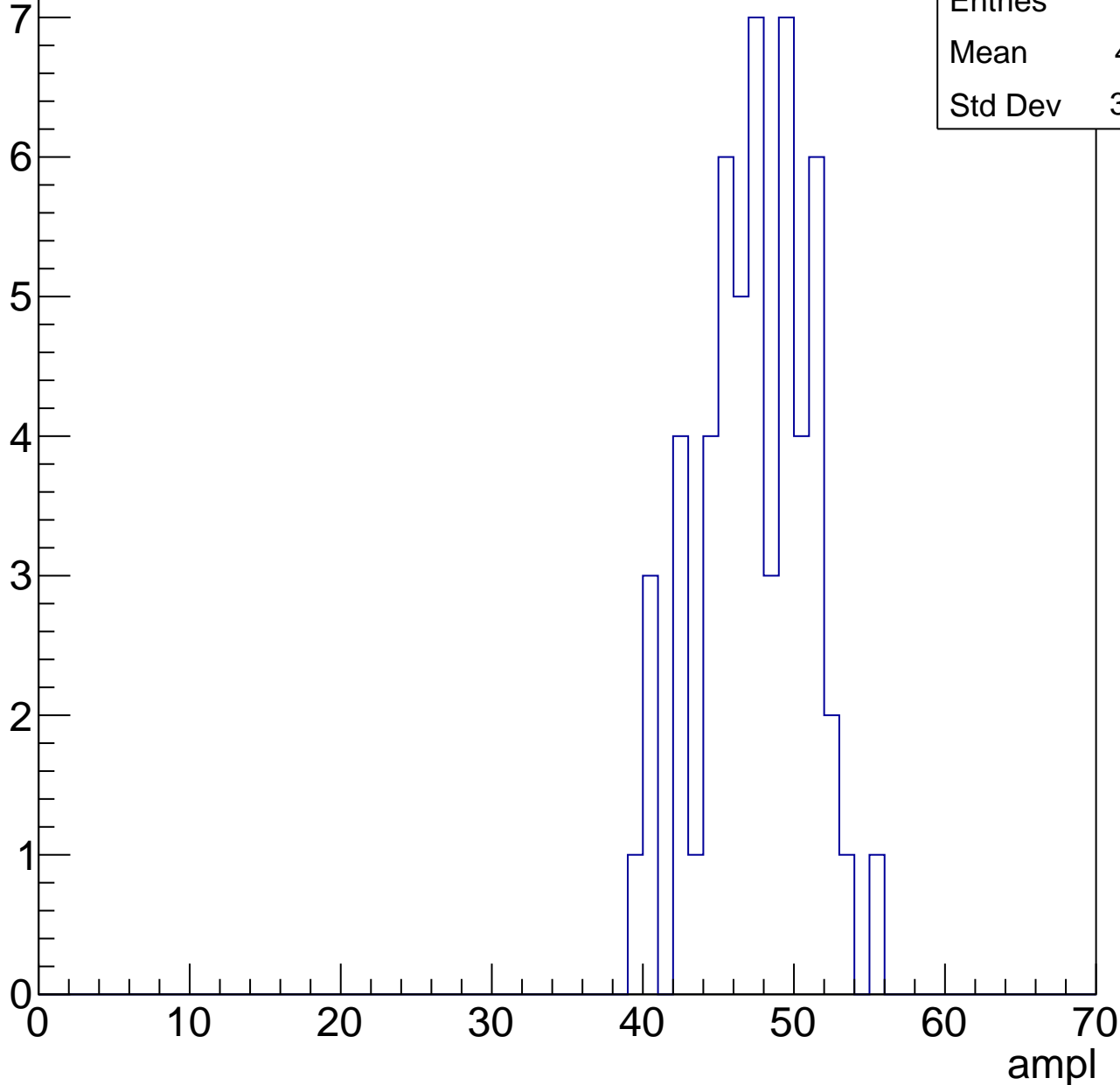
Gaus Width: 3.6289



B1L101S, U4-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



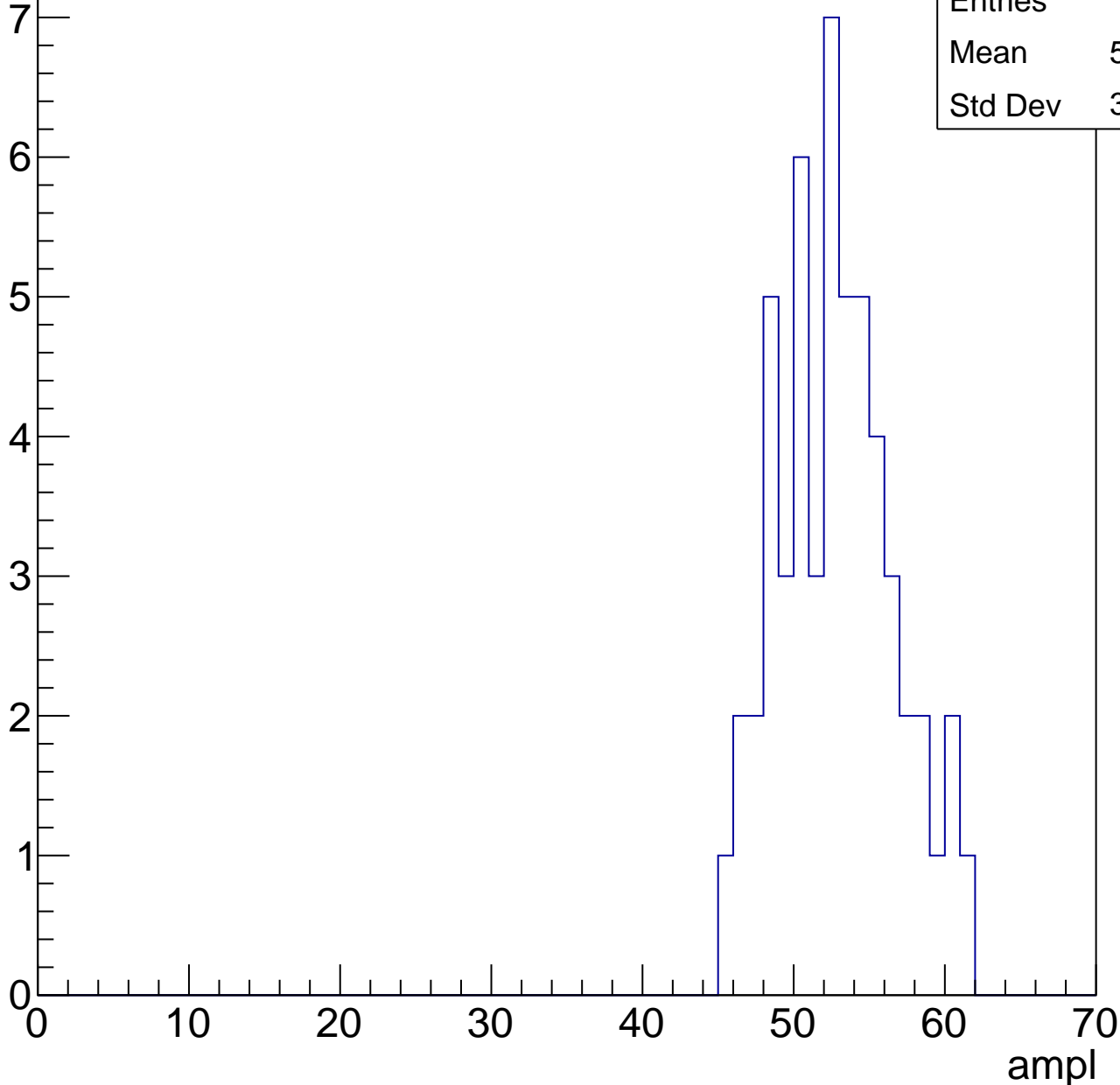
Entries	55
Mean	46.91
Std Dev	3.579

B1L101S, U4-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

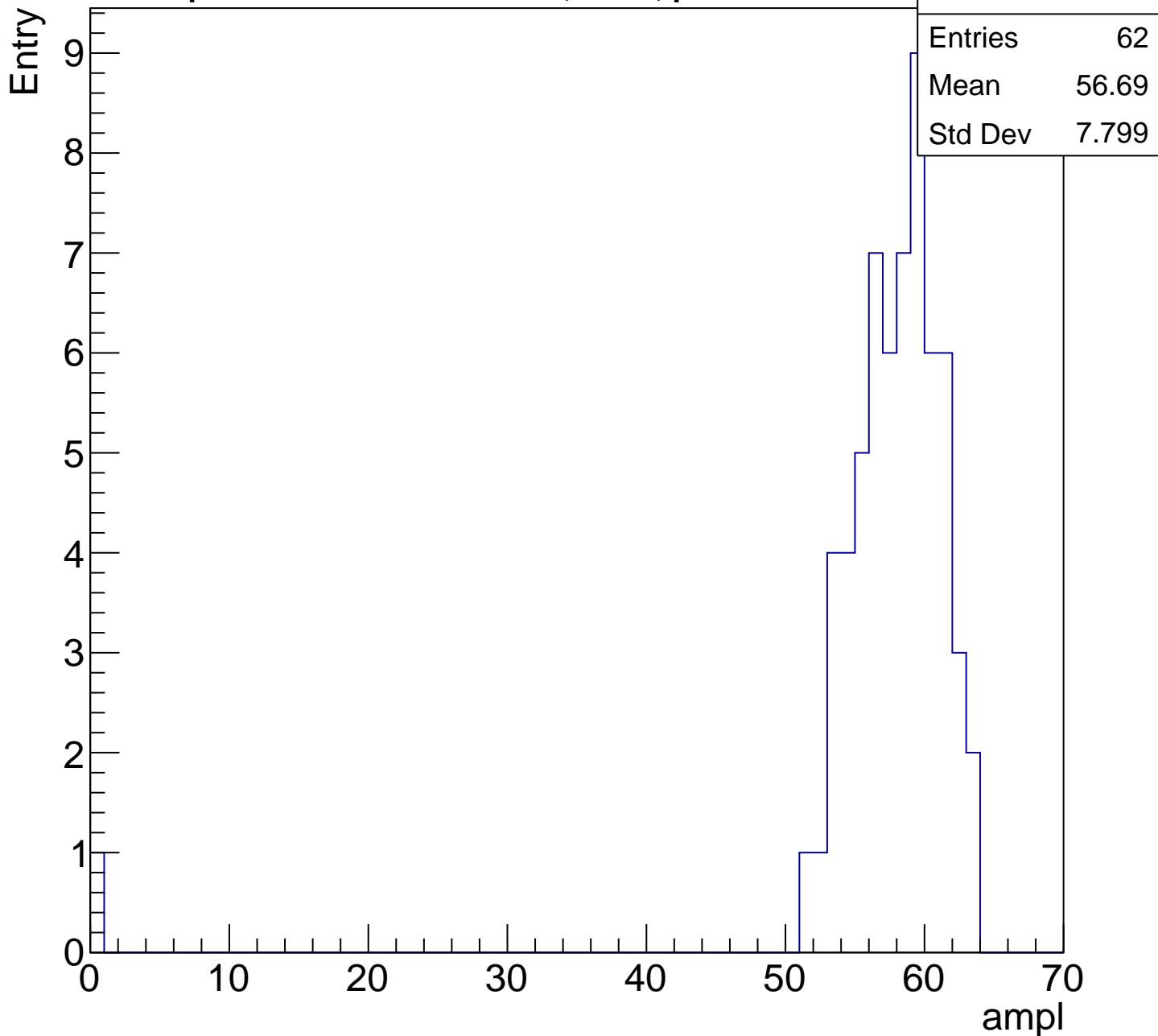
Entry

Entries	54
Mean	52.37
Std Dev	3.817



B1L101S, U4-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

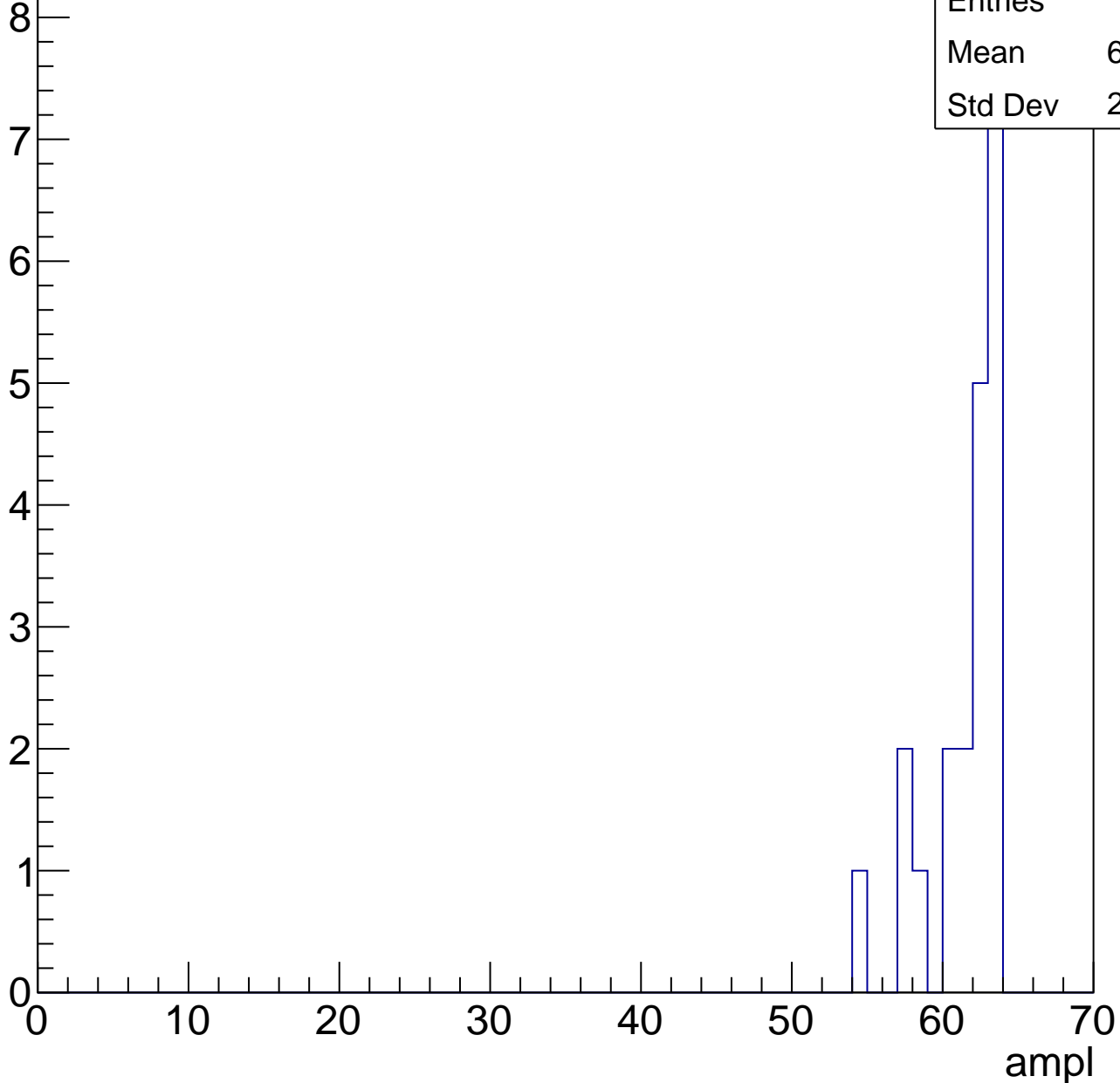


B1L101S, U4-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

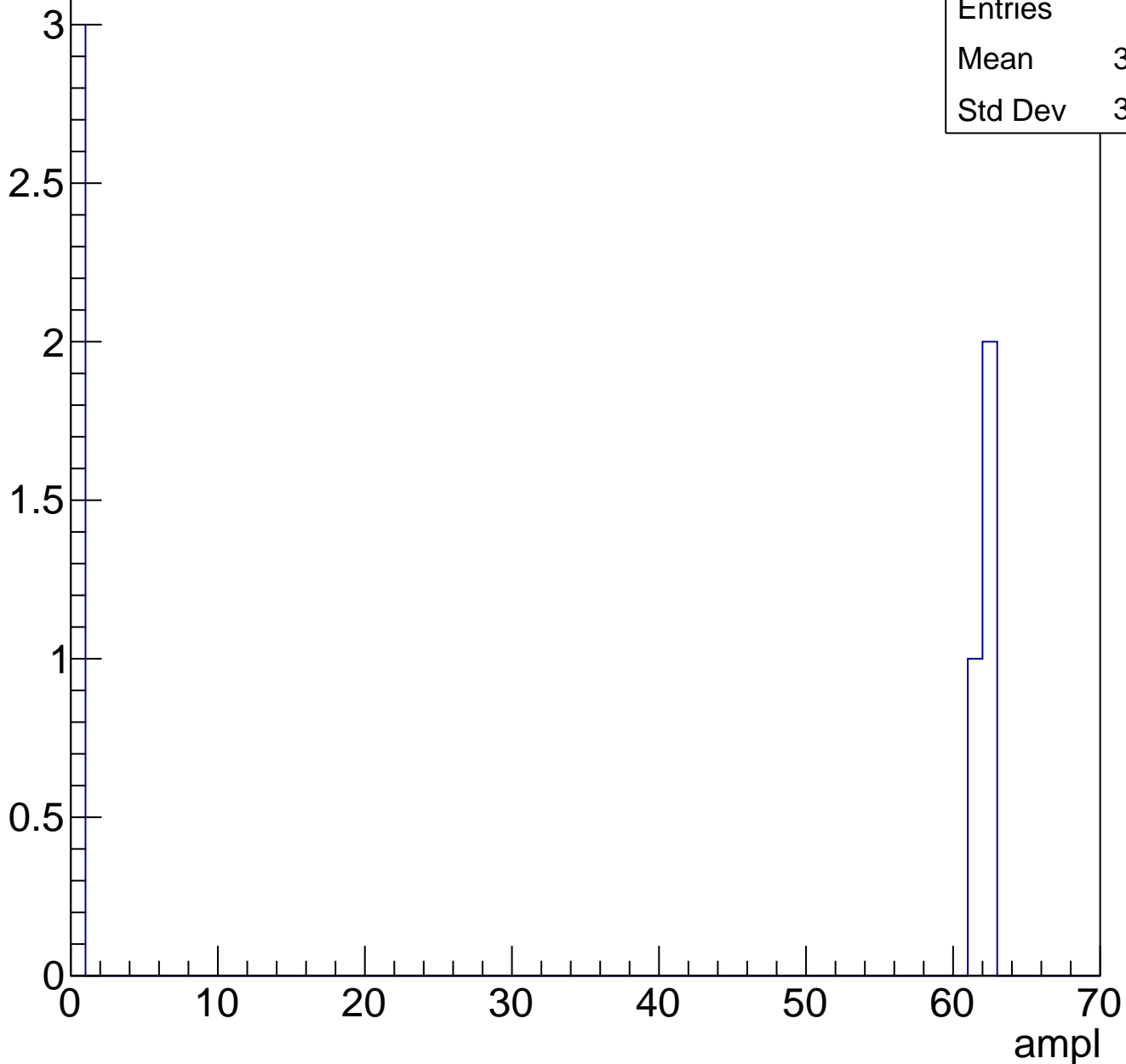
Entries	21
Mean	61.05
Std Dev	2.478



B1L101S, U4-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch63, adc0

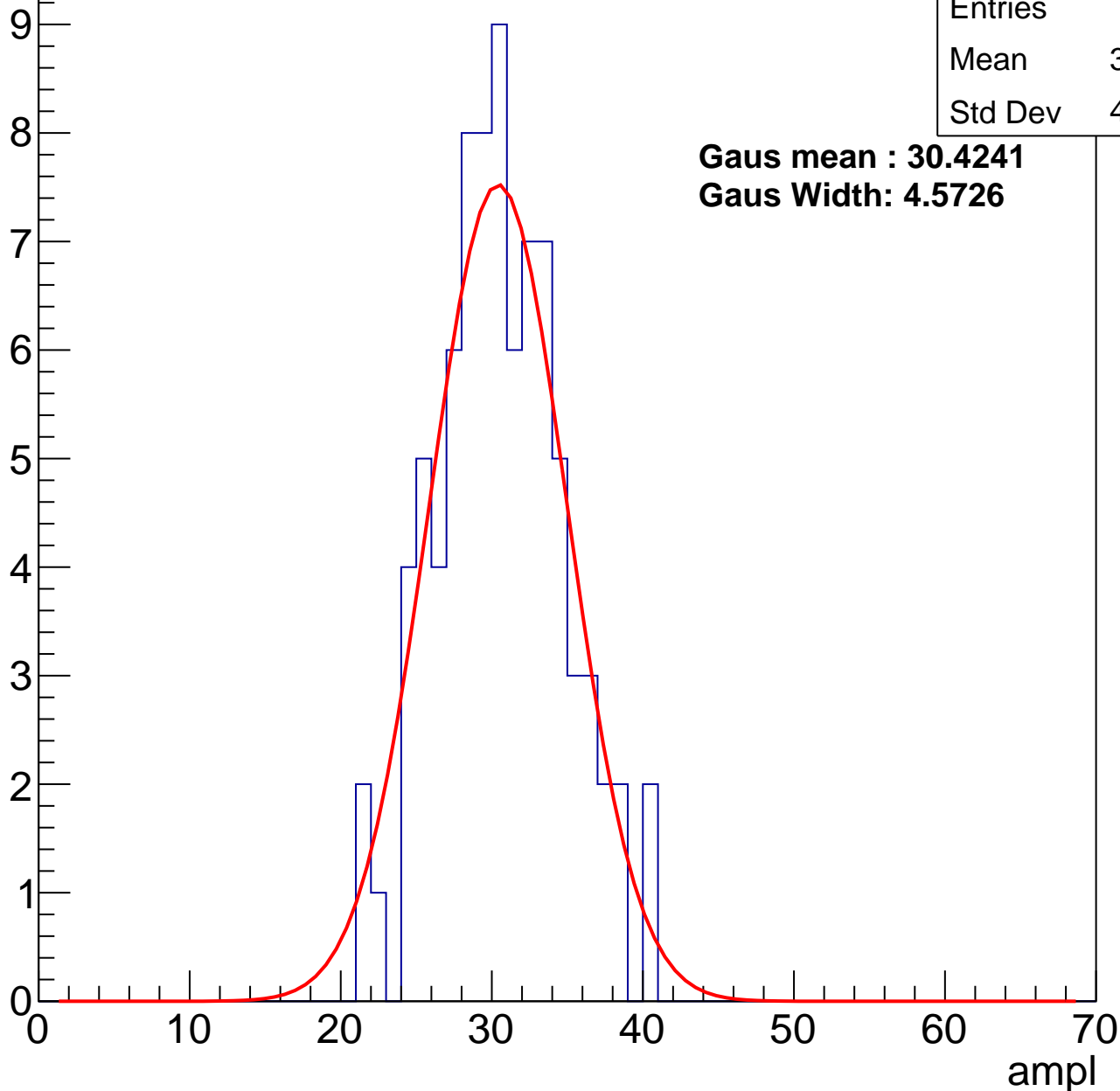
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	30.13
Std Dev	4.137

Gaus mean : 30.4241

Gaus Width: 4.5726



B1L101S, U4-ch63, adc1

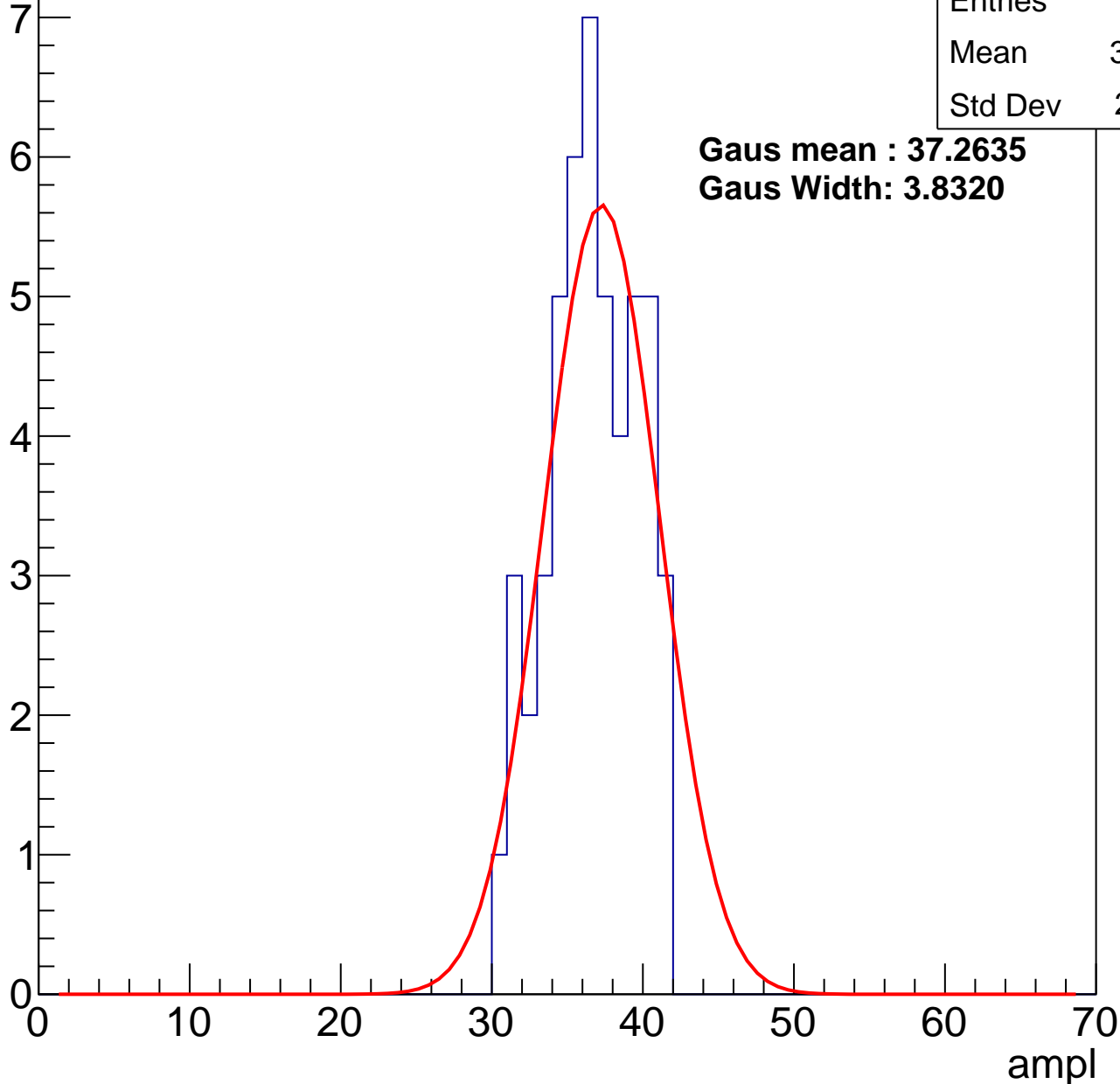
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	36.18
Std Dev	2.911

Gaus mean : 37.2635

Gaus Width: 3.8320



B1L101S, U4-ch63, adc2

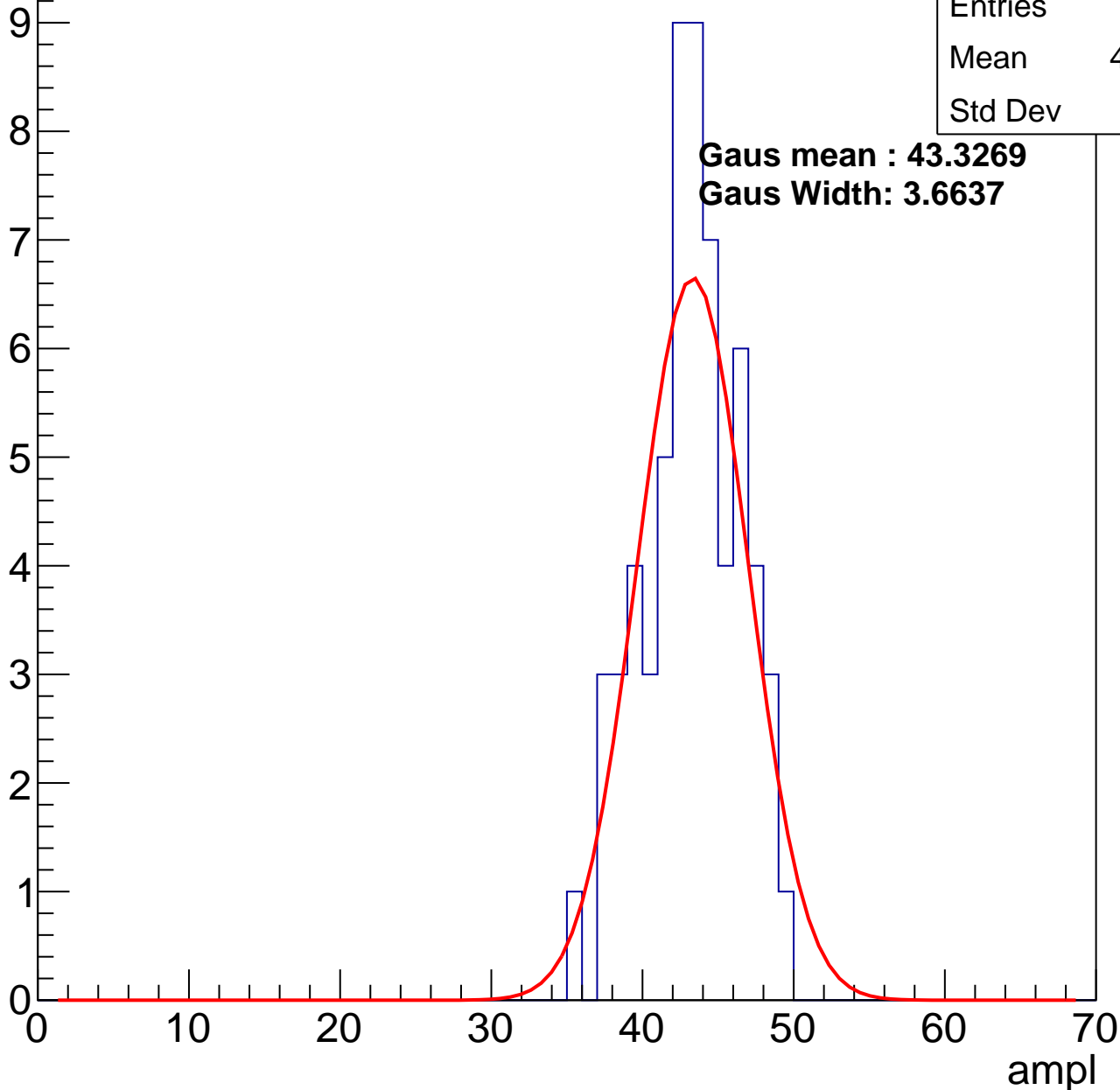
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.76
Std Dev	3.15

Gaus mean : 43.3269

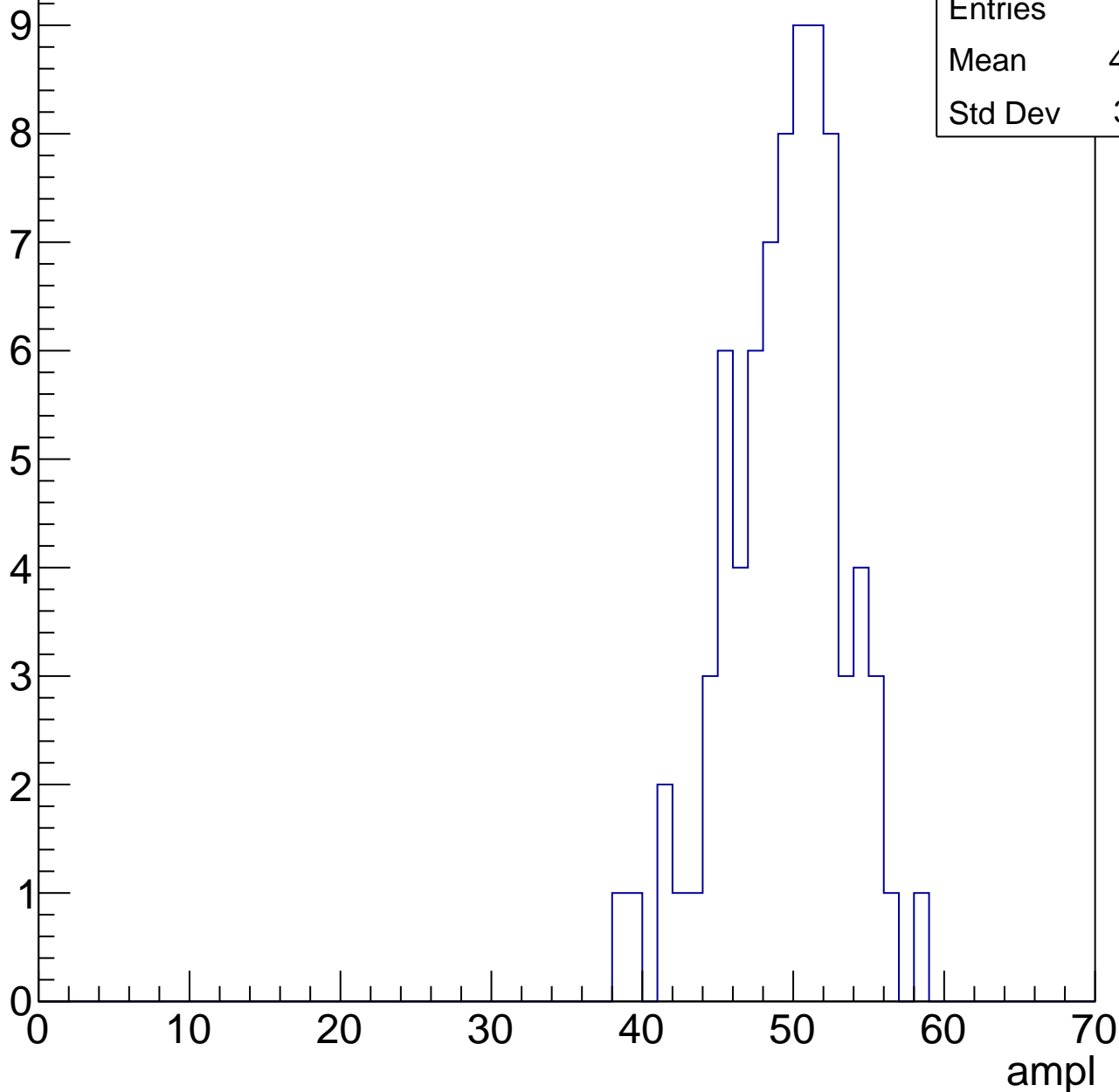
Gaus Width: 3.6637



B1L101S, U4-ch63, adc3

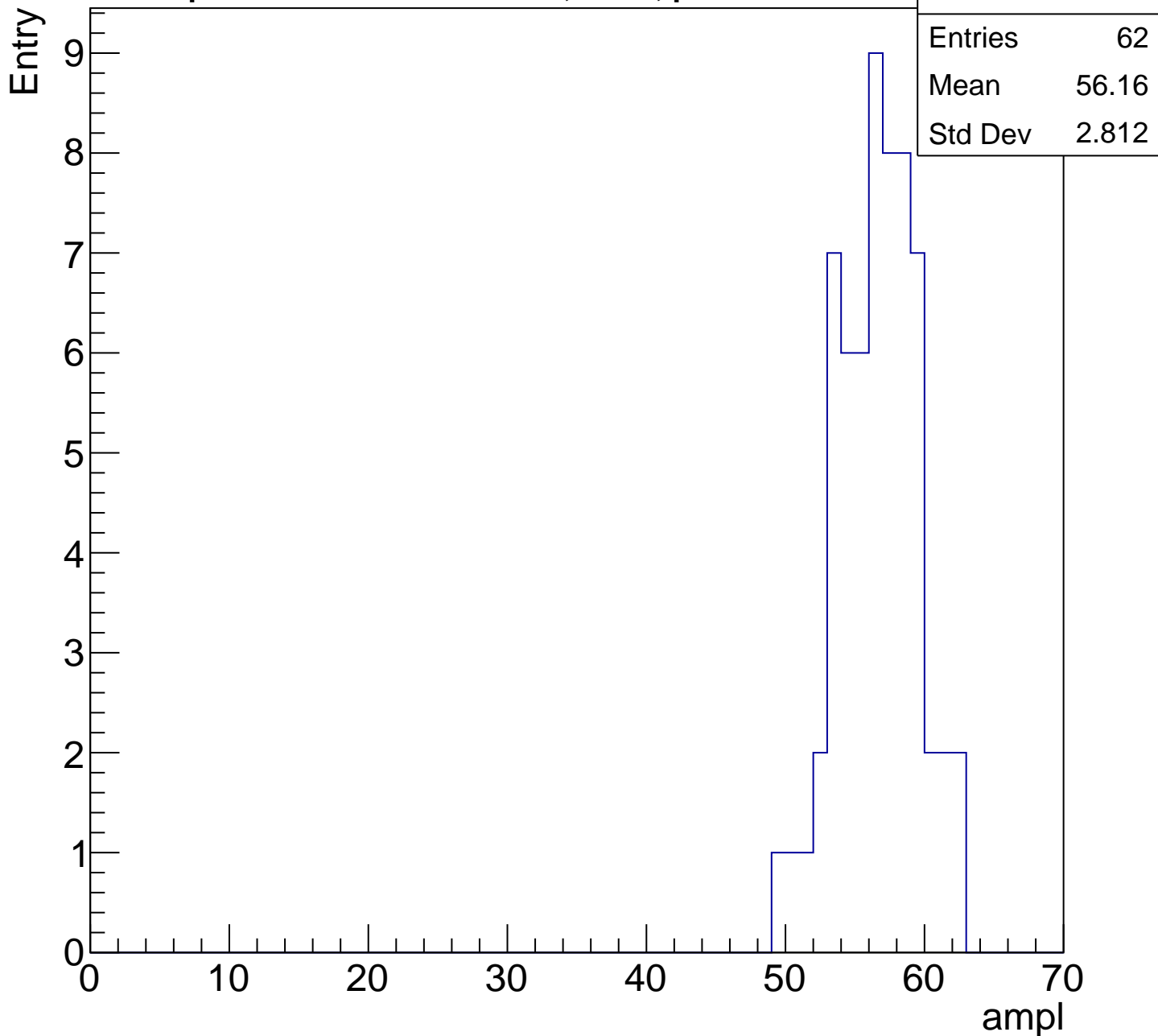
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

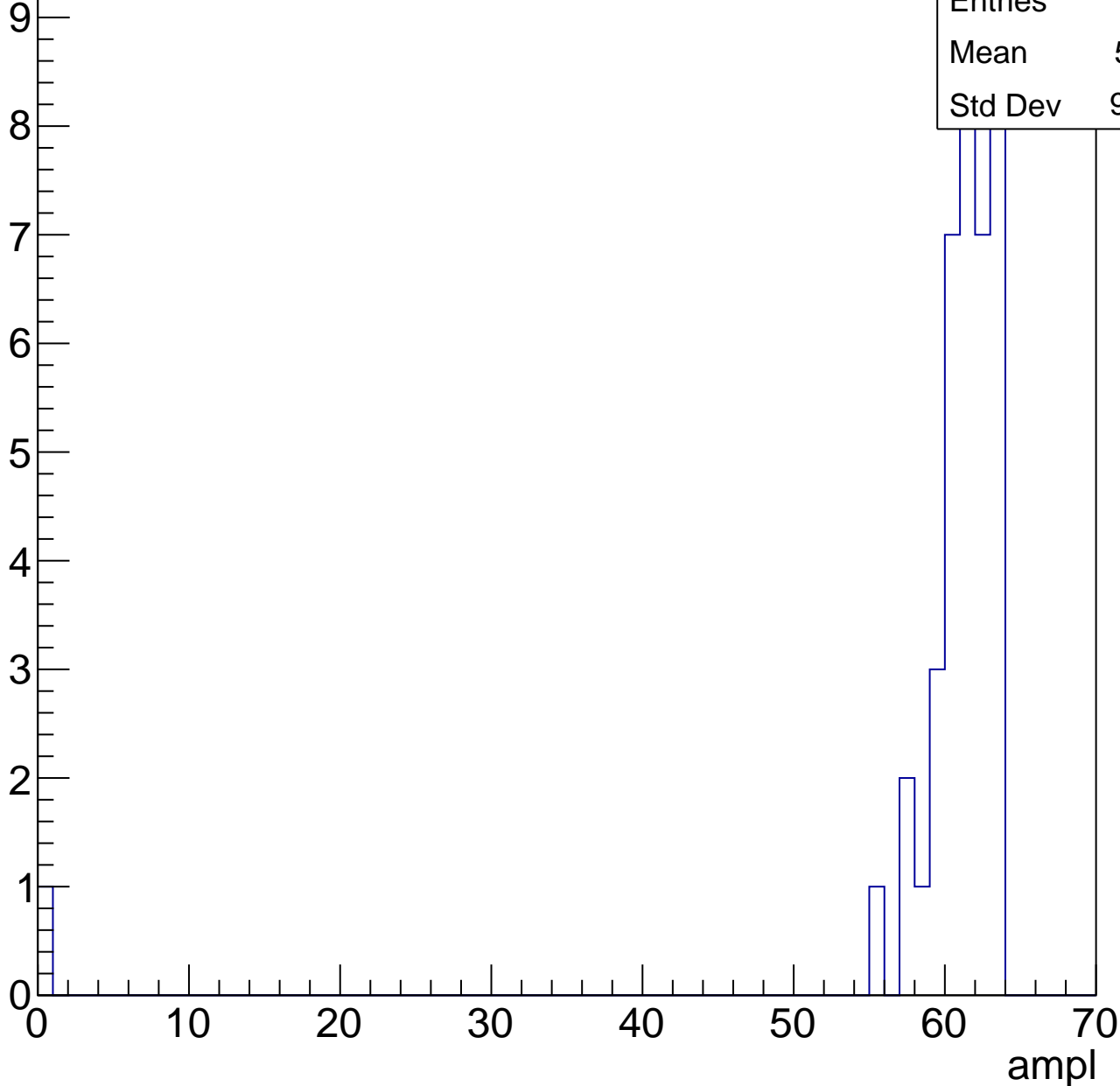


B1L101S, U4-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

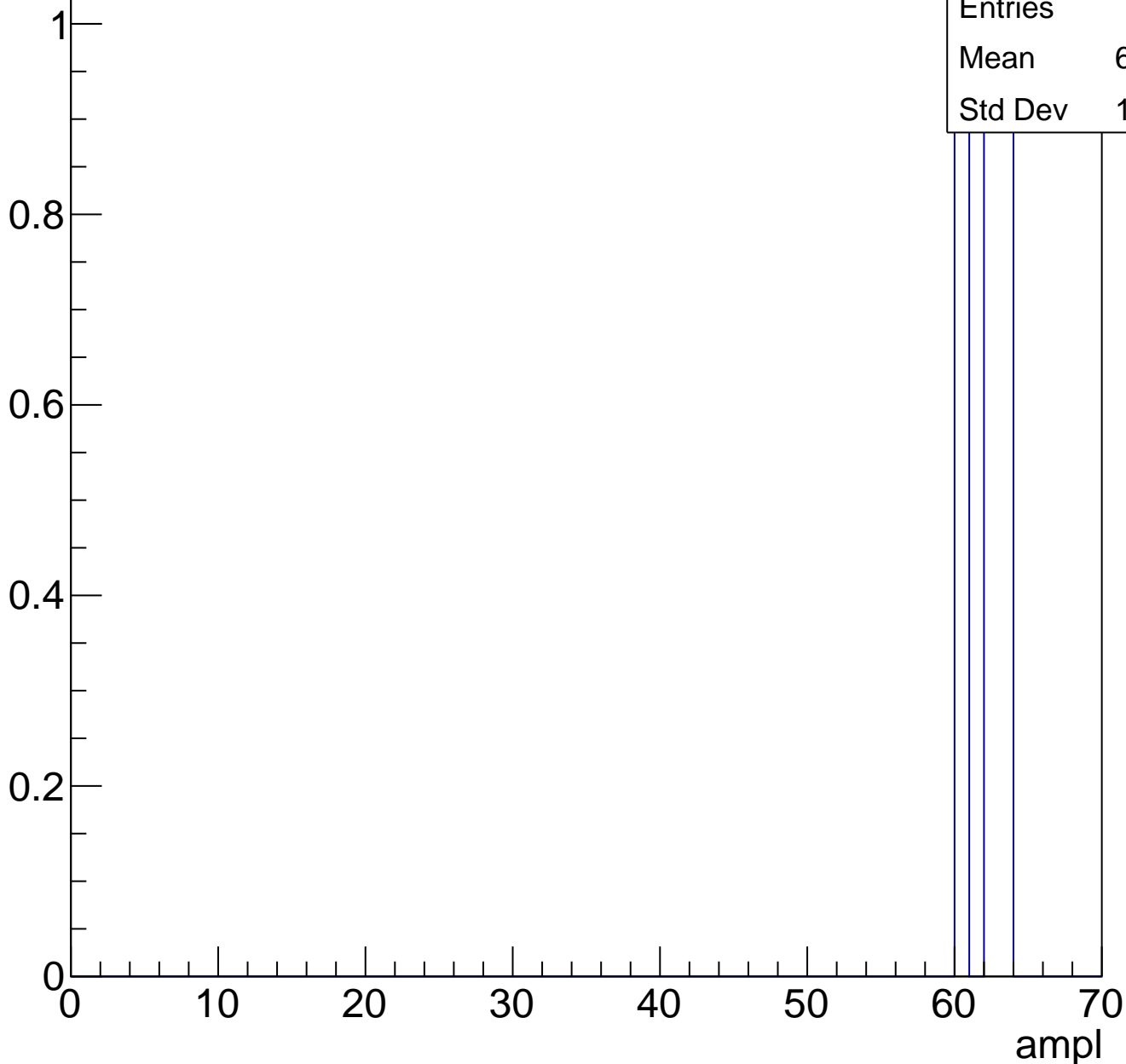
Entries	39
Mean	59.31
Std Dev	9.804



B1L101S, U4-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

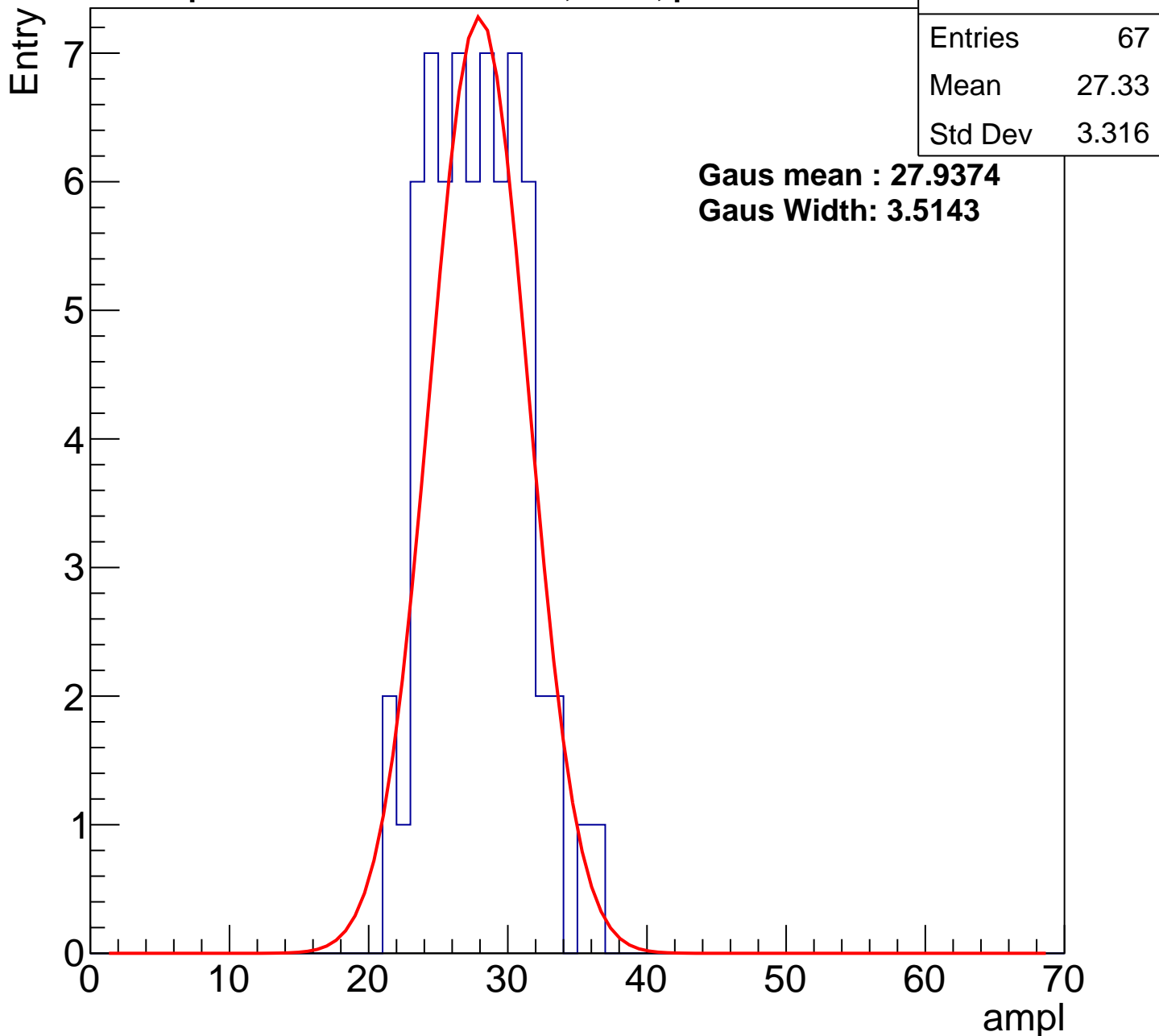
Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U4-ch64, adc0

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch64, adc1

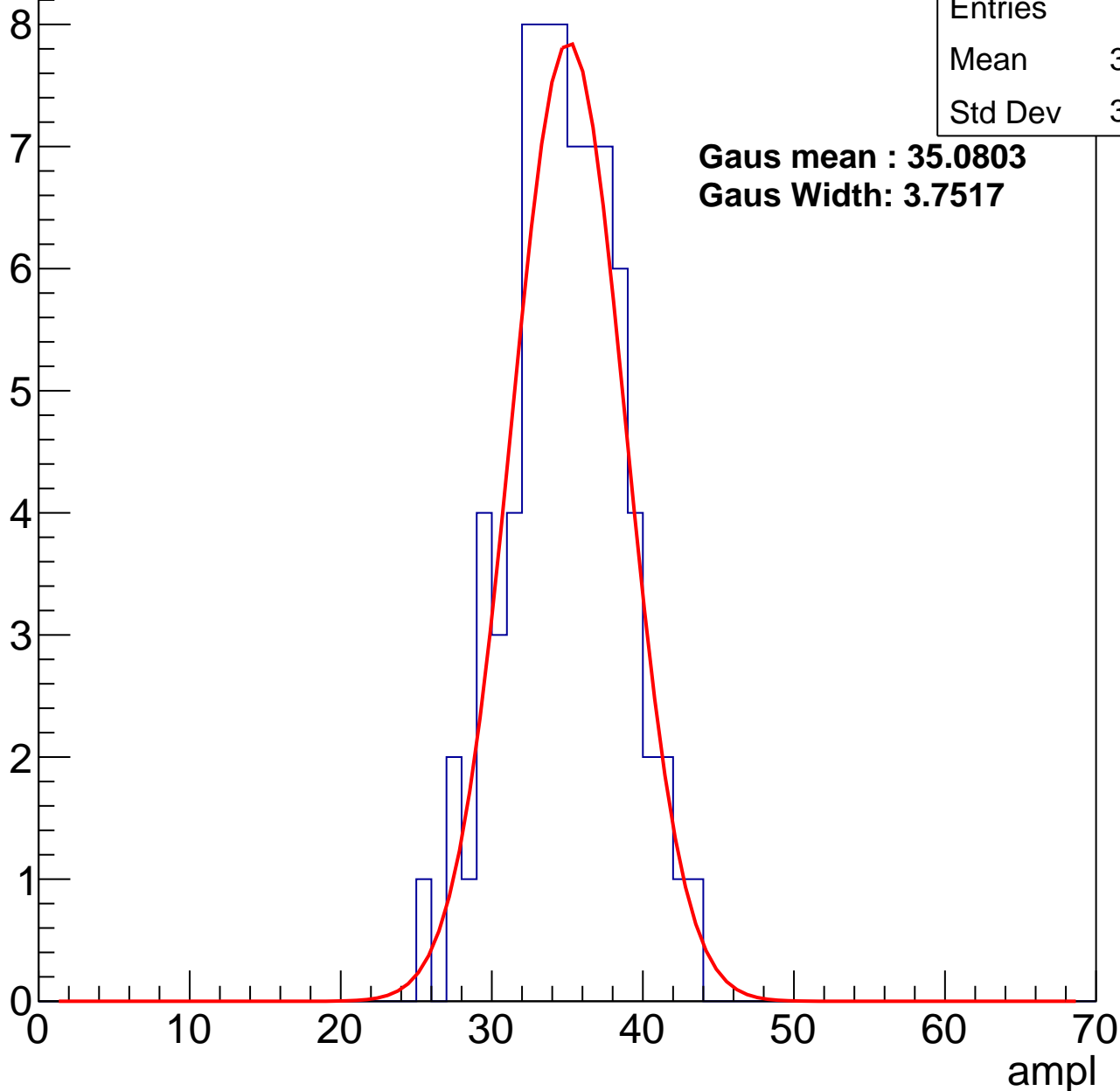
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	34.42
Std Dev	3.689

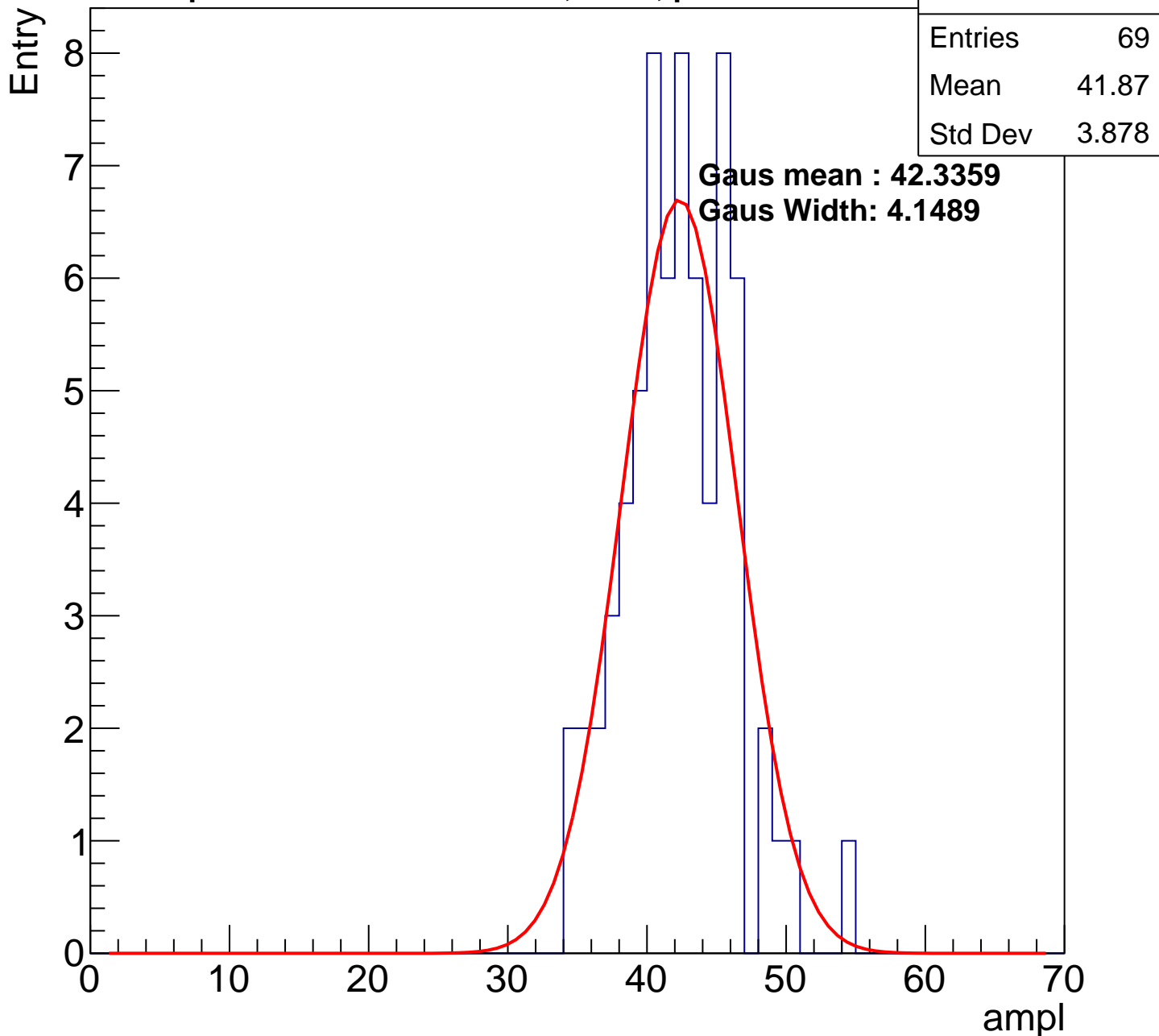
Gaus mean : 35.0803

Gaus Width: 3.7517



B1L101S, U4-ch64, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

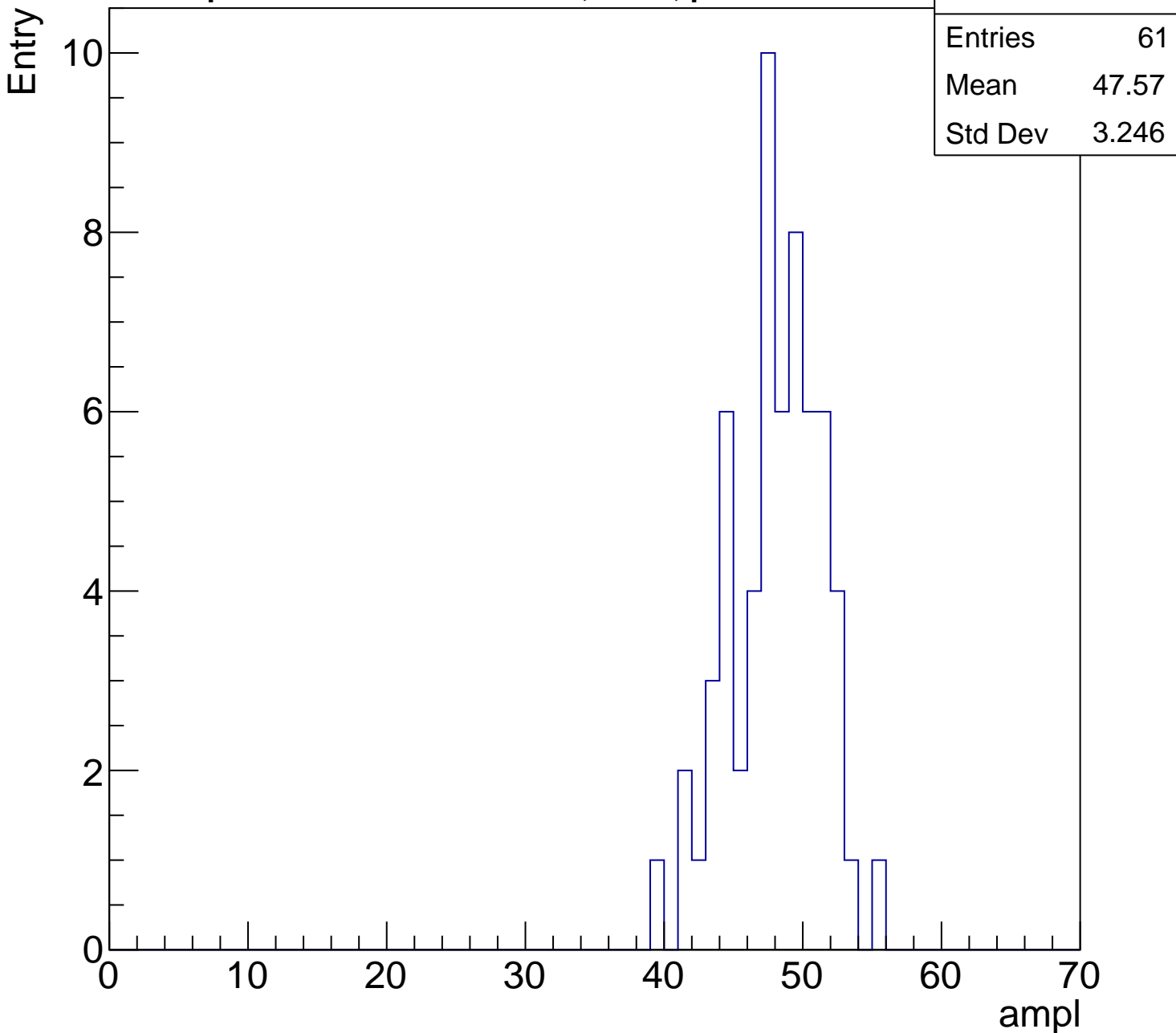
Entries	61
Mean	47.57
Std Dev	3.246

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

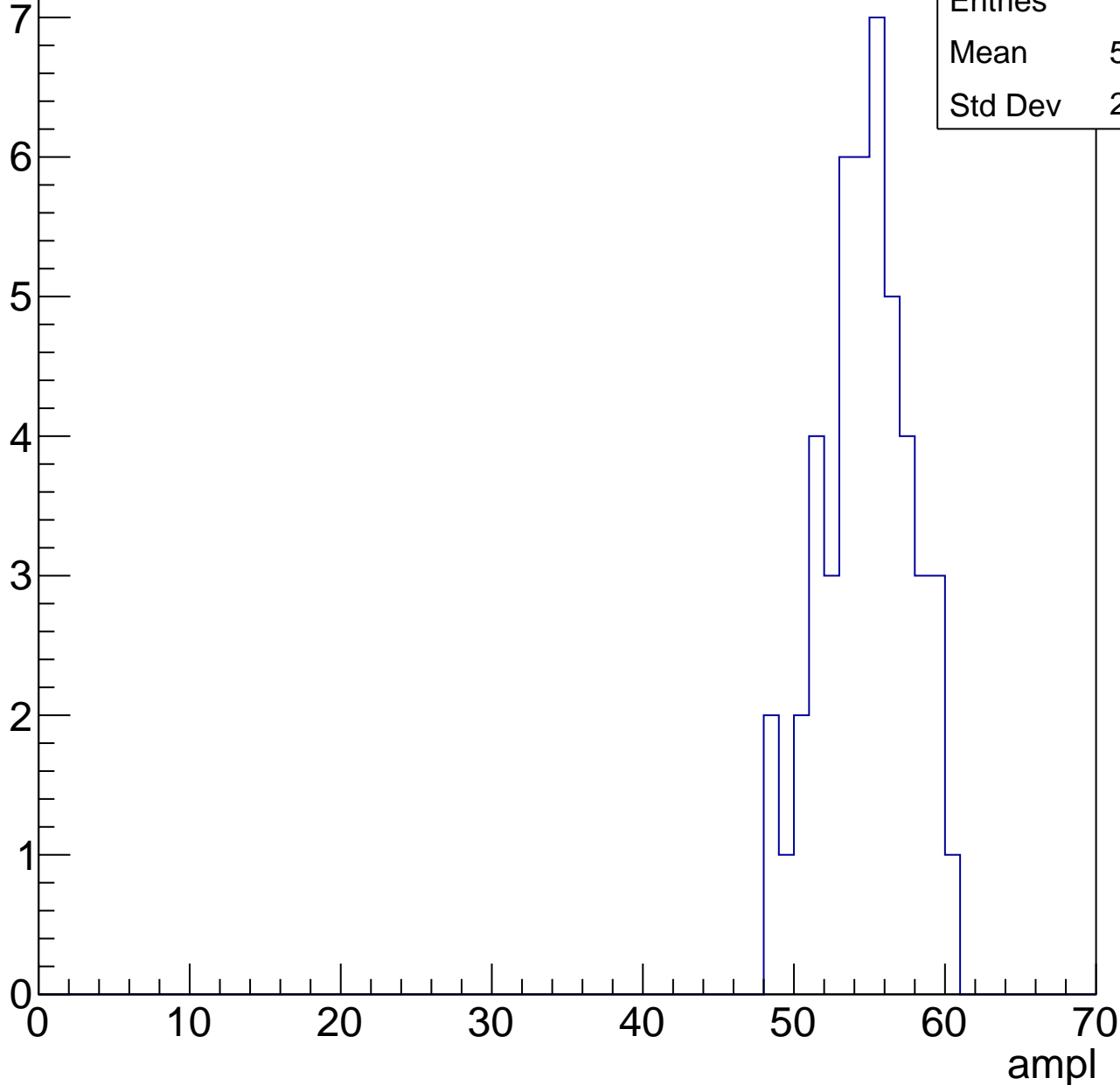
ampl



B1L101S, U4-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

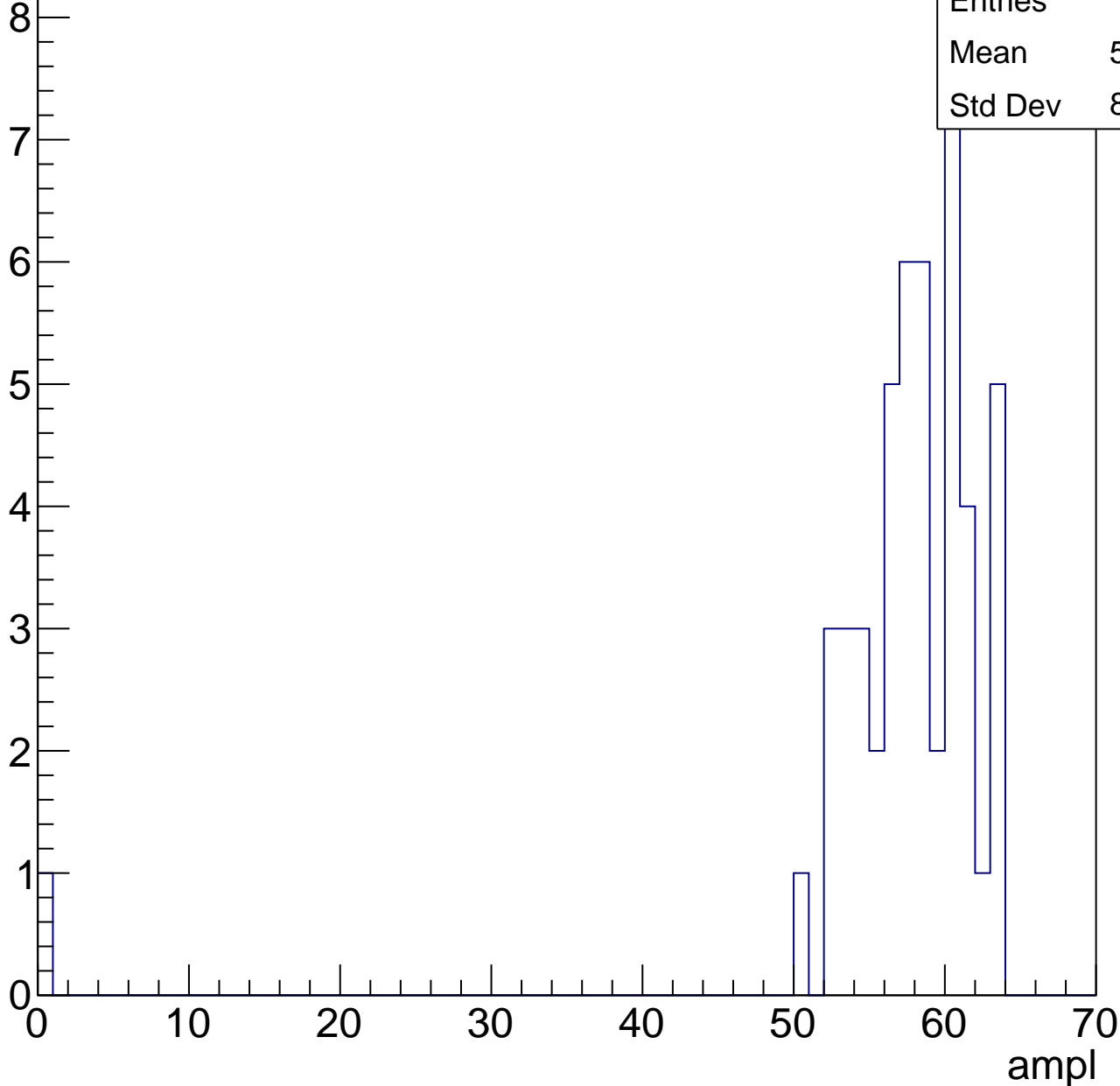


B1L101S, U4-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	56.52
Std Dev	8.723

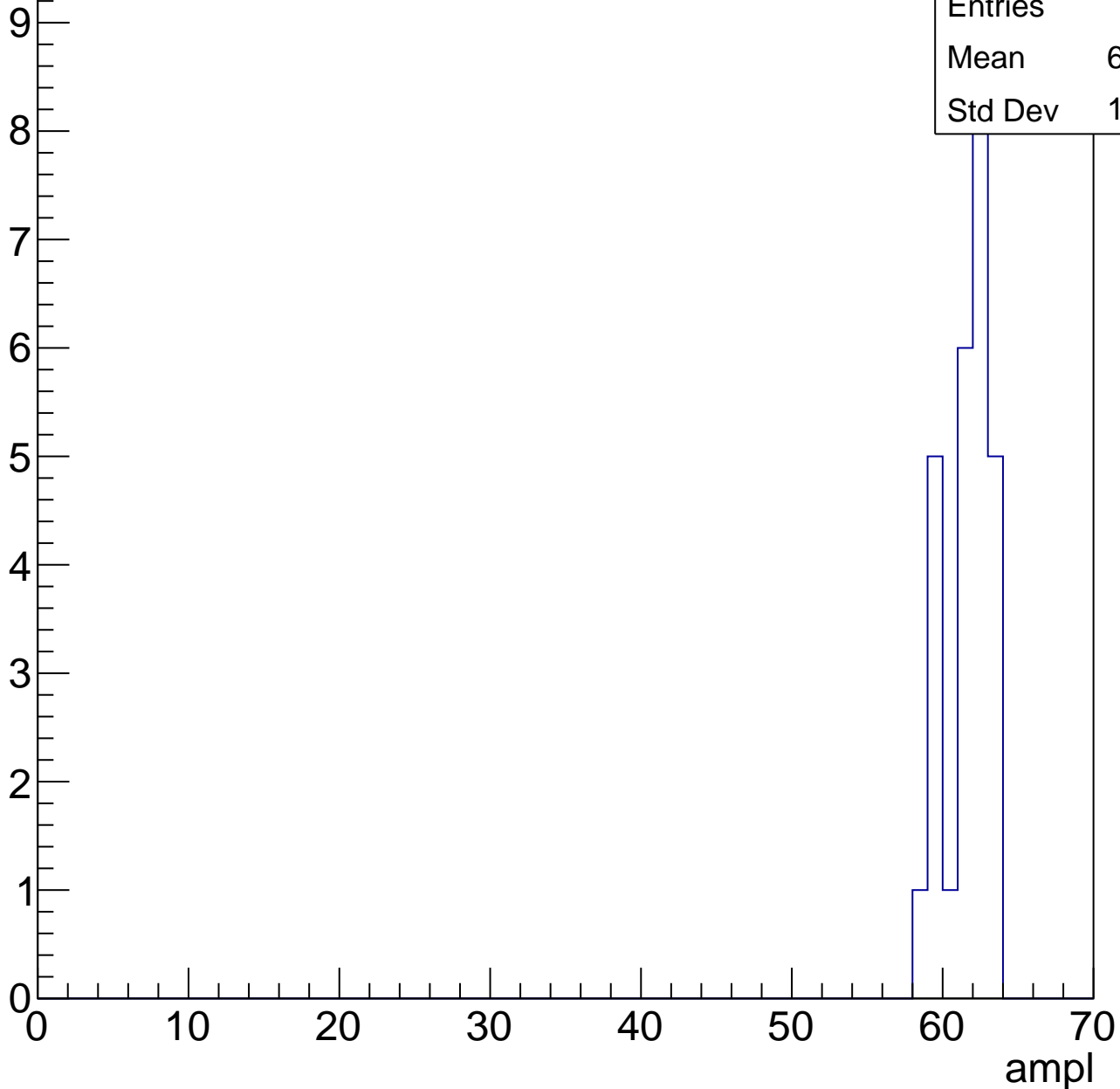


B1L101S, U4-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	61.19
Std Dev	1.467



B1L101S, U4-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch65, adc0

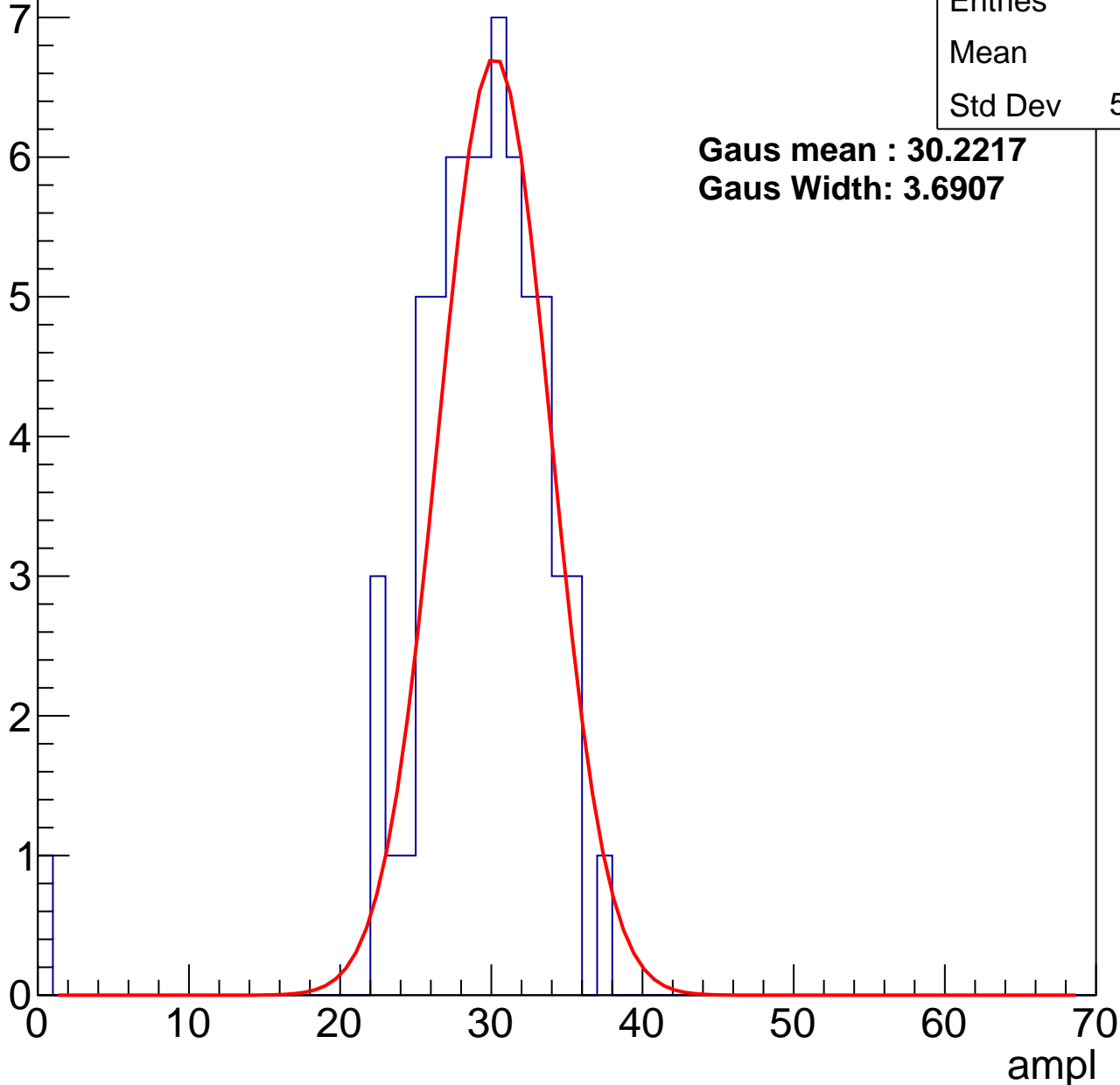
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	28.7
Std Dev	5.002

Gaus mean : 30.2217

Gaus Width: 3.6907



B1L101S, U4-ch65, adc1

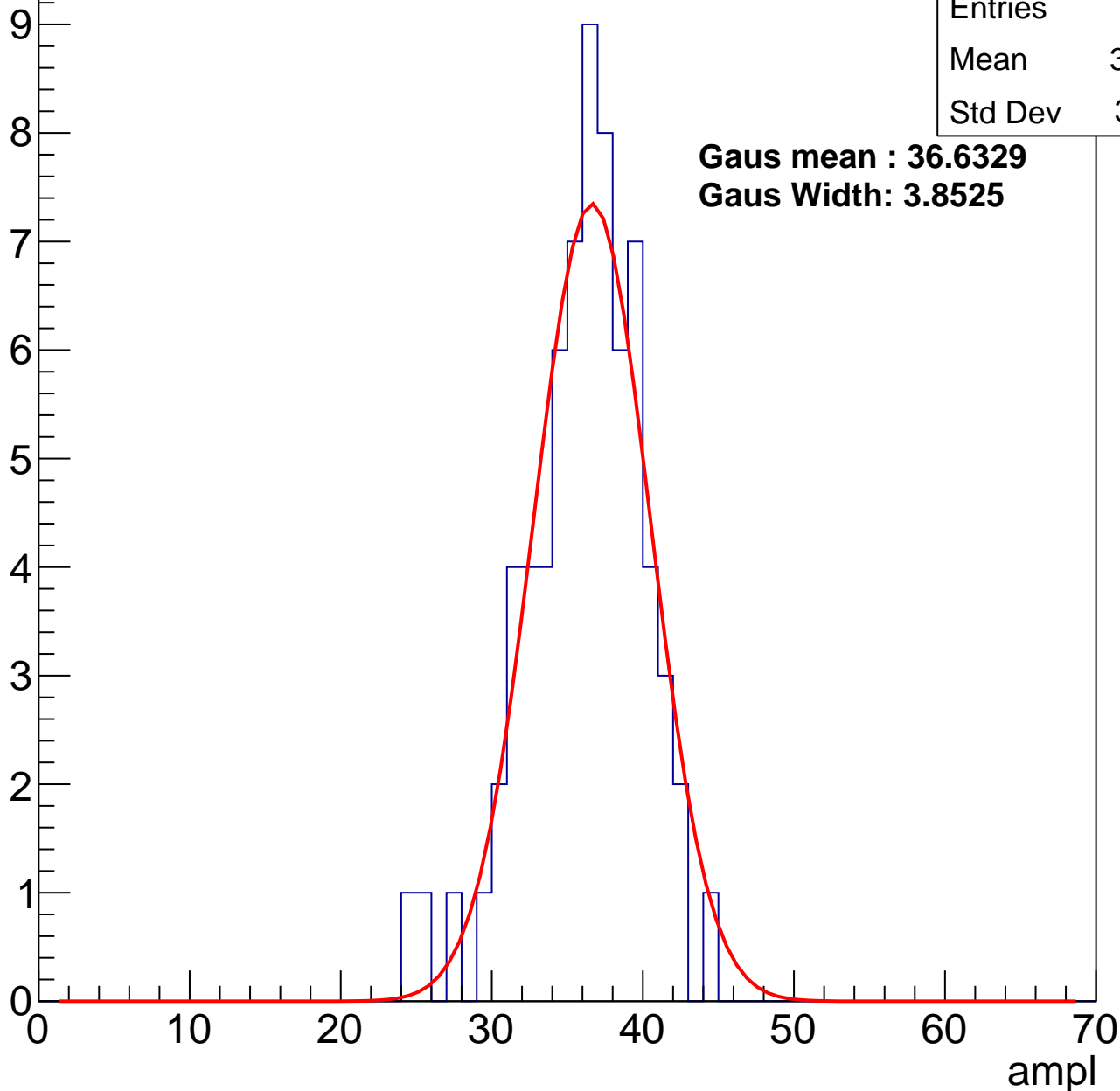
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.63
Std Dev	3.861

Gaus mean : 36.6329

Gaus Width: 3.8525



B1L101S, U4-ch65, adc2

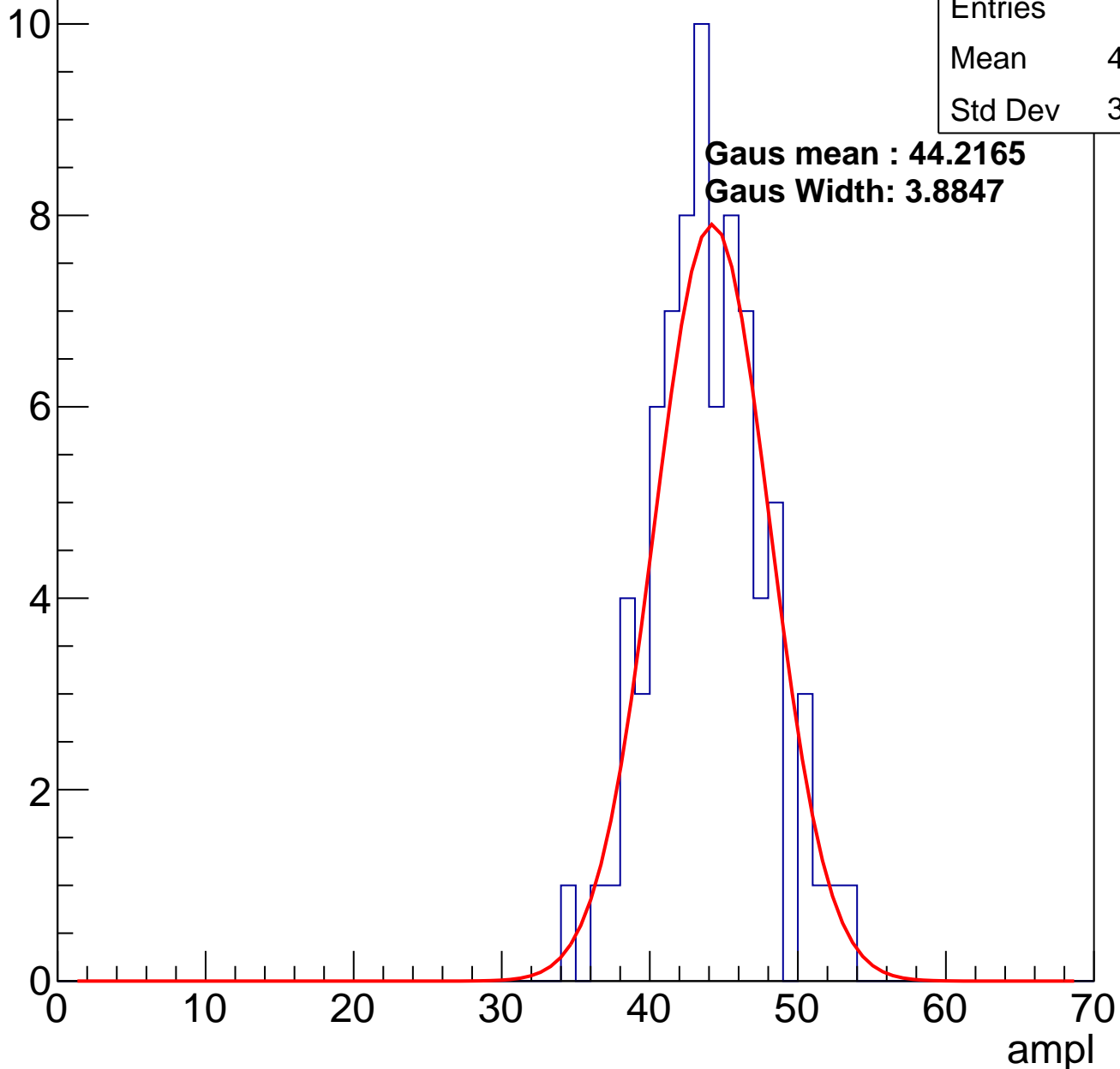
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	43.49
Std Dev	3.723

Gaus mean : 44.2165

Gaus Width: 3.8847

Entry

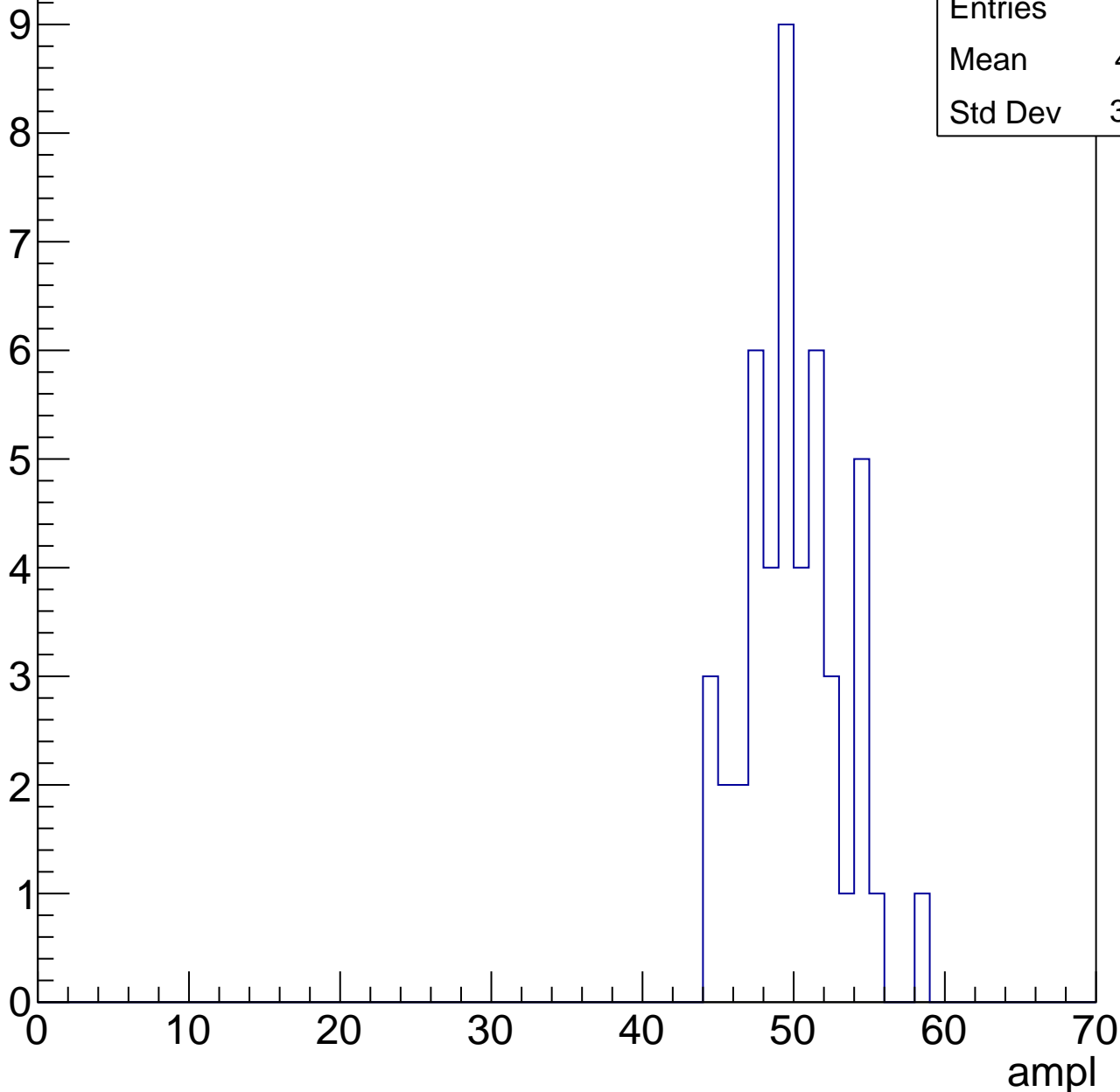


B1L101S, U4-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

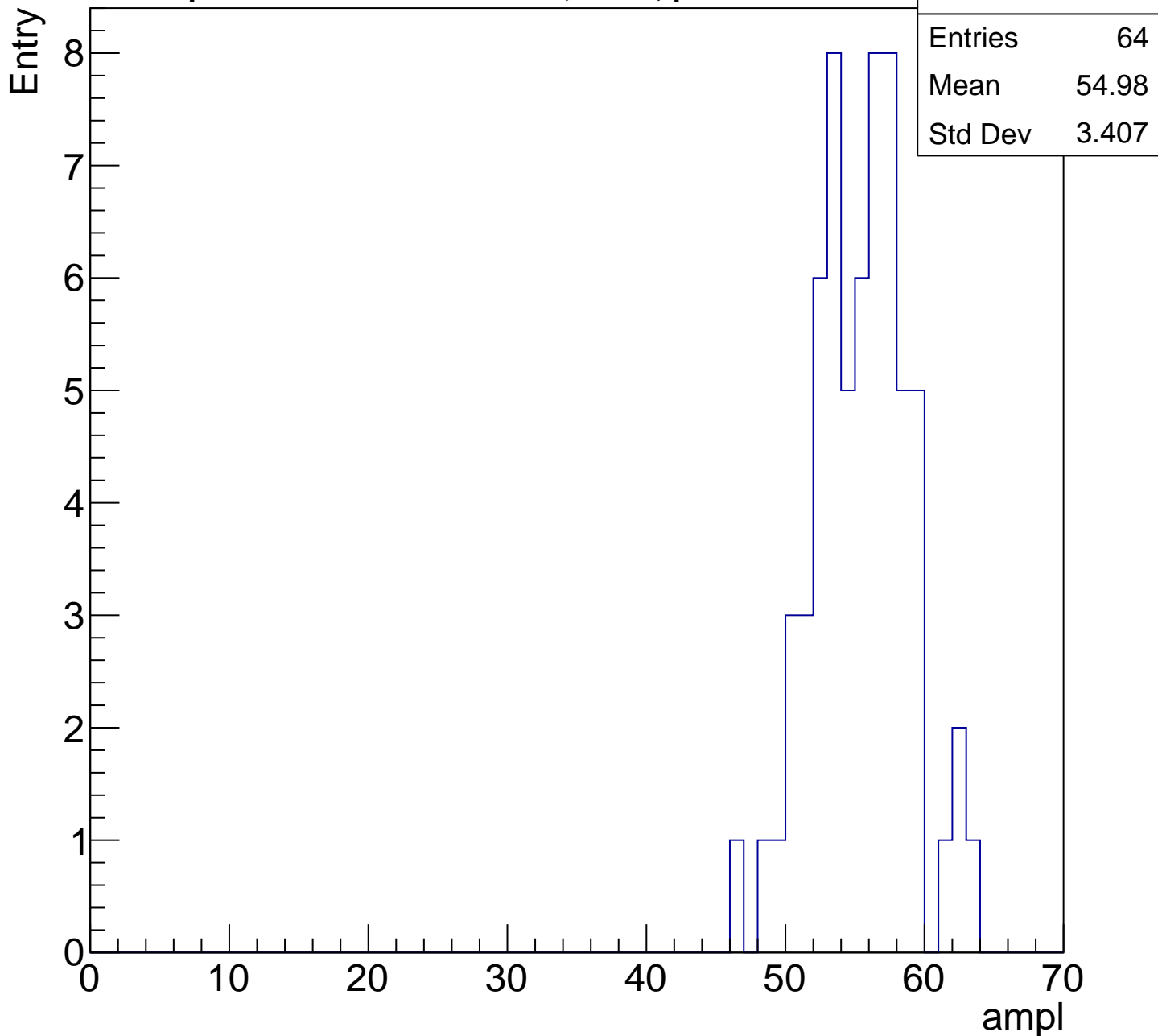
Entry

Entries	47
Mean	49.51
Std Dev	3.107



B1L101S, U4-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

45

Mean

59.91

Std Dev

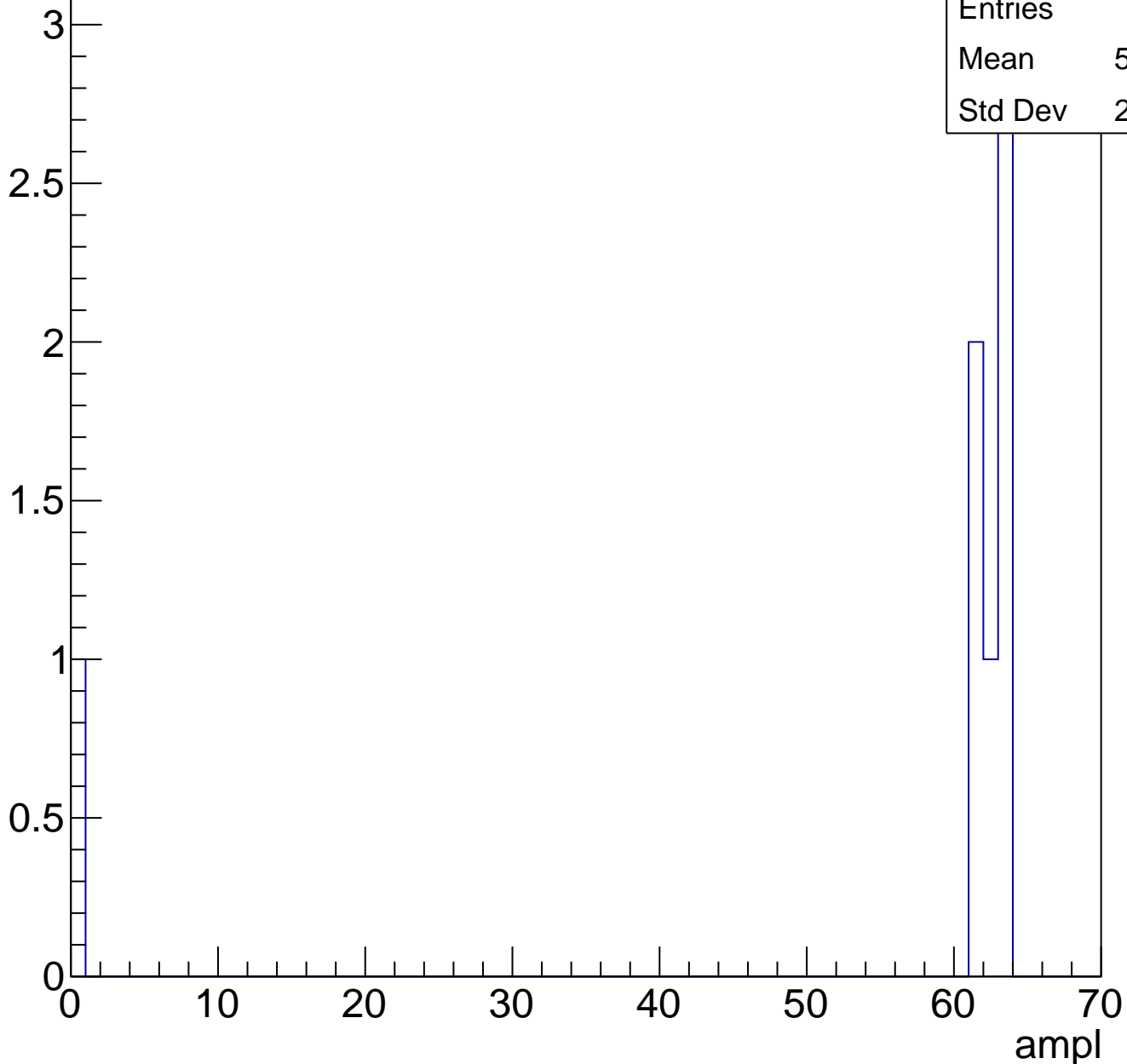
2.279



B1L101S, U4-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

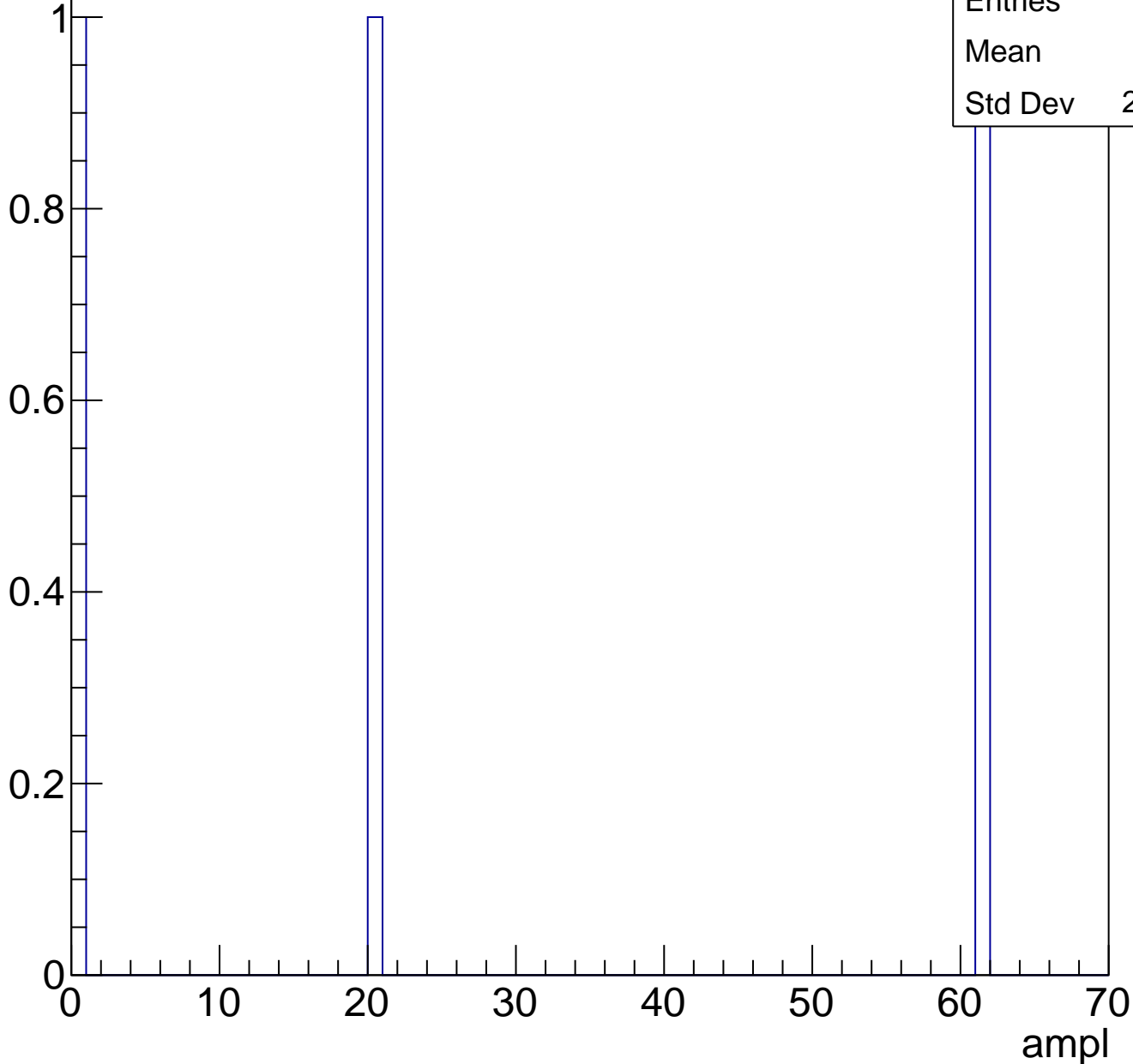
Entry



B1L101S, U4-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27
Std Dev	25.39

B1L101S, U4-ch66, adc0

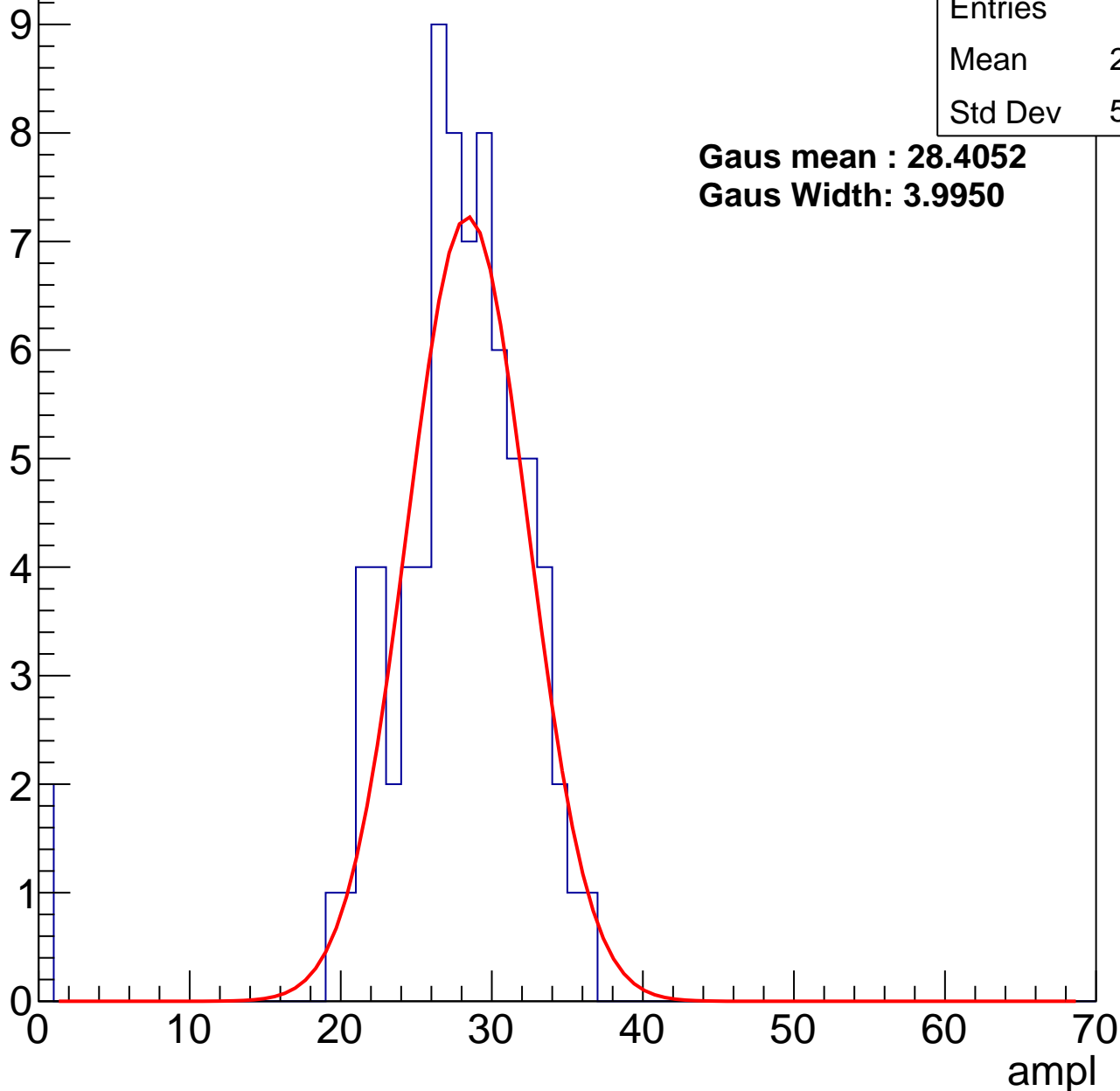
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	26.88
Std Dev	5.765

Gaus mean : 28.4052

Gaus Width: 3.9950



B1L101S, U4-ch66, adc1

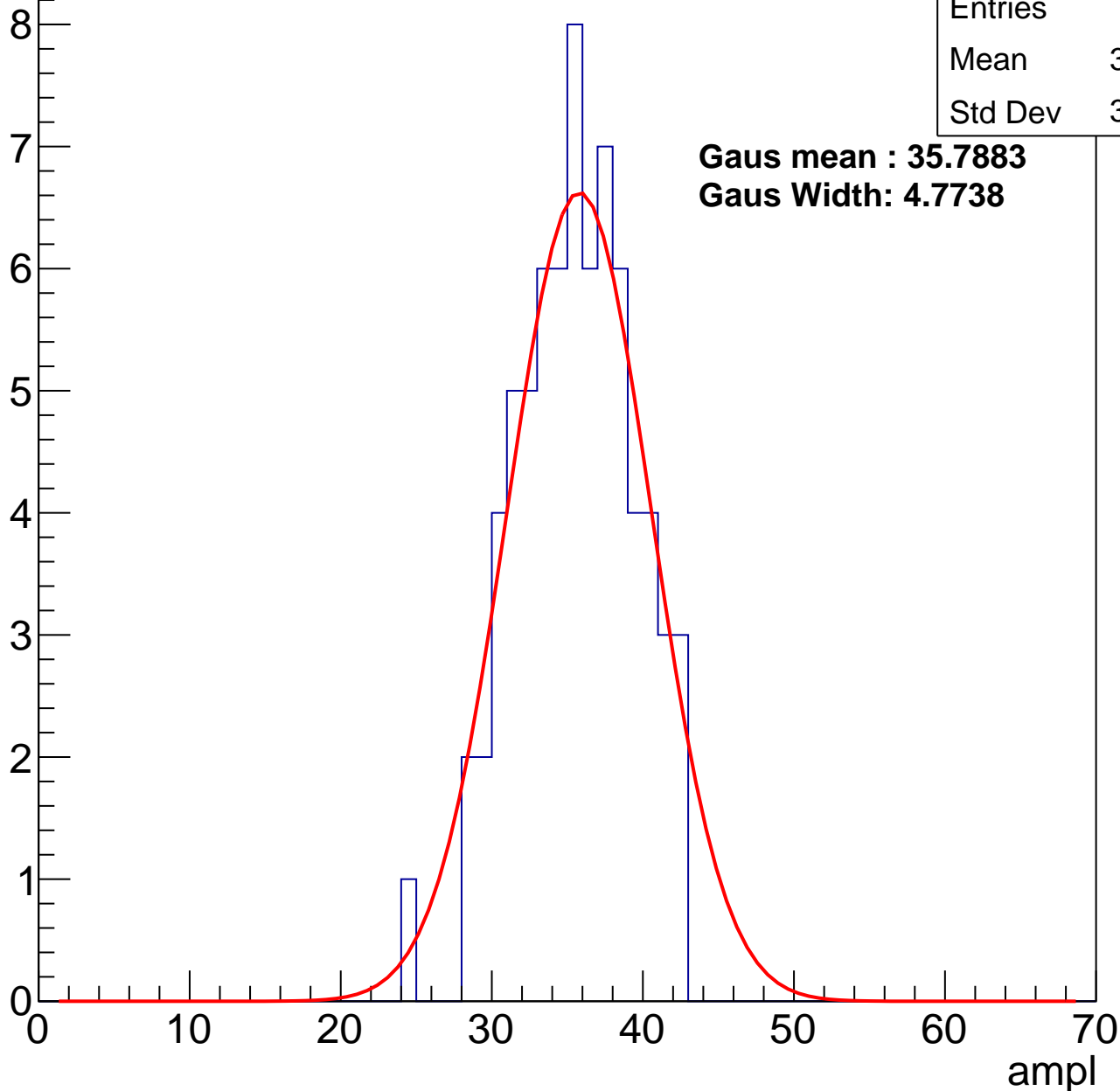
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	35.04
Std Dev	3.824

Gaus mean : 35.7883

Gaus Width: 4.7738



B1L101S, U4-ch66, adc2

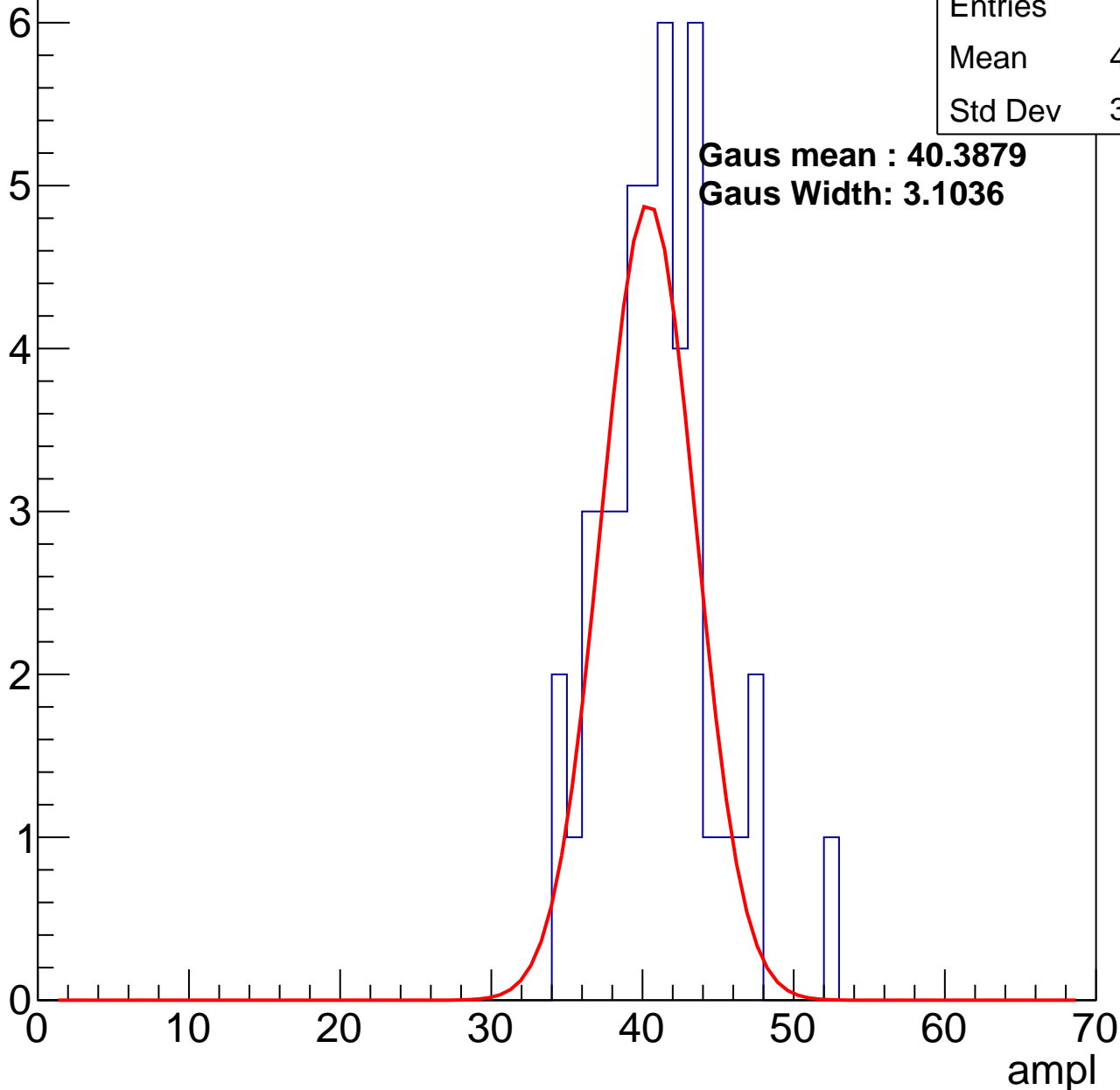
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	40.55
Std Dev	3.602

Gaus mean : 40.3879

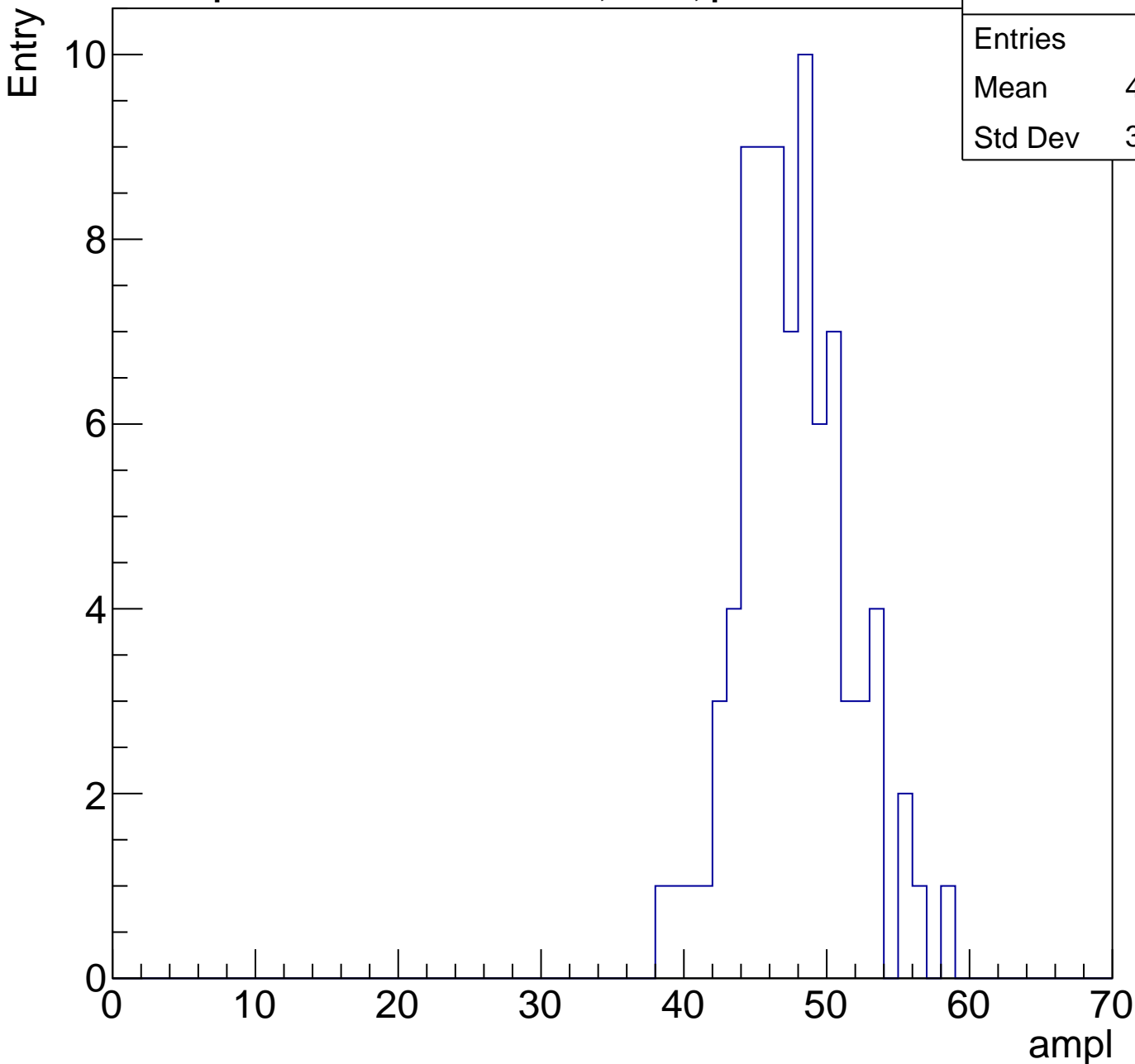
Gaus Width: 3.1036



B1L101S, U4-ch66, adc3

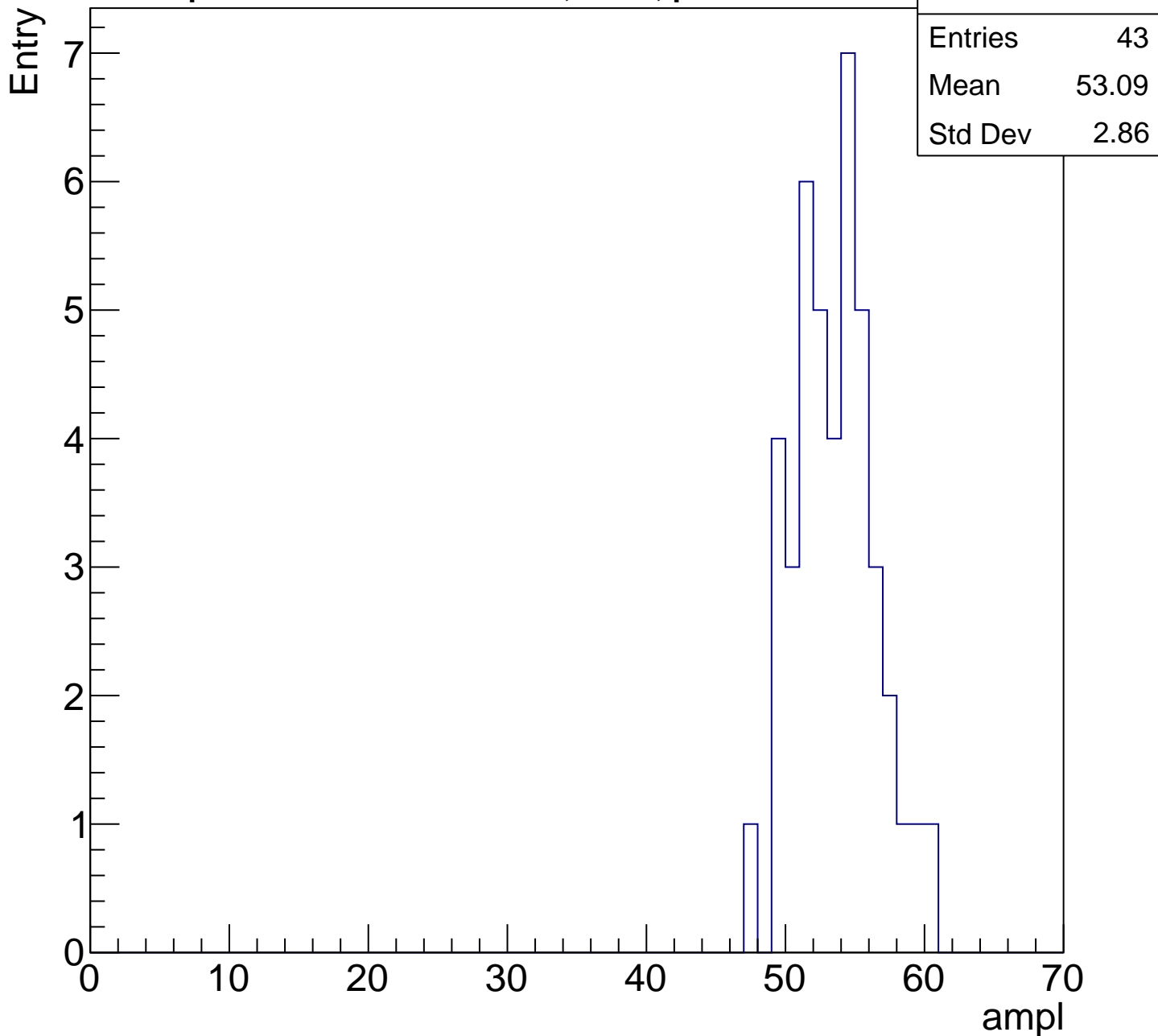
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	47.18
Std Dev	3.803



B1L101S, U4-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

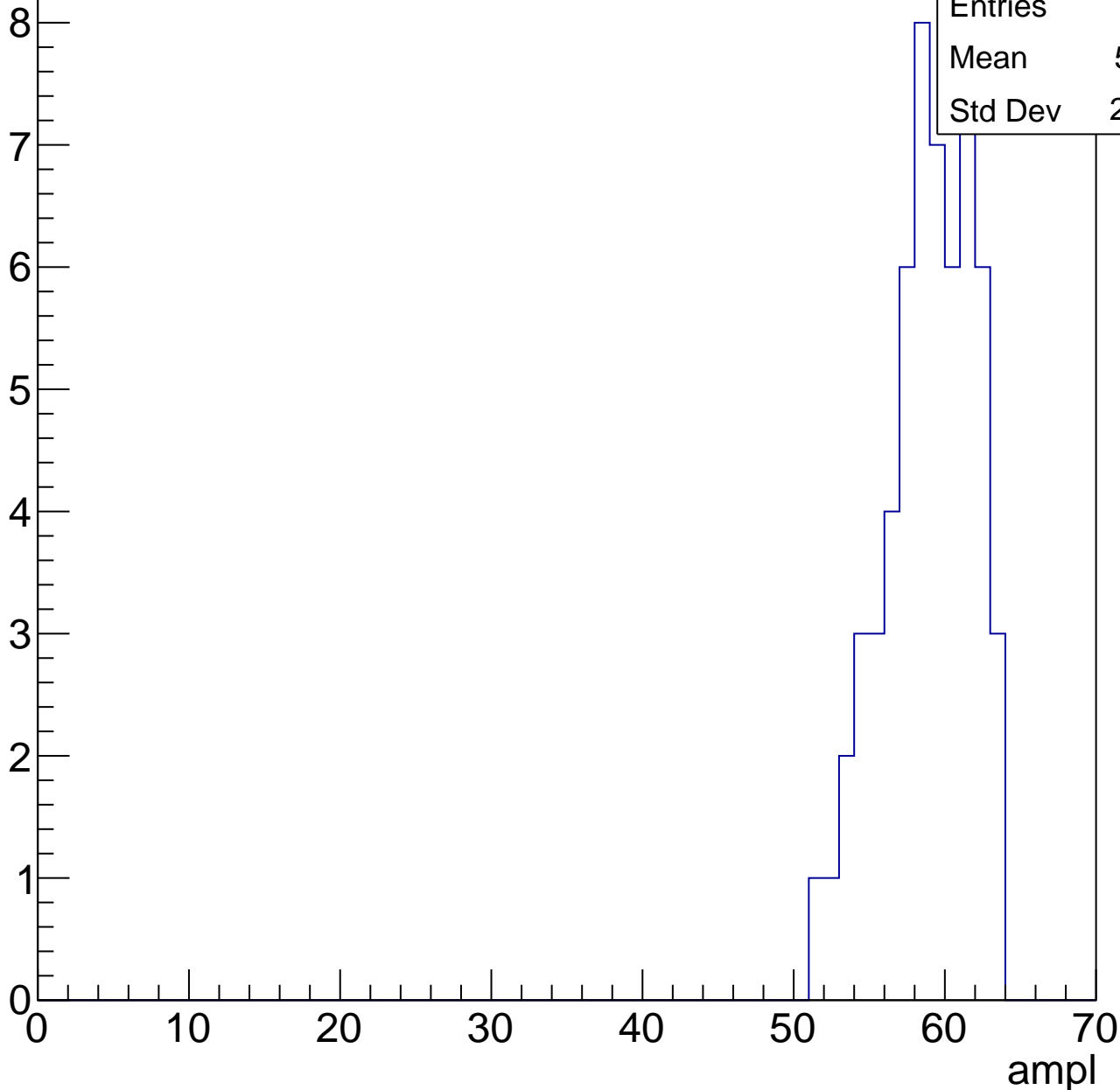


B1L101S, U4-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	58.41
Std Dev	2.925

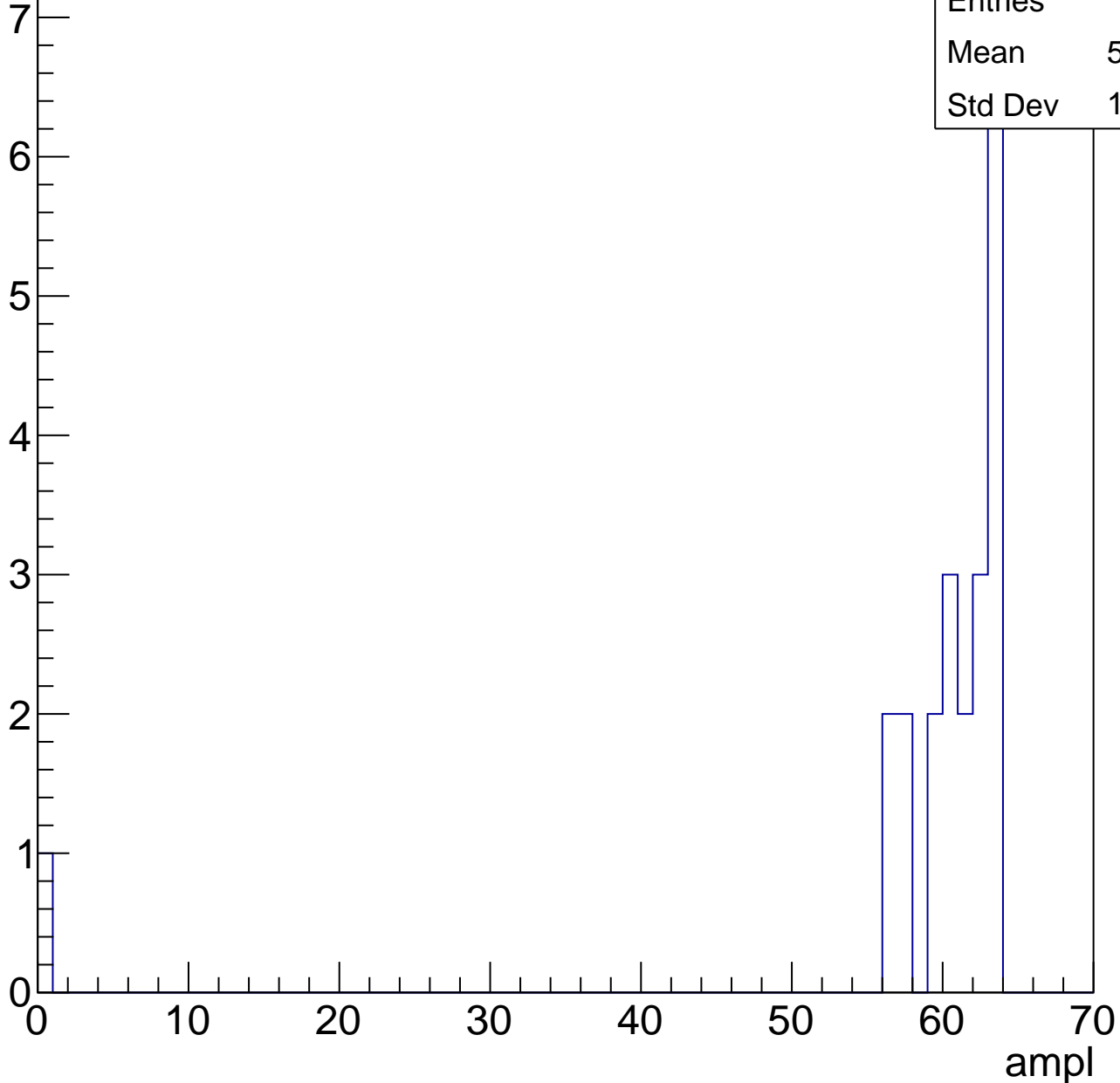


B1L101S, U4-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	57.86
Std Dev	12.84



B1L101S, U4-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch67, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	26.62
Std Dev	7.16

Gaus mean : 29.1275

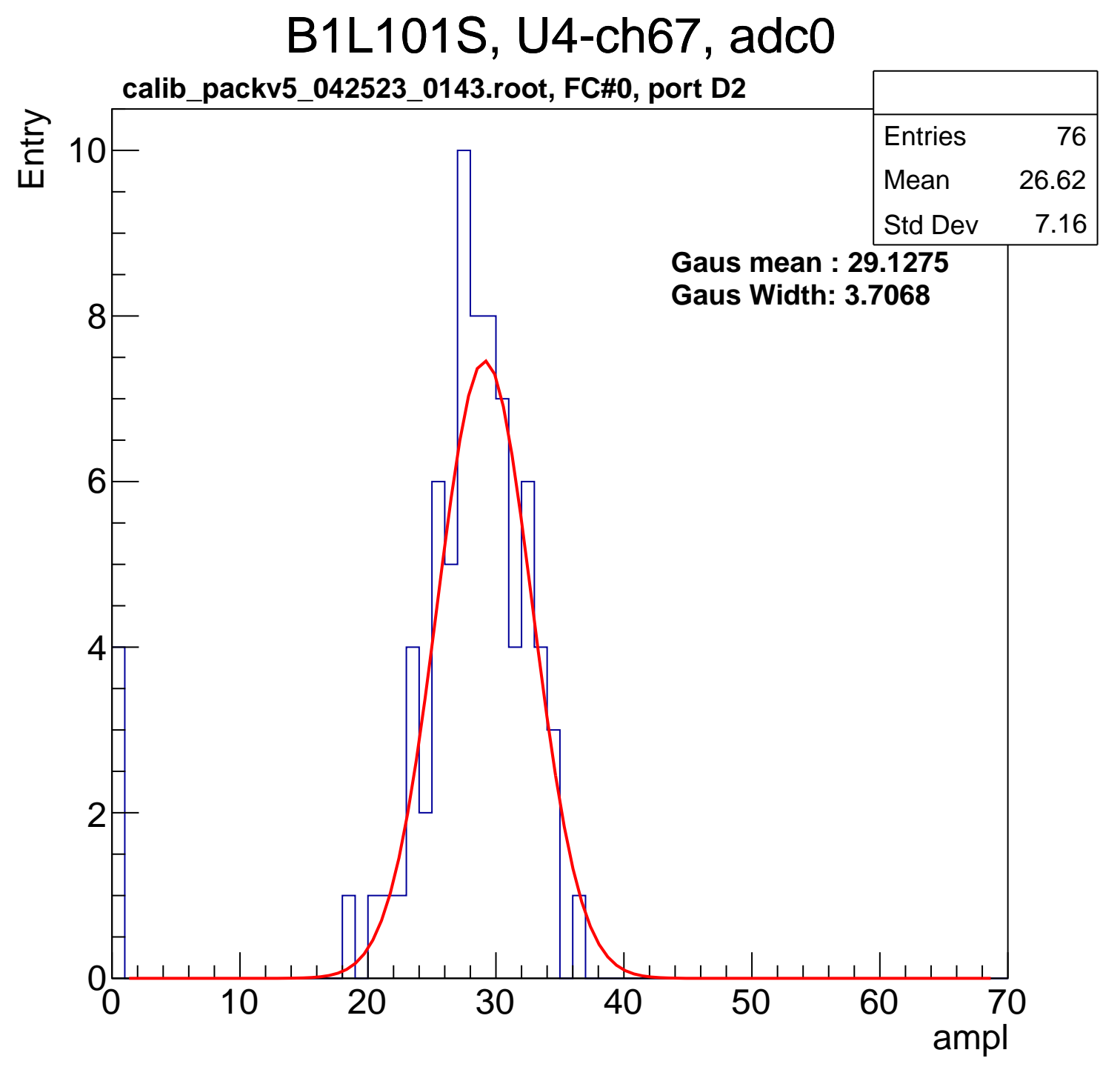
Gaus Width: 3.7068

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch67, adc1

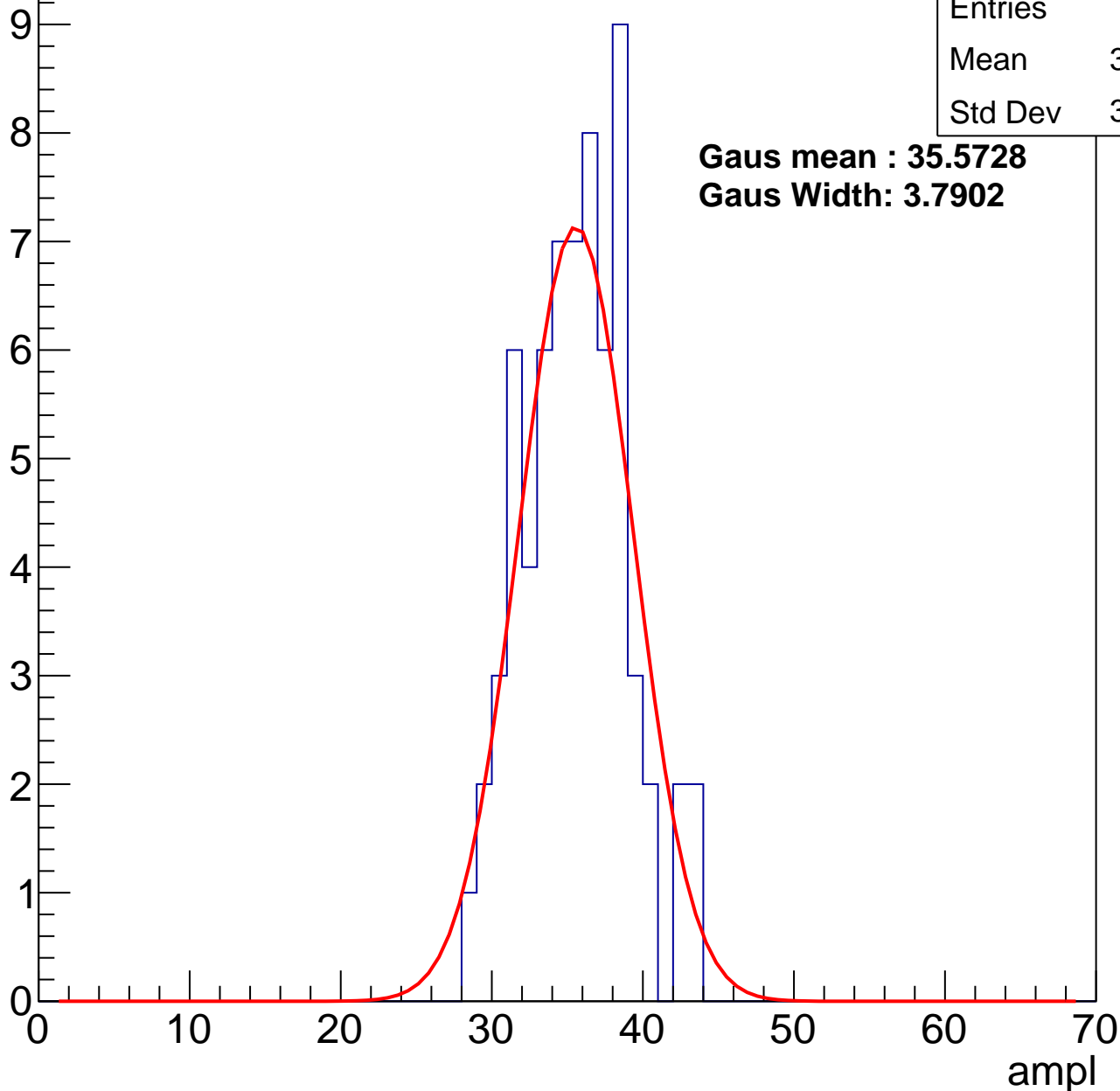
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.15
Std Dev	3.418

Gaus mean : 35.5728

Gaus Width: 3.7902



B1L101S, U4-ch67, adc2

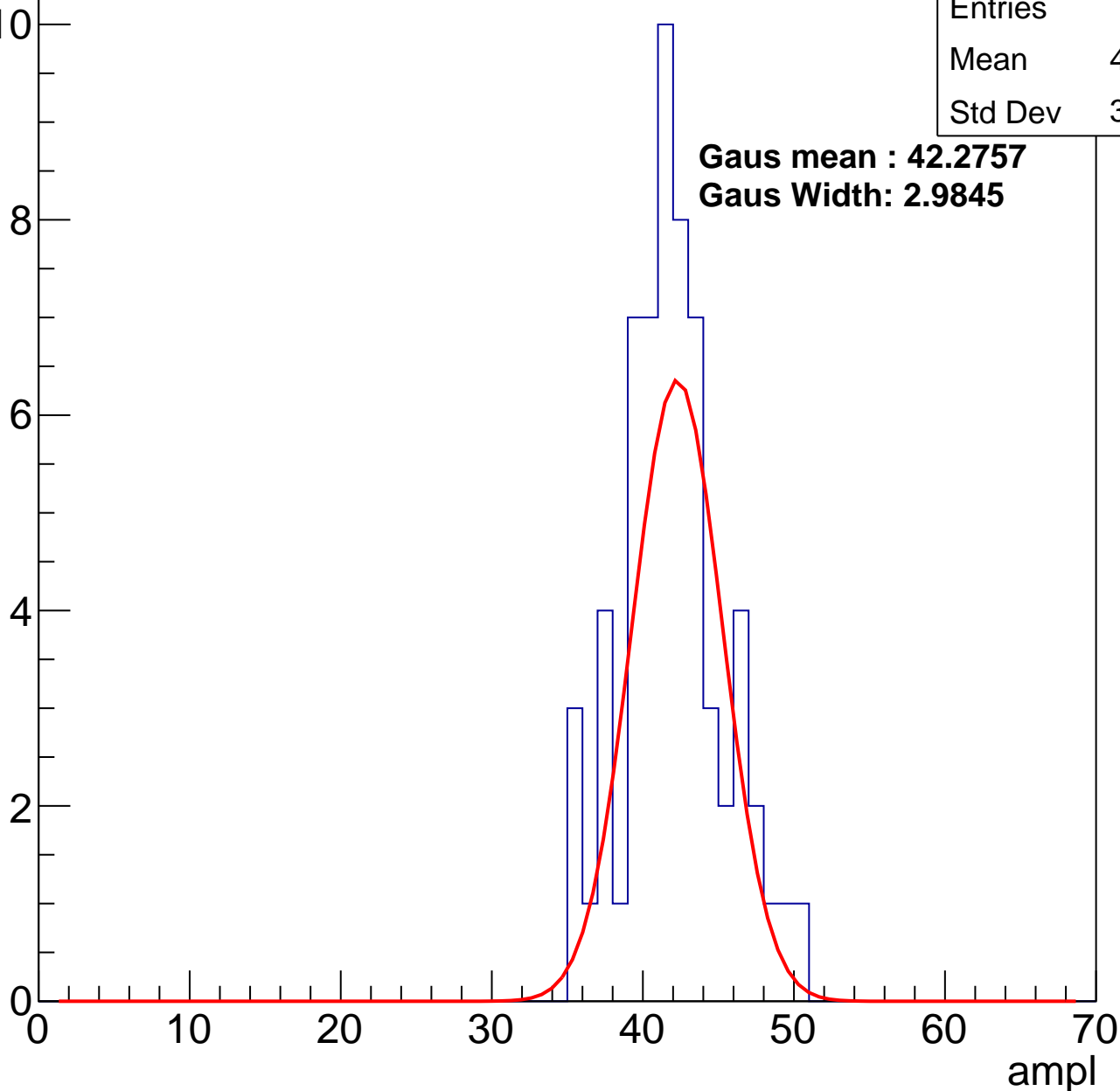
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.52
Std Dev	3.325

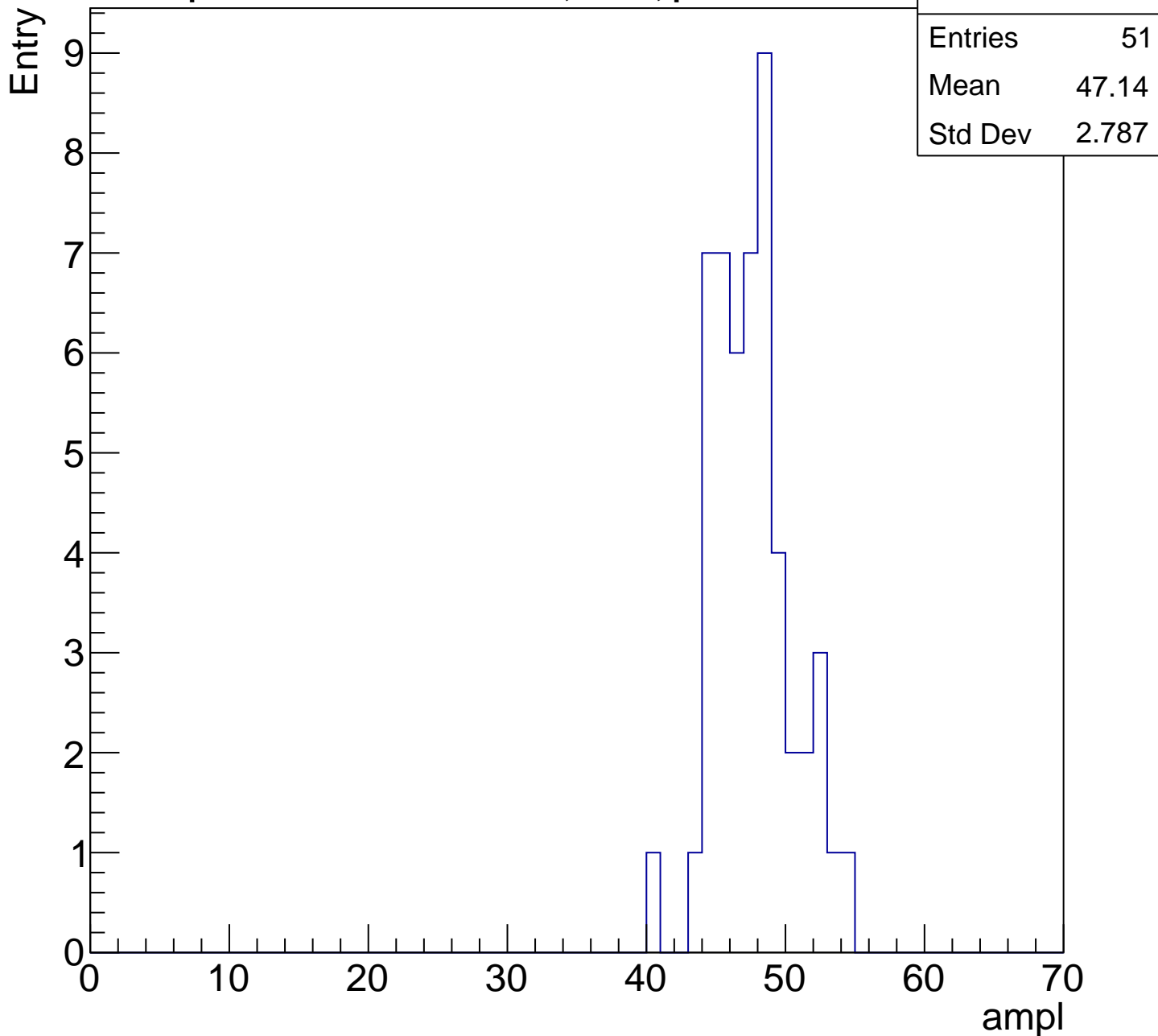
Gaus mean : 42.2757

Gaus Width: 2.9845



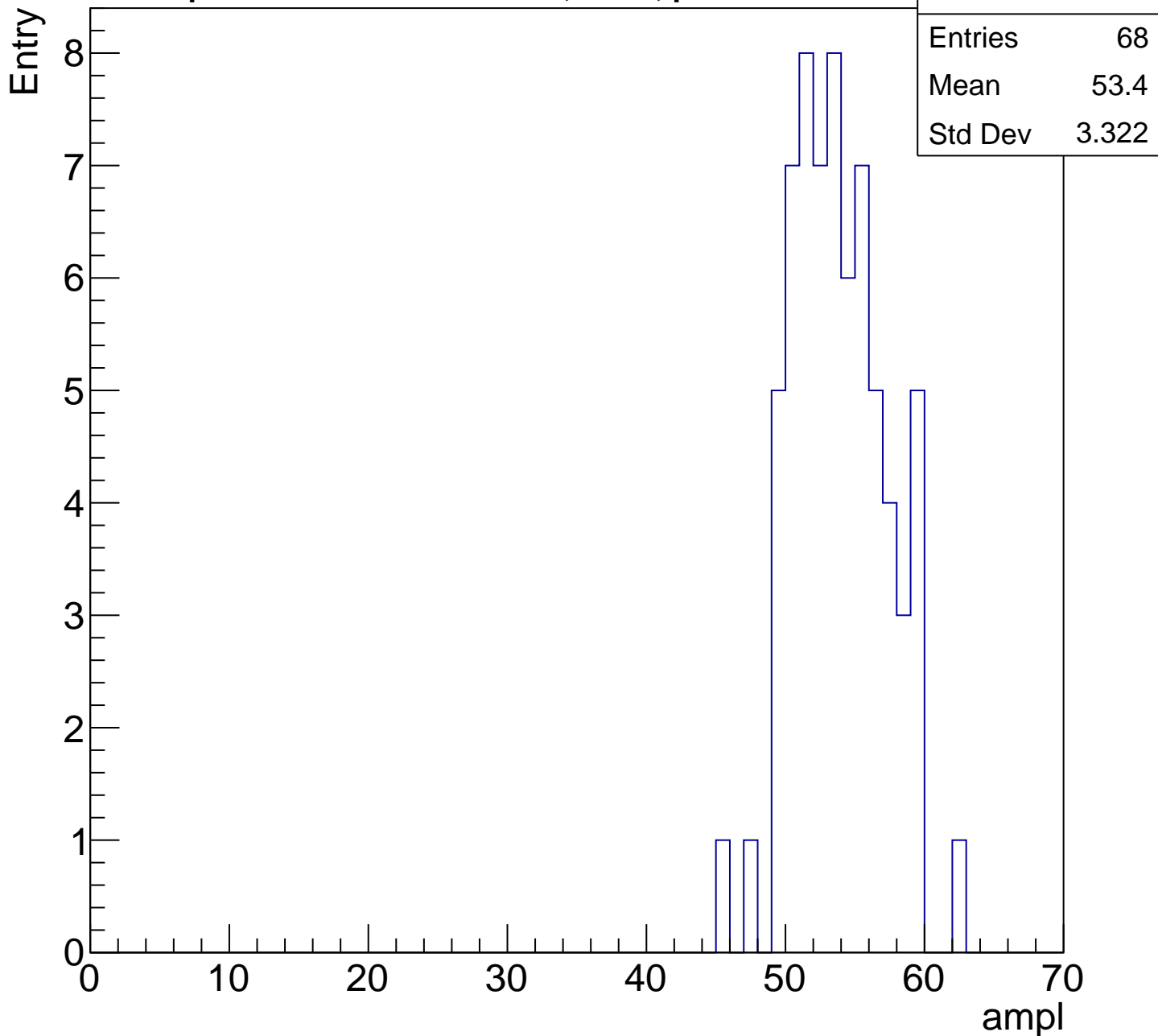
B1L101S, U4-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2



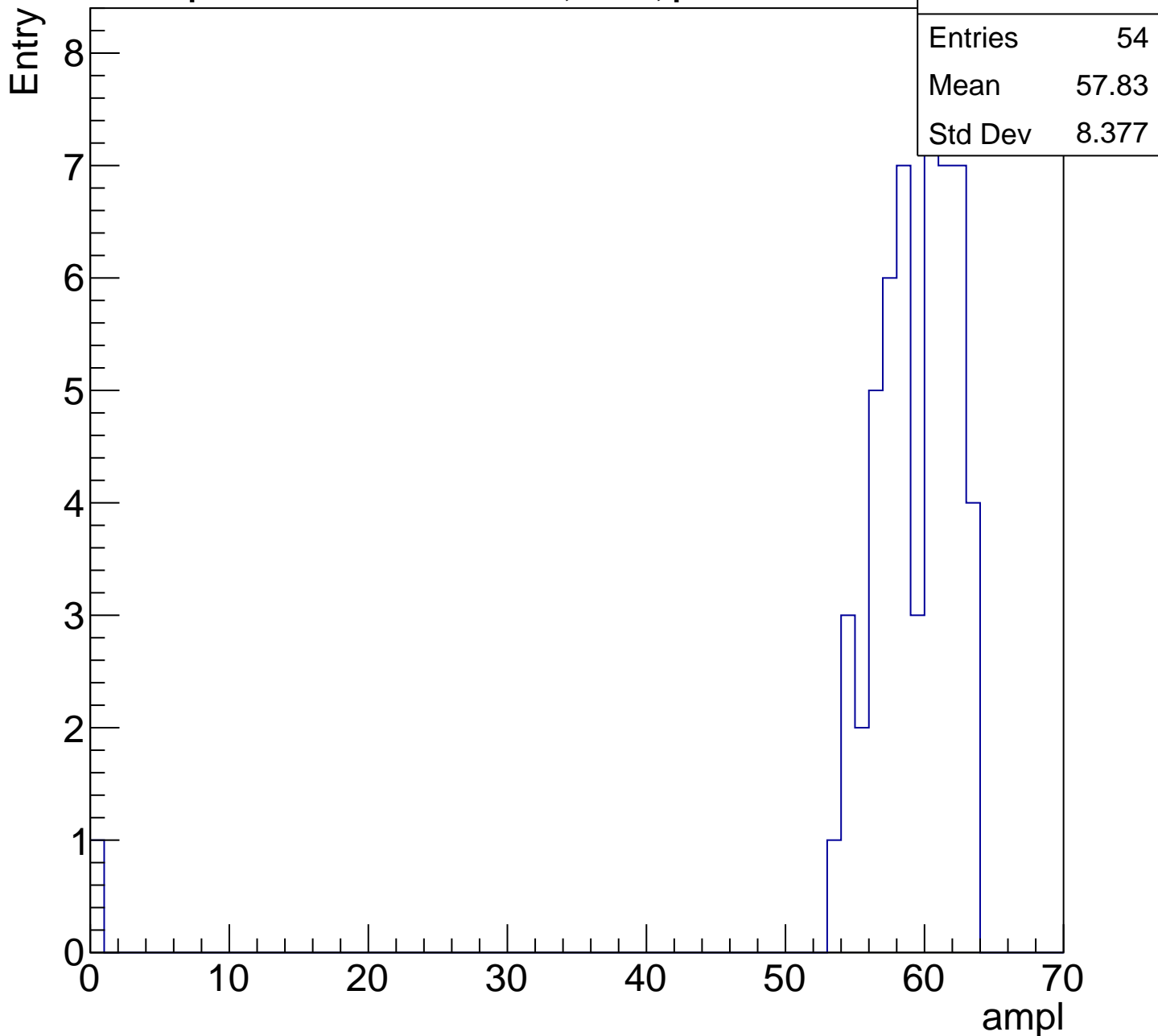
B1L101S, U4-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

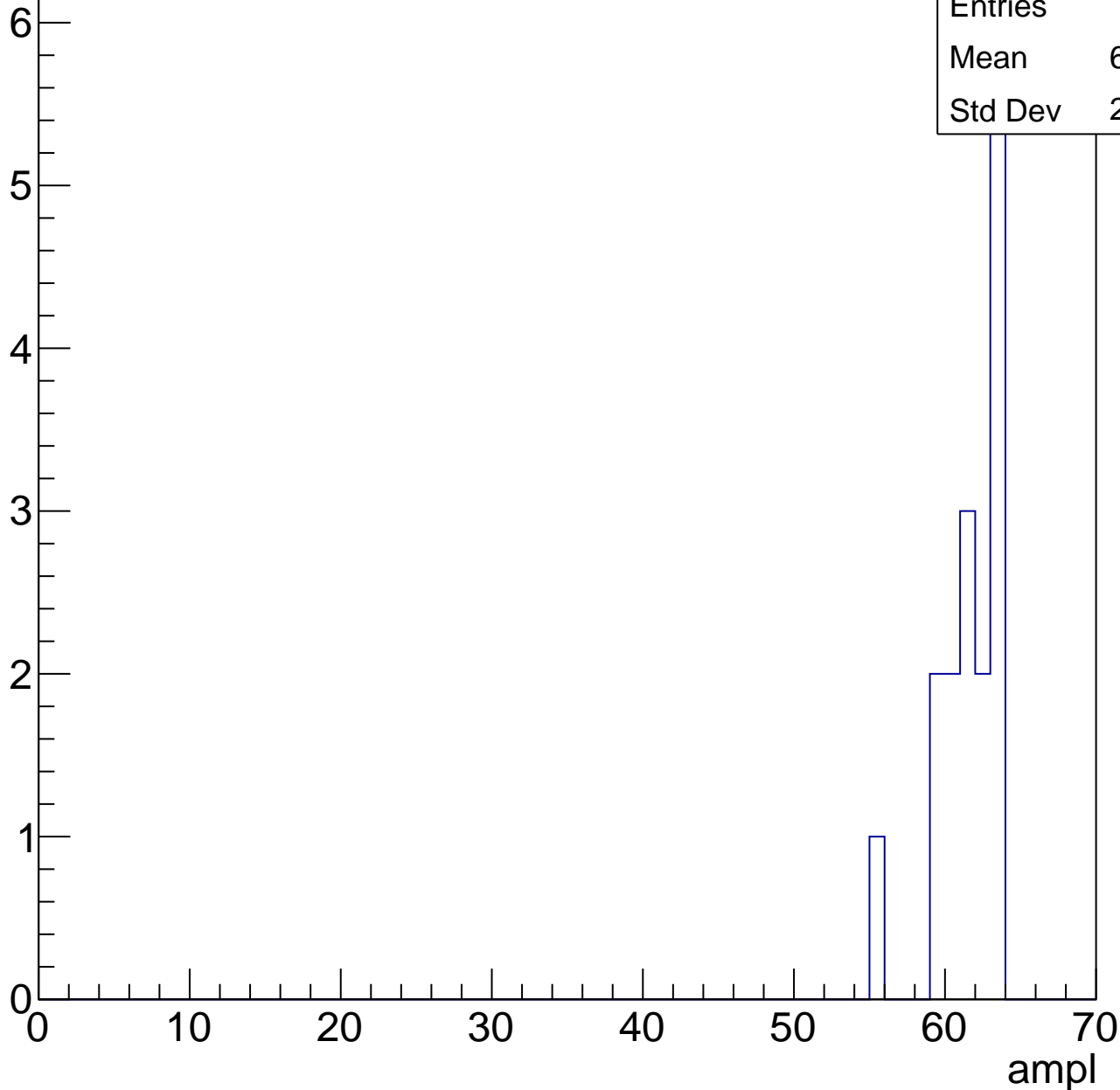


B1L101S, U4-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.12
Std Dev	2.118



B1L101S, U4-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch68, adc0

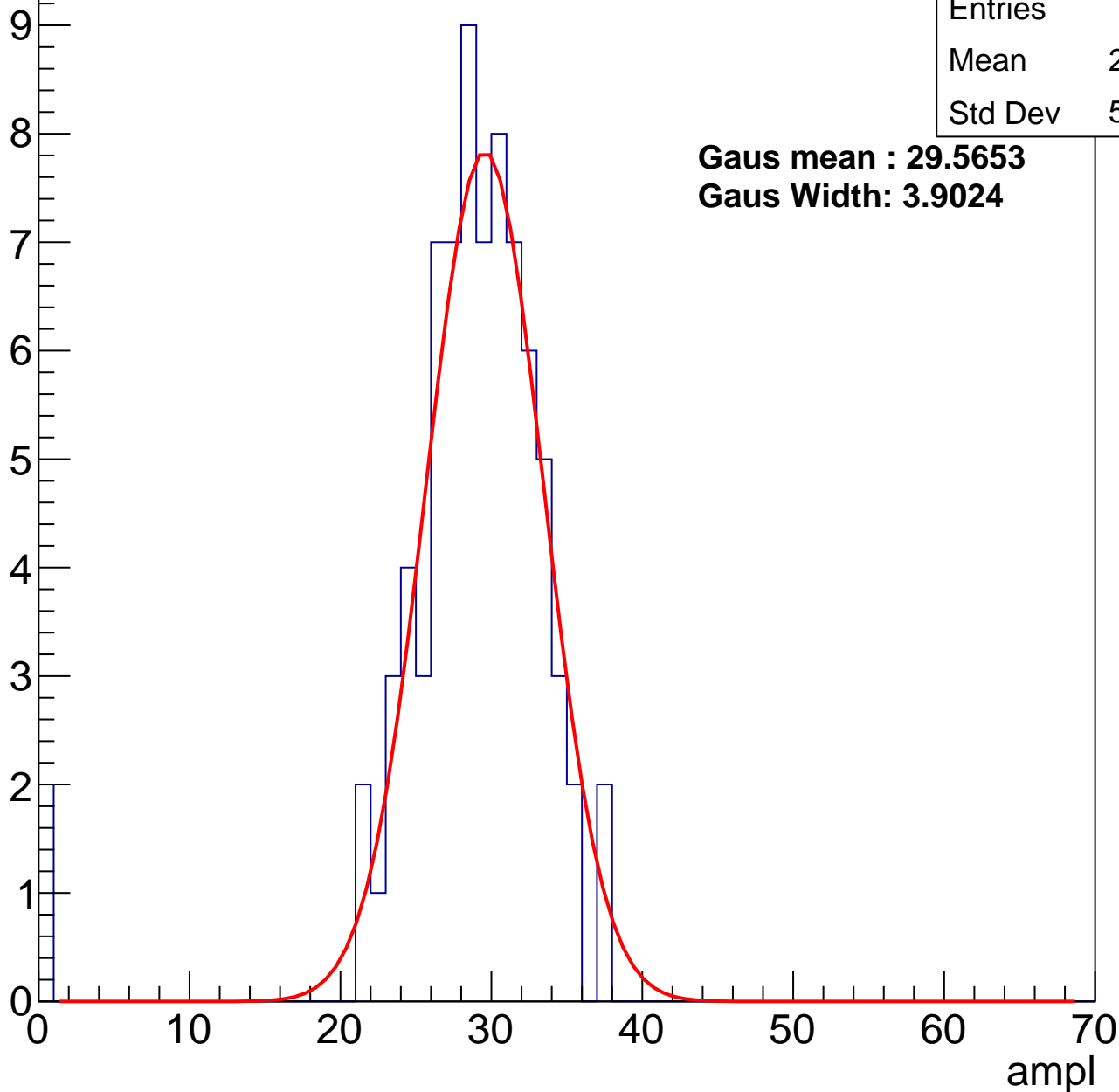
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.08
Std Dev	5.766

Gaus mean : 29.5653

Gaus Width: 3.9024



B1L101S, U4-ch68, adc1

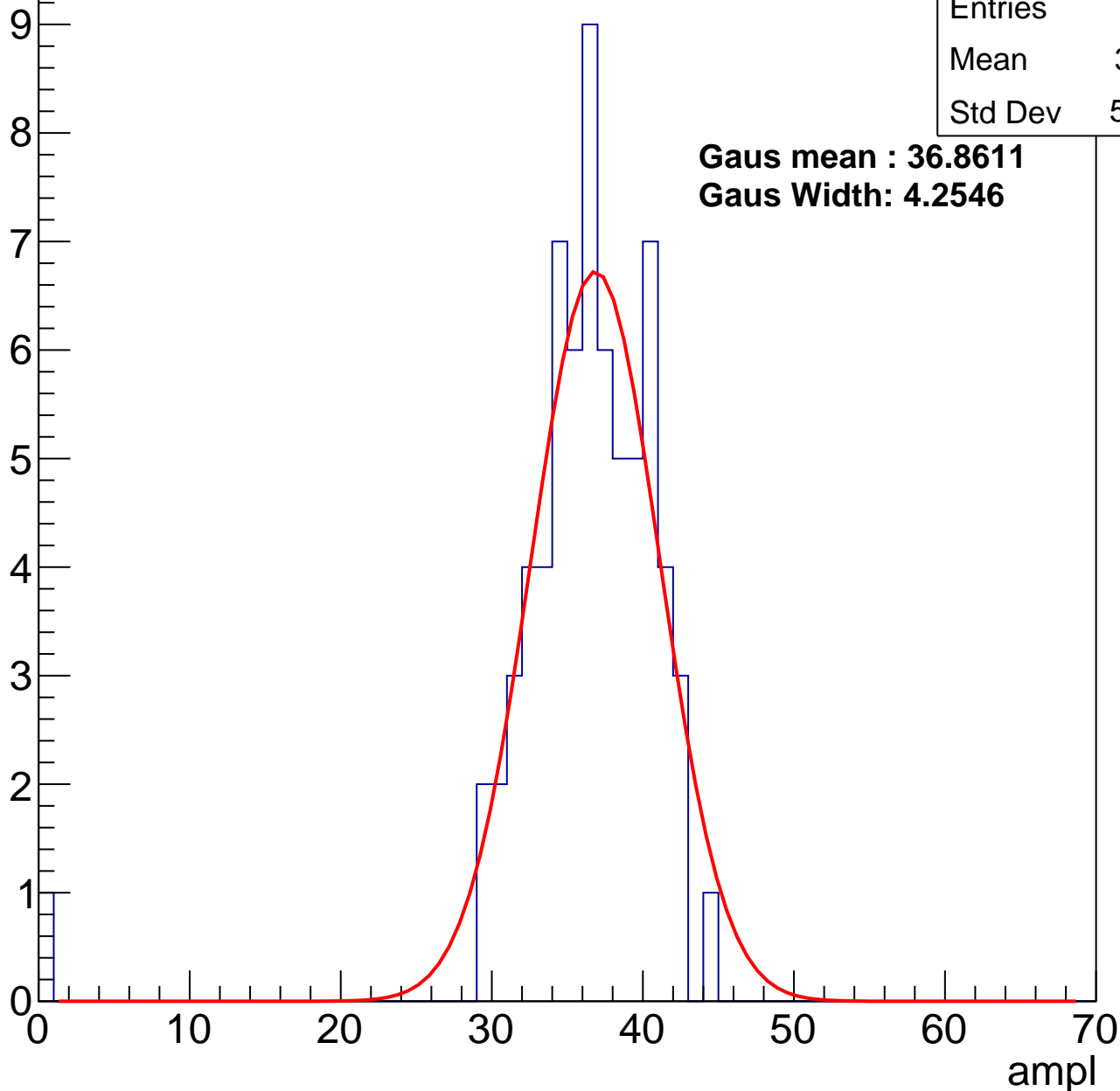
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.71
Std Dev	5.548

Gaus mean : 36.8611

Gaus Width: 4.2546



B1L101S, U4-ch68, adc2

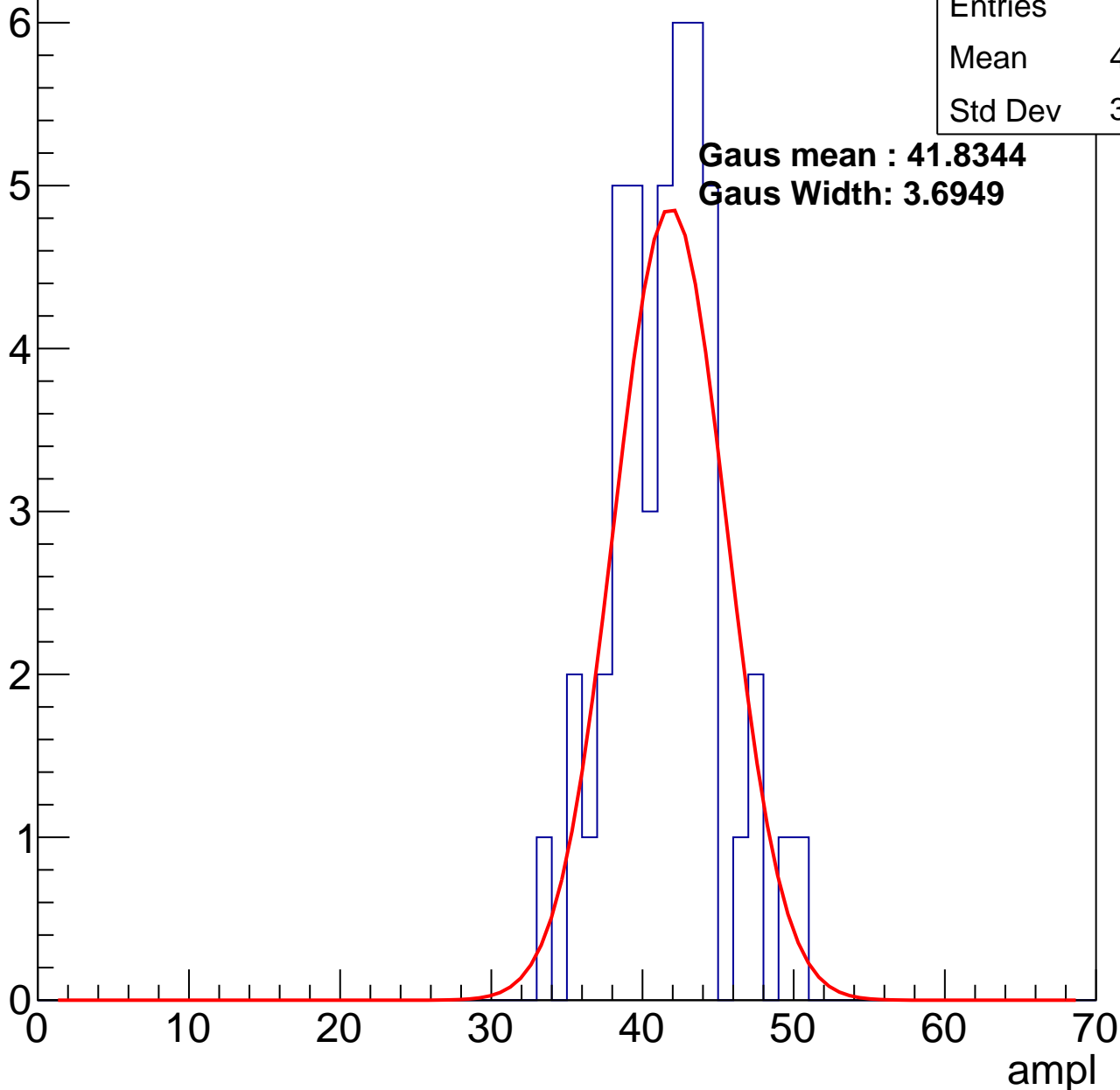
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	41.13
Std Dev	3.542

Gaus mean : 41.8344

Gaus Width: 3.6949

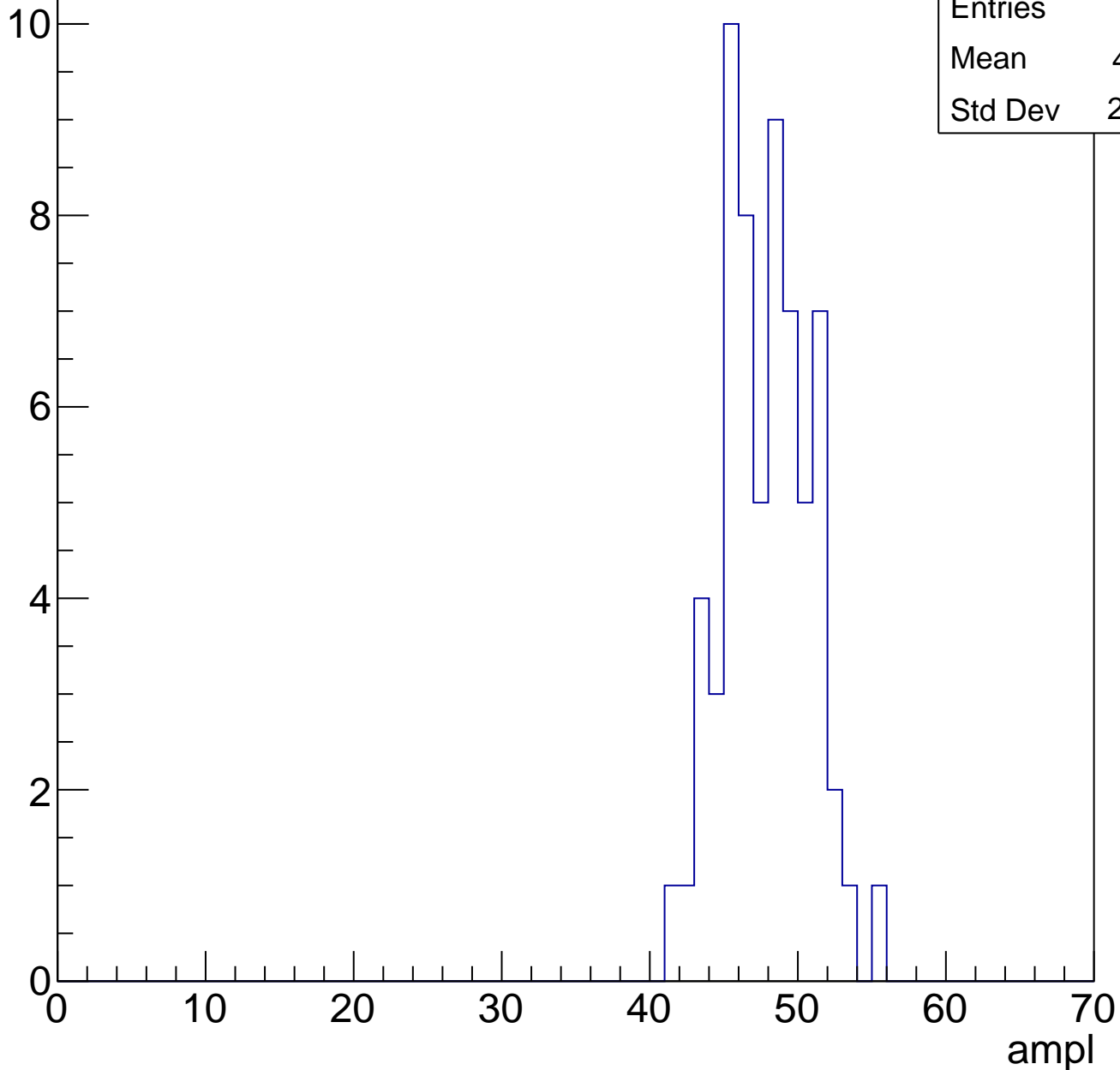


B1L101S, U4-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	47.41
Std Dev	2.887

Entry

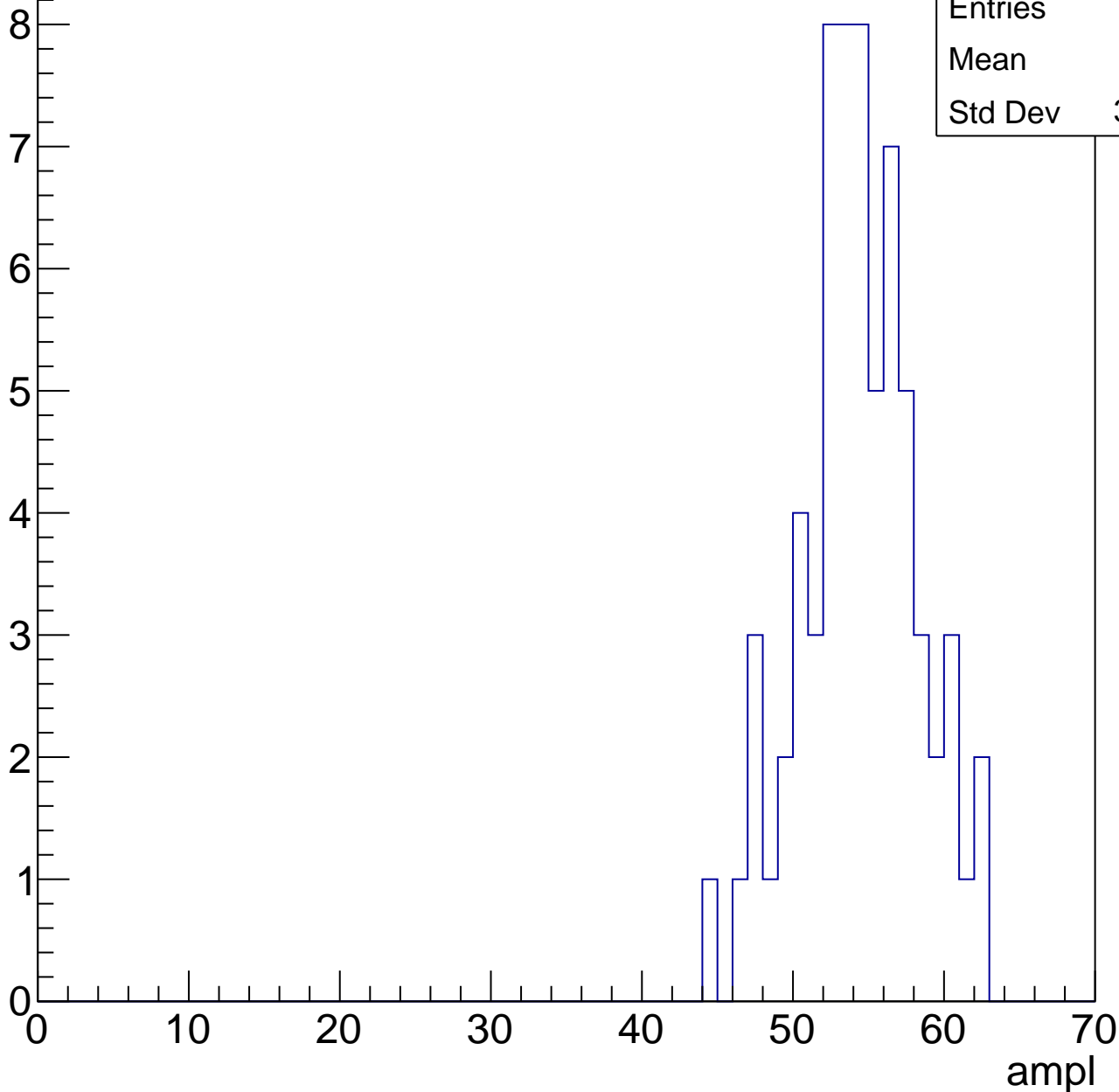


B1L101S, U4-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	53.9
Std Dev	3.841



B1L101S, U4-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 57.92

Std Dev 8.447

ampl

0

10

20

30

40

50

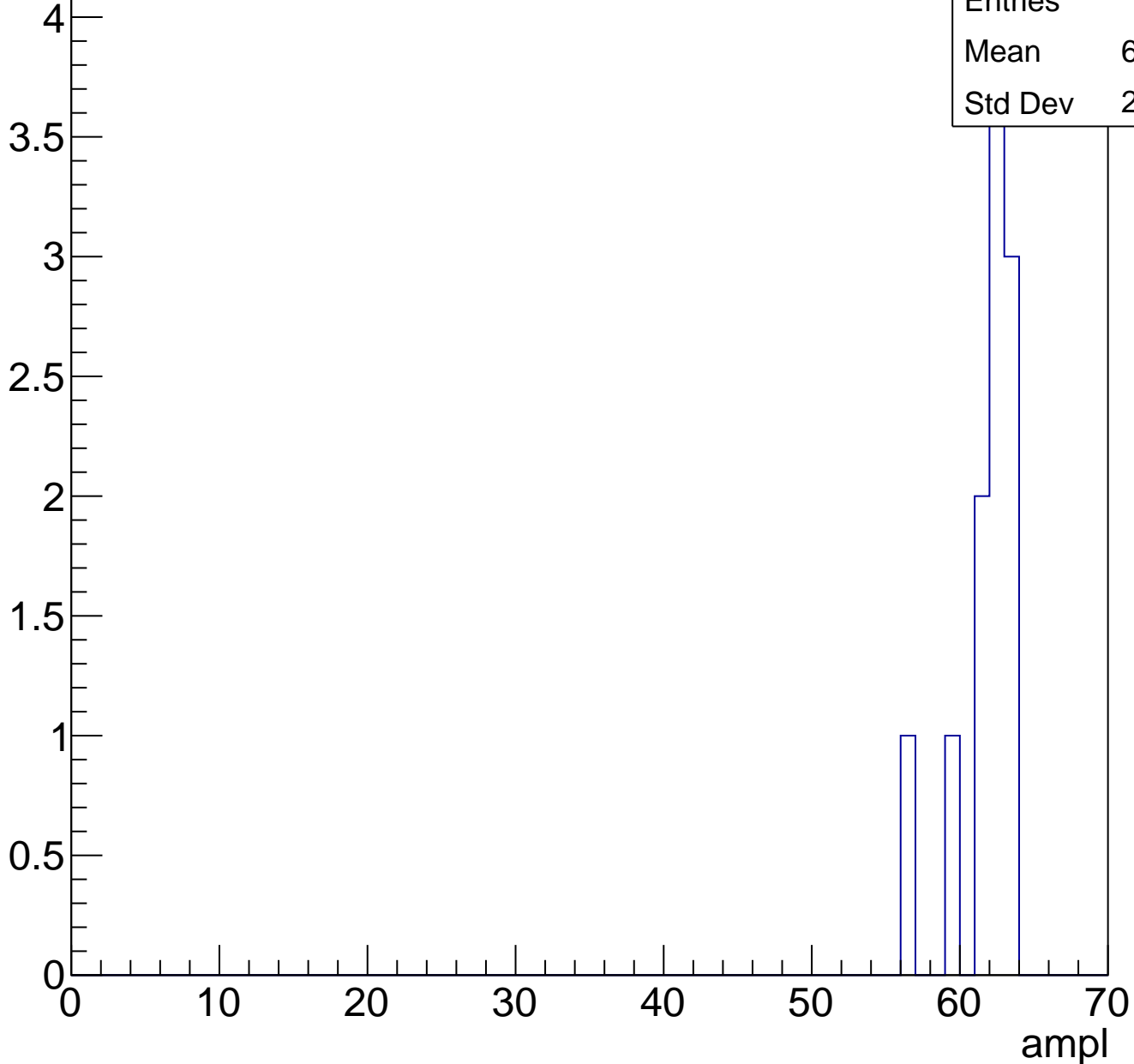
60

70

B1L101S, U4-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

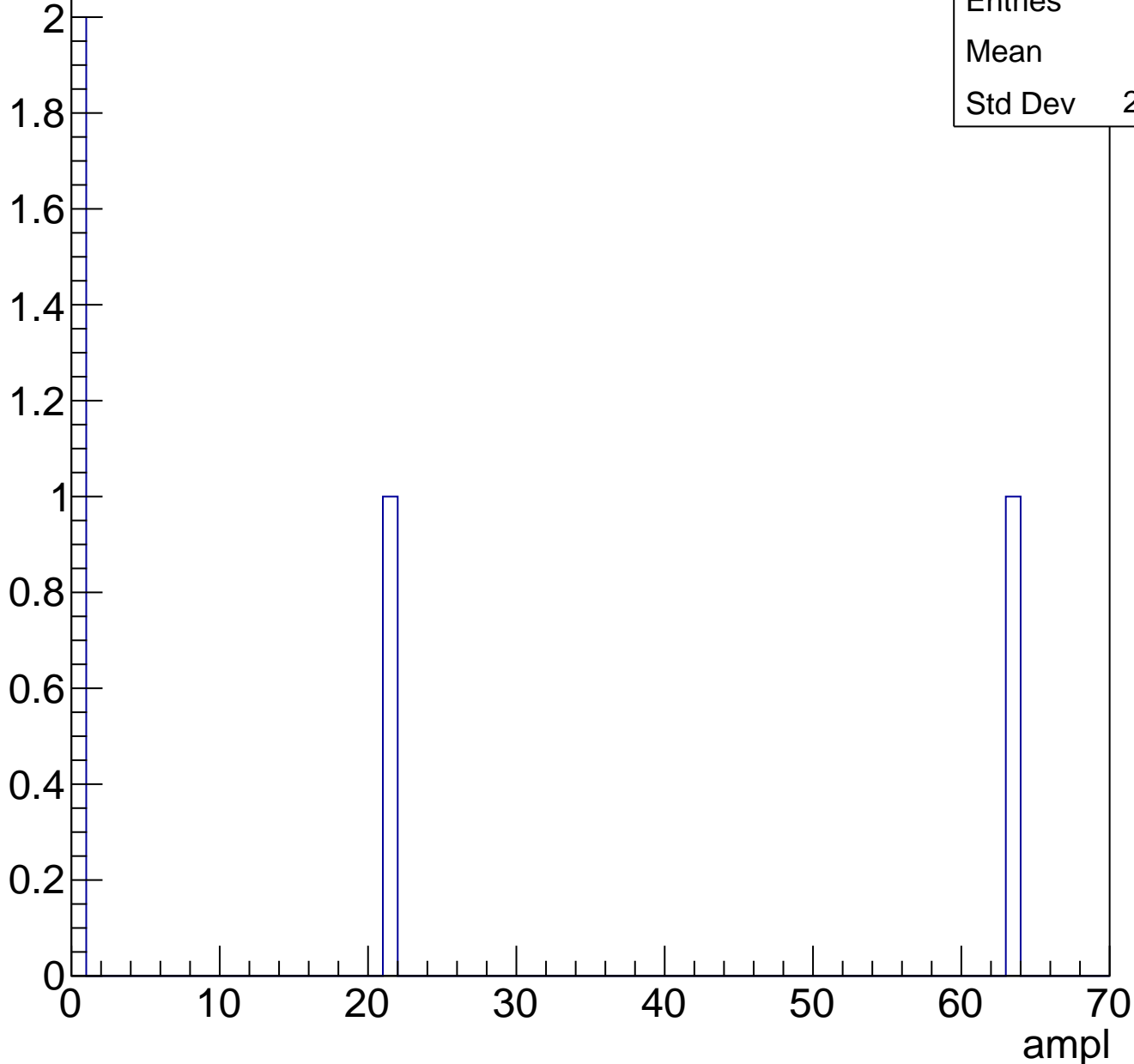
Entry



B1L101S, U4-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	21
Std Dev	25.72

B1L101S, U4-ch69, adc0

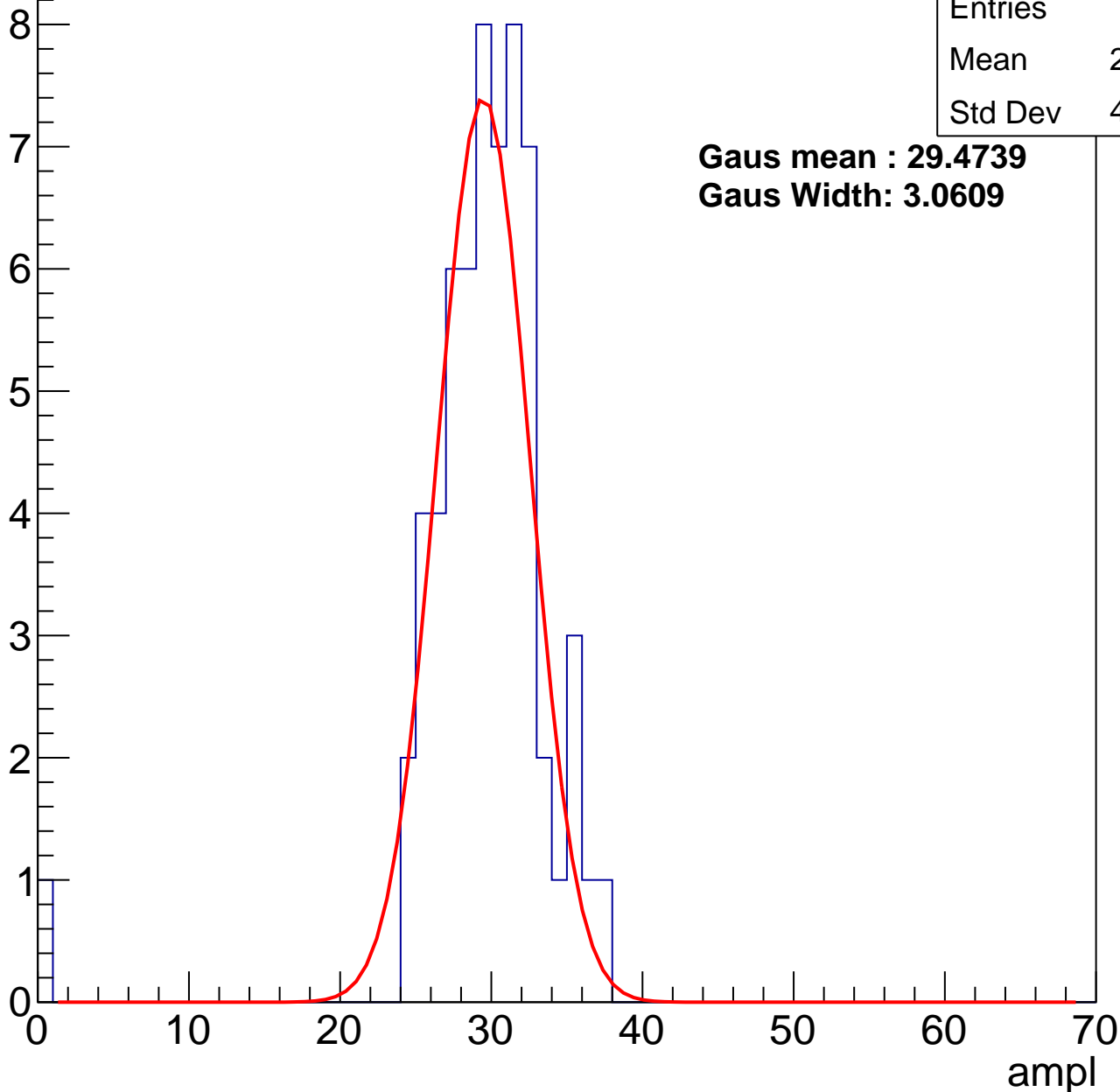
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.08
Std Dev	4.792

Gaus mean : 29.4739

Gaus Width: 3.0609



B1L101S, U4-ch69, adc1

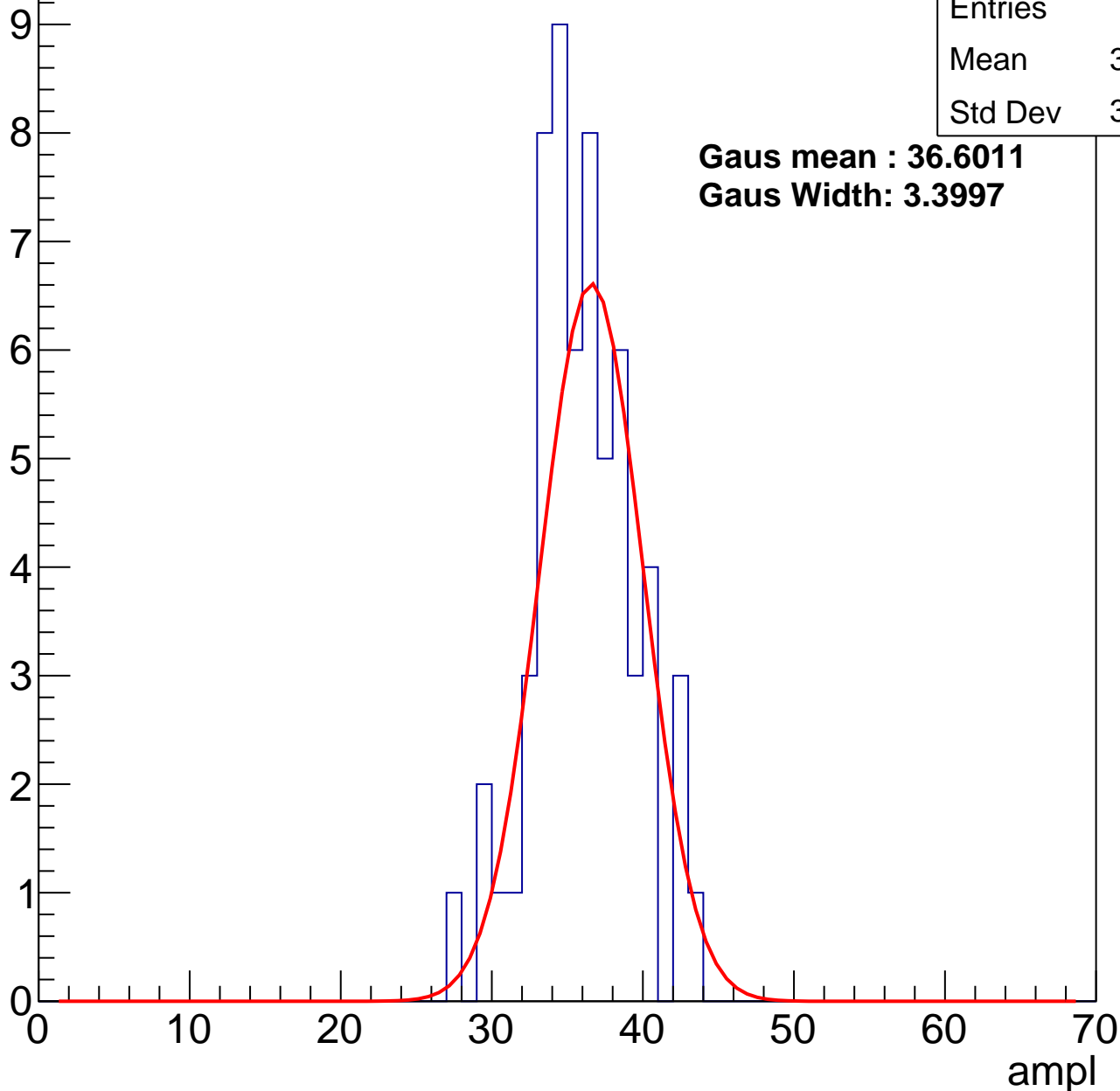
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.56
Std Dev	3.307

Gaus mean : 36.6011

Gaus Width: 3.3997



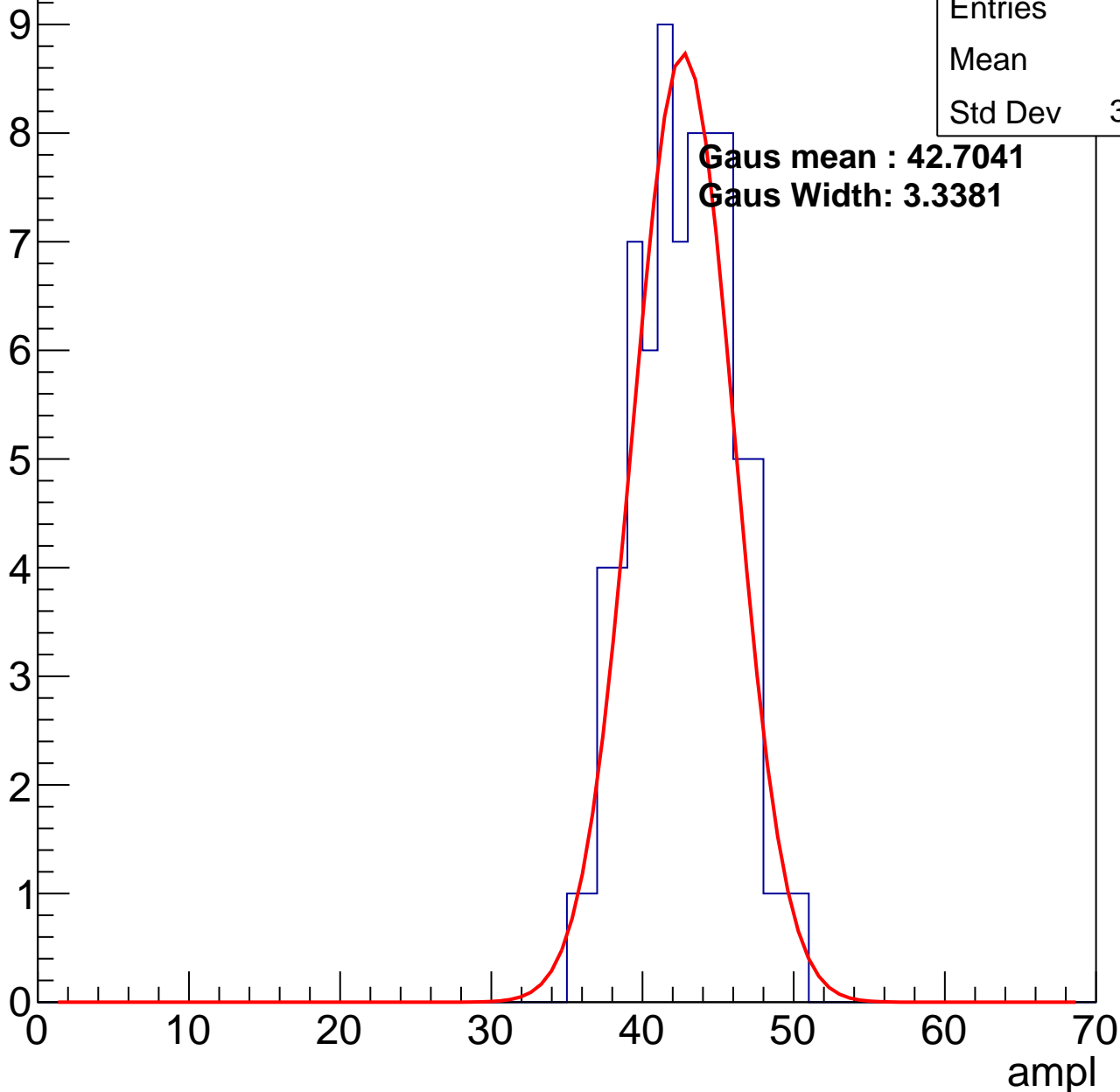
B1L101S, U4-ch69, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	42.3
Std Dev	3.253

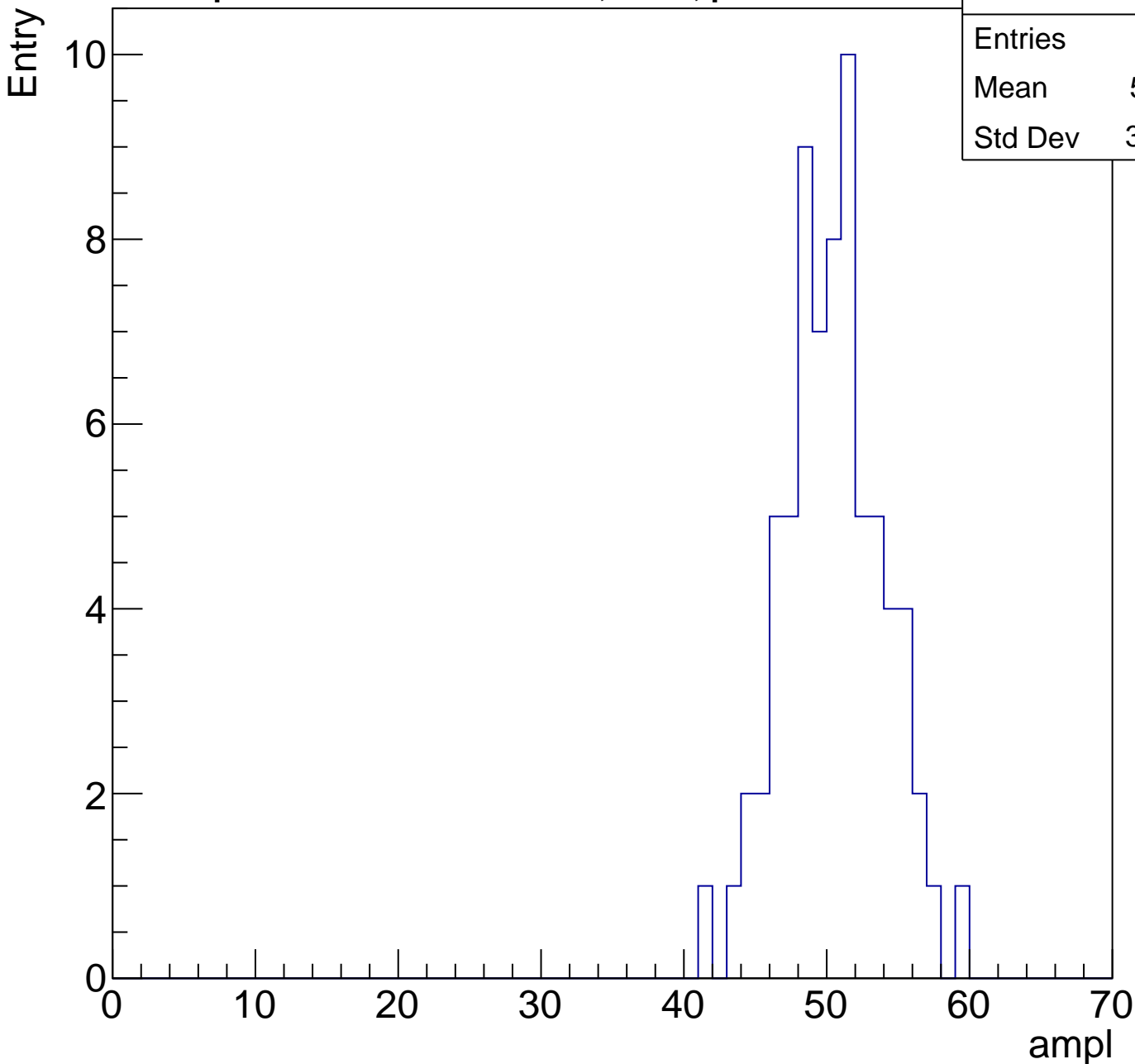
Gaus mean : 42.7041
Gaus Width: 3.3381



B1L101S, U4-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	50.01
Std Dev	3.458

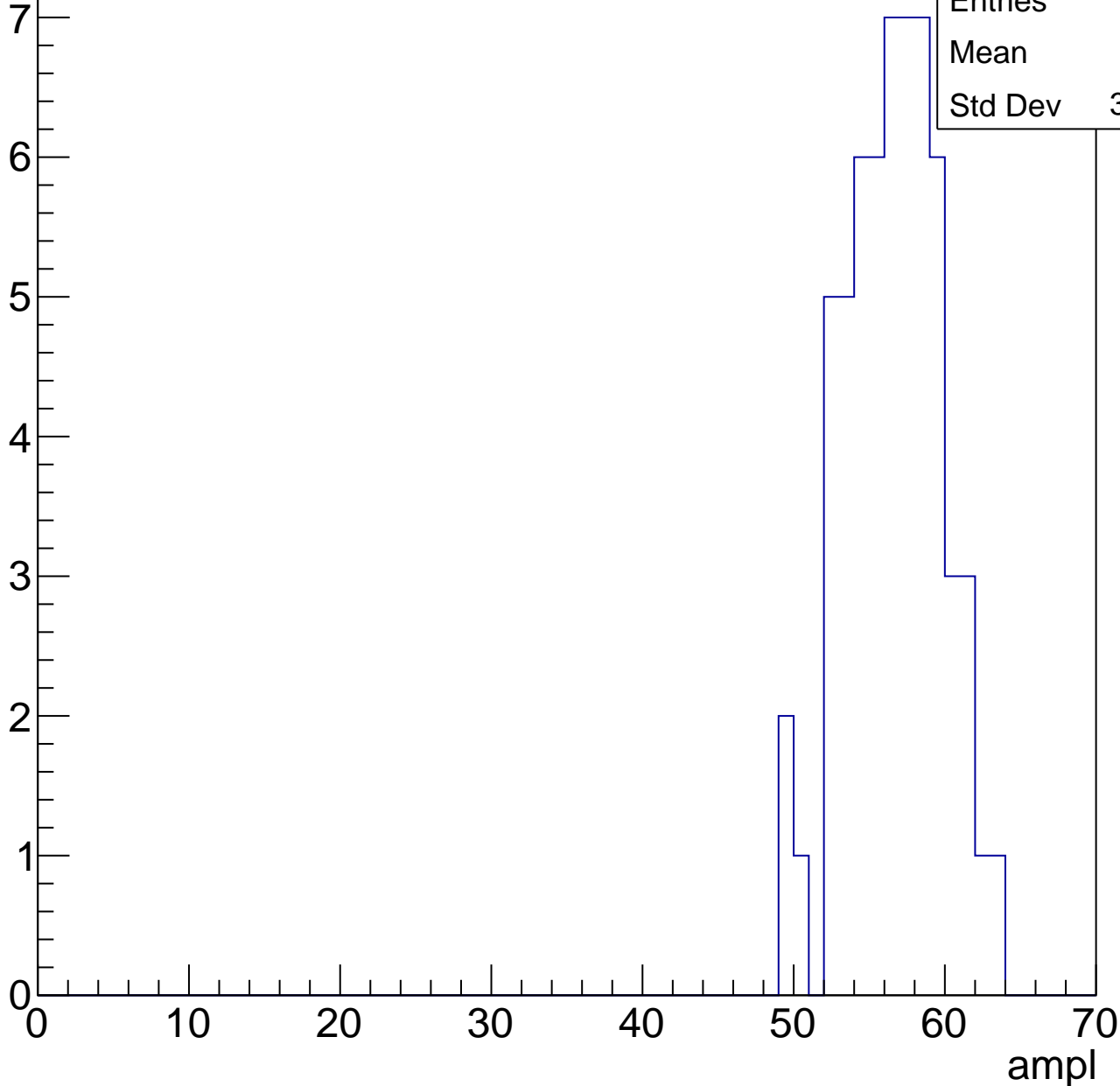


B1L101S, U4-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56.1
Std Dev	3.124



B1L101S, U4-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

60.56

Std Dev

1.958

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

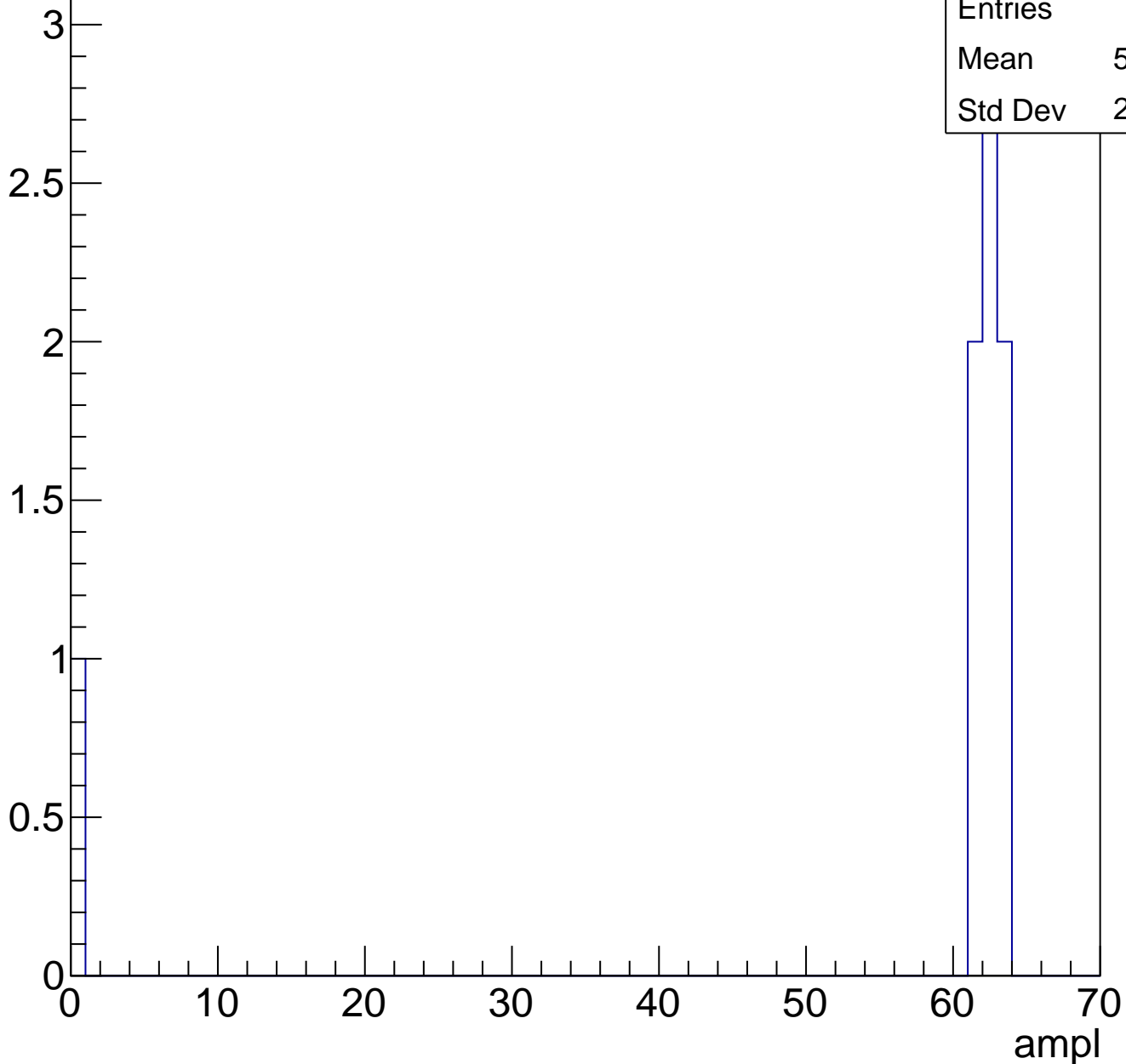
6

7

B1L101S, U4-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch70, adc0

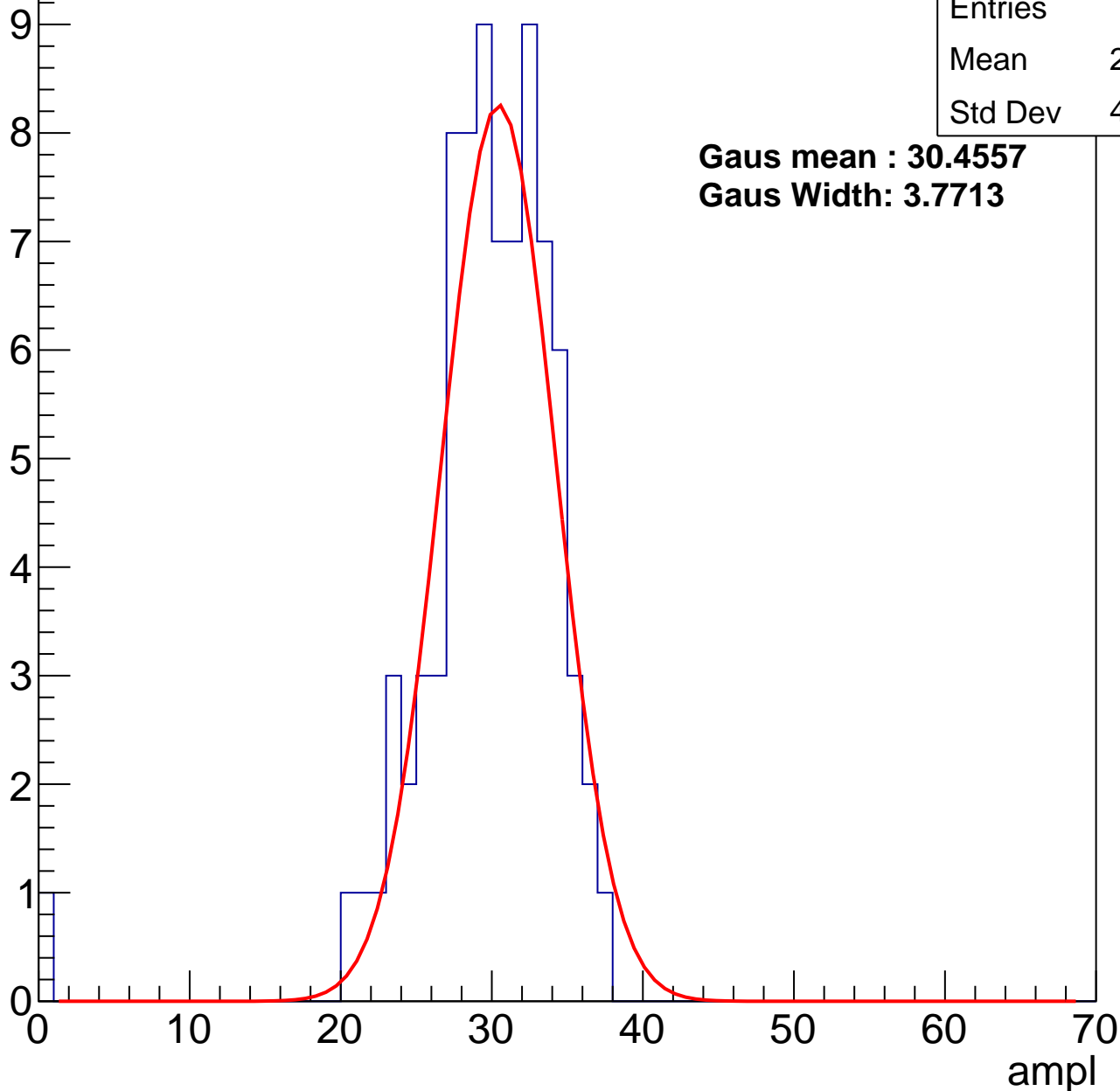
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	29.24
Std Dev	4.878

Gaus mean : 30.4557

Gaus Width: 3.7713



B1L101S, U4-ch70, adc1

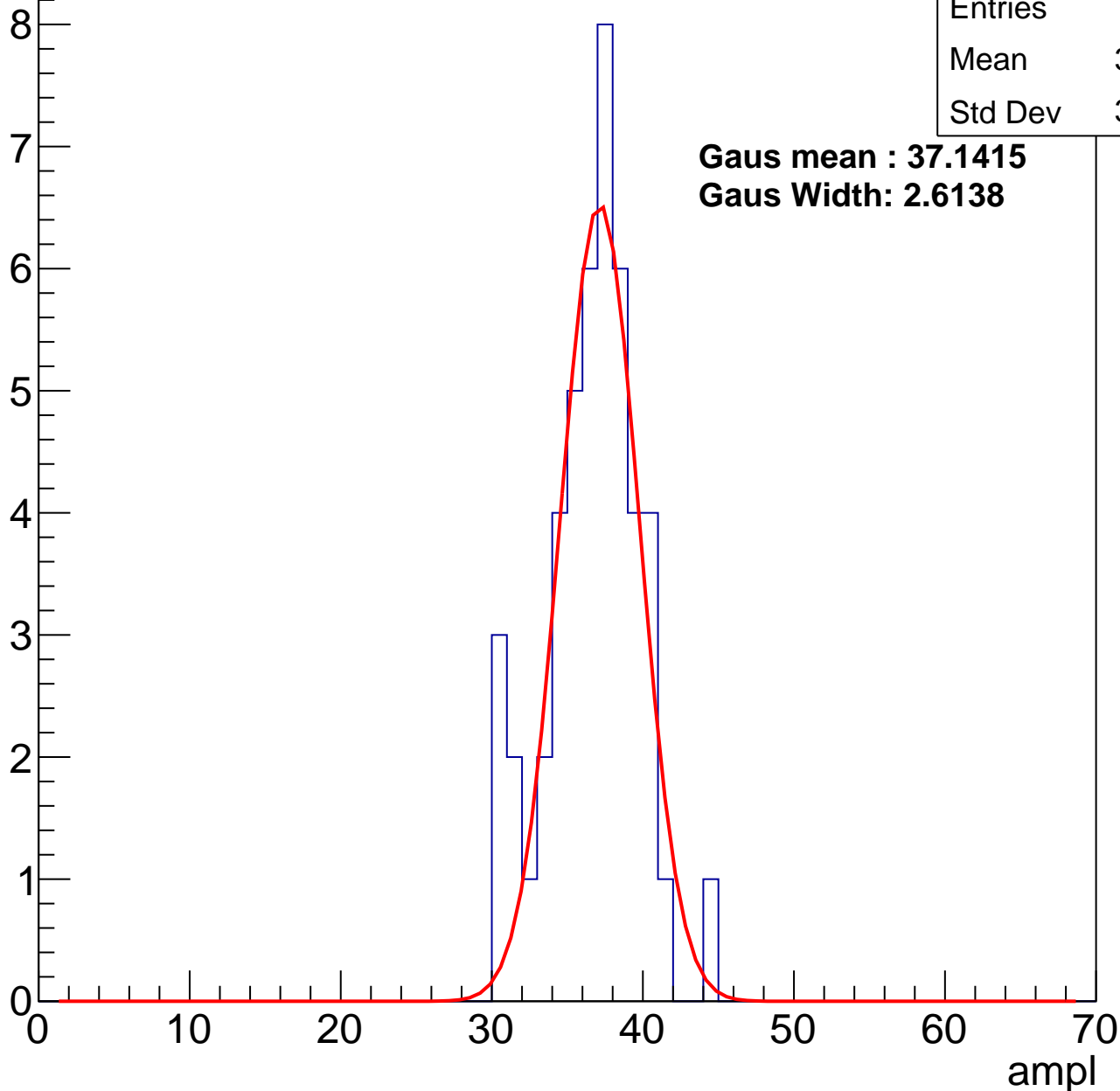
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	36.21
Std Dev	3.031

Gaus mean : 37.1415

Gaus Width: 2.6138



B1L101S, U4-ch70, adc2

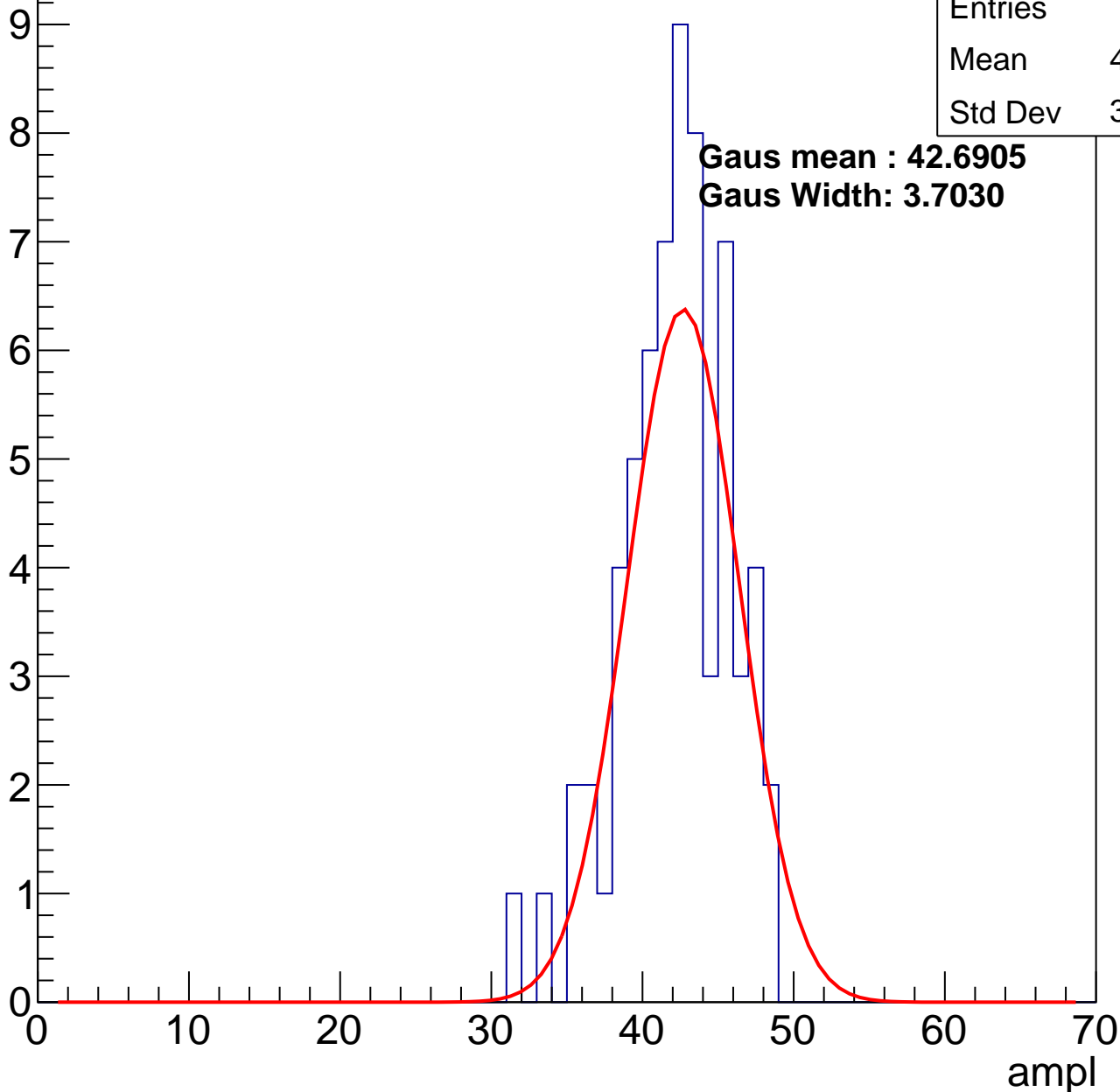
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	41.66
Std Dev	3.579

Gaus mean : 42.6905

Gaus Width: 3.7030

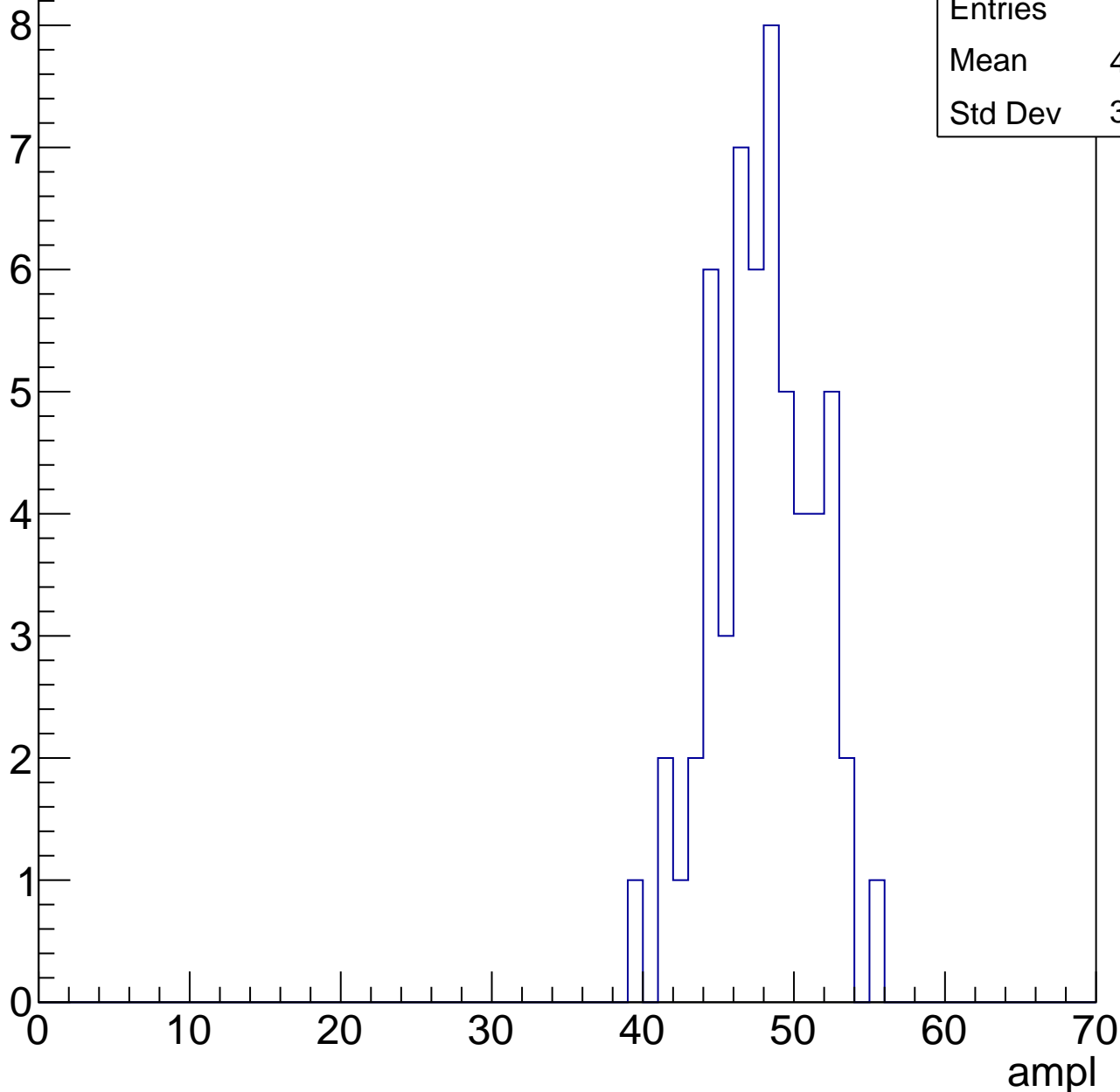


B1L101S, U4-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

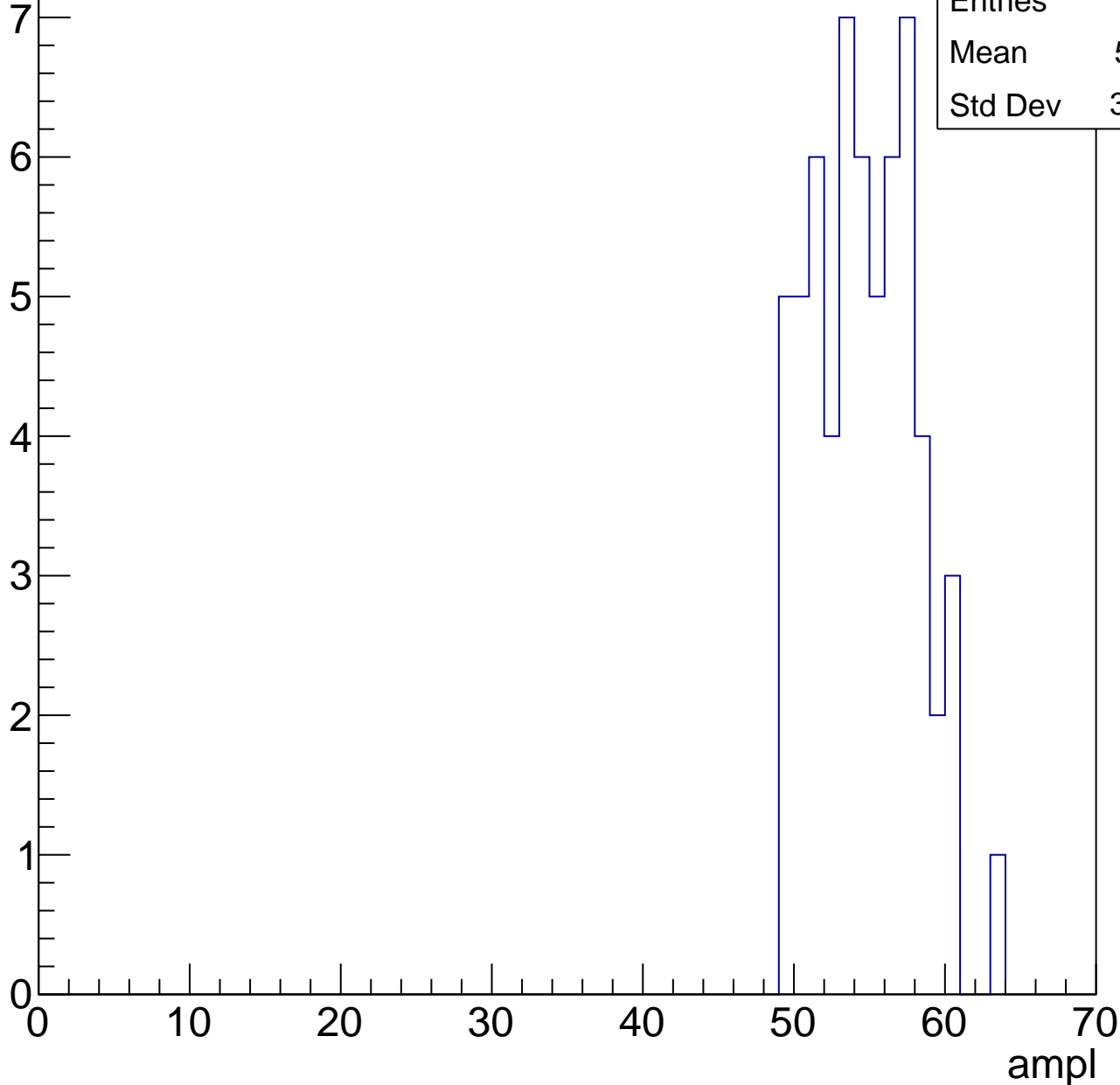
Entries	57
Mean	47.47
Std Dev	3.357



B1L101S, U4-ch70, adc4

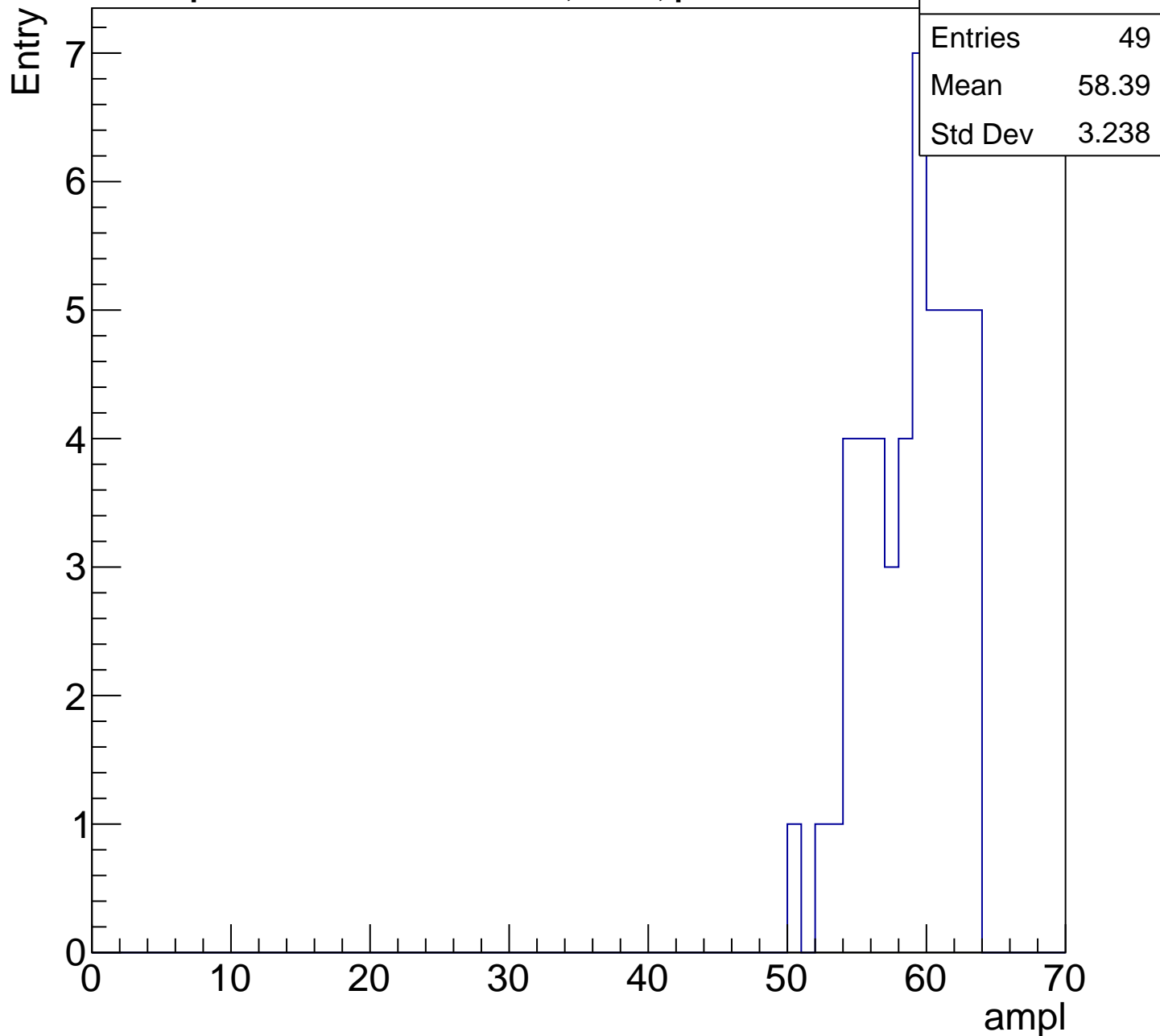
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

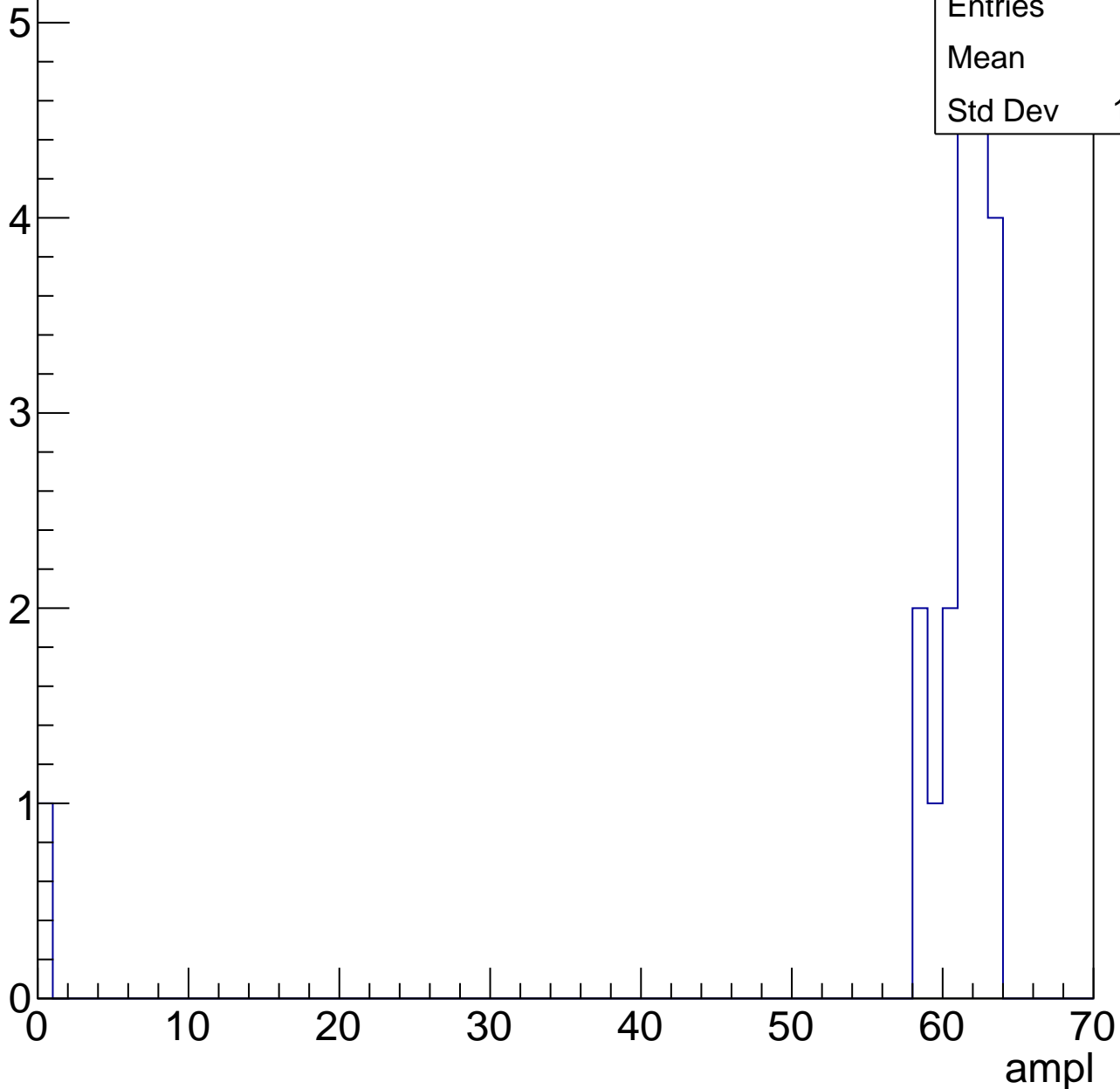


B1L101S, U4-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	58.1
Std Dev	13.41



B1L101S, U4-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch71, adc0

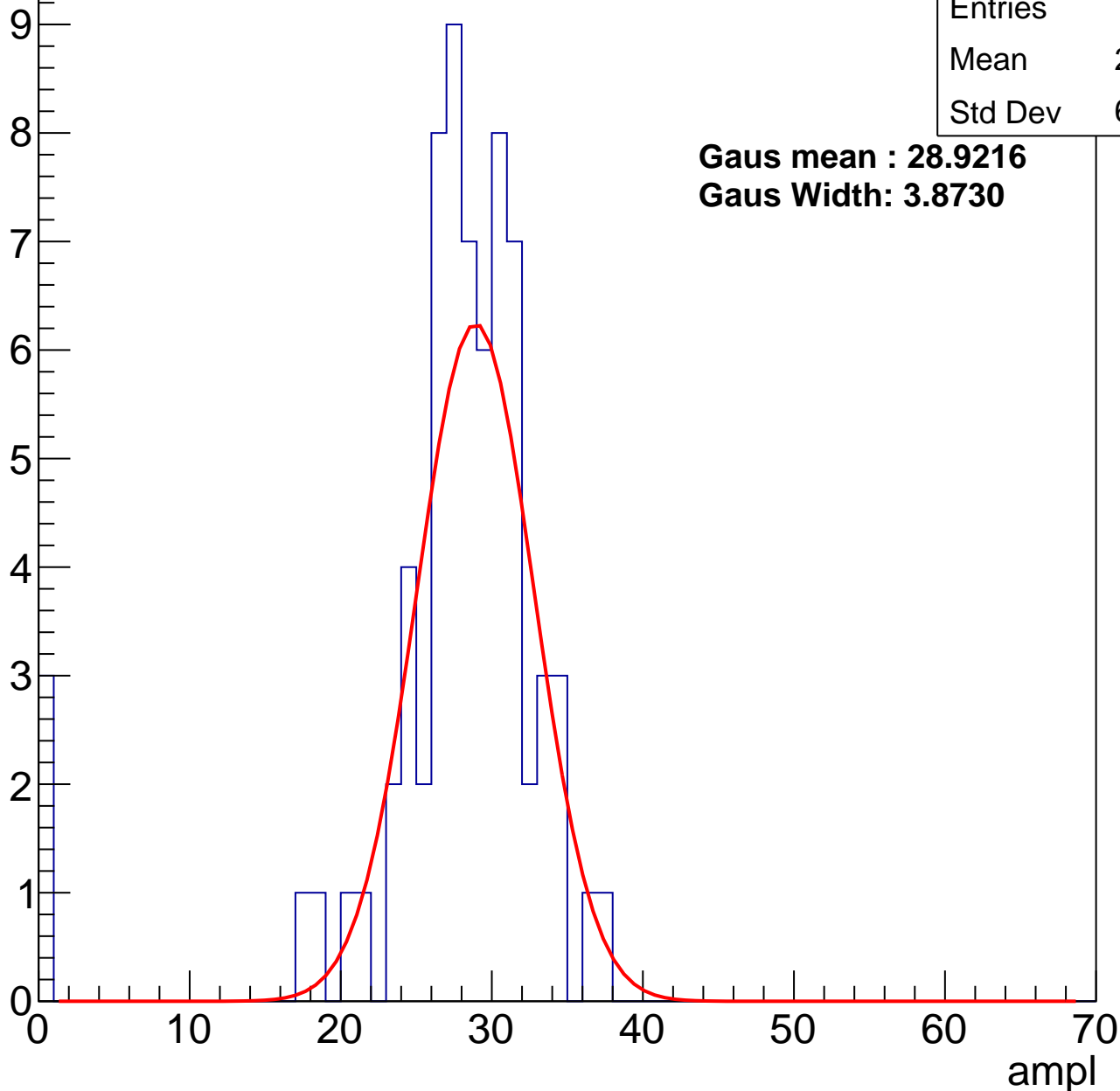
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	26.91
Std Dev	6.801

Gaus mean : 28.9216

Gaus Width: 3.8730



B1L101S, U4-ch71, adc1

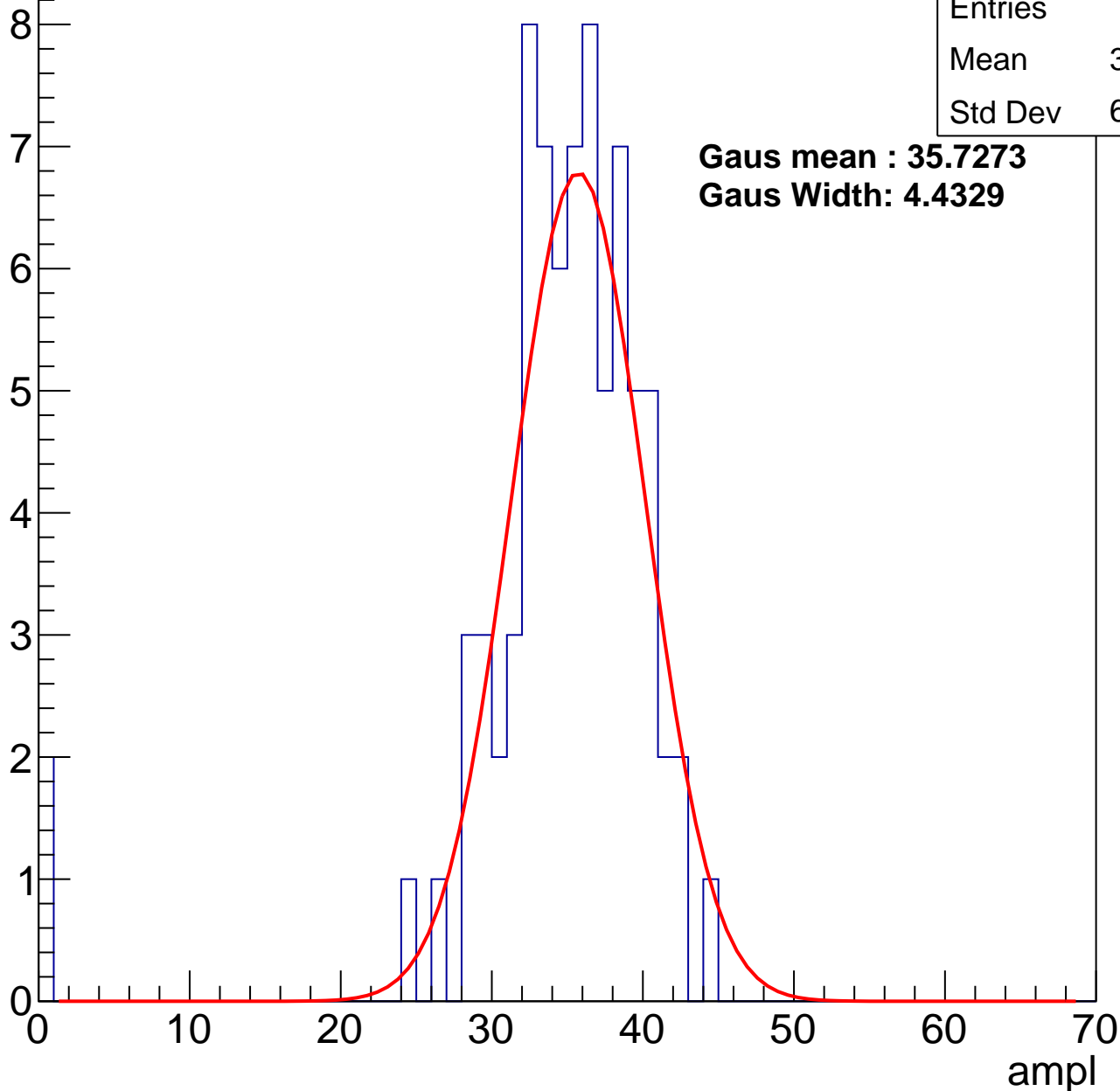
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	34.03
Std Dev	6.778

Gaus mean : 35.7273

Gaus Width: 4.4329



B1L101S, U4-ch71, adc2

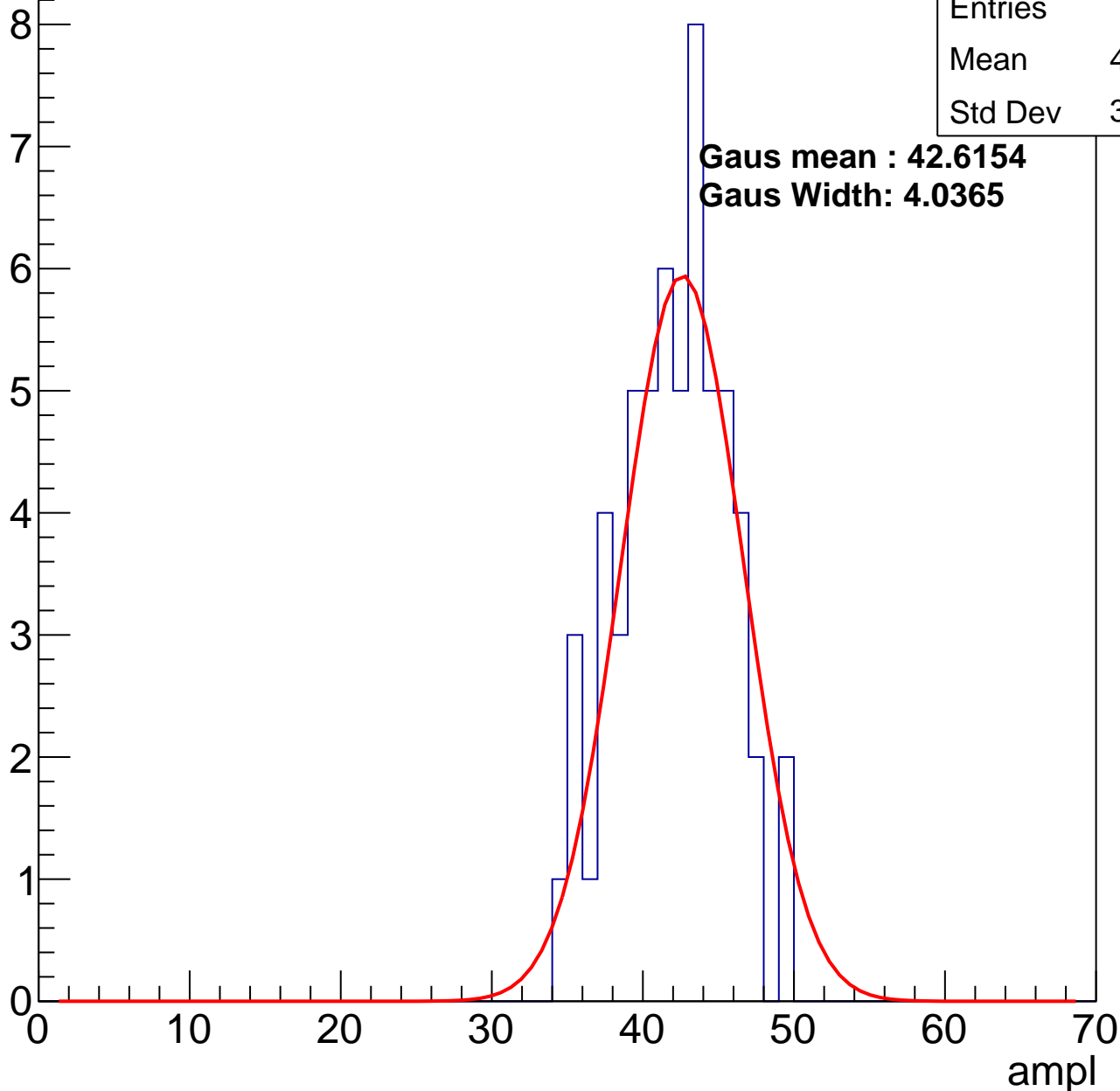
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	41.58
Std Dev	3.542

Gaus mean : 42.6154

Gaus Width: 4.0365



B1L101S, U4-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

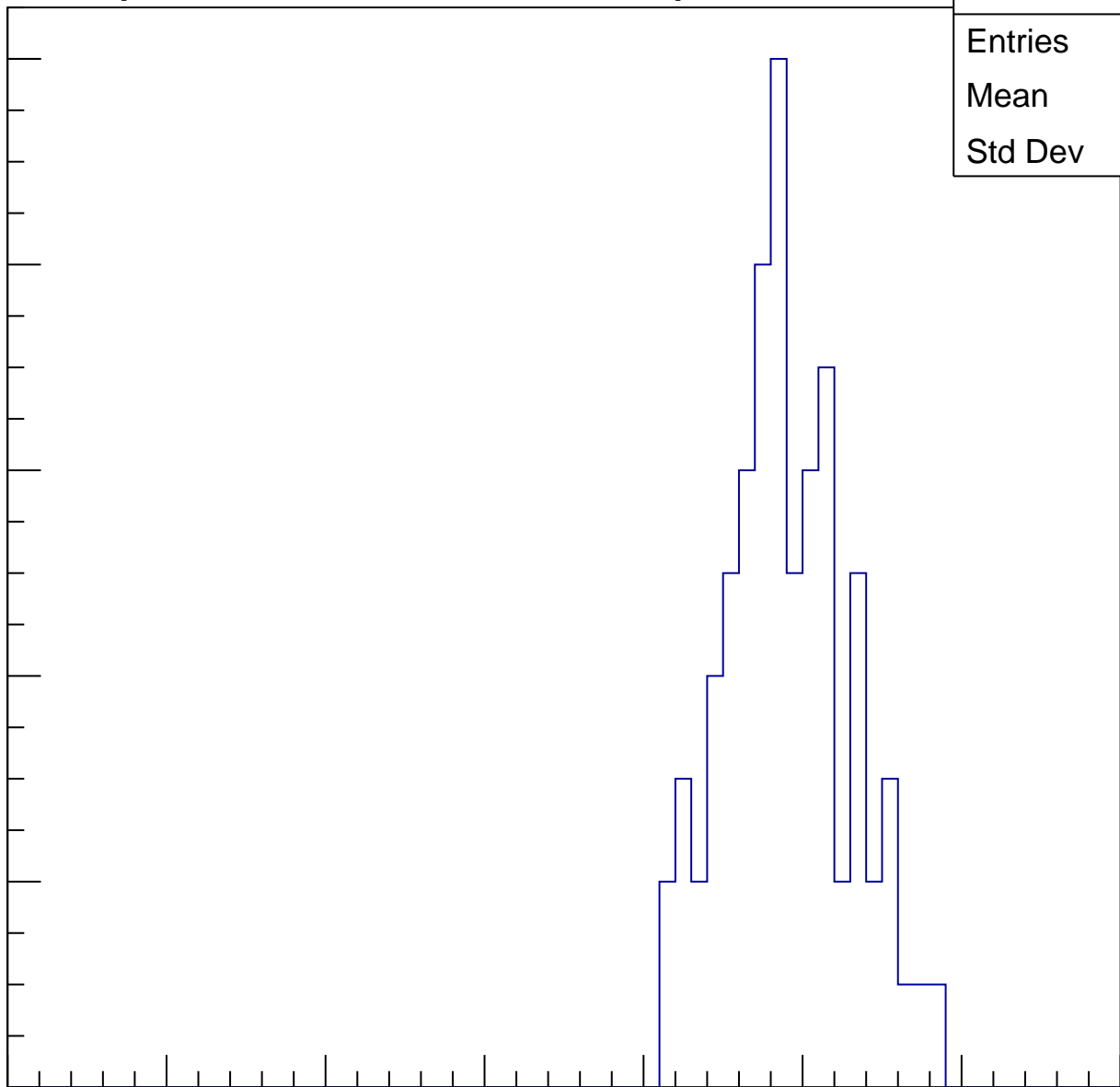
Entries	73
Mean	48.52
Std Dev	3.865

Entry

10
8
6
4
2
0

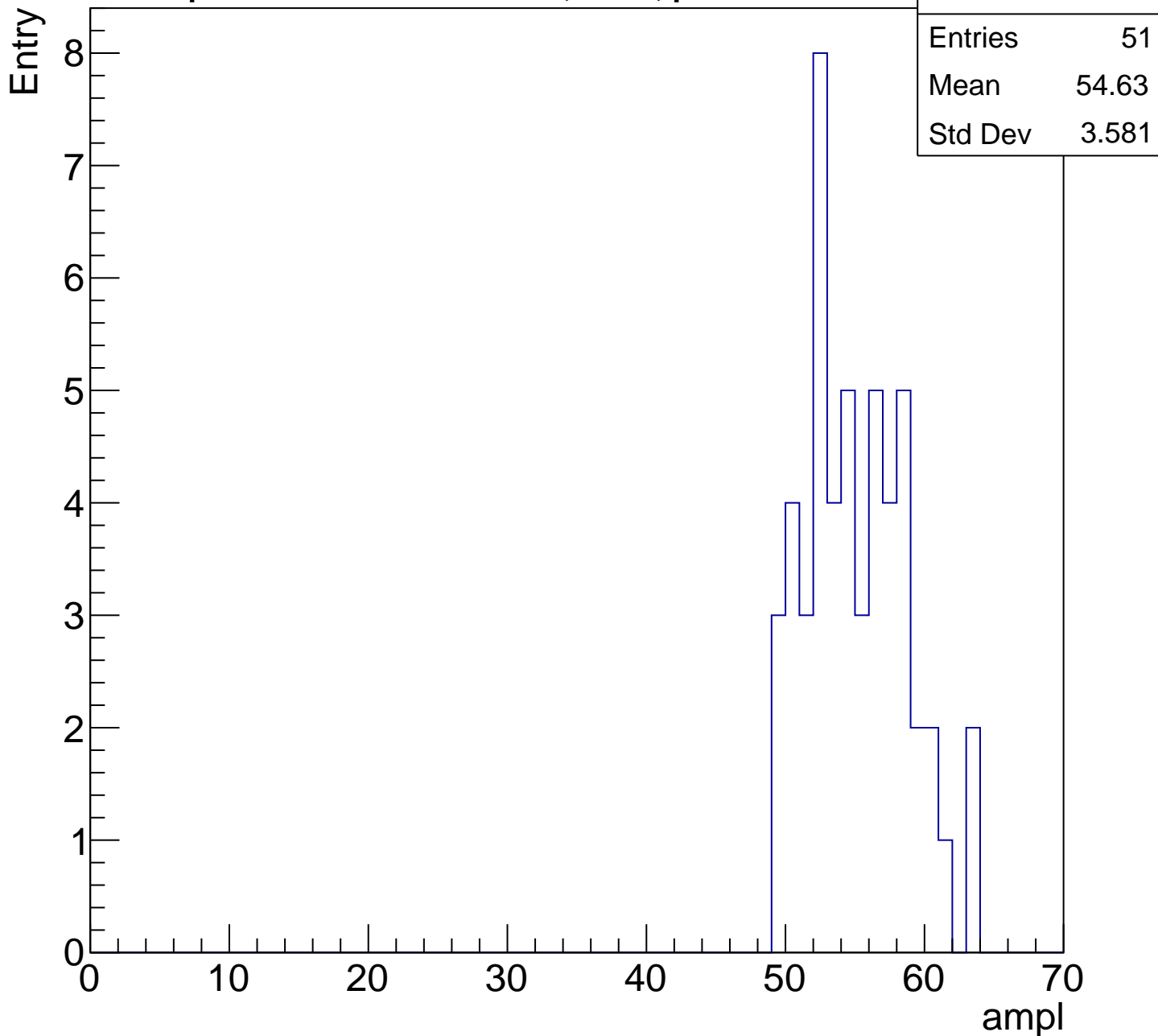
0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

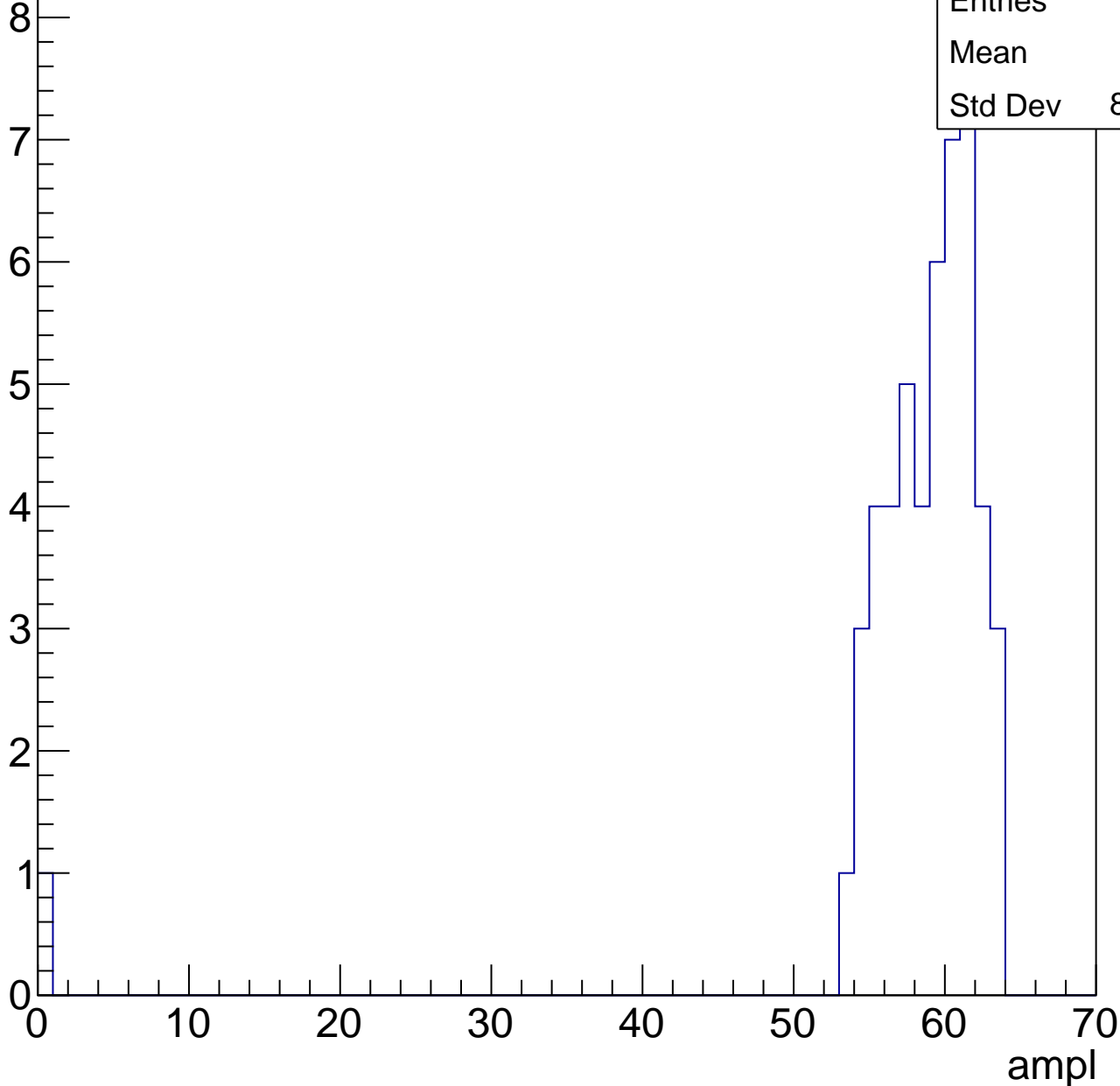


B1L101S, U4-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	57.5
Std Dev	8.633

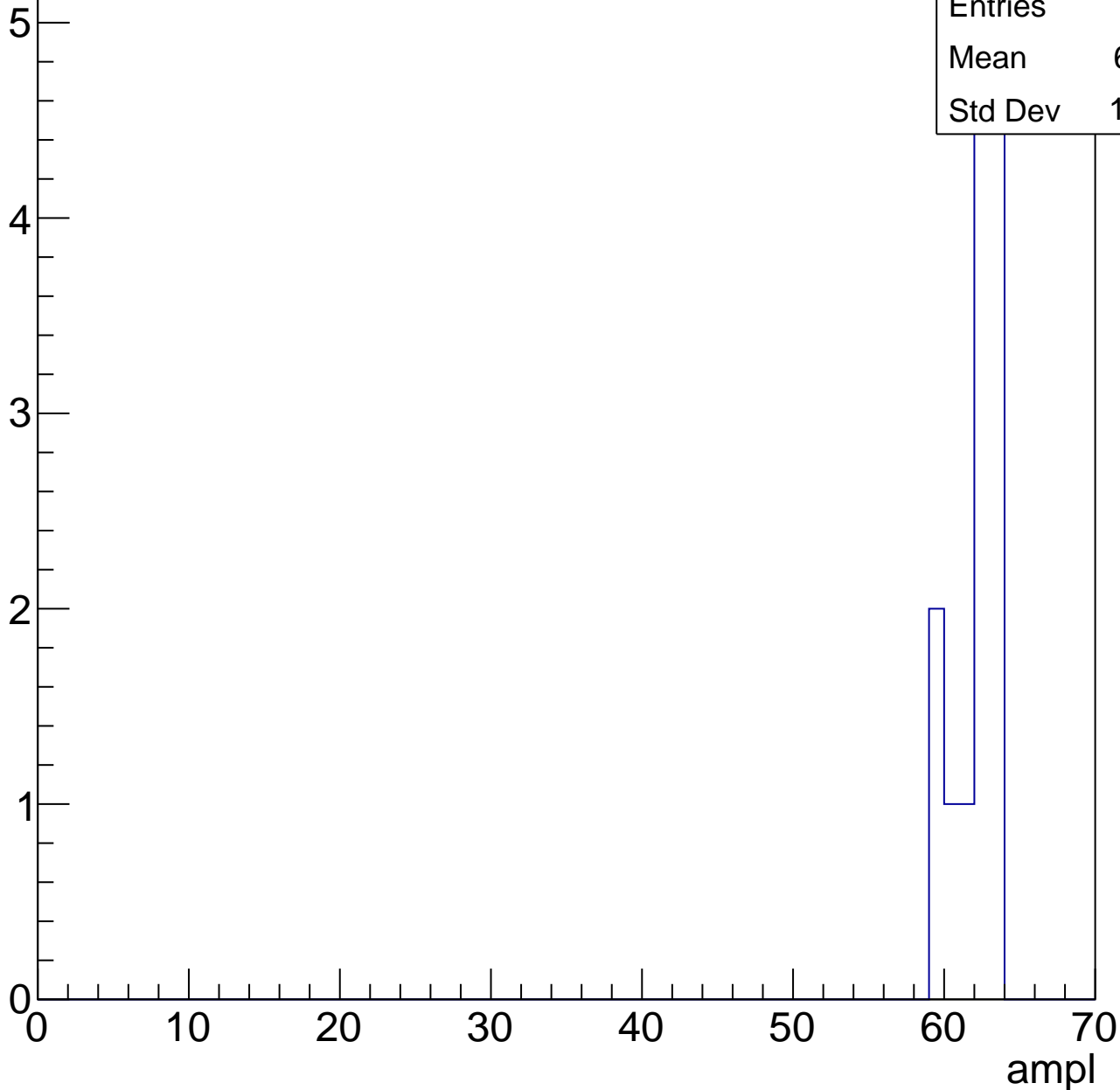


B1L101S, U4-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

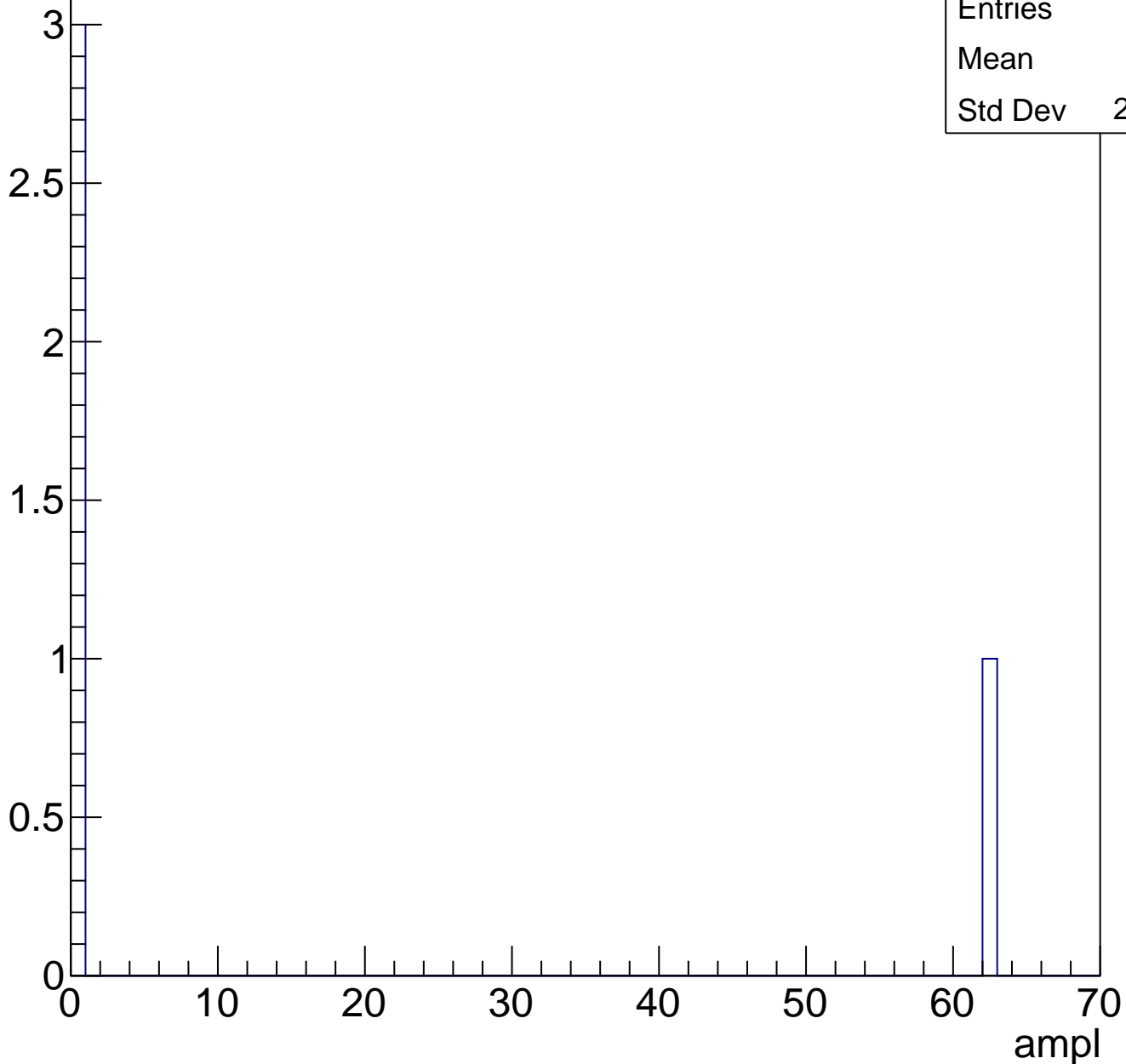
Entries	14
Mean	61.71
Std Dev	1.385



B1L101S, U4-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch72, adc0

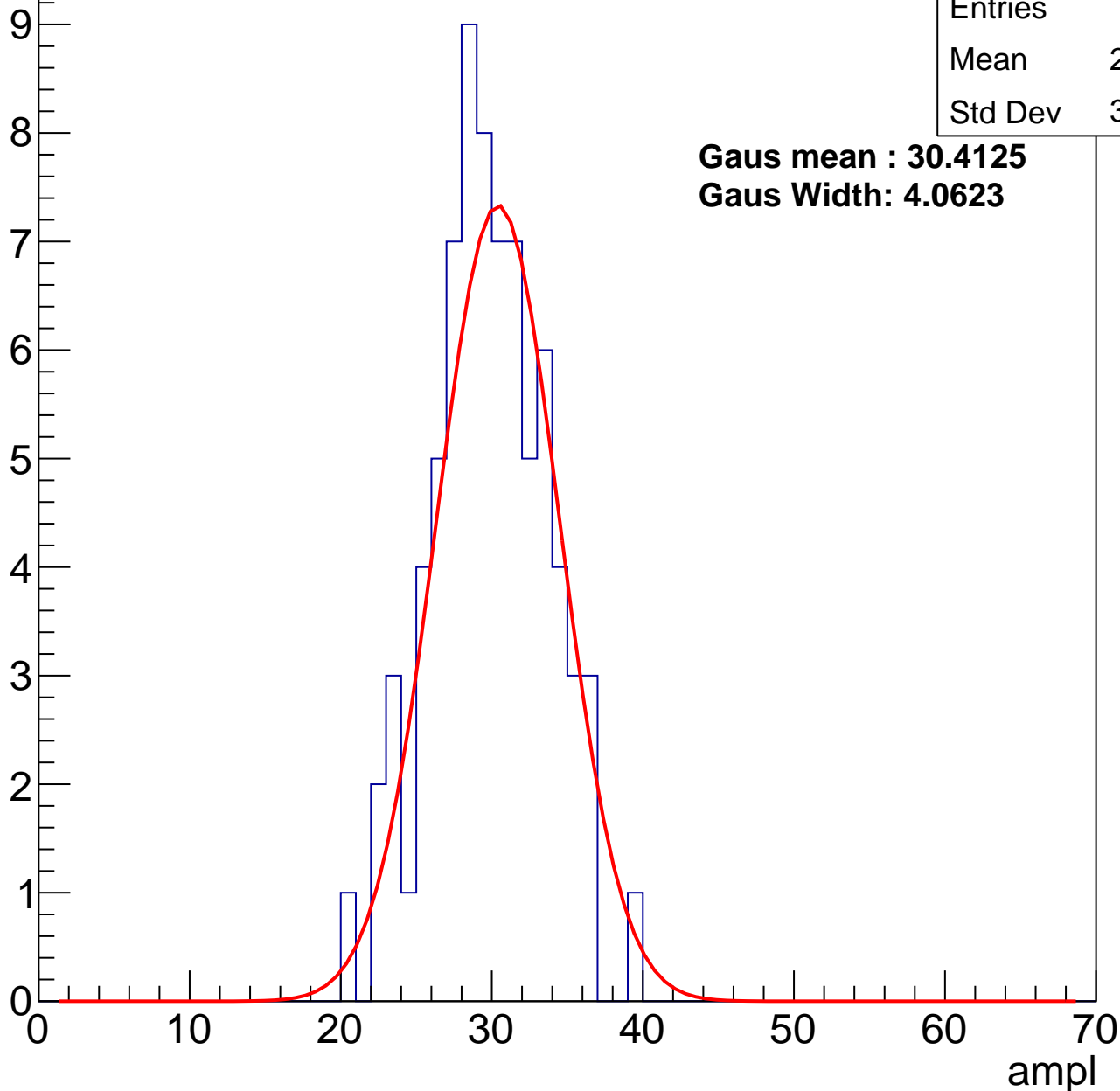
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.38
Std Dev	3.766

Gaus mean : 30.4125

Gaus Width: 4.0623



B1L101S, U4-ch72, adc1

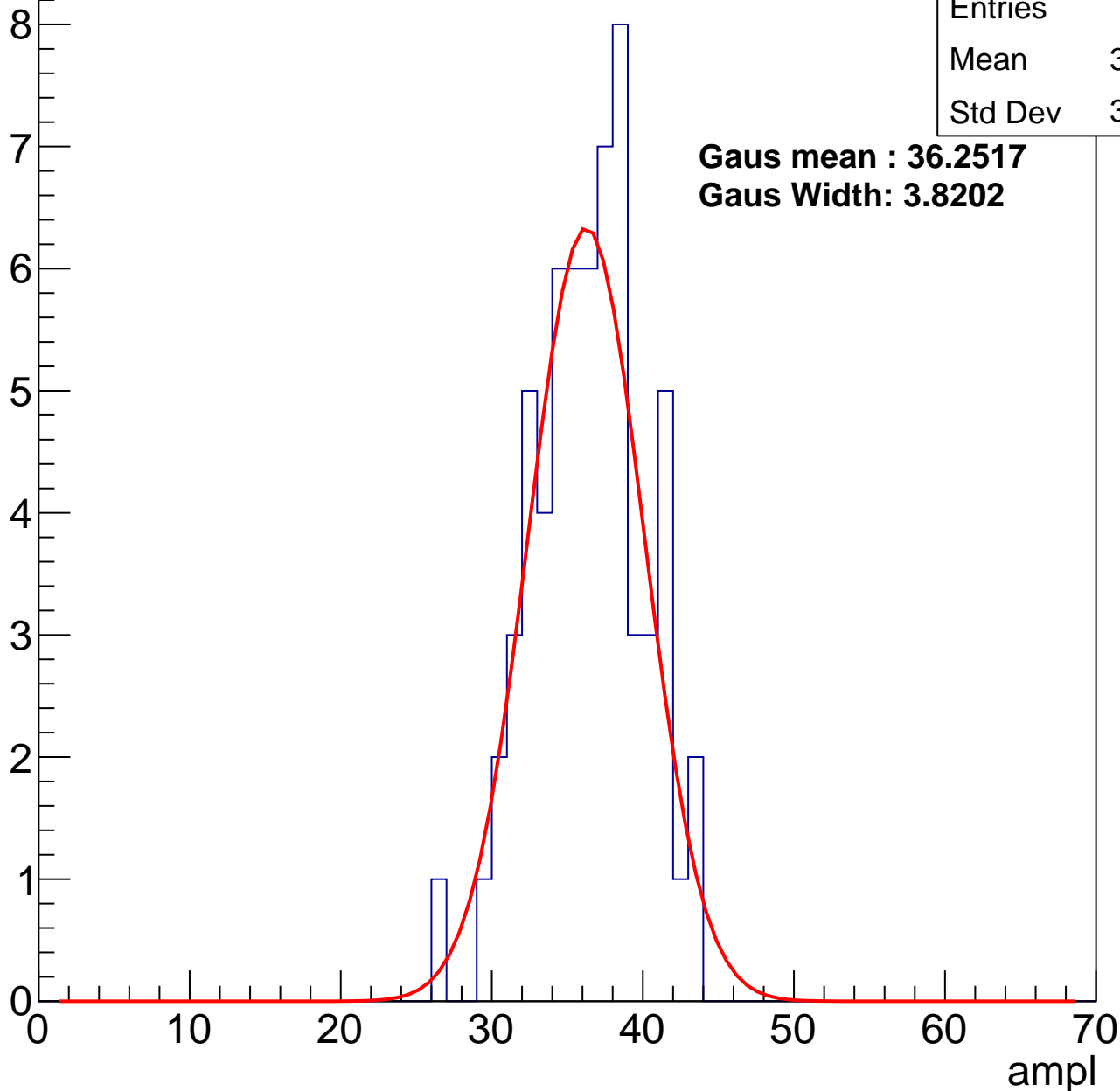
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.92
Std Dev	3.596

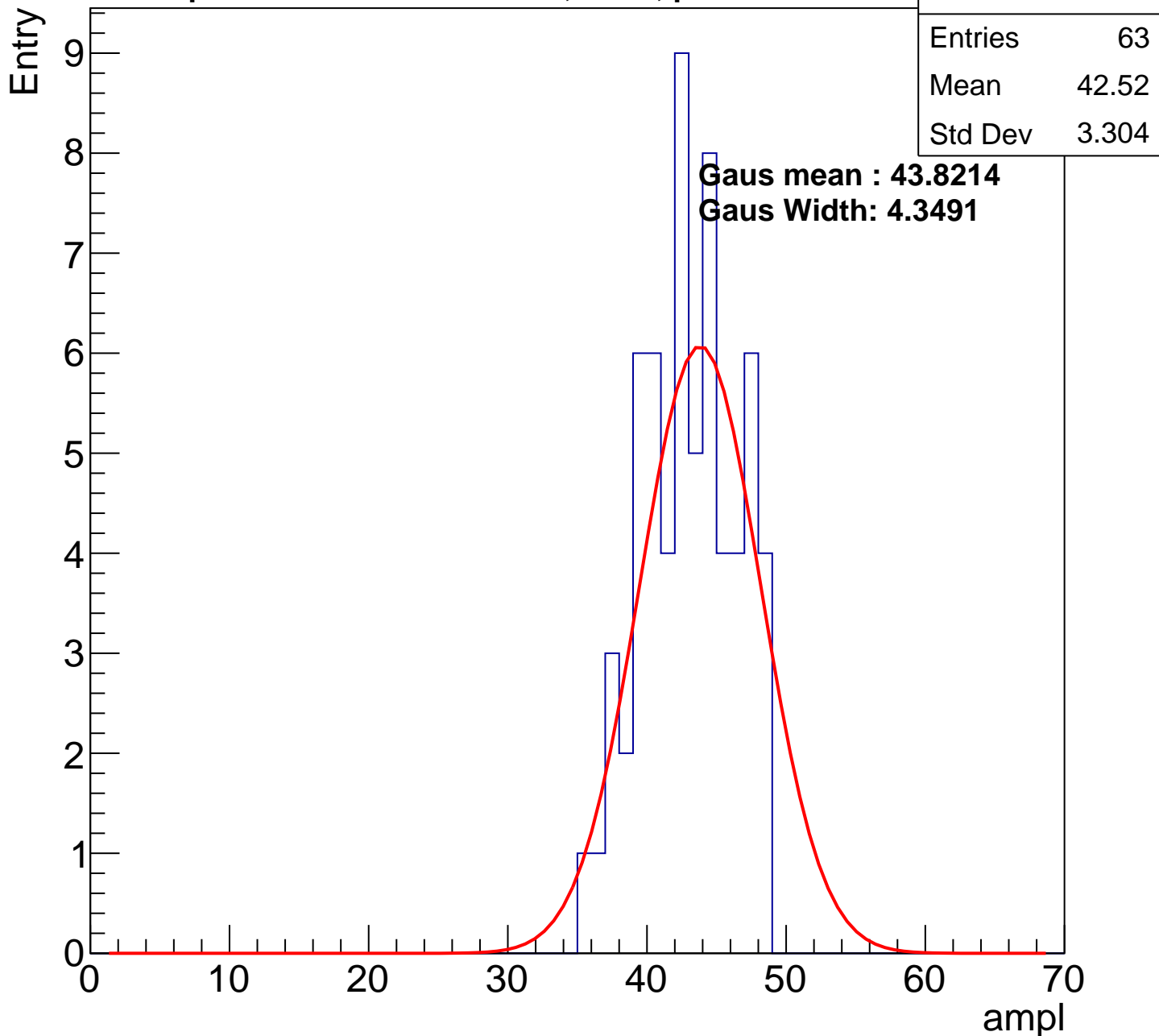
Gaus mean : 36.2517

Gaus Width: 3.8202



B1L101S, U4-ch72, adc2

calib_packv5_042523_0143.root, FC#0, port D2

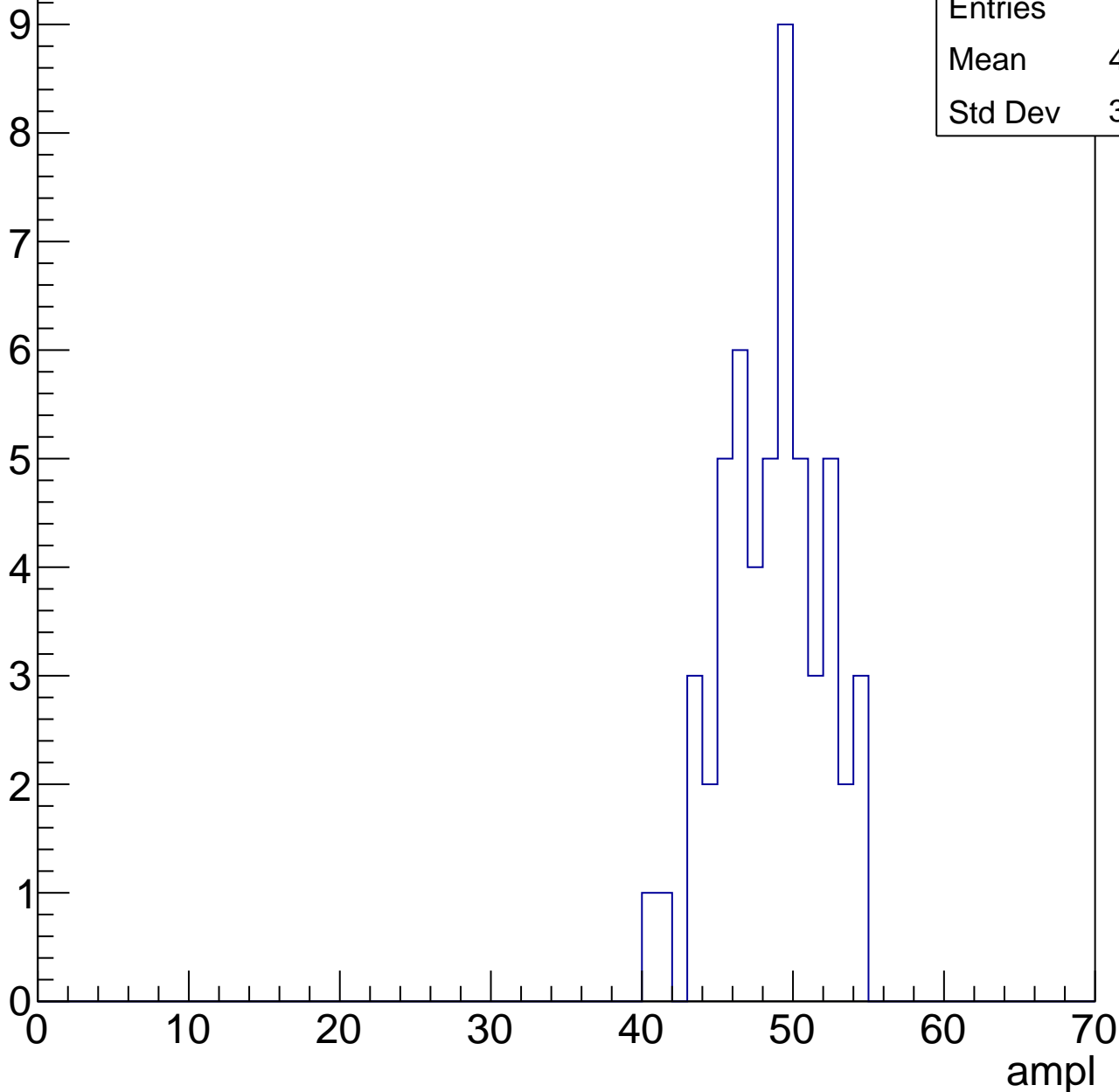


B1L101S, U4-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	48.13
Std Dev	3.294

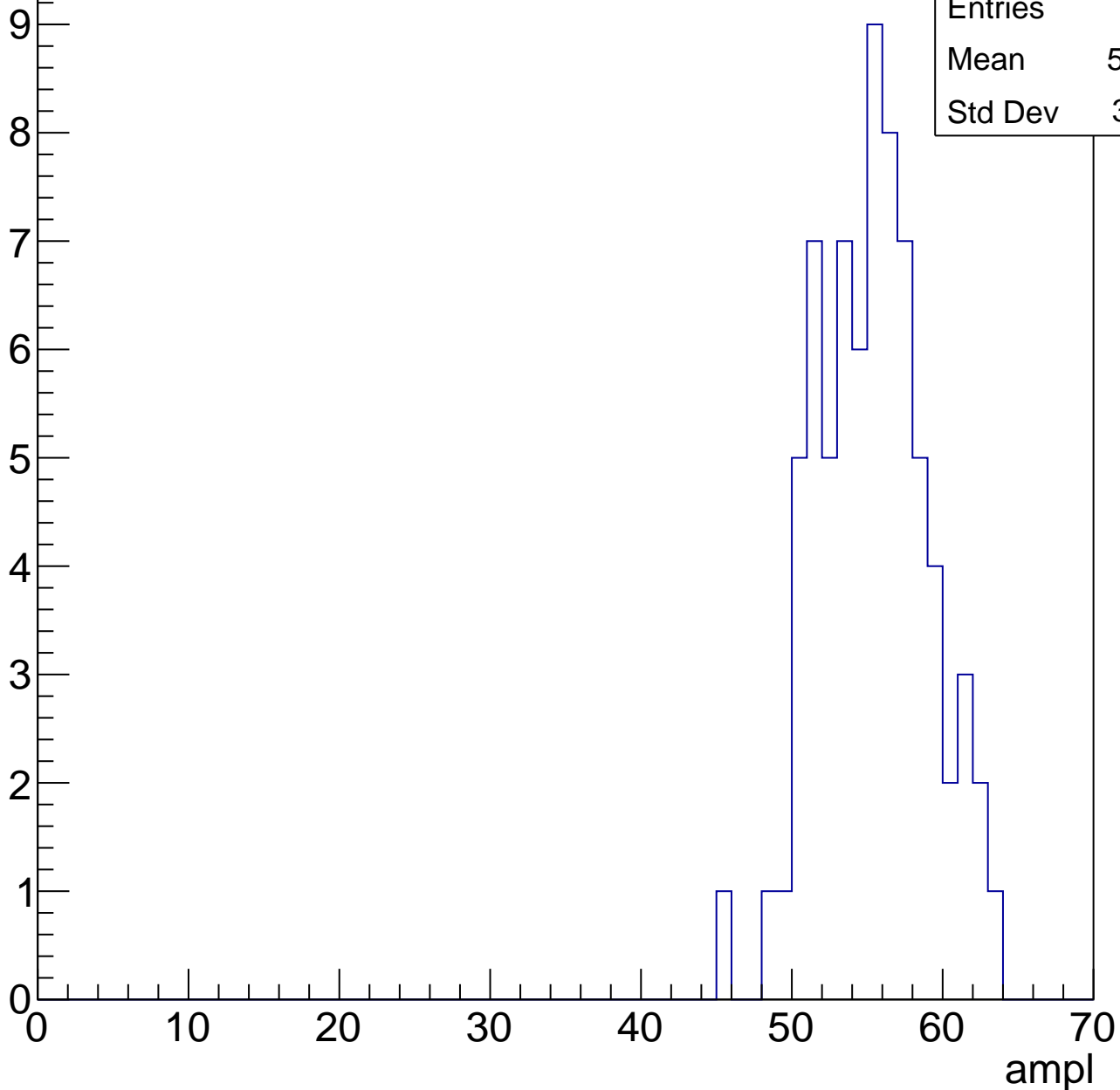


B1L101S, U4-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

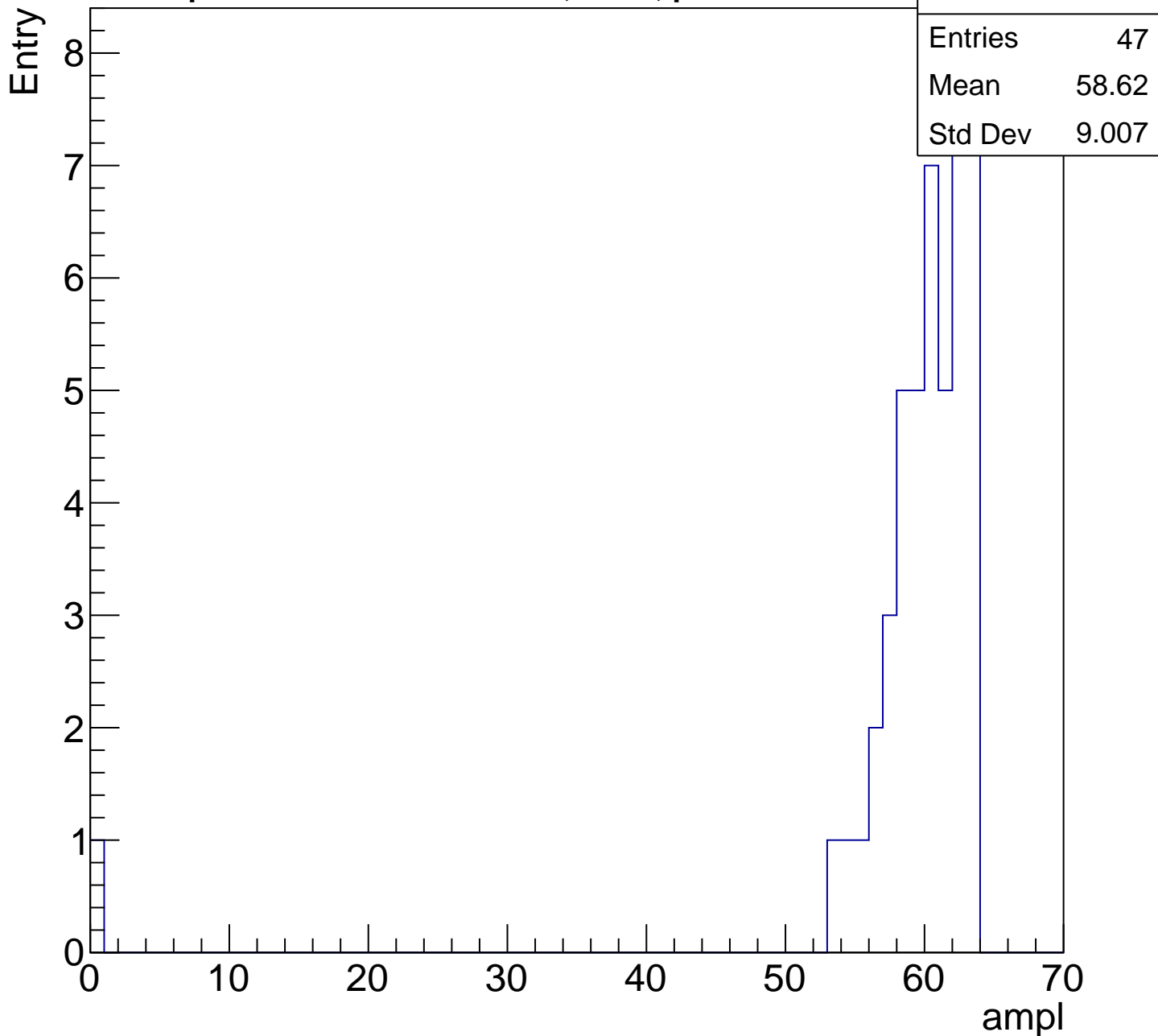
Entry

Entries	74
Mean	54.89
Std Dev	3.611



B1L101S, U4-ch72, adc5

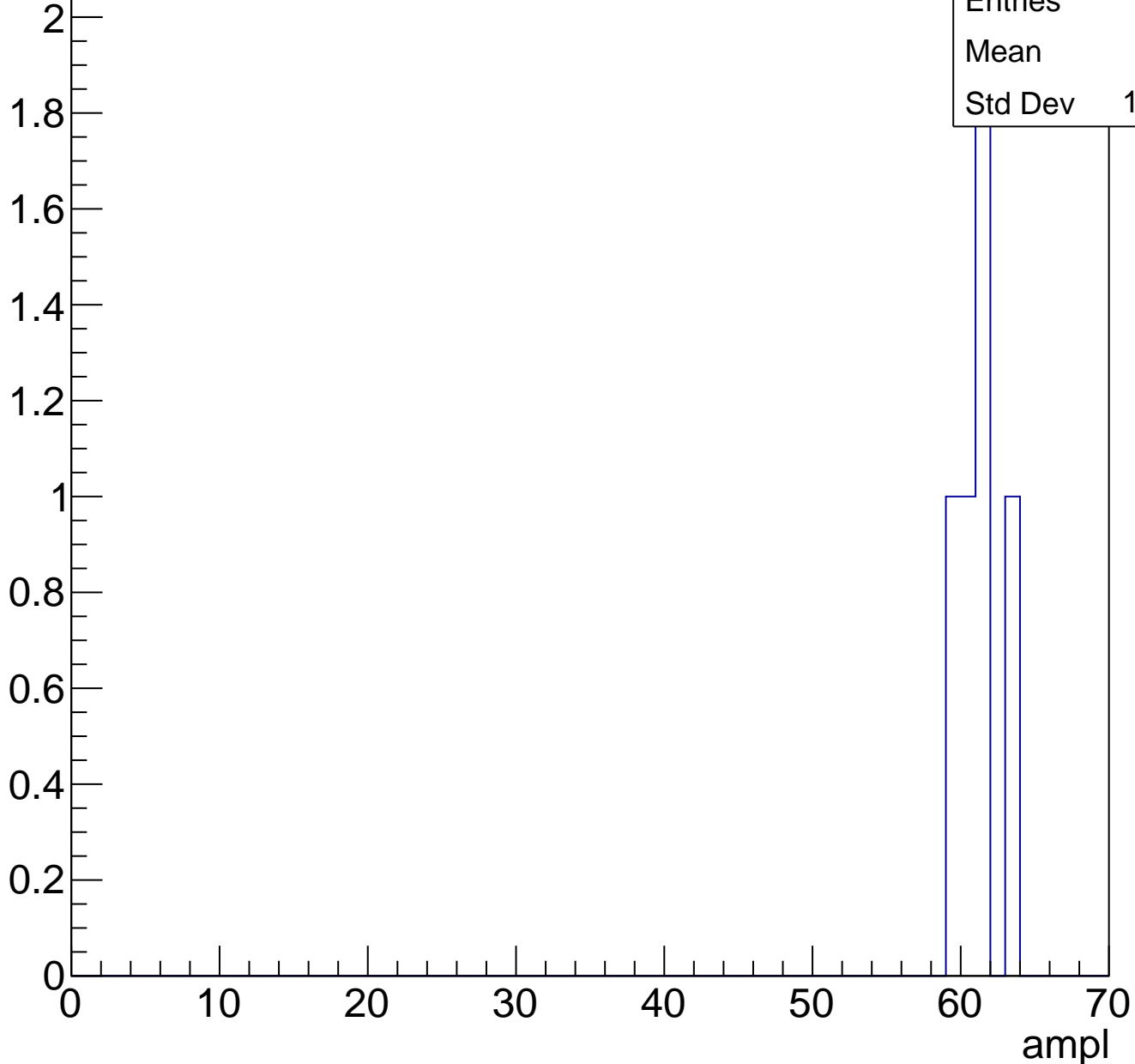
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch73, adc0

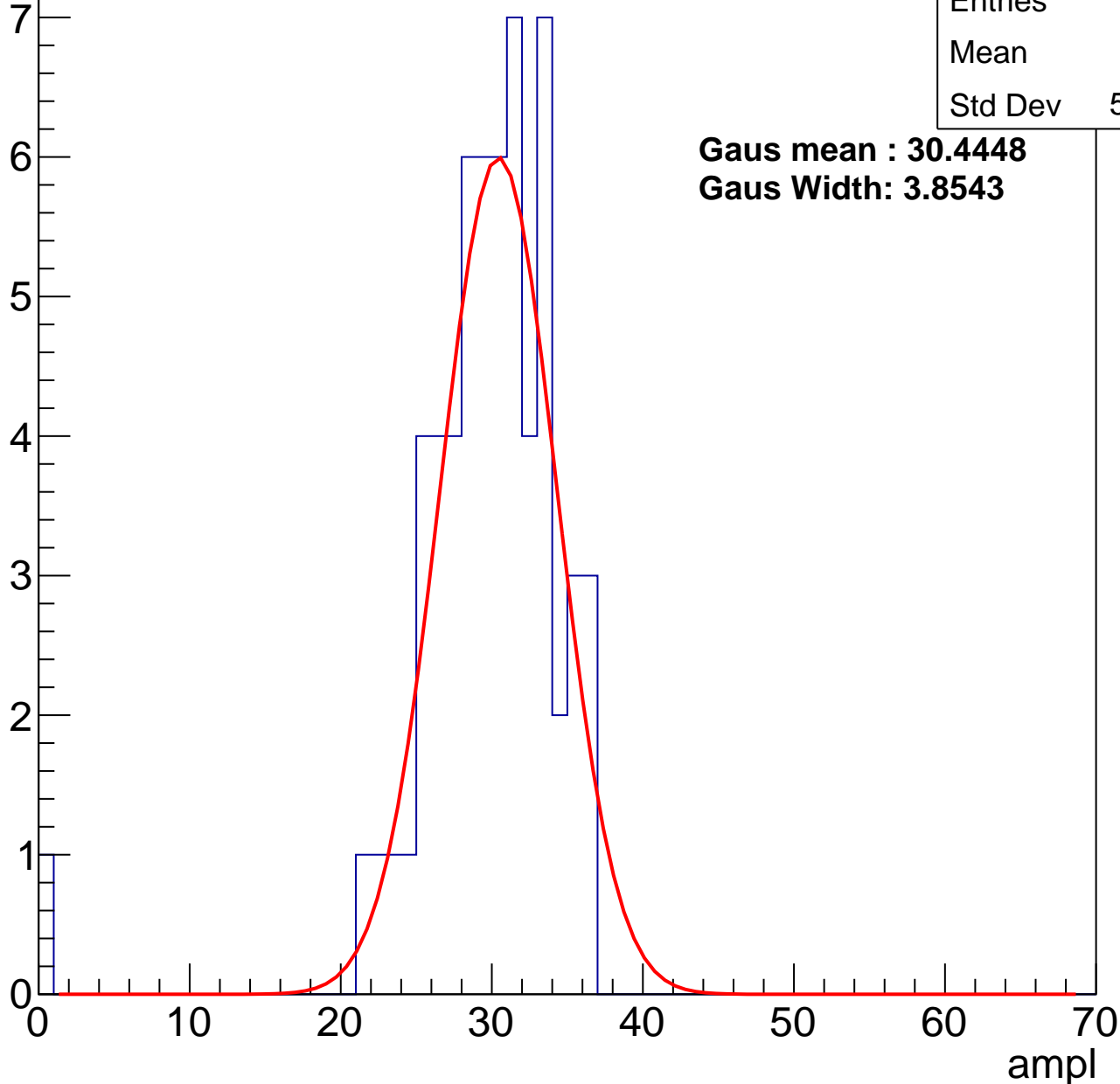
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.2
Std Dev	5.156

Gaus mean : 30.4448

Gaus Width: 3.8543



B1L101S, U4-ch73, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	36.88
Std Dev	3.814

Gaus mean : 37.1222

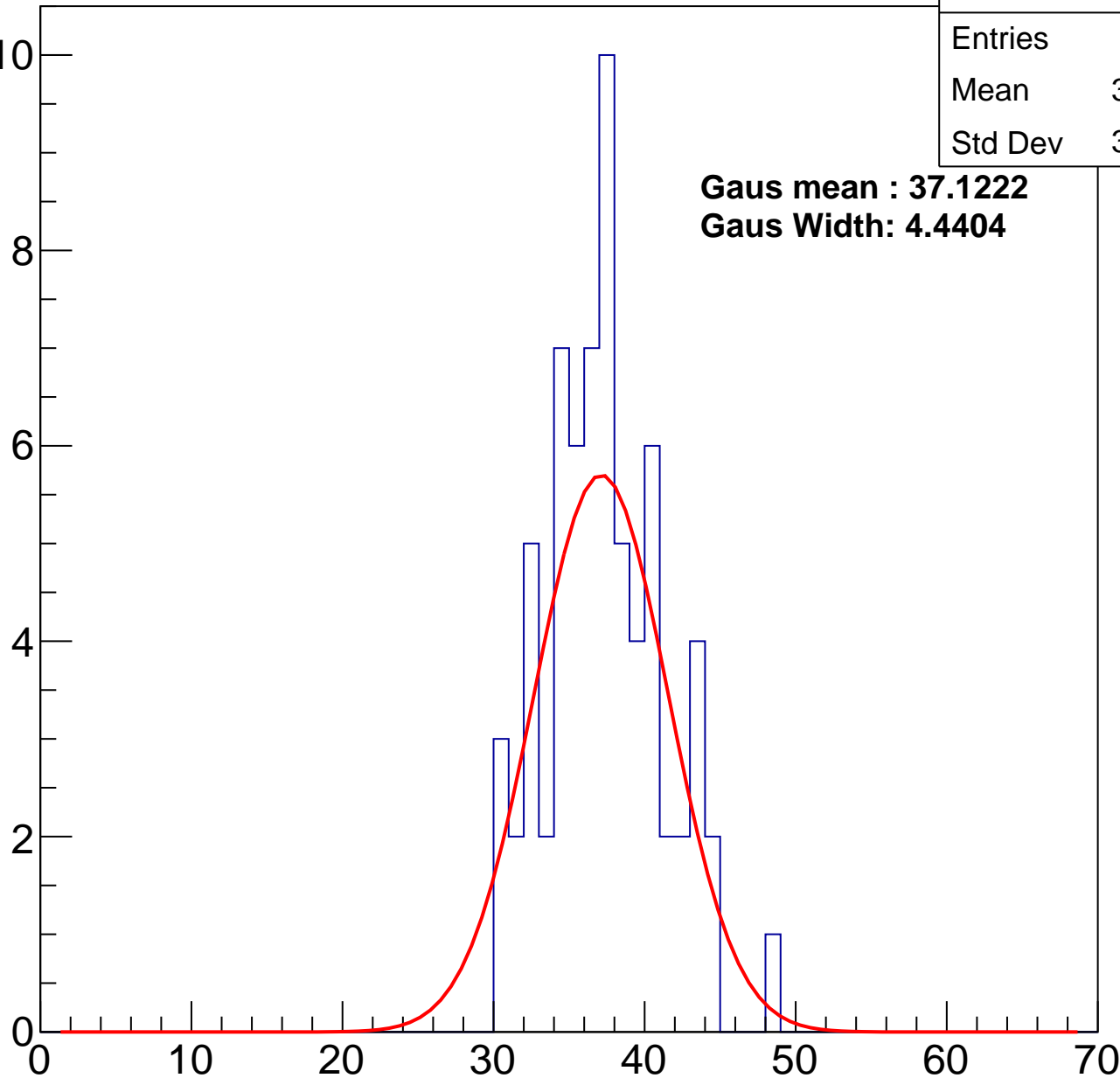
Gaus Width: 4.4404

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch73, adc2

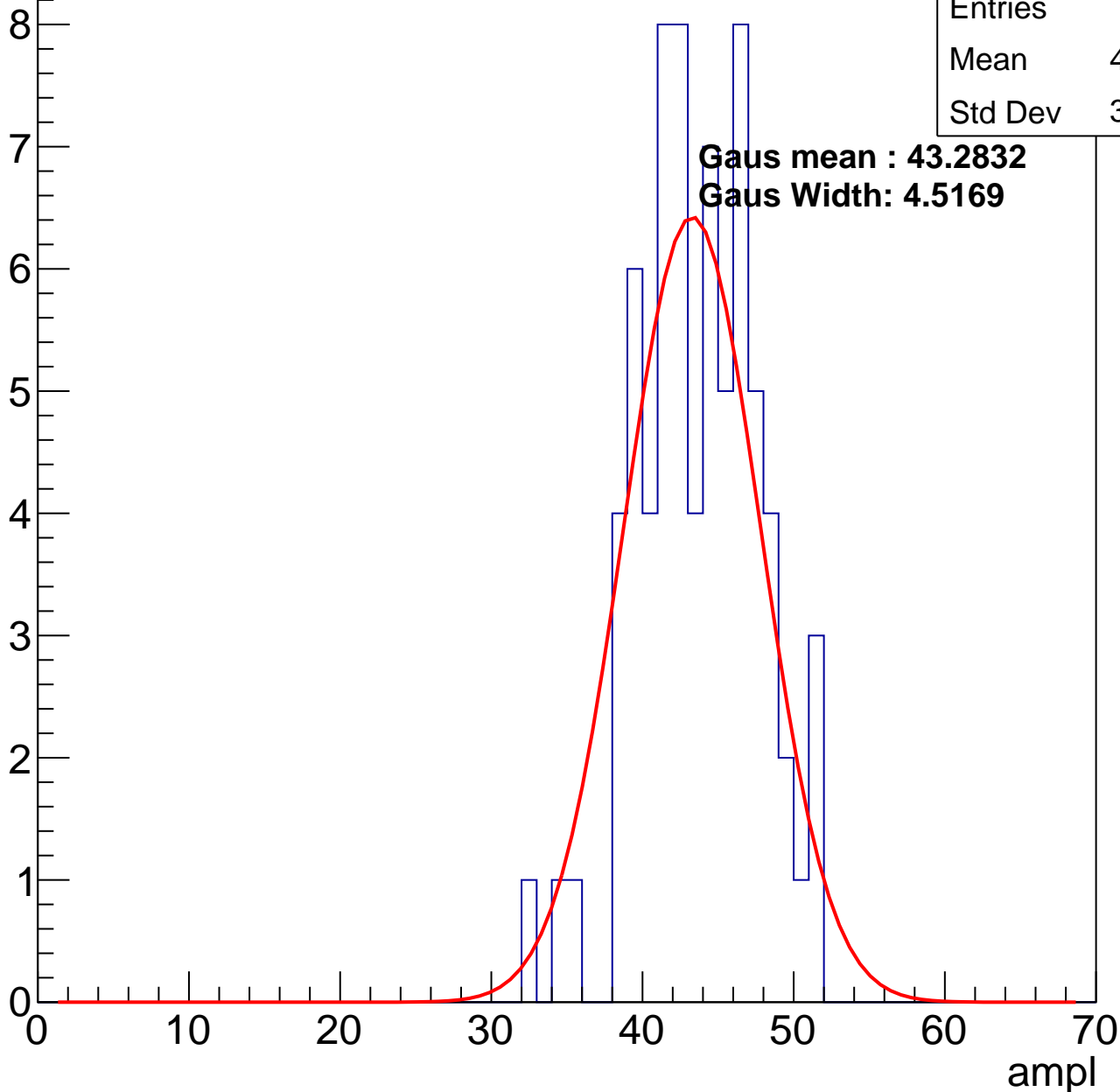
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.22
Std Dev	3.955

Gaus mean : 43.2832

Gaus Width: 4.5169

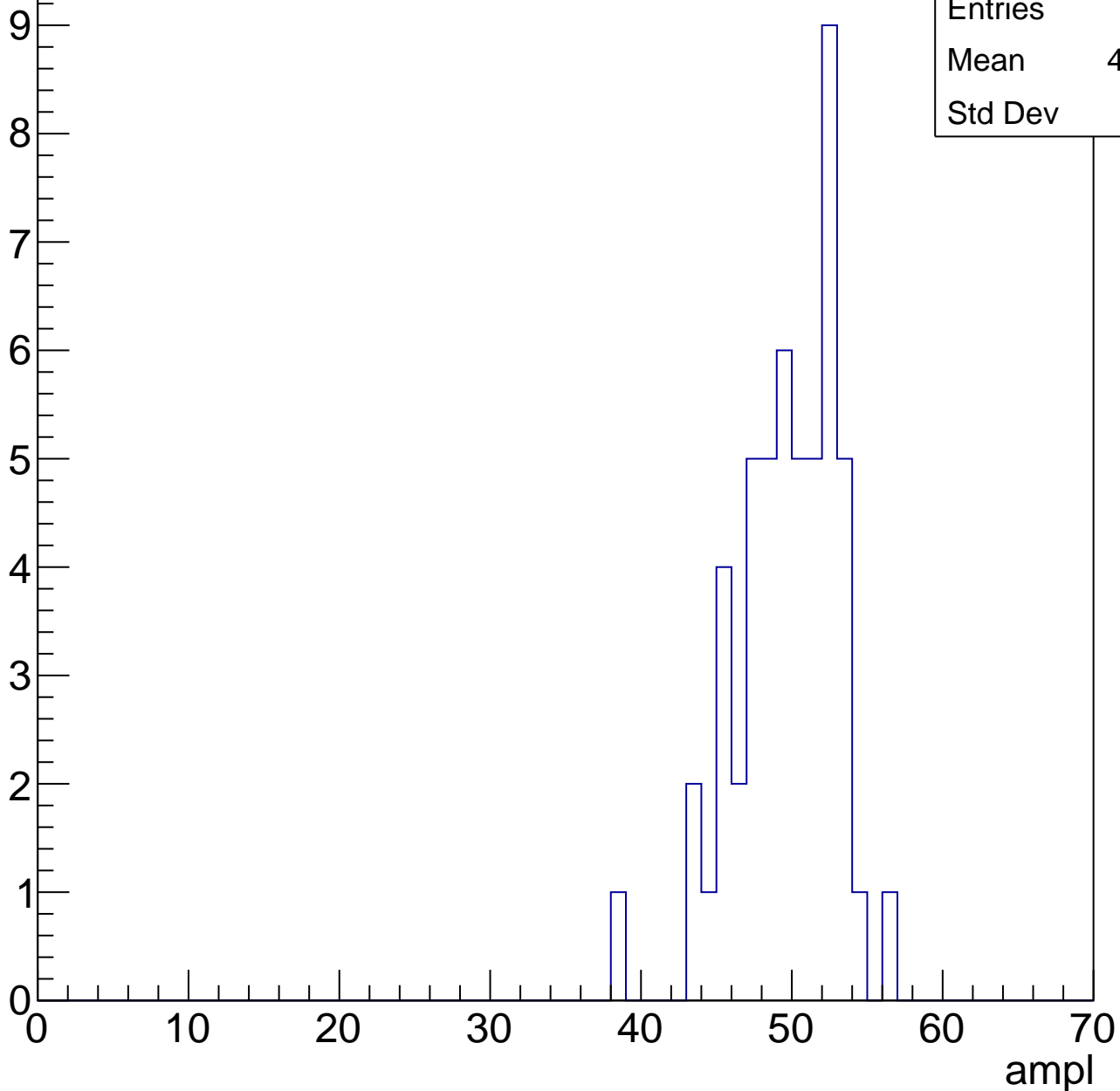


B1L101S, U4-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	49.17
Std Dev	3.35

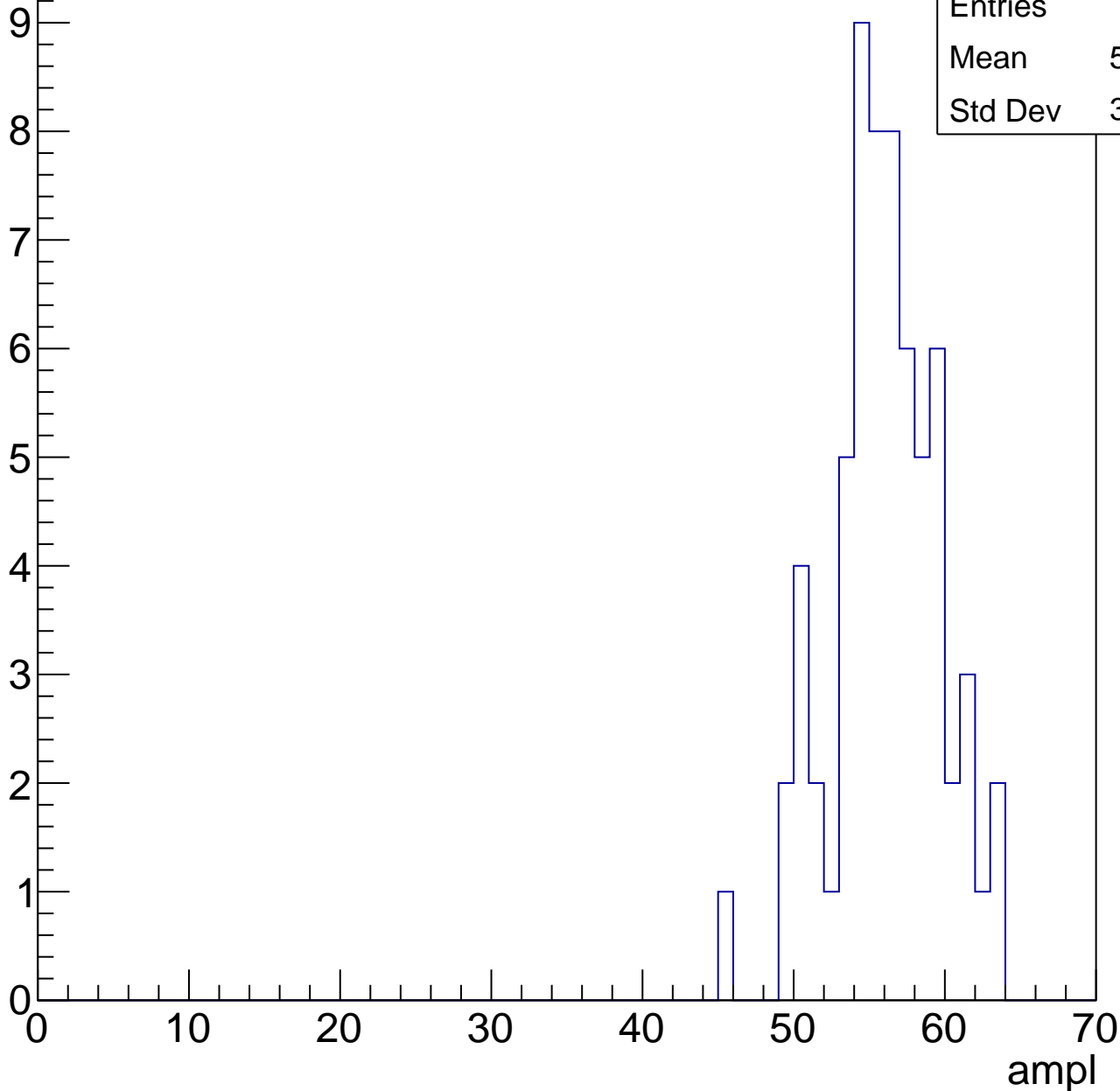


B1L101S, U4-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

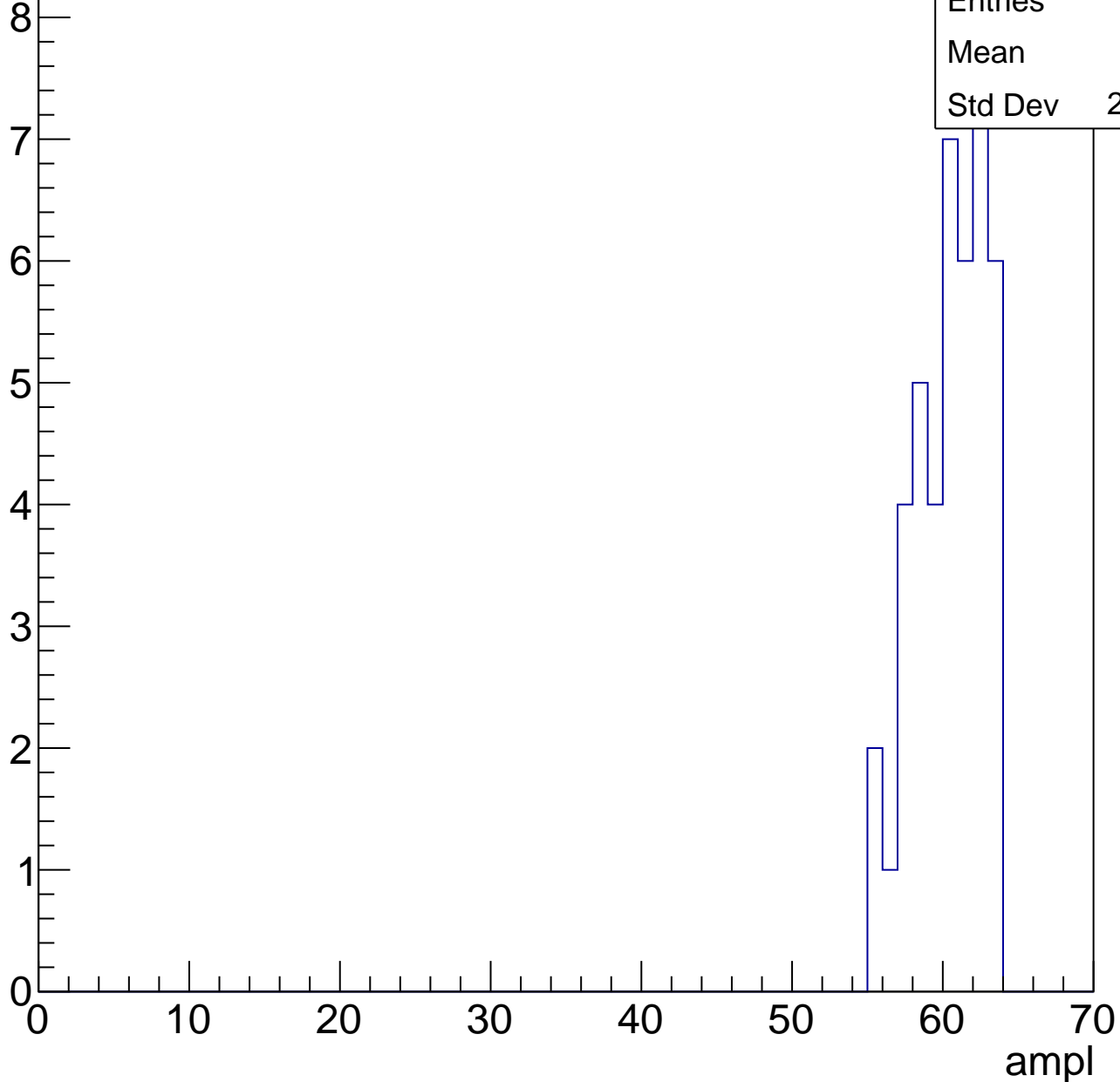
Entries	65
Mean	55.58
Std Dev	3.569



B1L101S, U4-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

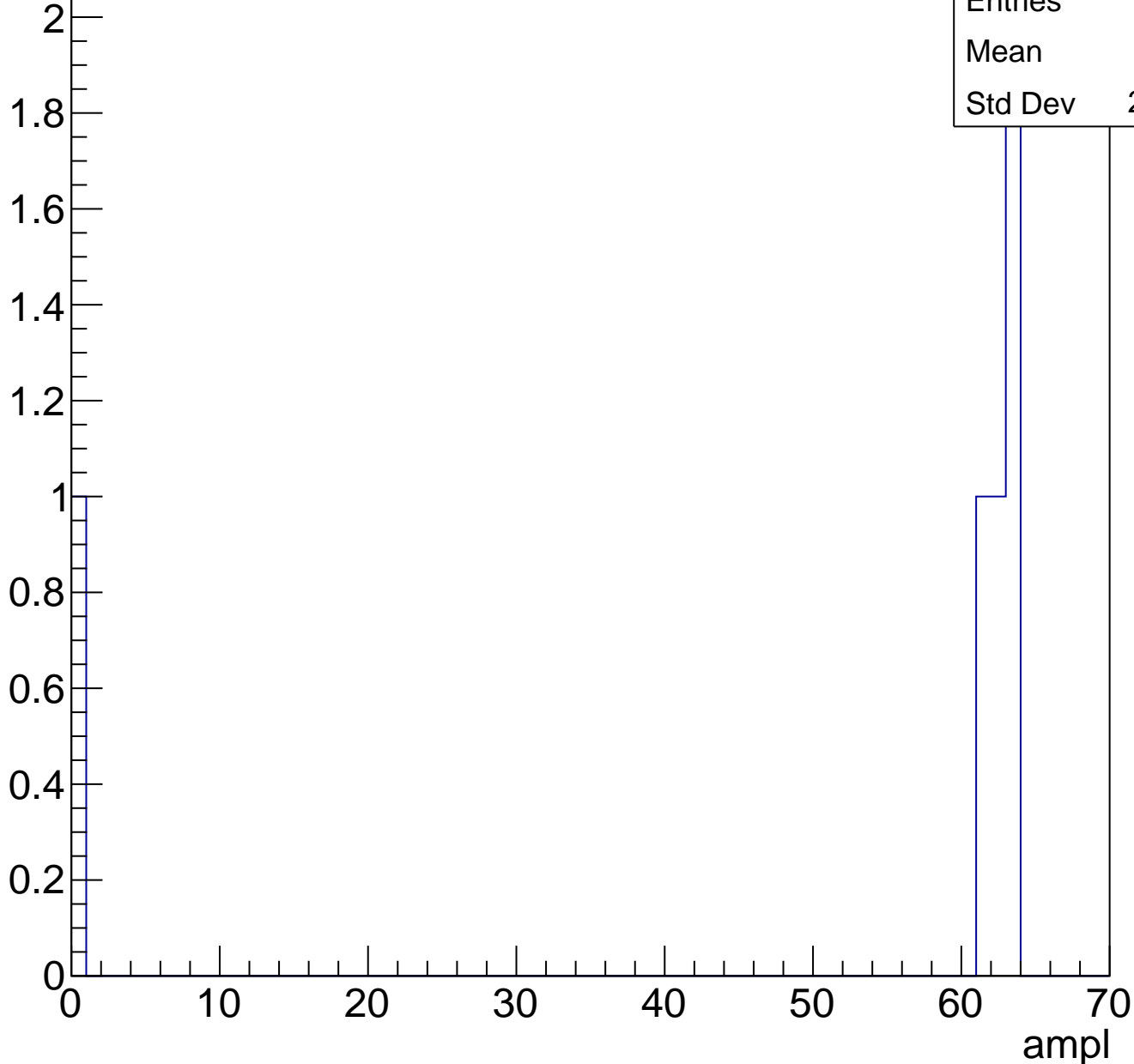


Entries	43
Mean	60
Std Dev	2.252

B1L101S, U4-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

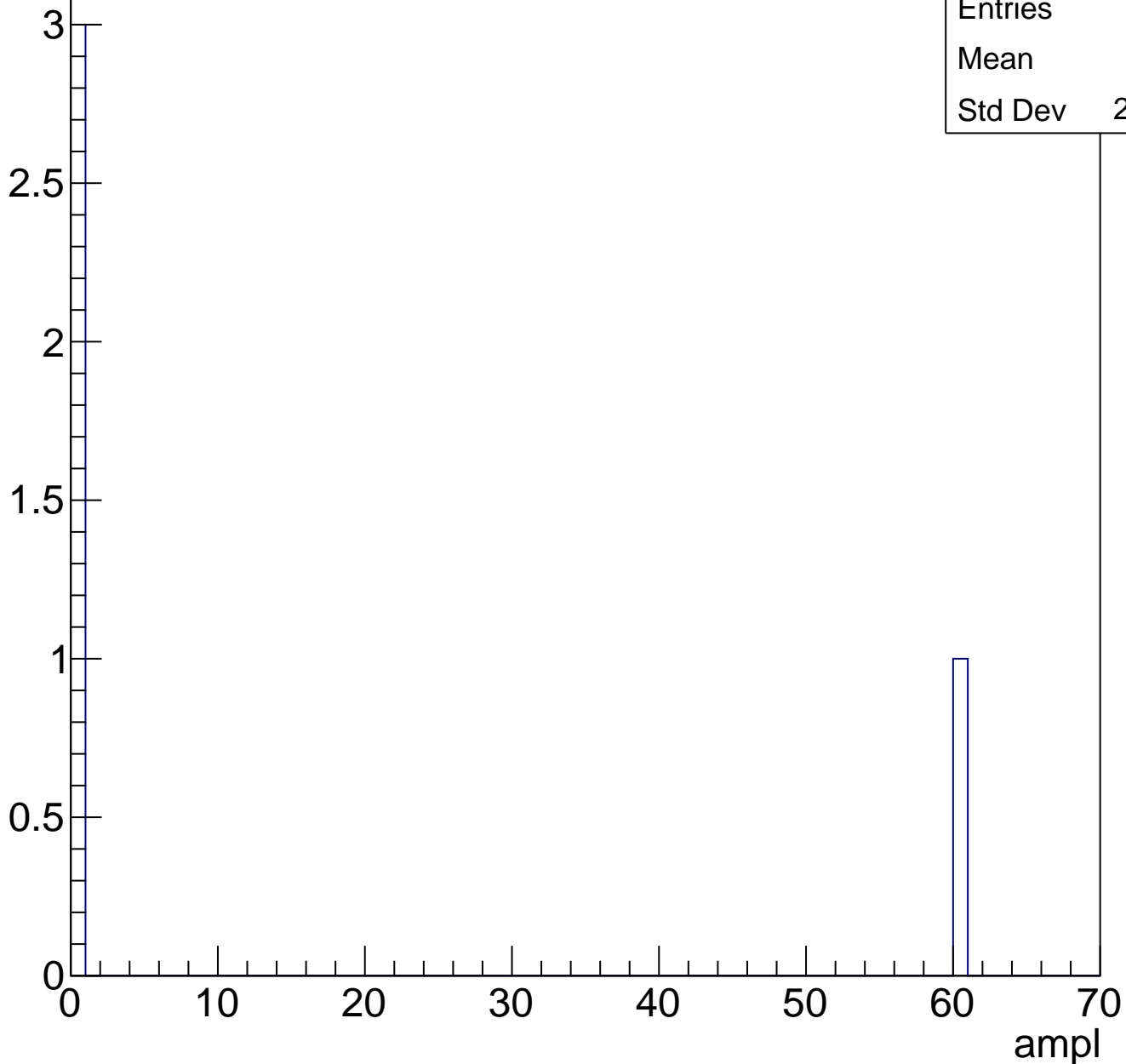
Entry



B1L101S, U4-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch74, adc0

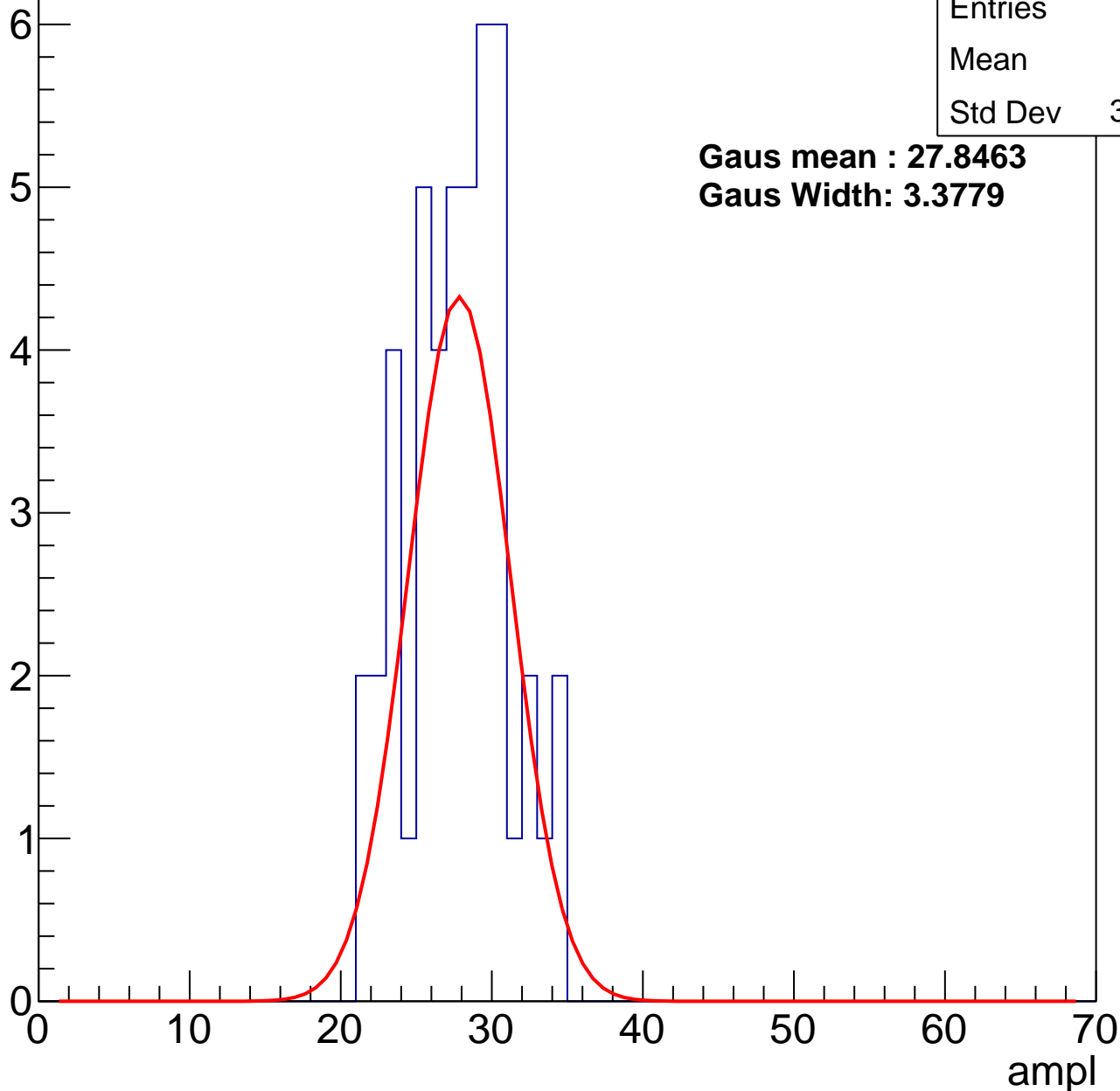
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	27.3
Std Dev	3.289

Gaus mean : 27.8463

Gaus Width: 3.3779



B1L101S, U4-ch74, adc1

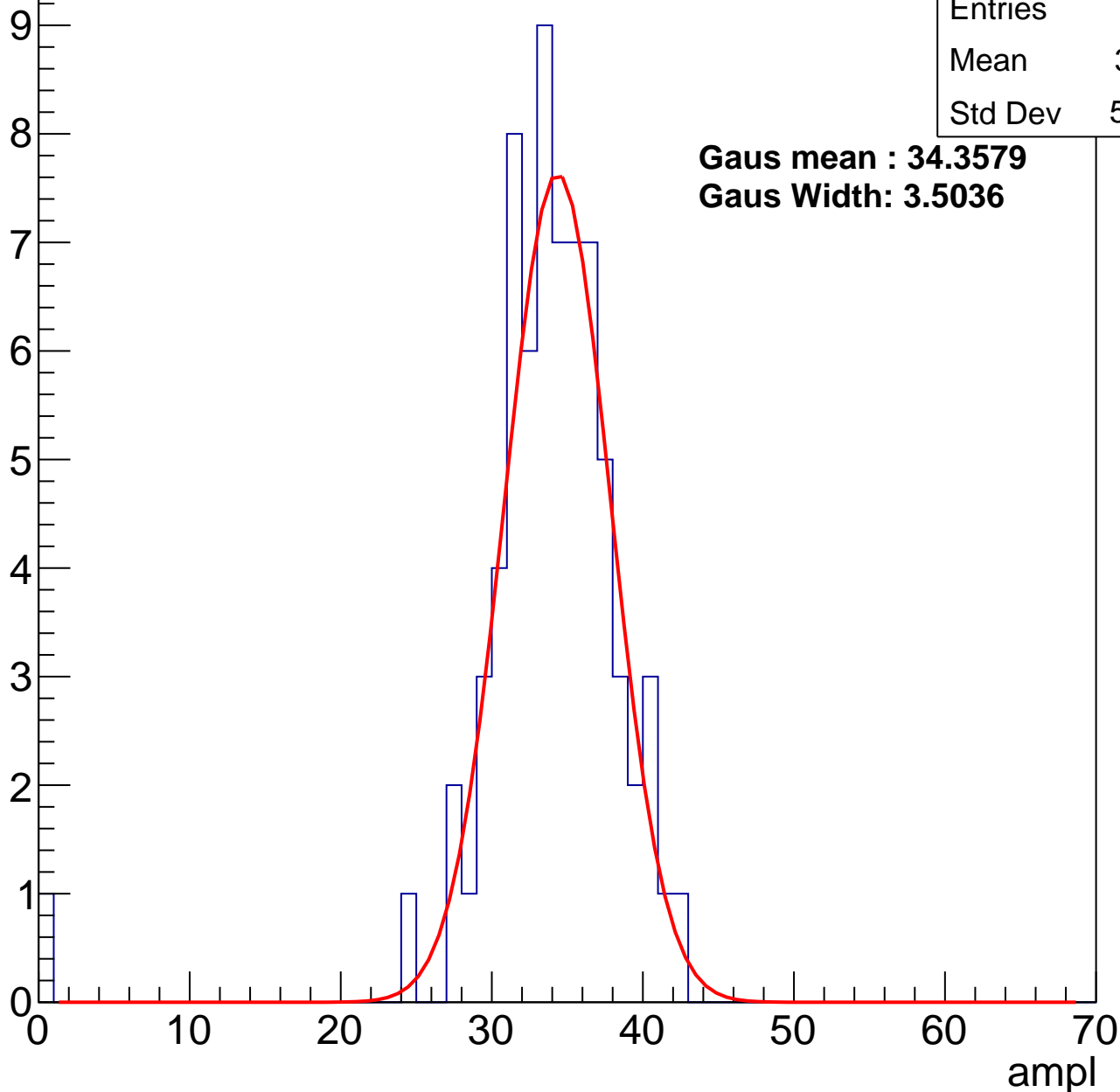
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	33.31
Std Dev	5.306

Gaus mean : 34.3579

Gaus Width: 3.5036



B1L101S, U4-ch74, adc2

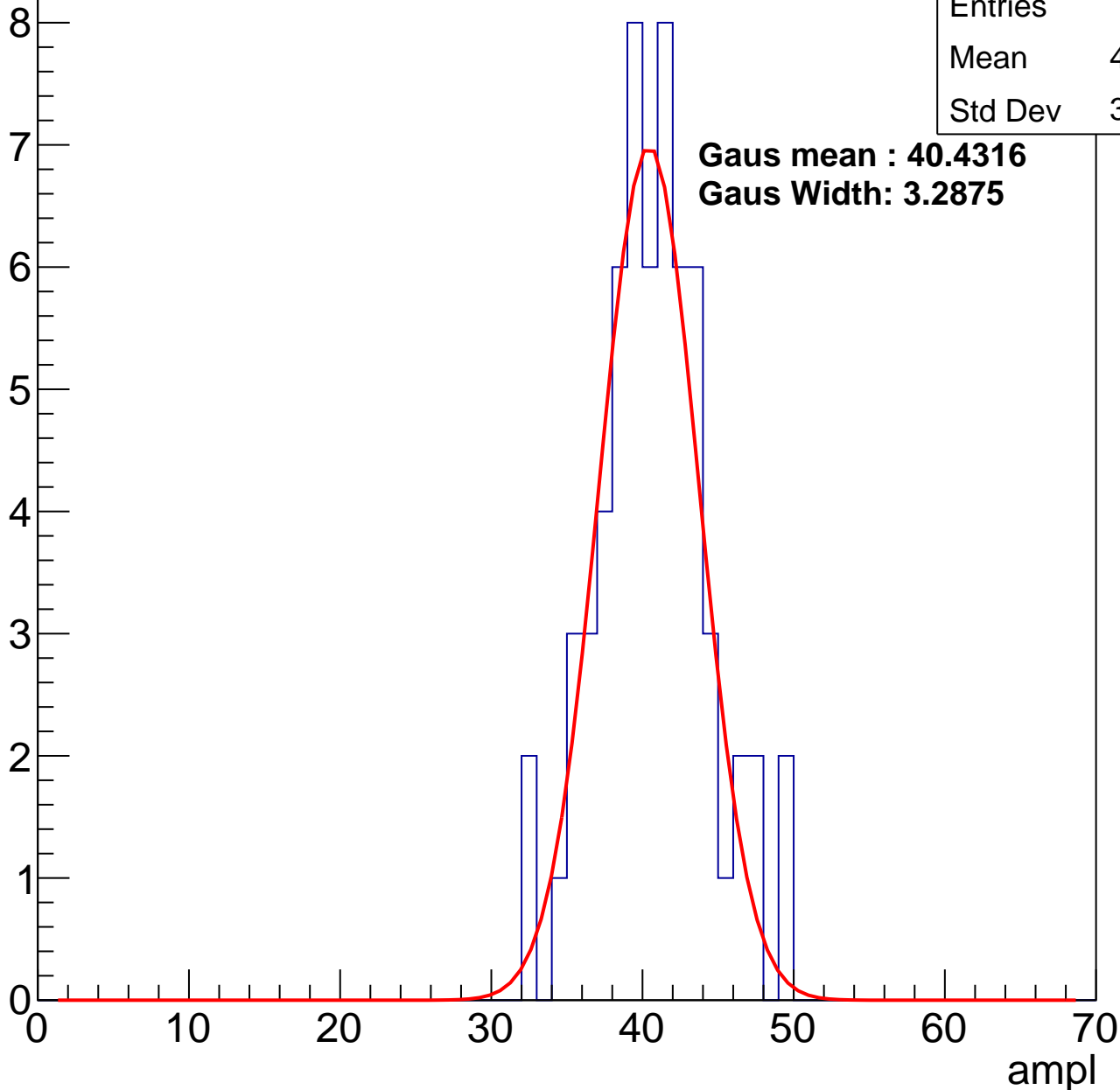
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	40.29
Std Dev	3.653

Gaus mean : 40.4316

Gaus Width: 3.2875

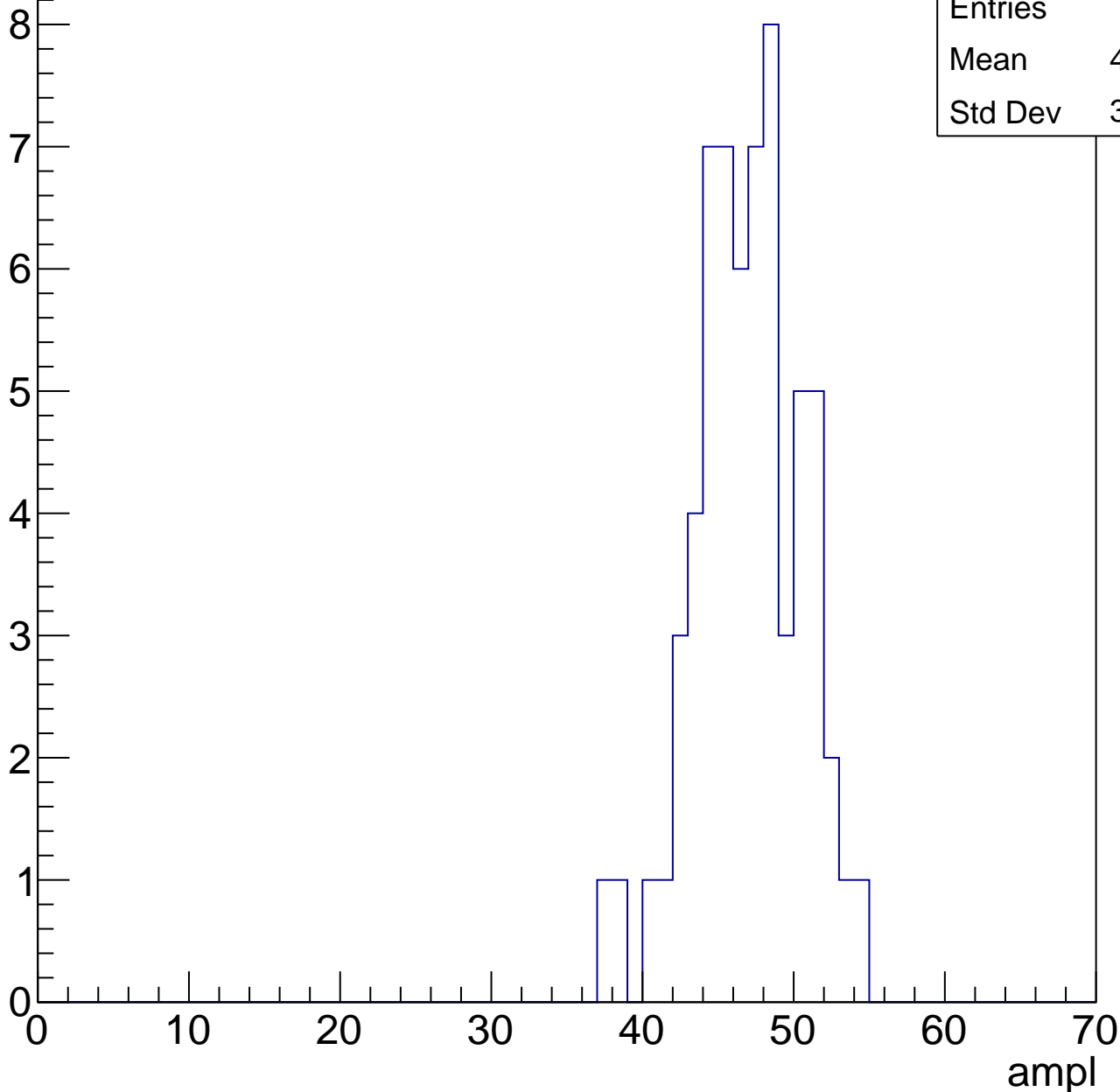


B1L101S, U4-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	46.49
Std Dev	3.486

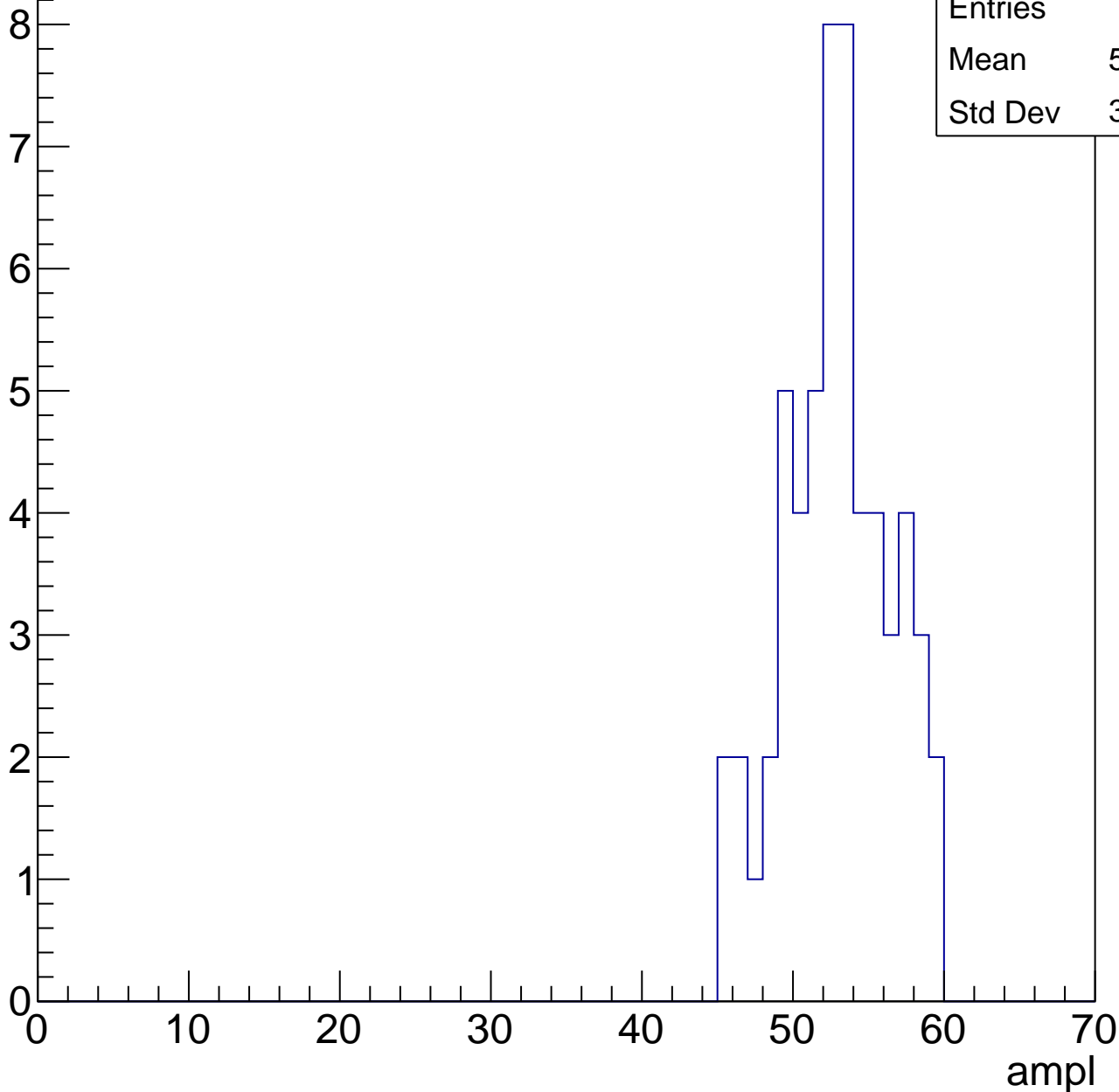


B1L101S, U4-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	52.44
Std Dev	3.494

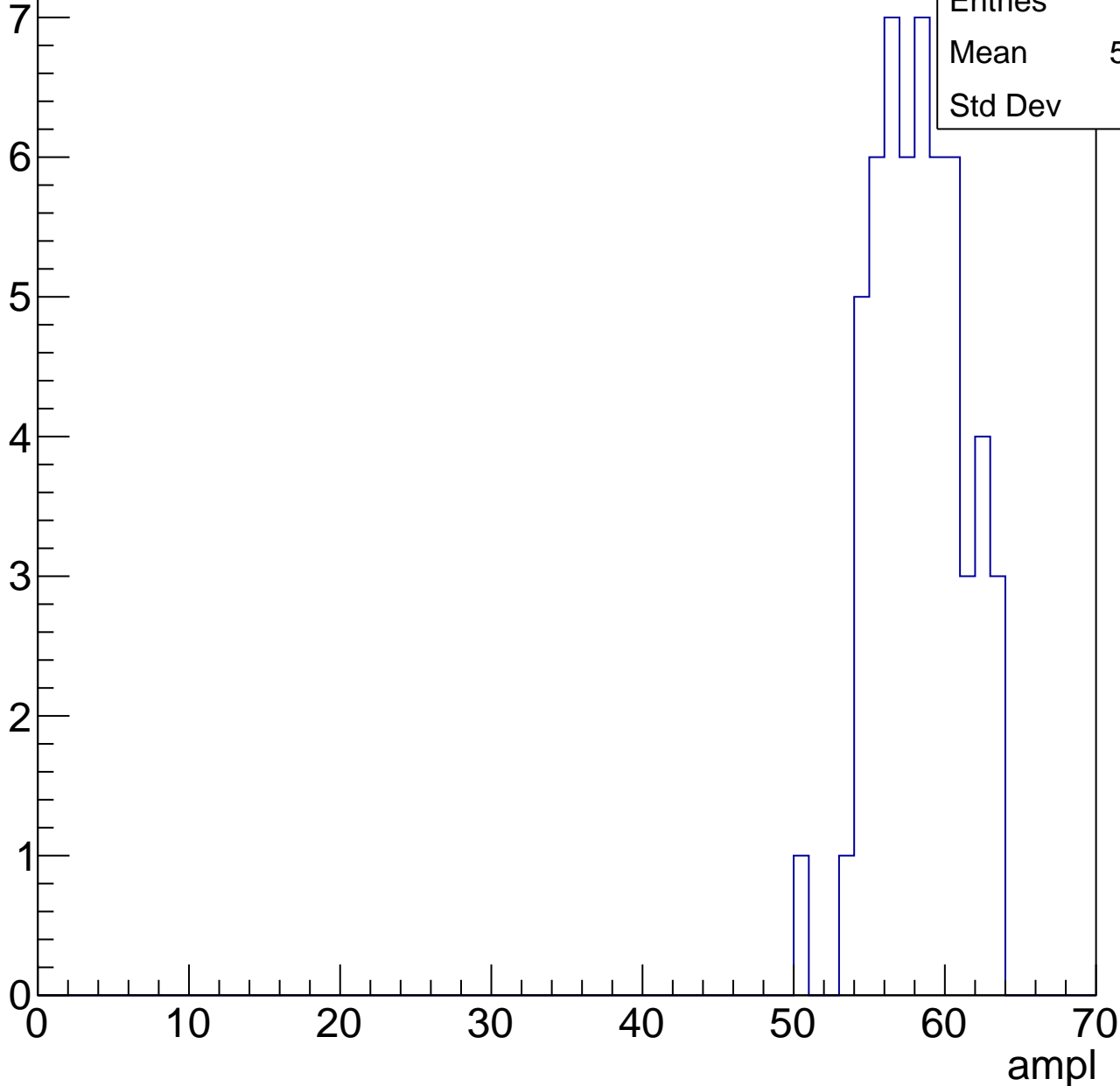


B1L101S, U4-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.76
Std Dev	2.86

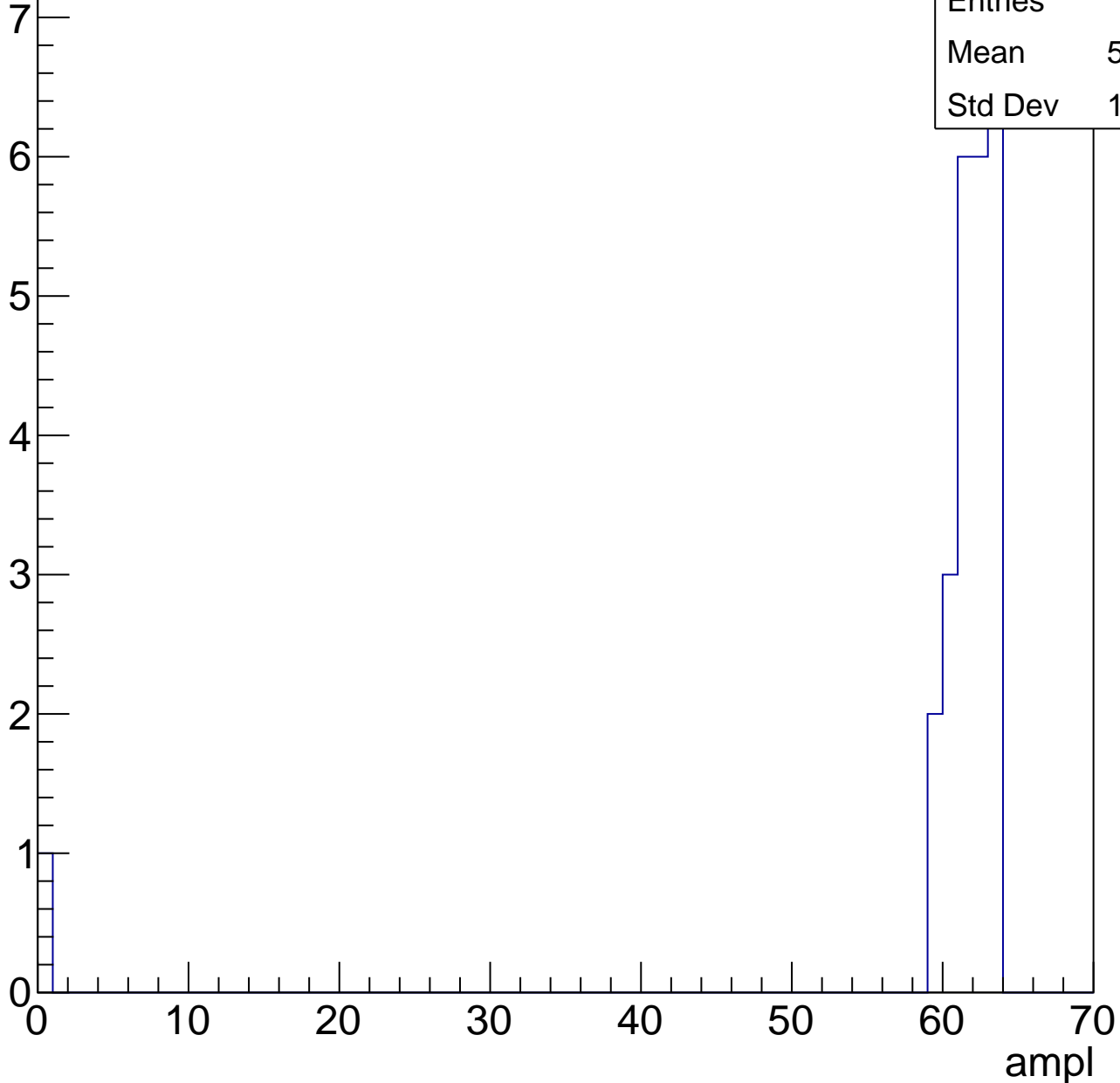


B1L101S, U4-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

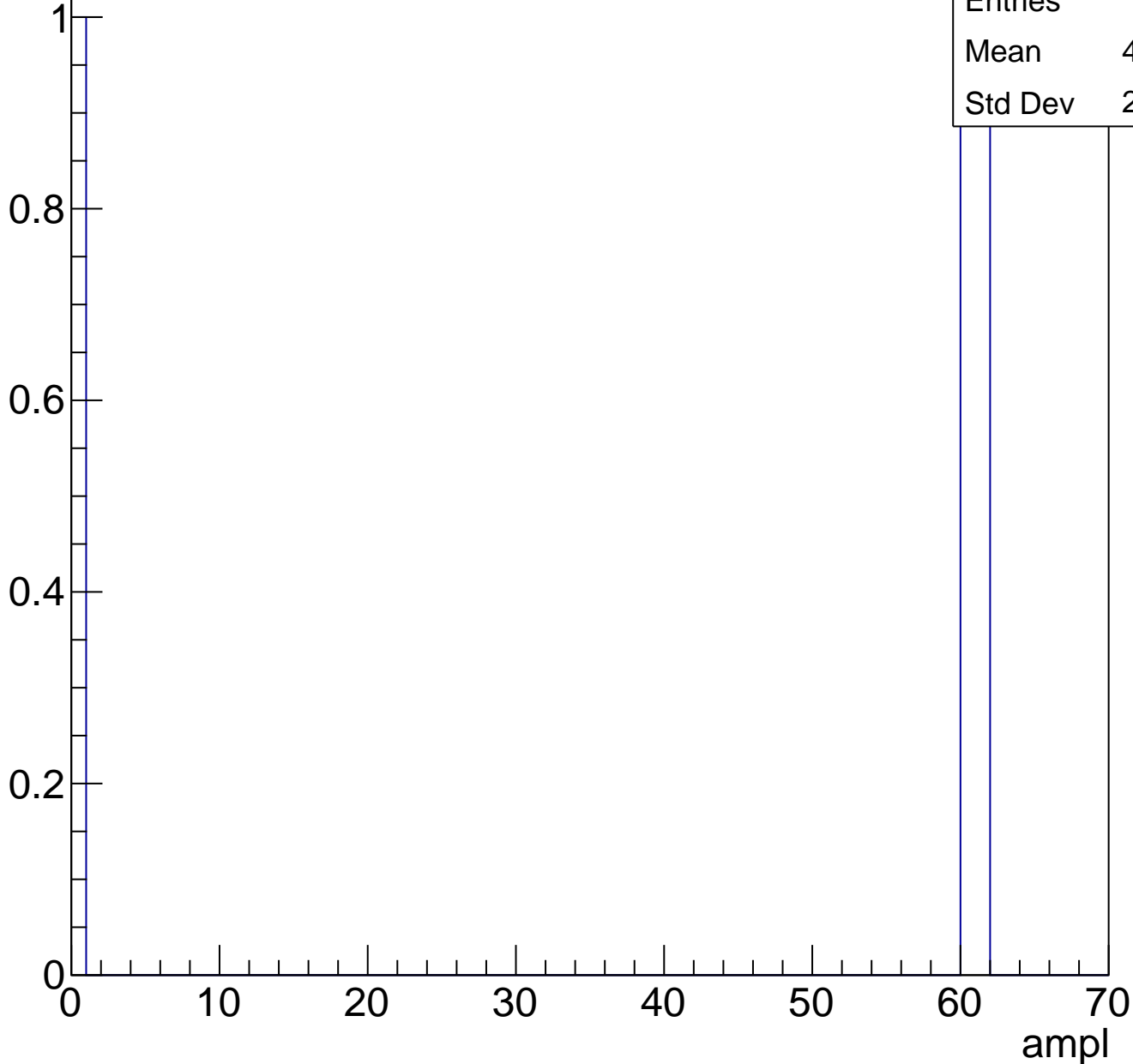
Entries	25
Mean	59.08
Std Dev	12.12



B1L101S, U4-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



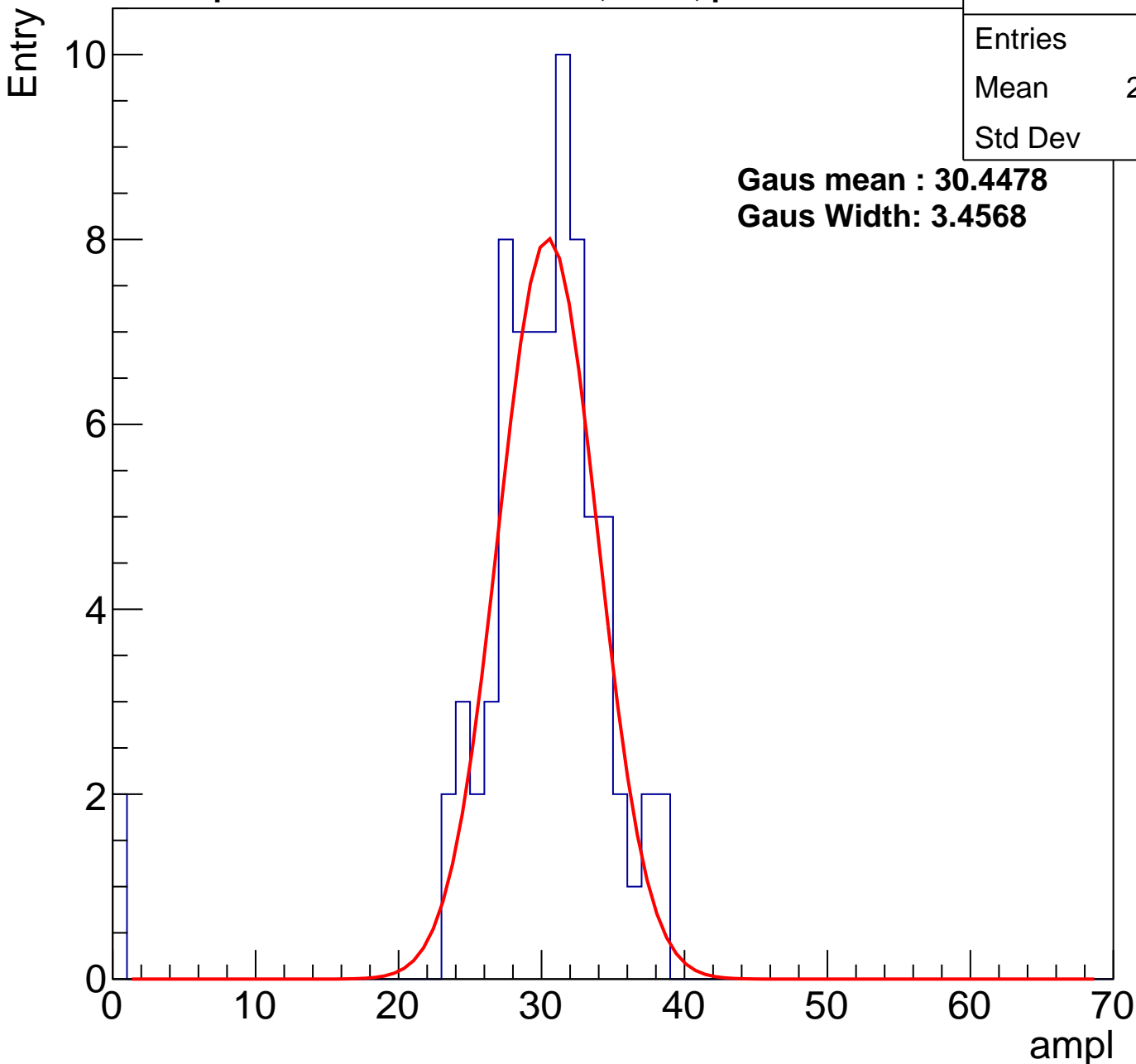
B1L101S, U4-ch75, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	29.32
Std Dev	5.91

Gaus mean : 30.4478

Gaus Width: 3.4568



B1L101S, U4-ch75, adc1

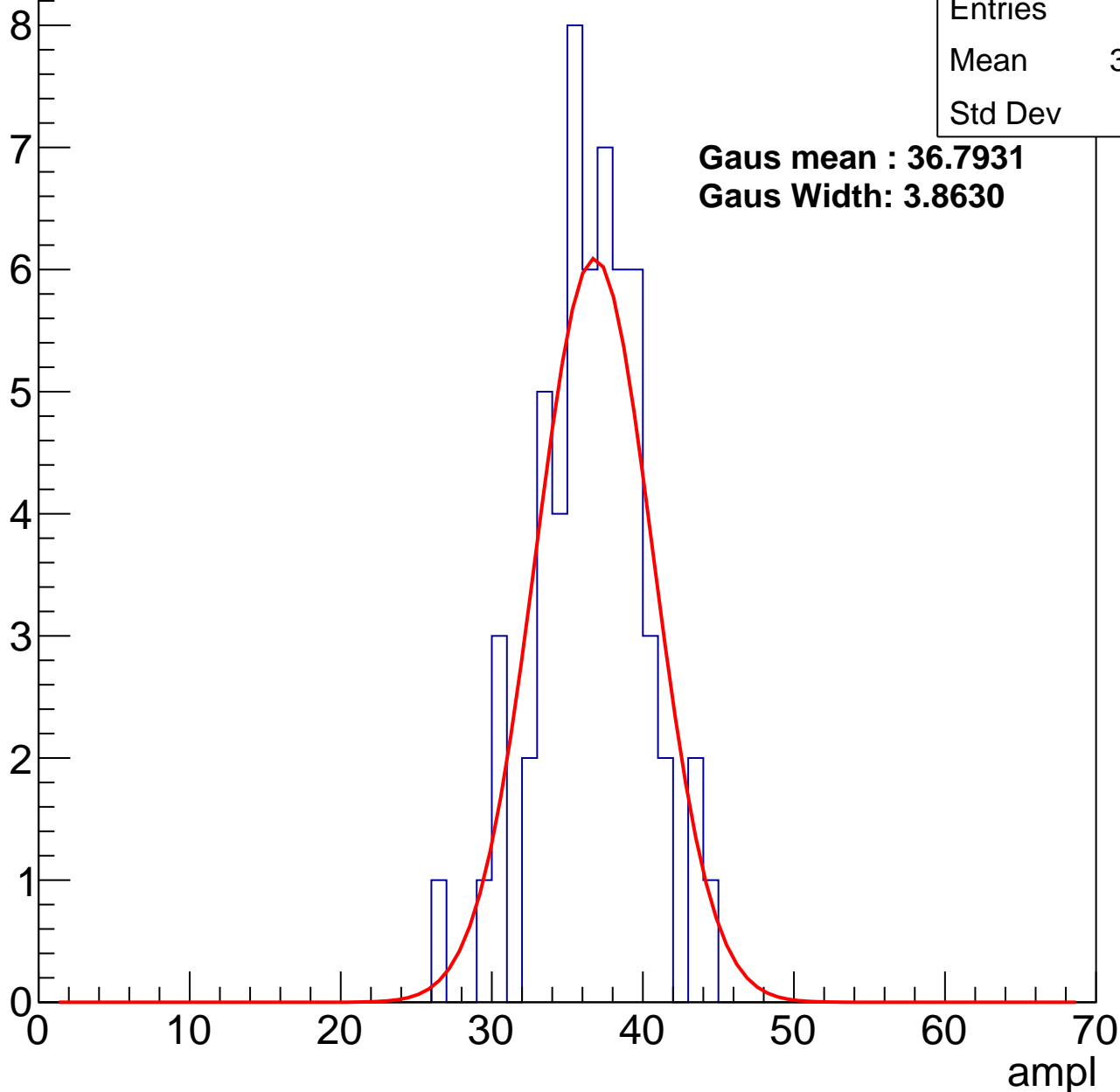
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.12
Std Dev	3.51

Gaus mean : 36.7931

Gaus Width: 3.8630



B1L101S, U4-ch75, adc2

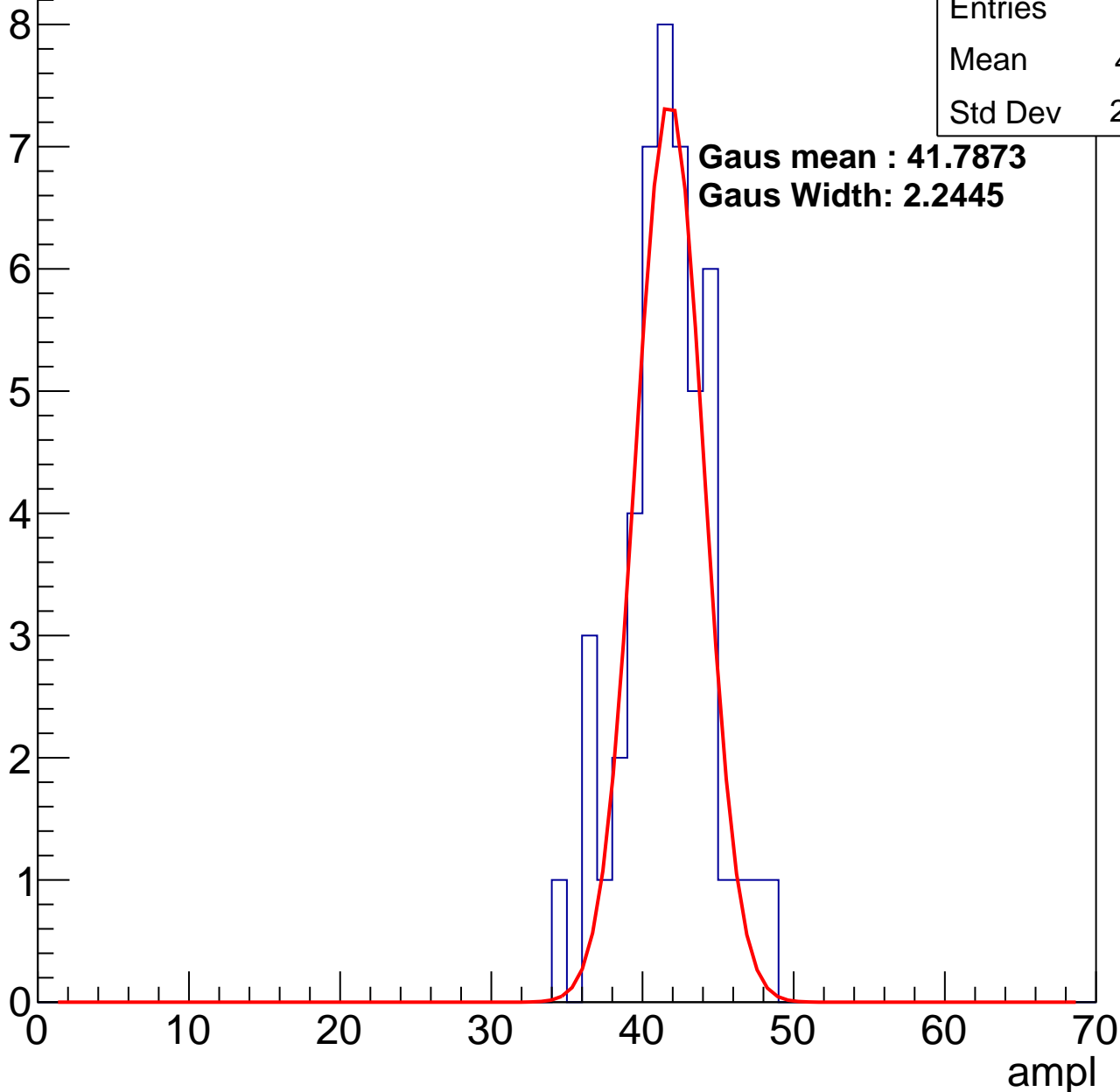
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	41.21
Std Dev	2.835

Gaus mean : 41.7873

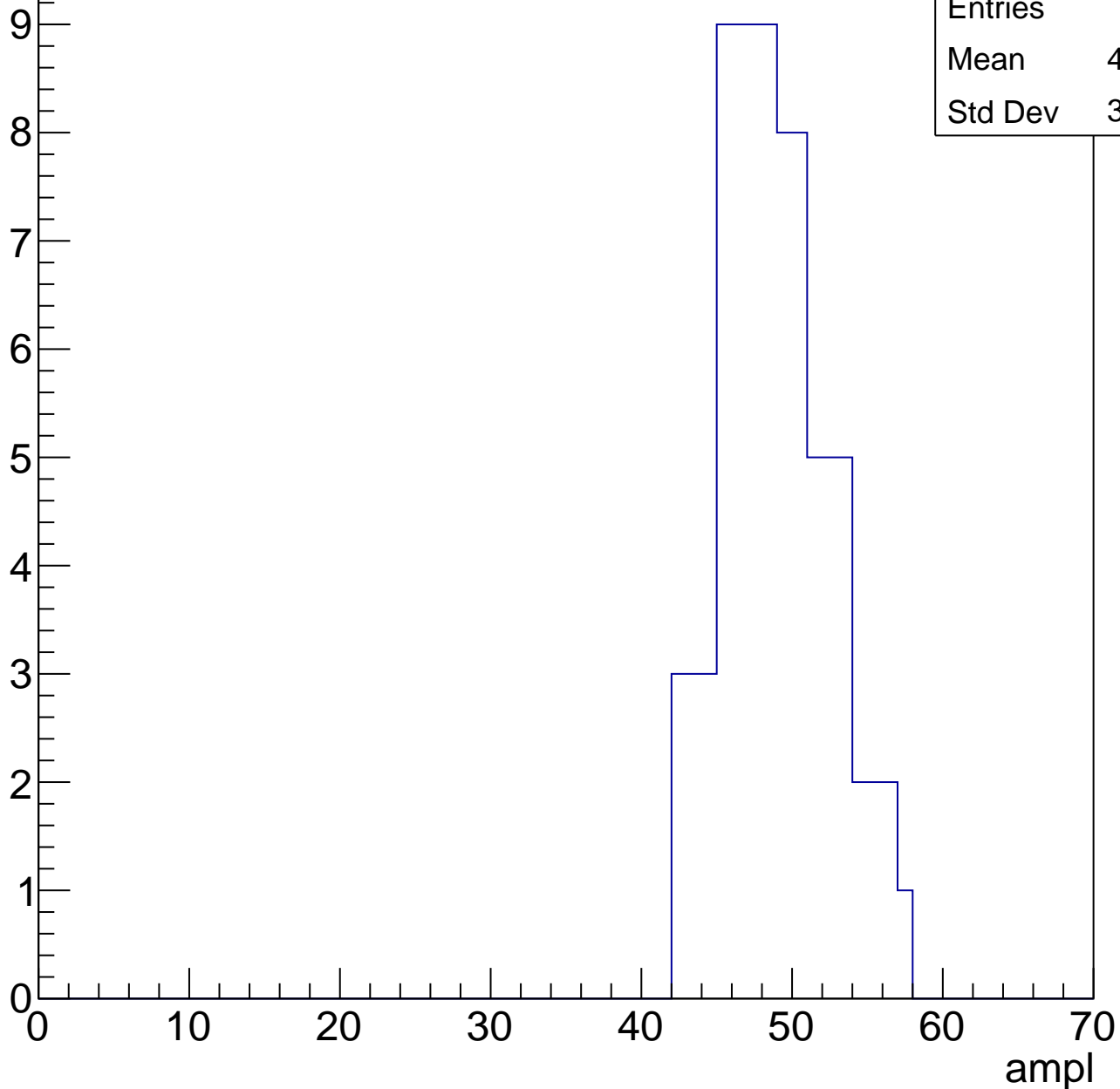
Gaus Width: 2.2445



B1L101S, U4-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



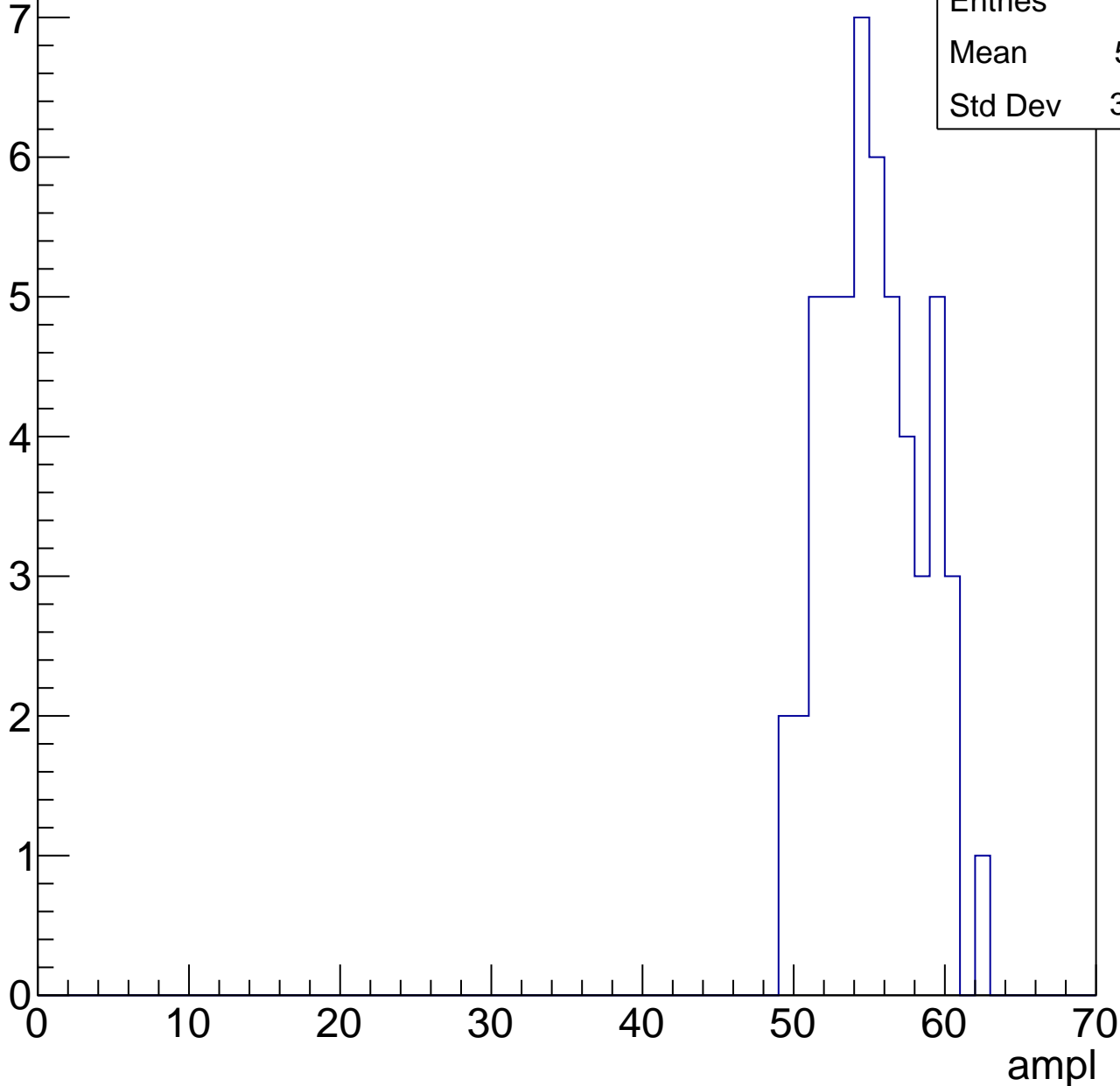
Entries	83
Mean	48.43
Std Dev	3.489

B1L101S, U4-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	54.81
Std Dev	3.145

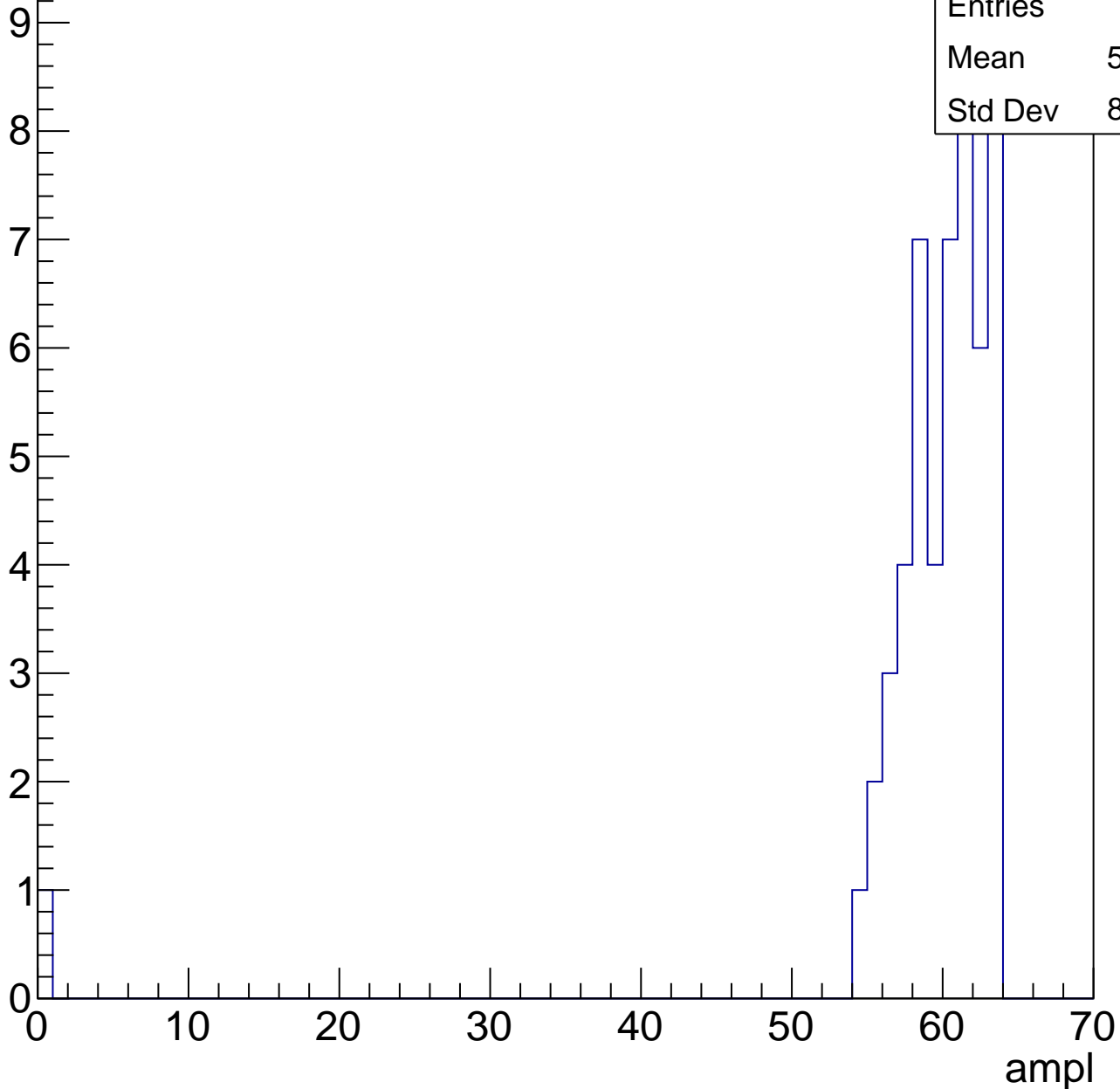


B1L101S, U4-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

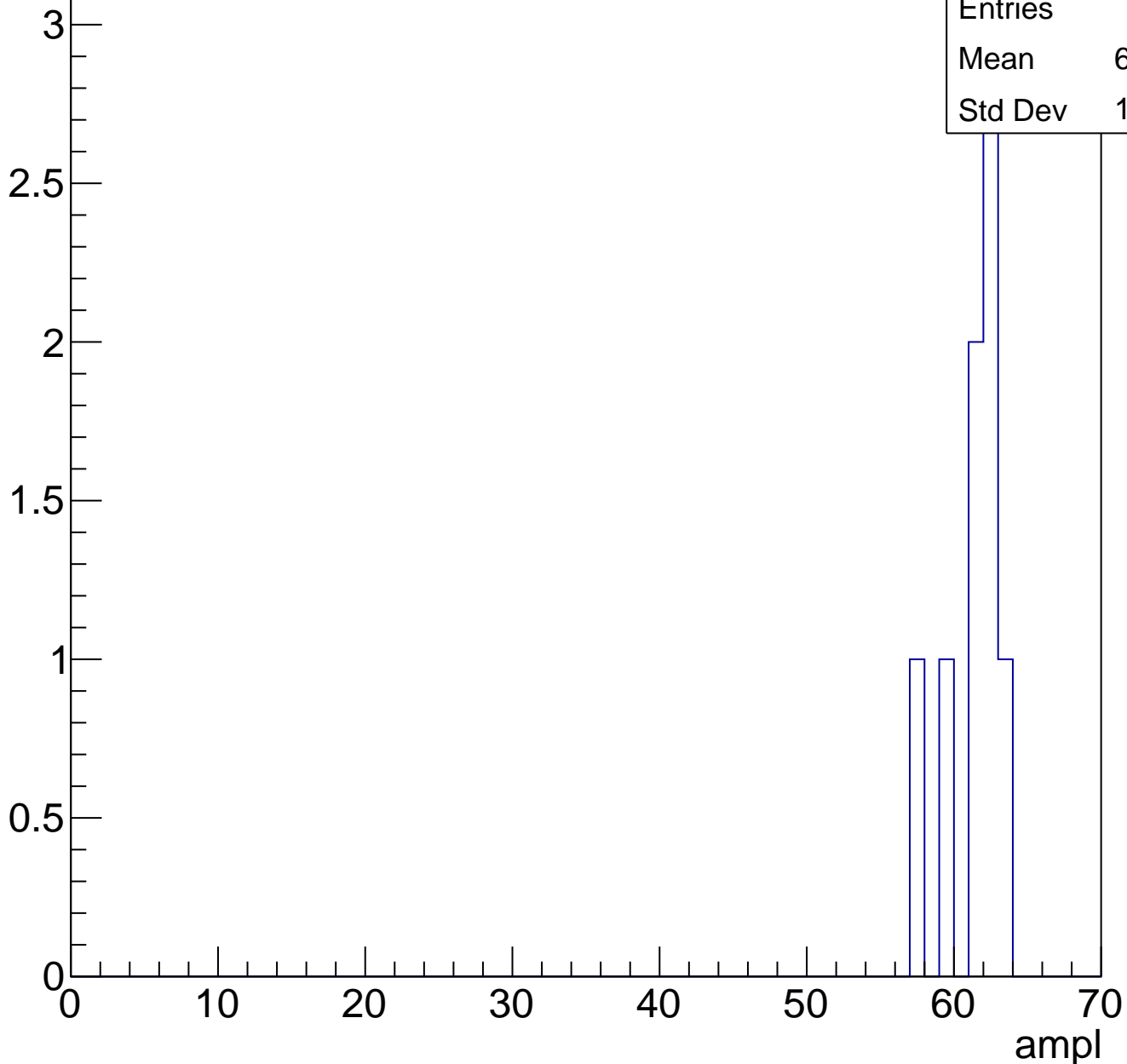
Entries	52
Mean	58.63
Std Dev	8.569



B1L101S, U4-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch76, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	26.51
Std Dev	7.877

Gaus mean : 29.0276

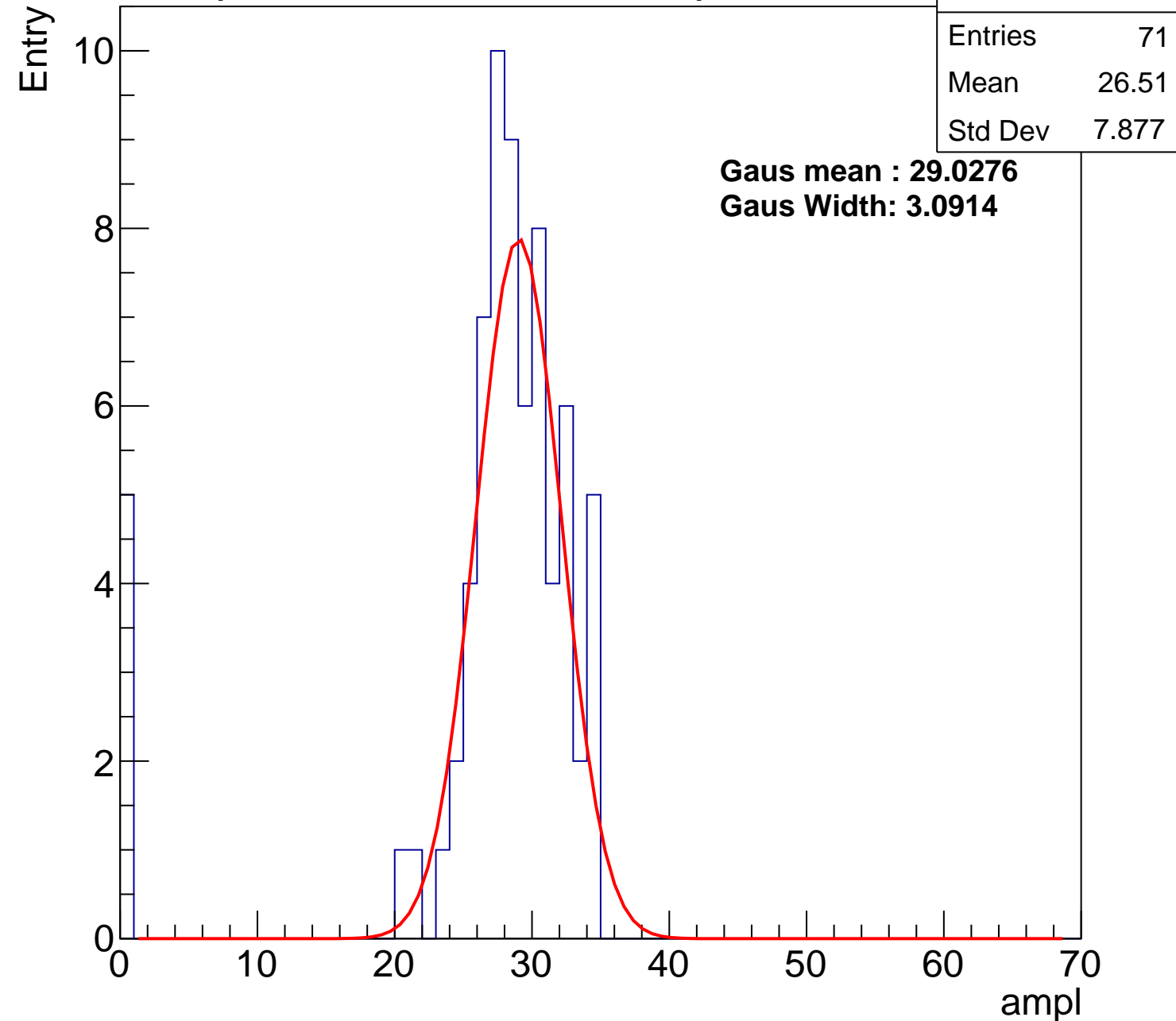
Gaus Width: 3.0914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch76, adc1

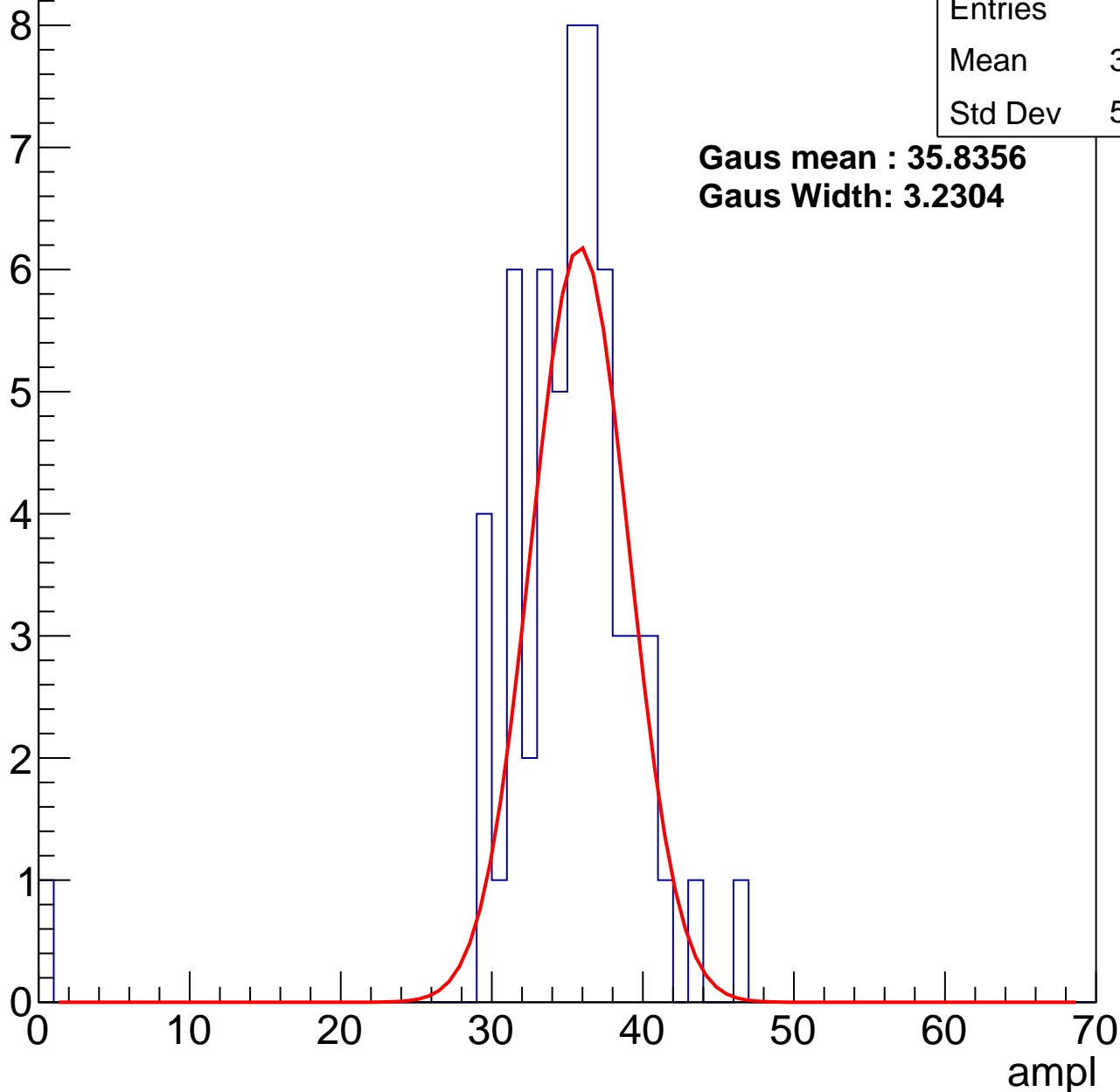
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.49
Std Dev	5.712

Gaus mean : 35.8356

Gaus Width: 3.2304



B1L101S, U4-ch76, adc2

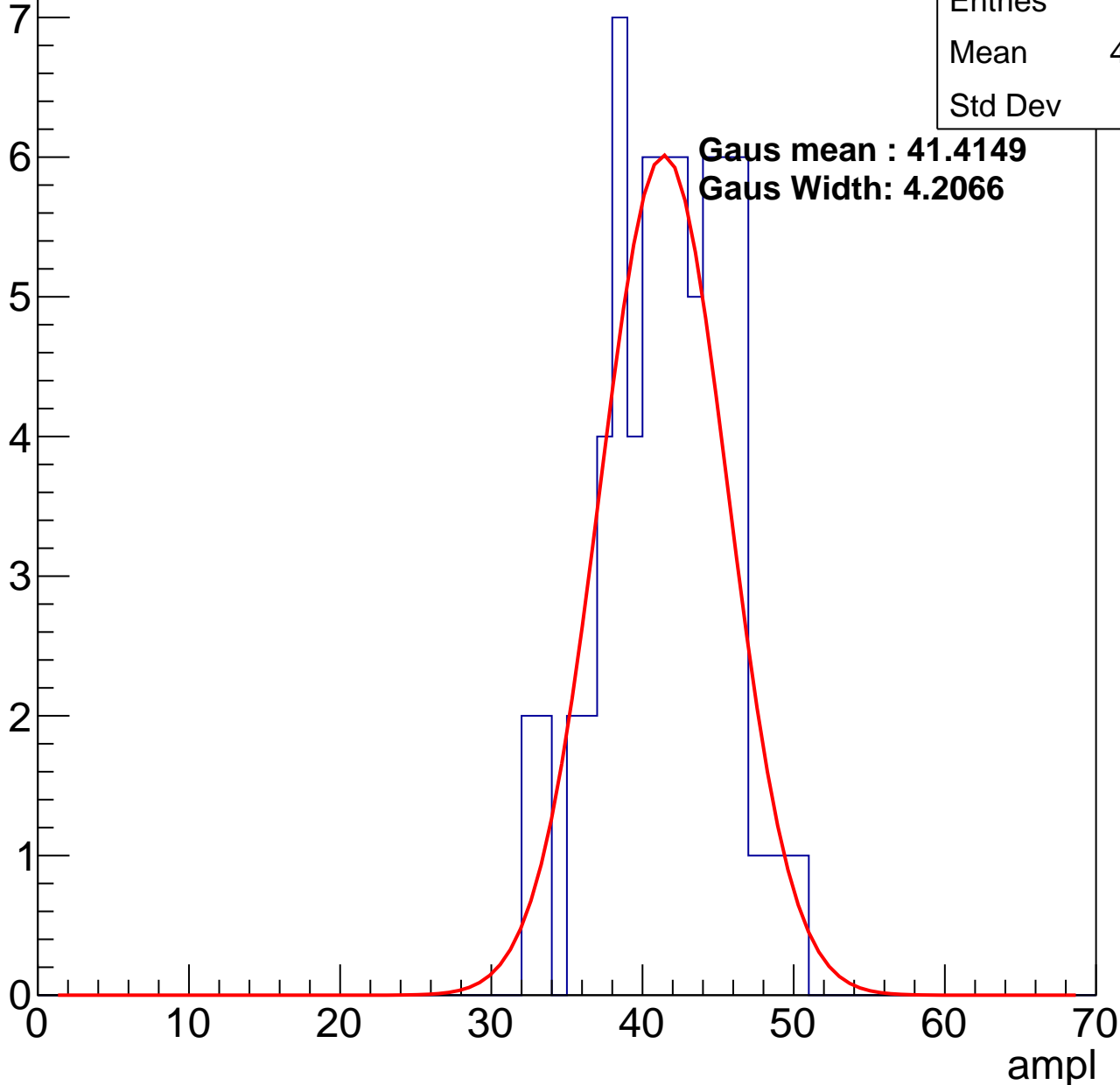
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.16
Std Dev	4.05

Gaus mean : 41.4149

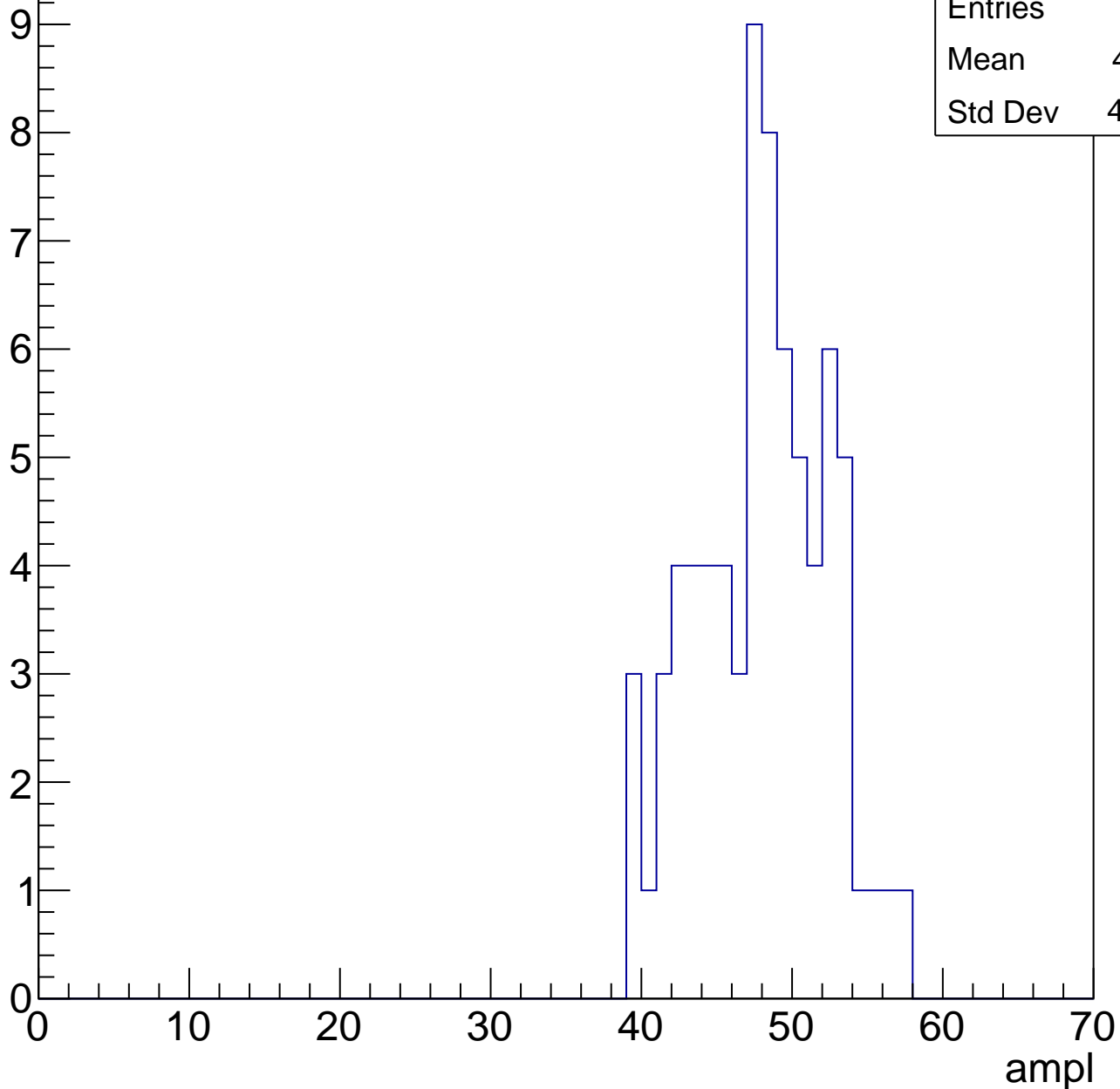
Gaus Width: 4.2066



B1L101S, U4-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



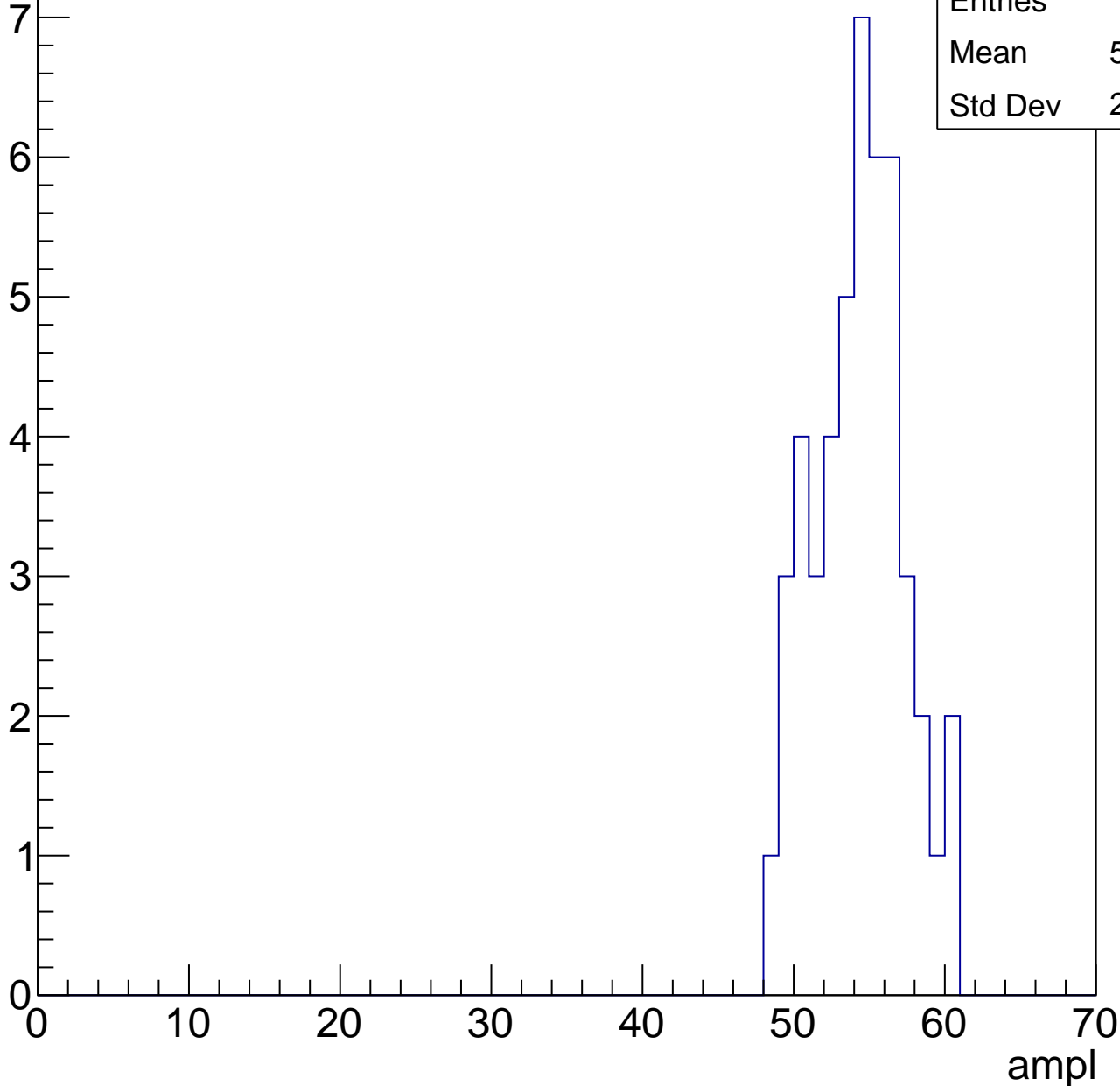
Entries	73
Mean	47.51
Std Dev	4.243

B1L101S, U4-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

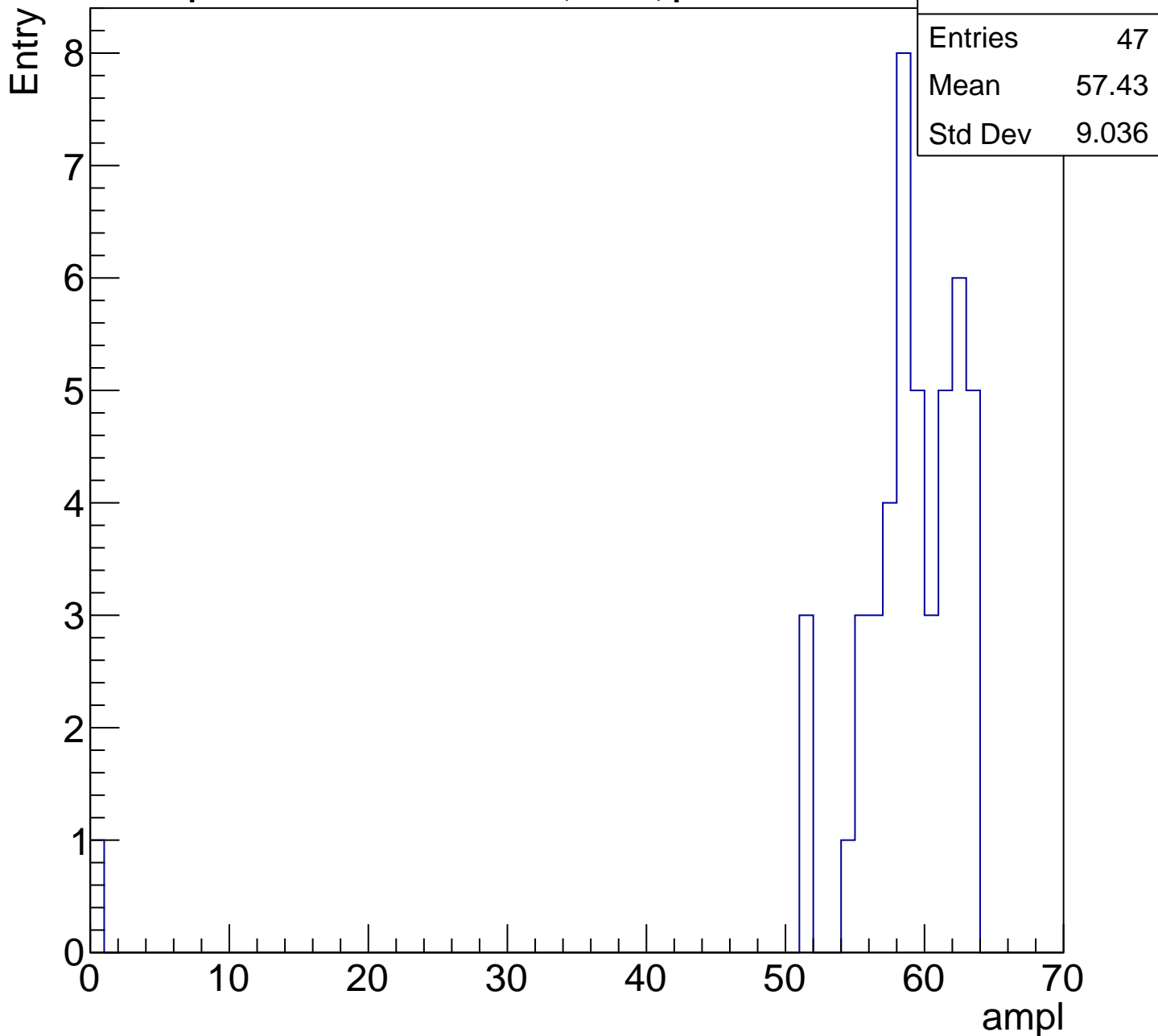
Entry

Entries	47
Mean	53.85
Std Dev	2.946



B1L101S, U4-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

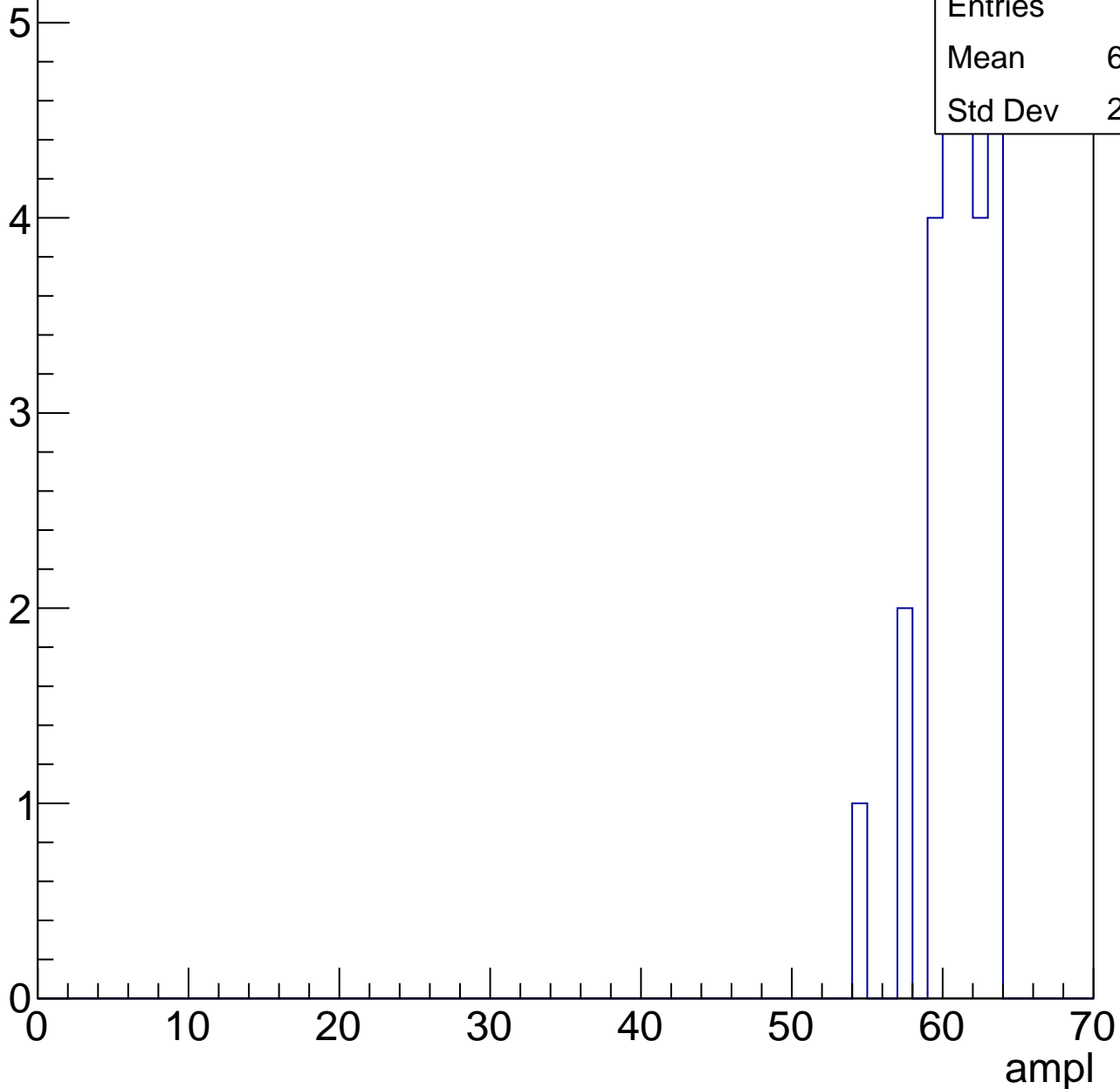


B1L101S, U4-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	60.46
Std Dev	2.135



B1L101S, U4-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch77, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	28.37
Std Dev	5.766

Gaus mean : 29.9967

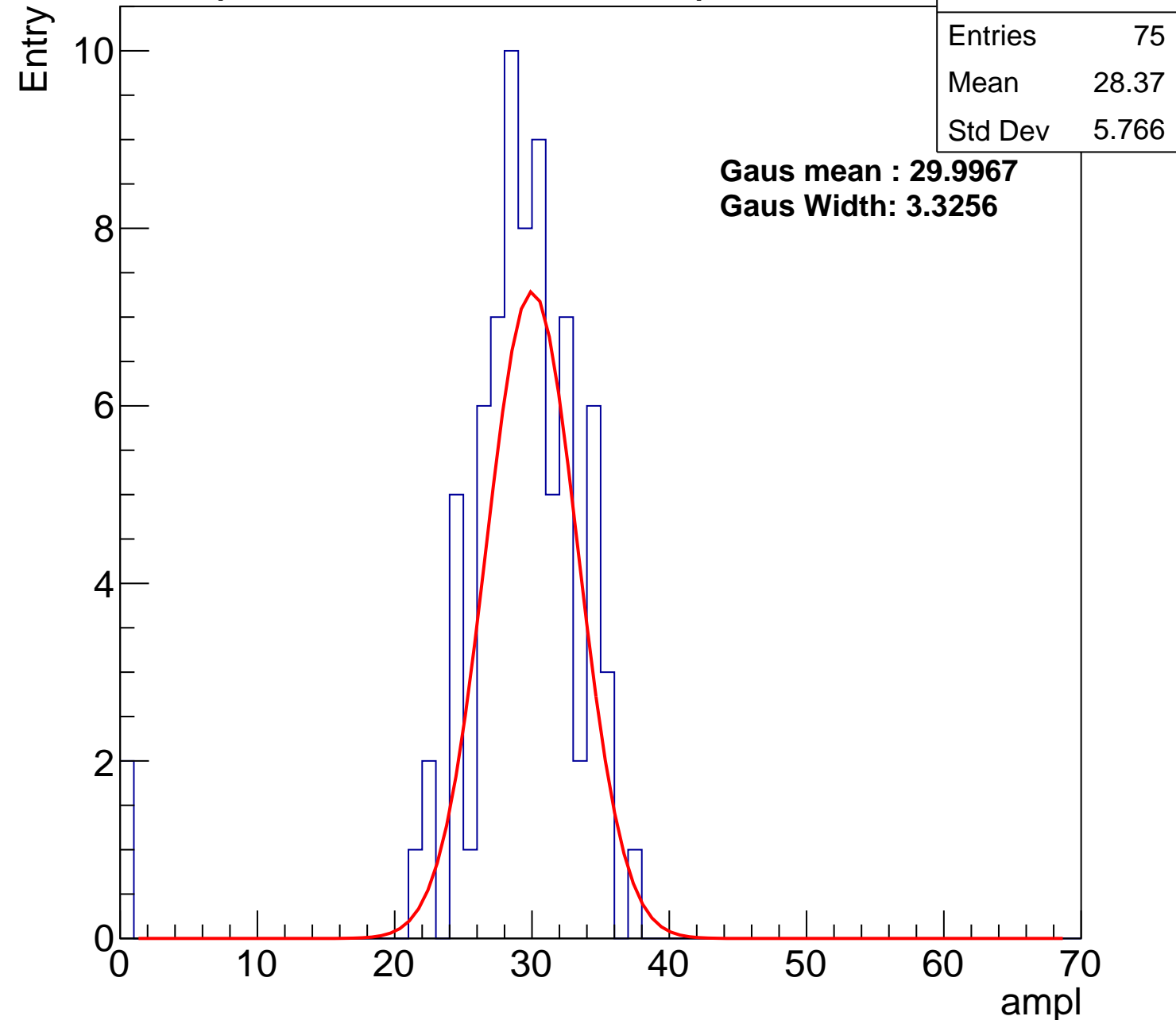
Gaus Width: 3.3256

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch77, adc1

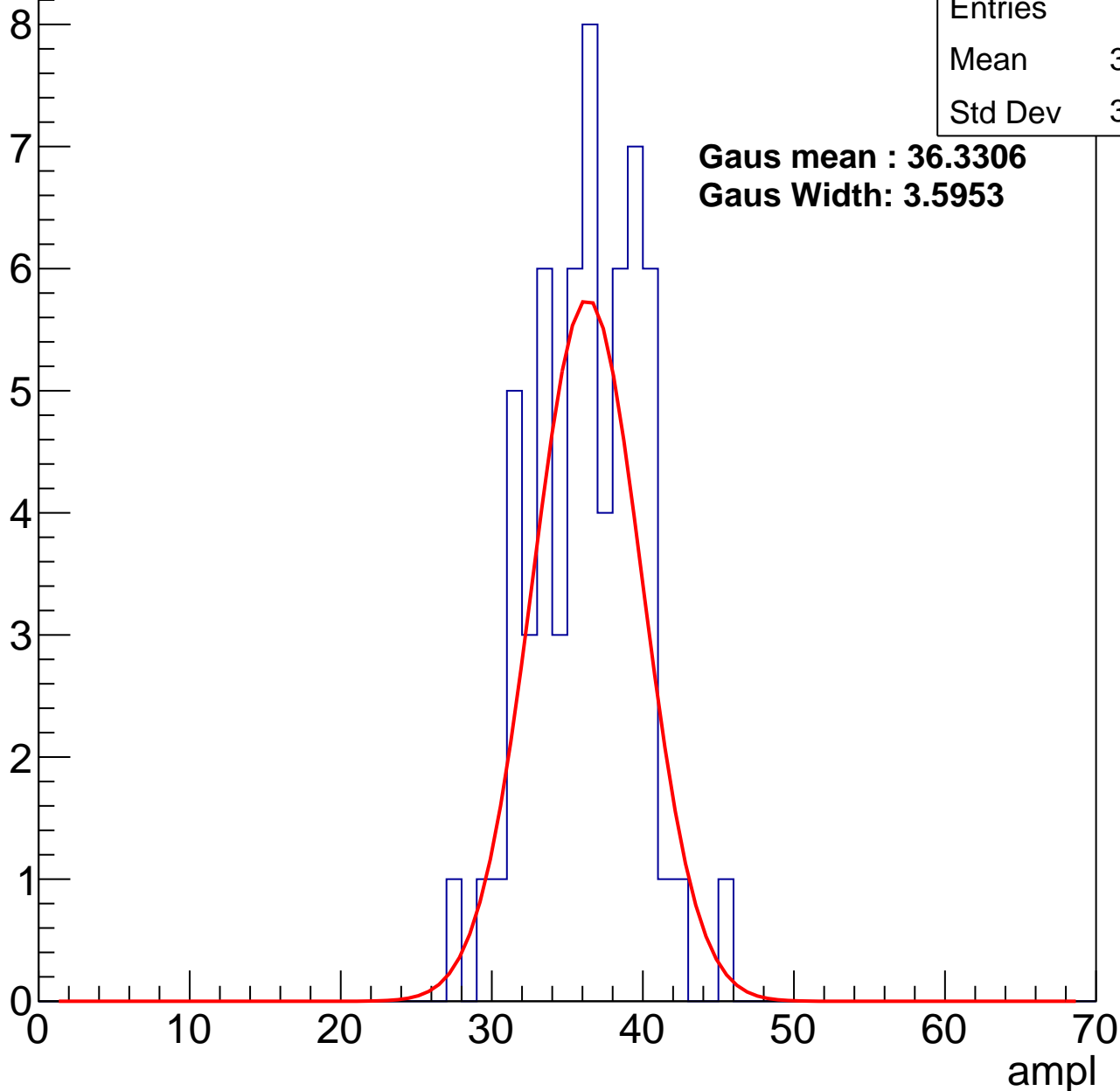
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.87
Std Dev	3.509

Gaus mean : 36.3306

Gaus Width: 3.5953



B1L101S, U4-ch77, adc2

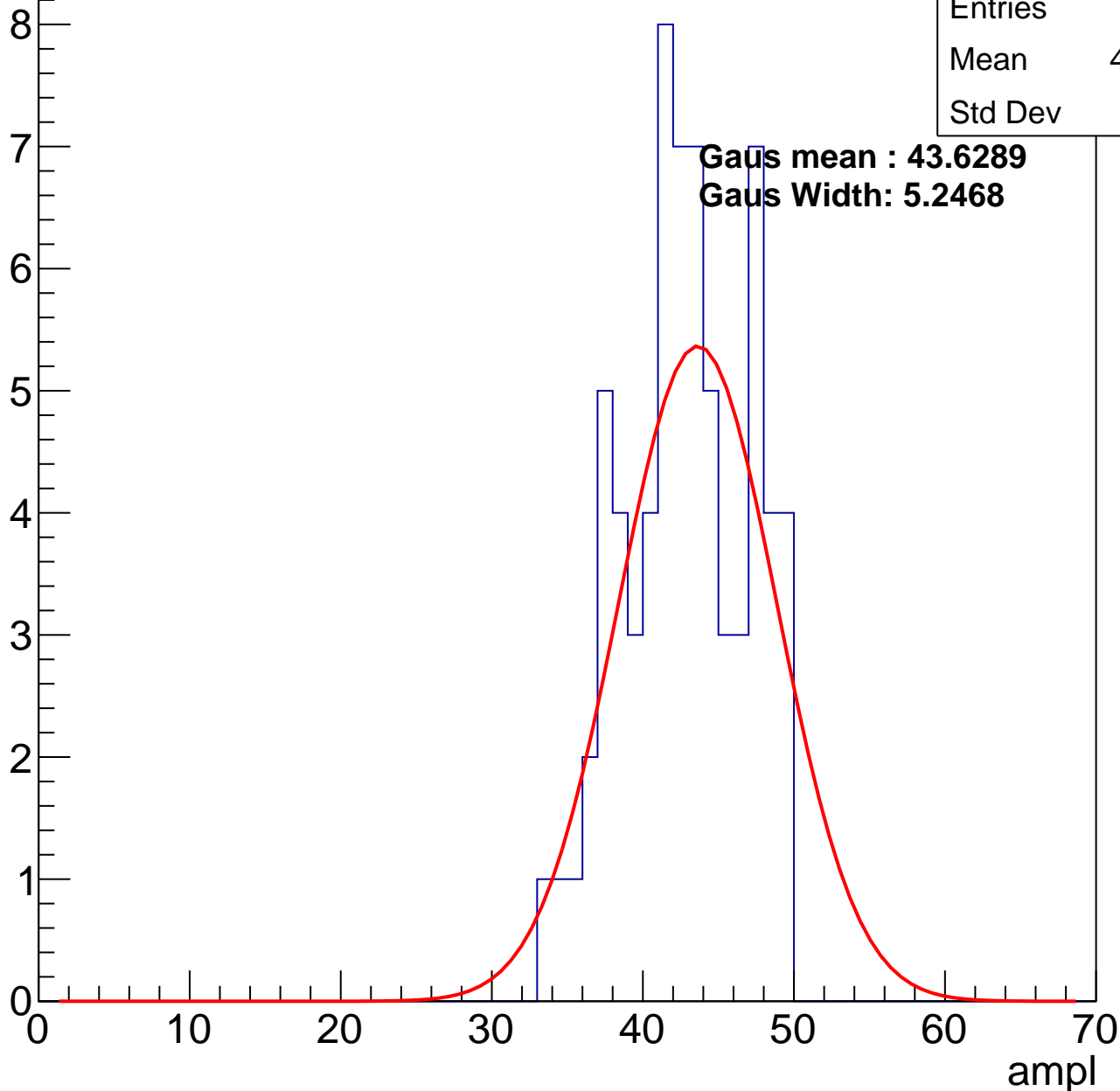
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.33
Std Dev	4.02

Gaus mean : 43.6289

Gaus Width: 5.2468

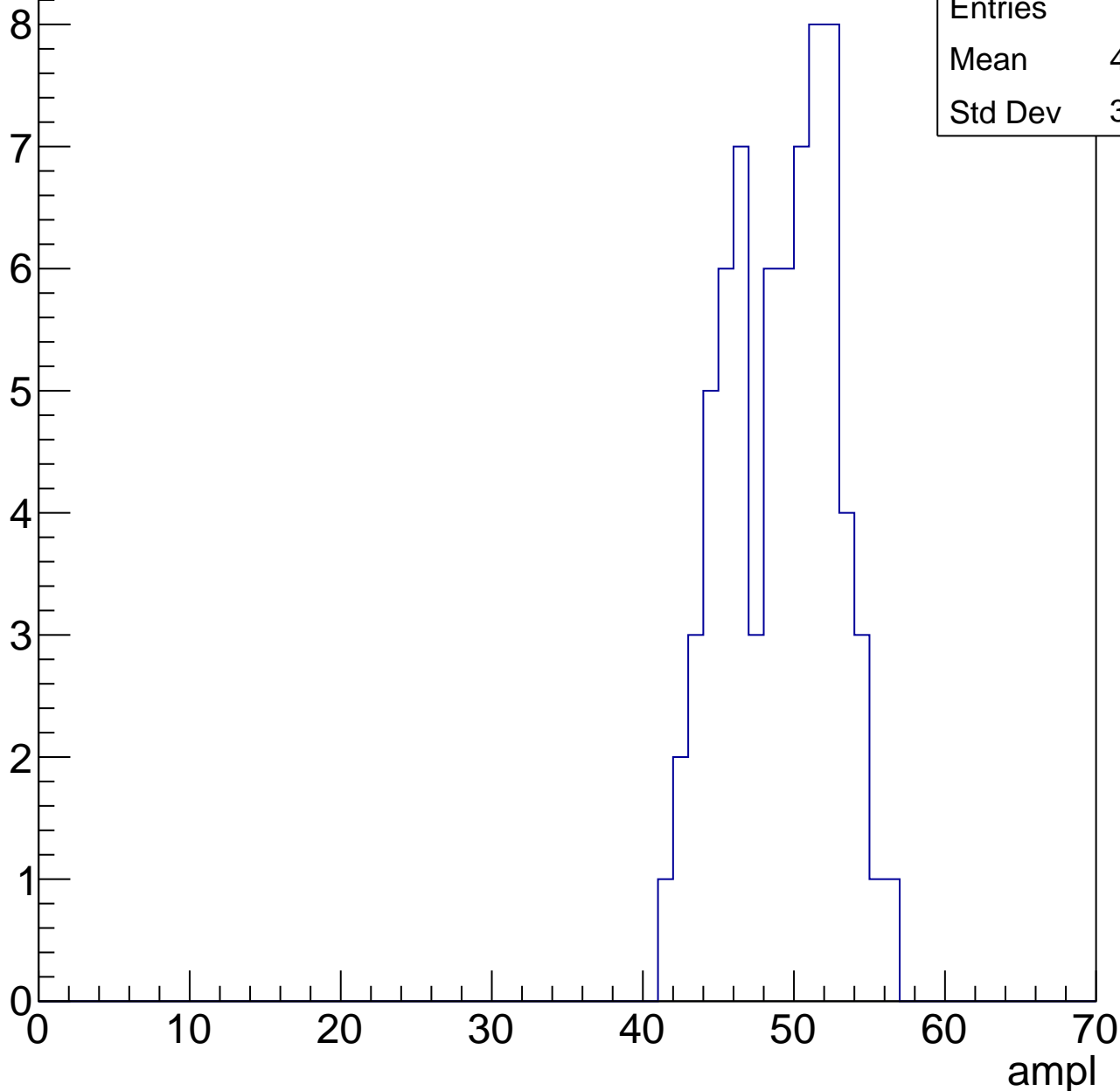


B1L101S, U4-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

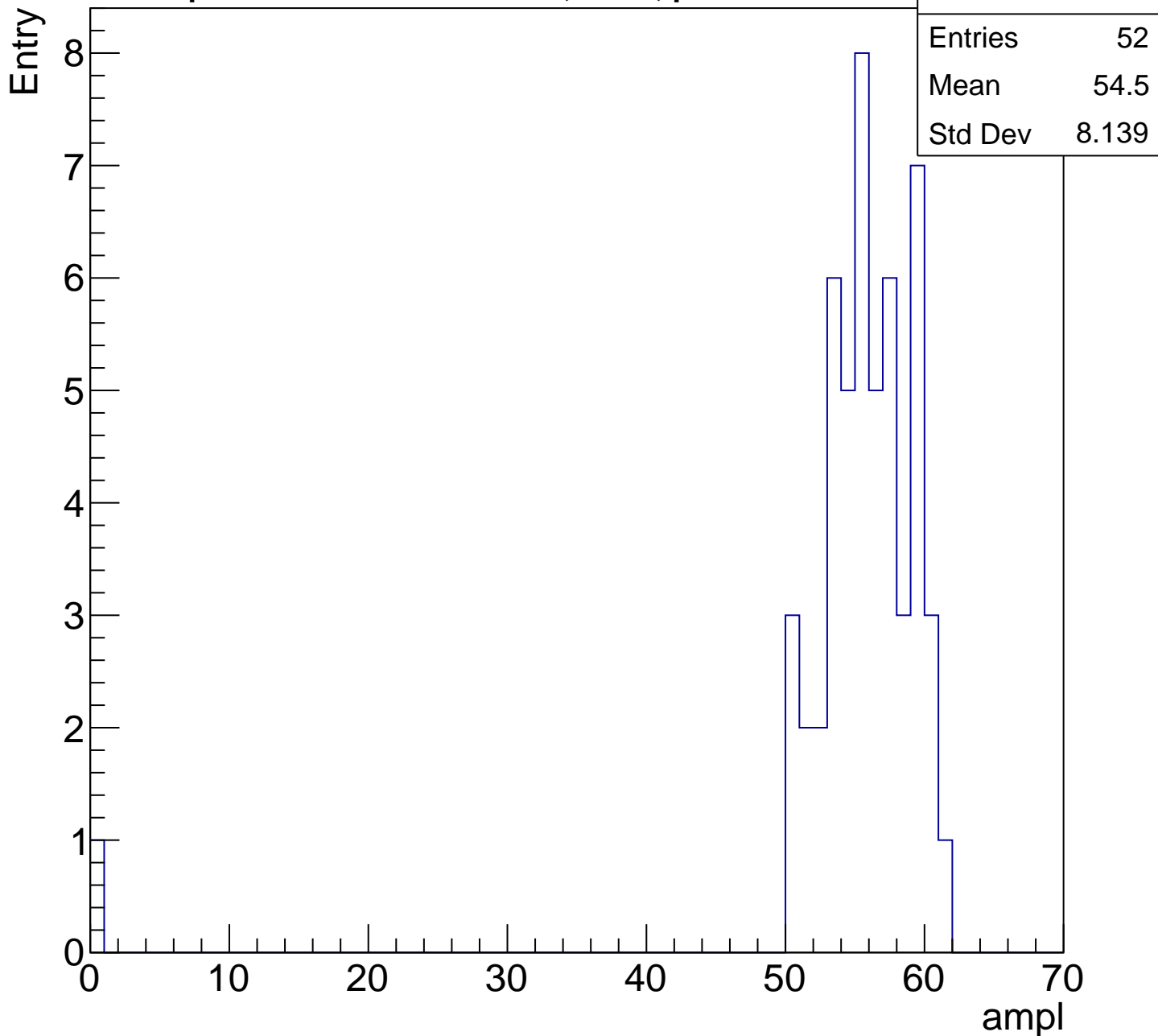
Entry

Entries	71
Mean	48.56
Std Dev	3.547



B1L101S, U4-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch77, adc5

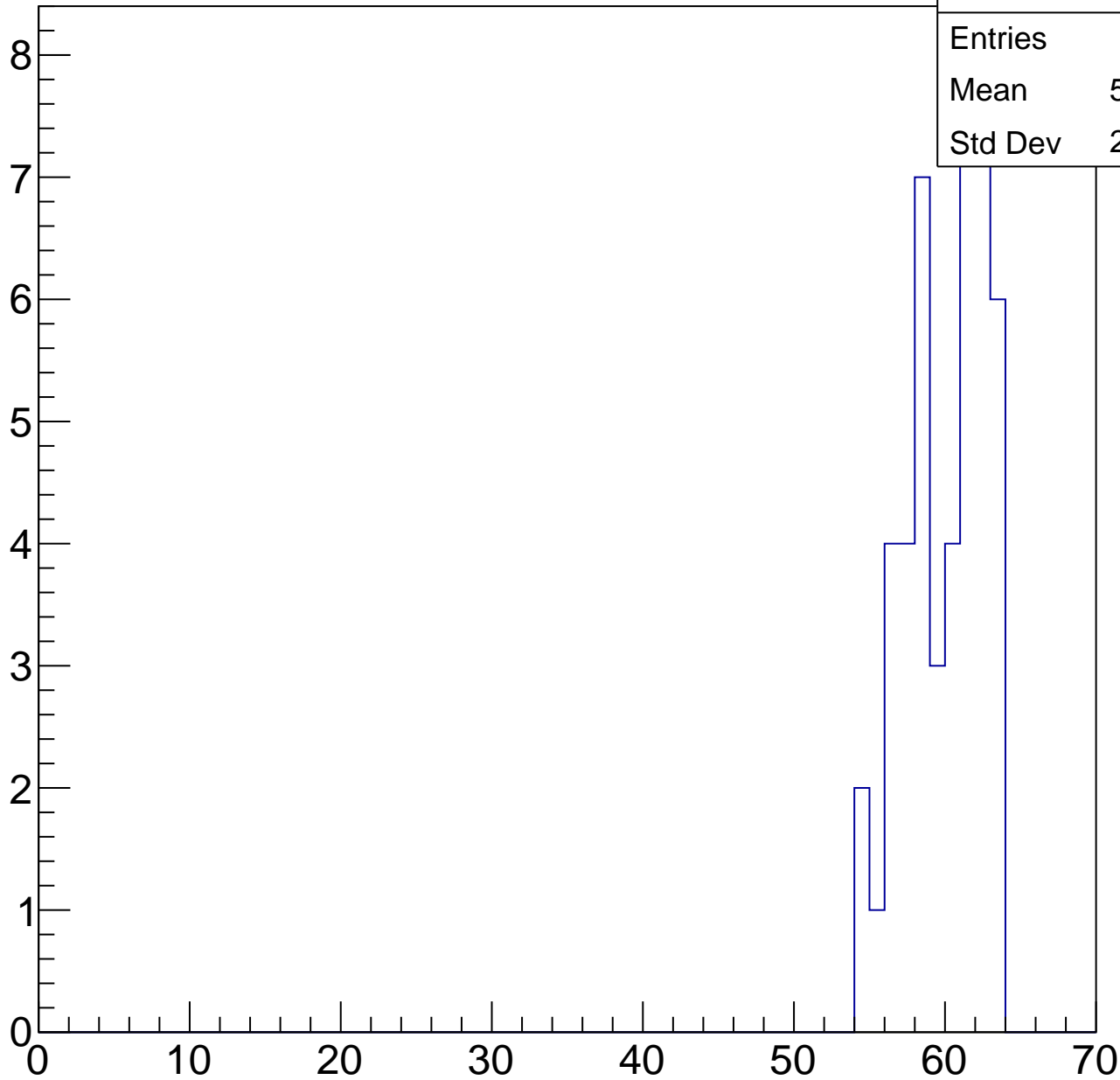
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.57
Std Dev	2.583

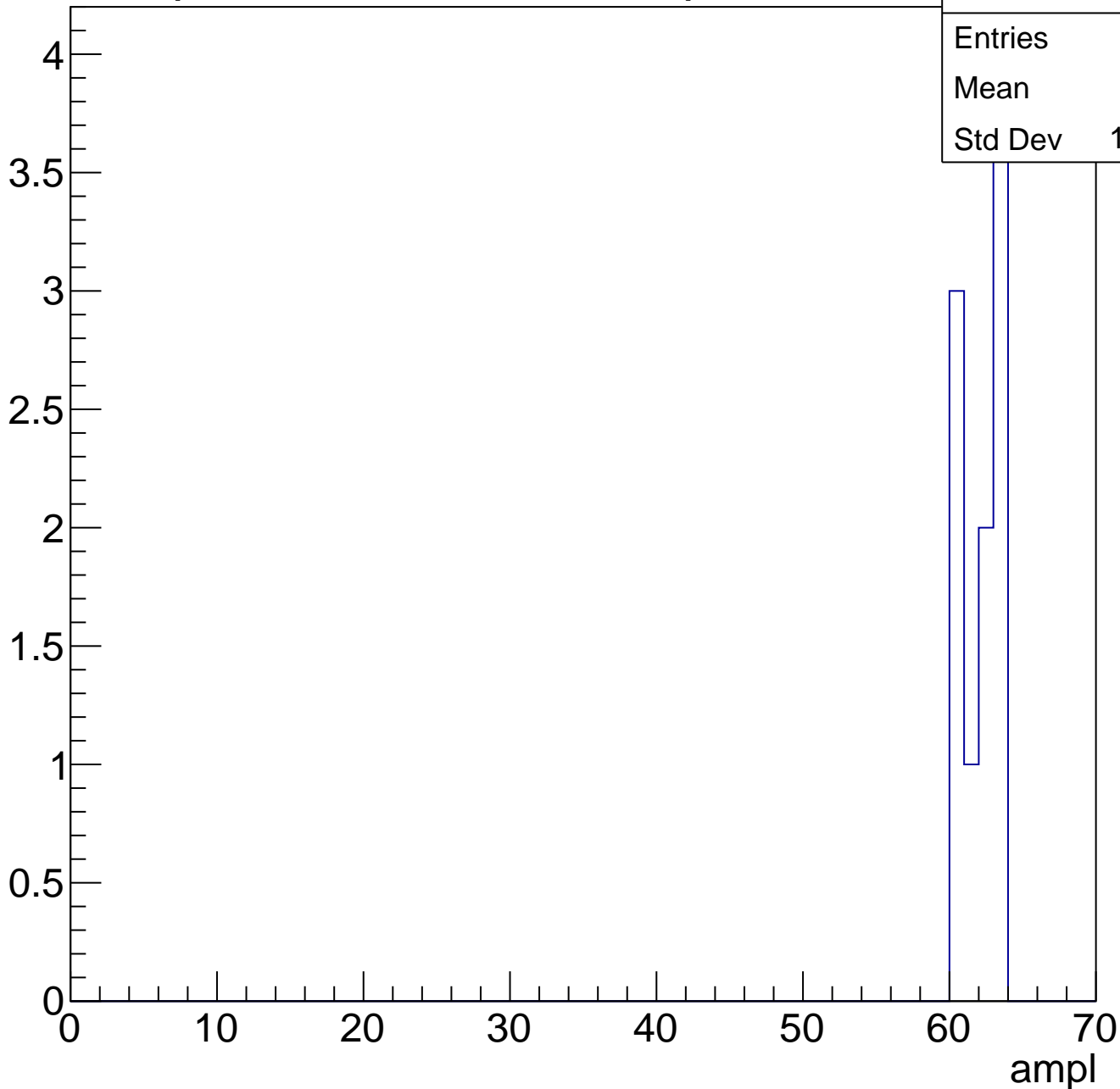
ampl



B1L101S, U4-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch78, adc0

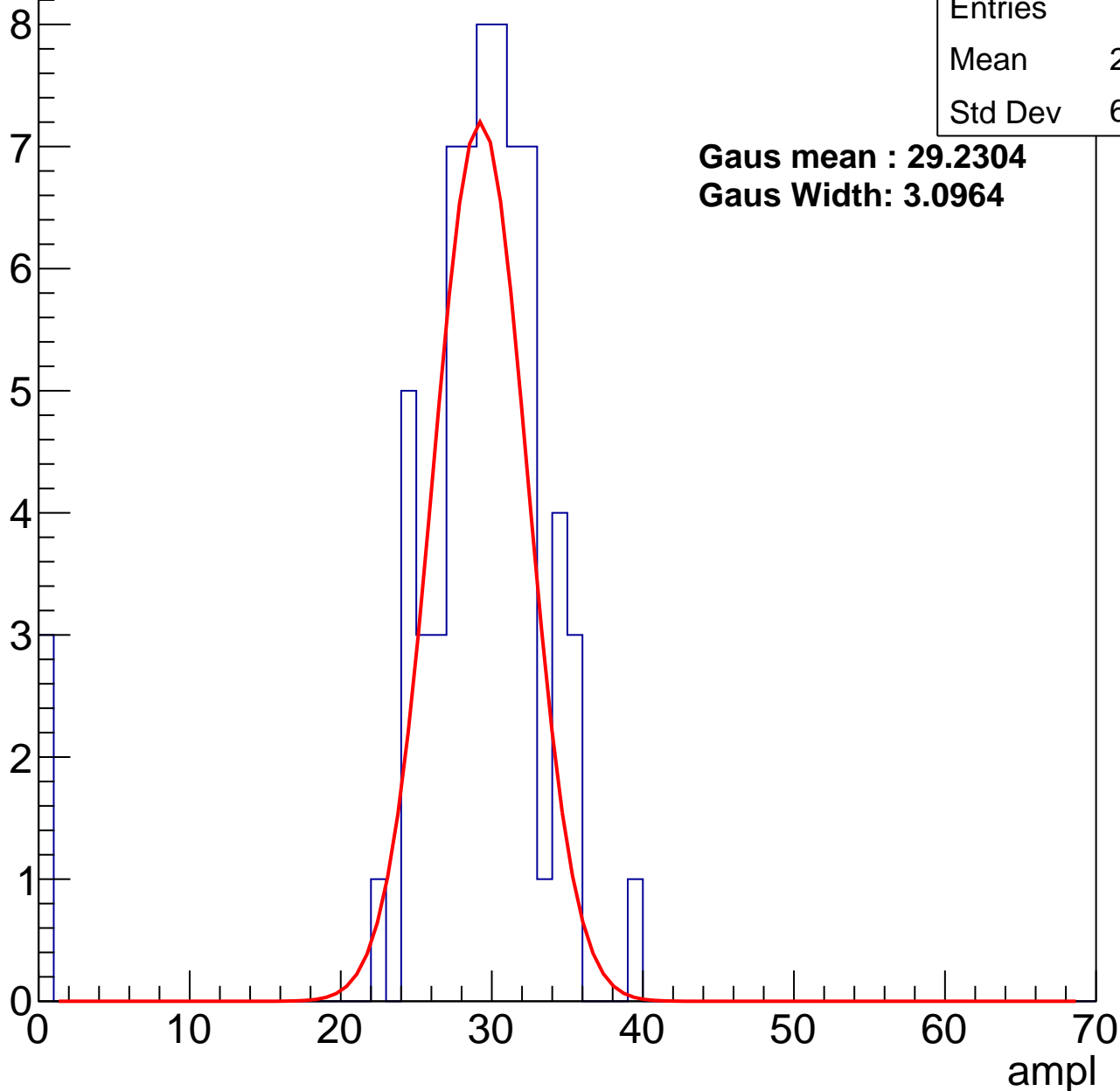
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.03
Std Dev	6.823

Gaus mean : 29.2304

Gaus Width: 3.0964



B1L101S, U4-ch78, adc1

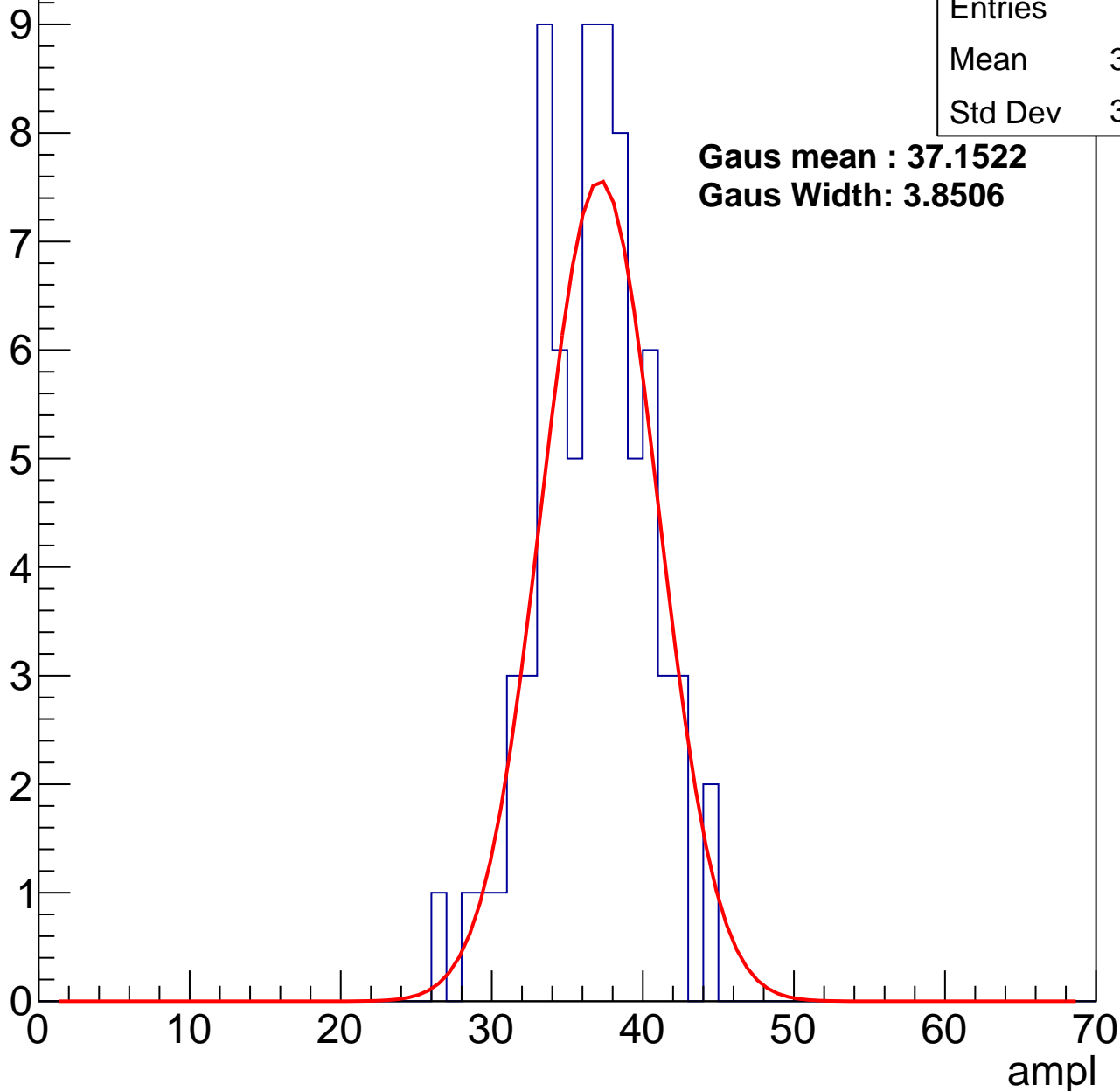
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.15
Std Dev	3.603

Gaus mean : 37.1522

Gaus Width: 3.8506



B1L101S, U4-ch78, adc2

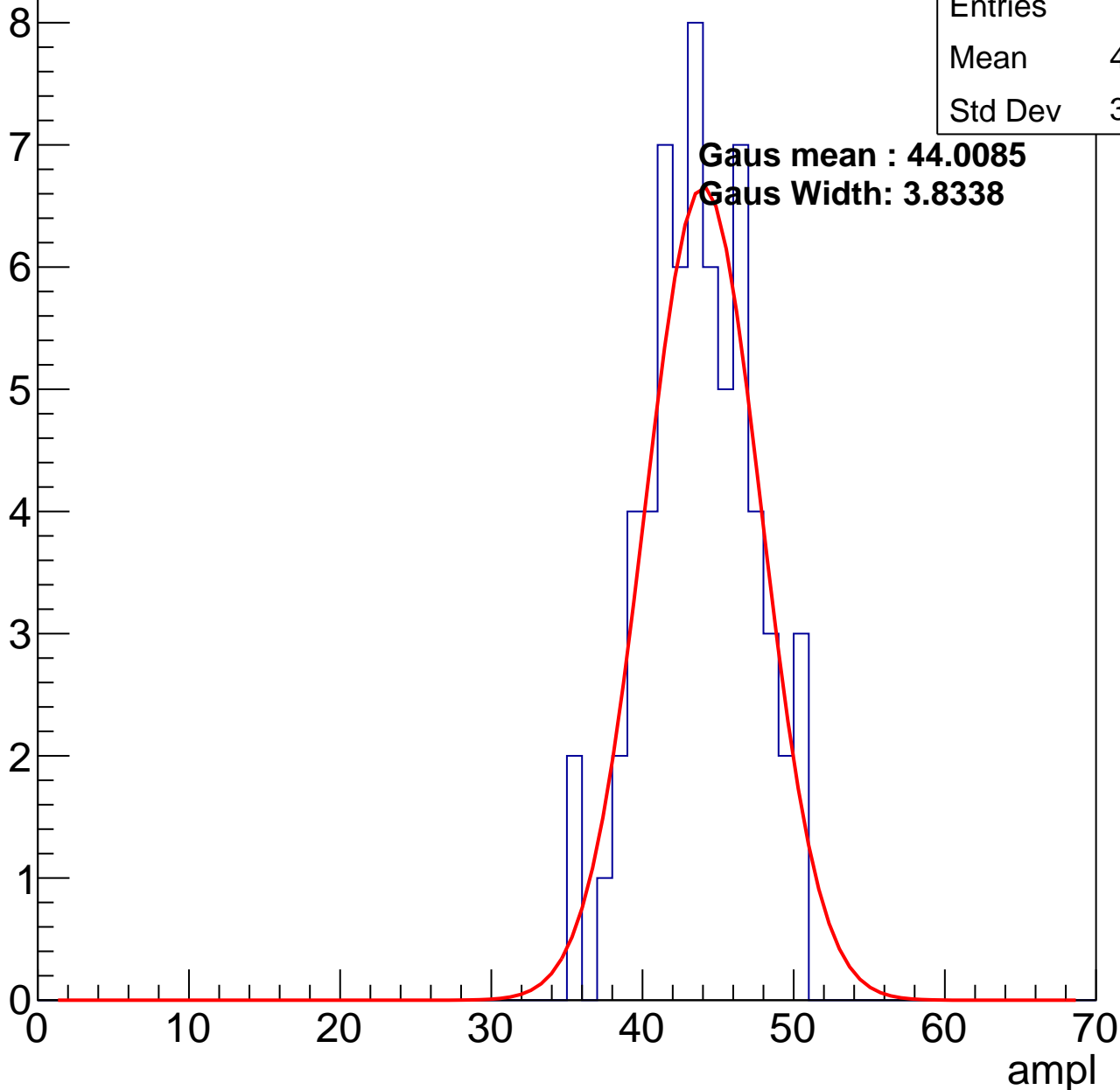
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.33
Std Dev	3.509

Gaus mean : 44.0085

Gaus Width: 3.8338

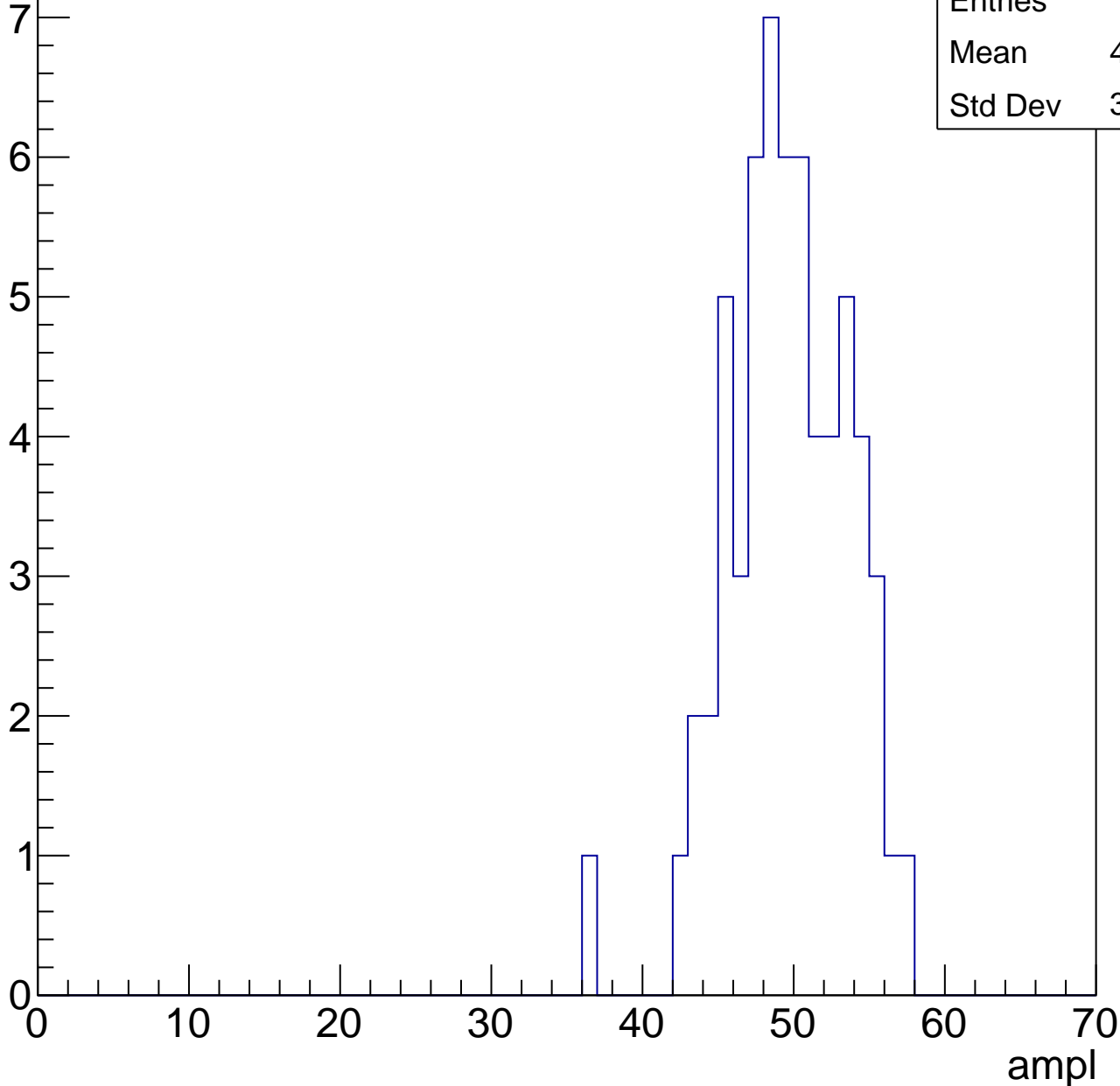


B1L101S, U4-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.15
Std Dev	3.916

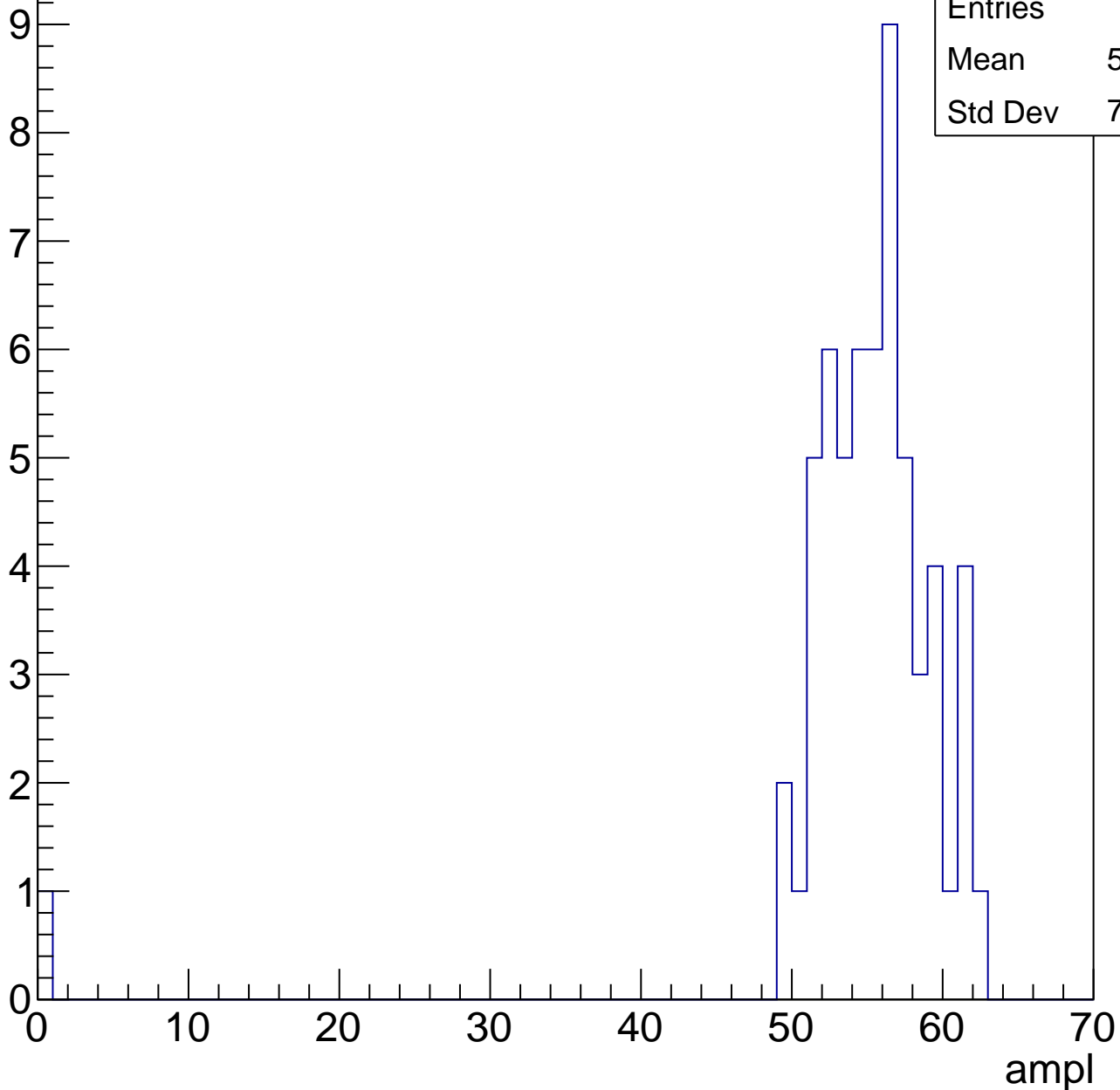


B1L101S, U4-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.22
Std Dev	7.794



B1L101S, U4-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	37
Mean	59.7
Std Dev	2.567

ampl

0

10

20

30

40

50

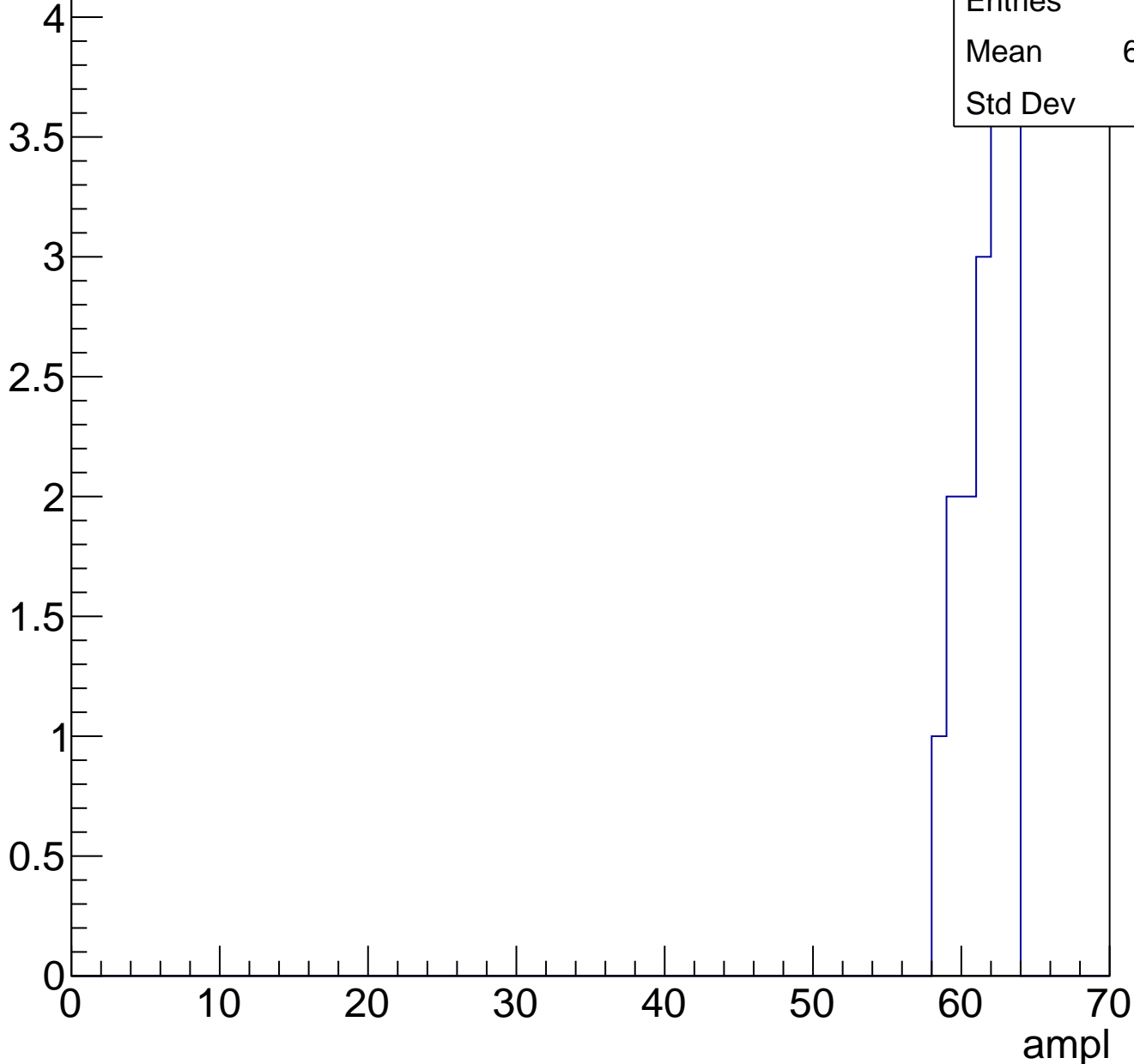
60

70

B1L101S, U4-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch79, adc0

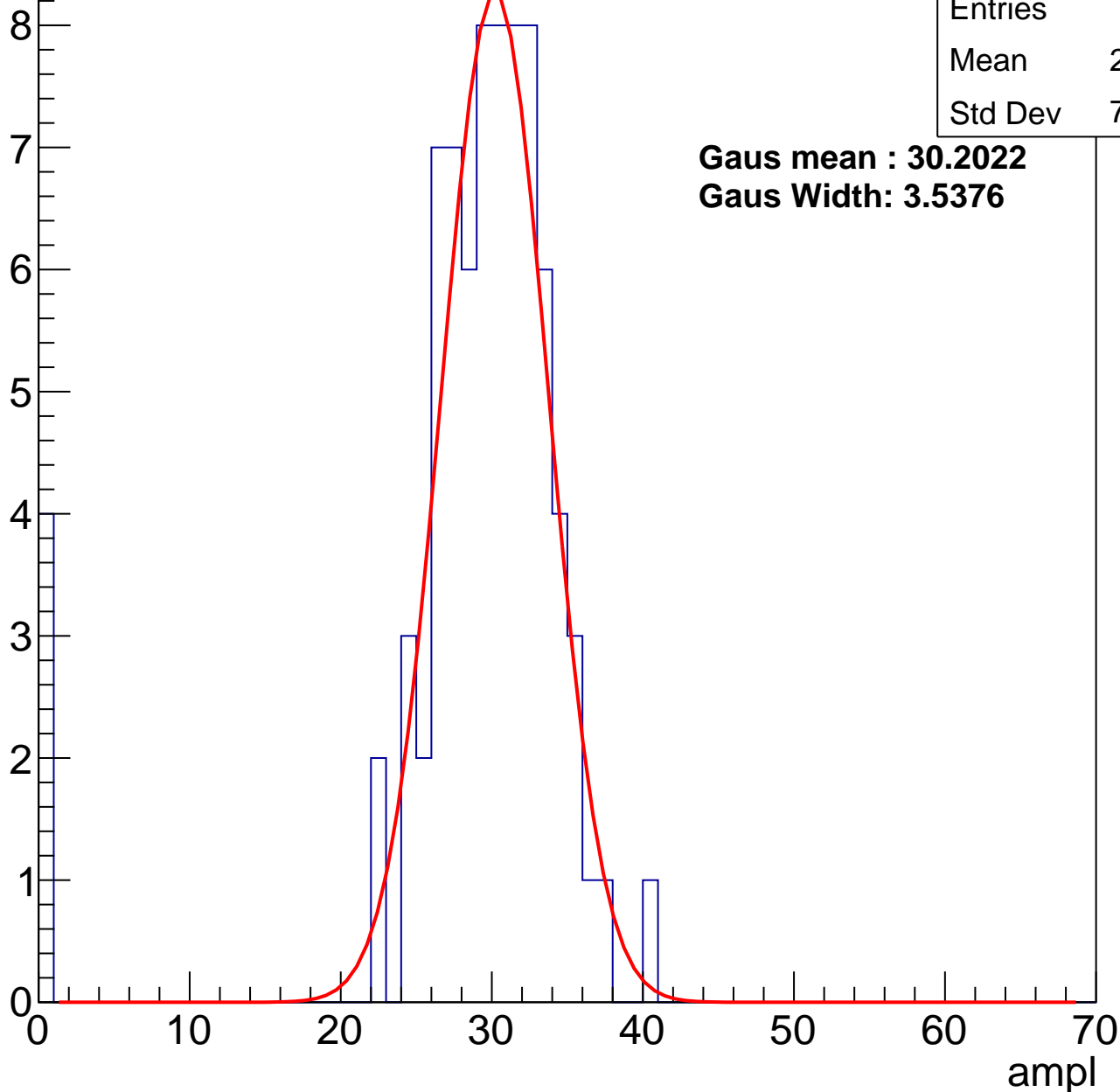
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	28.27
Std Dev	7.355

Gaus mean : 30.2022

Gaus Width: 3.5376



B1L101S, U4-ch79, adc1

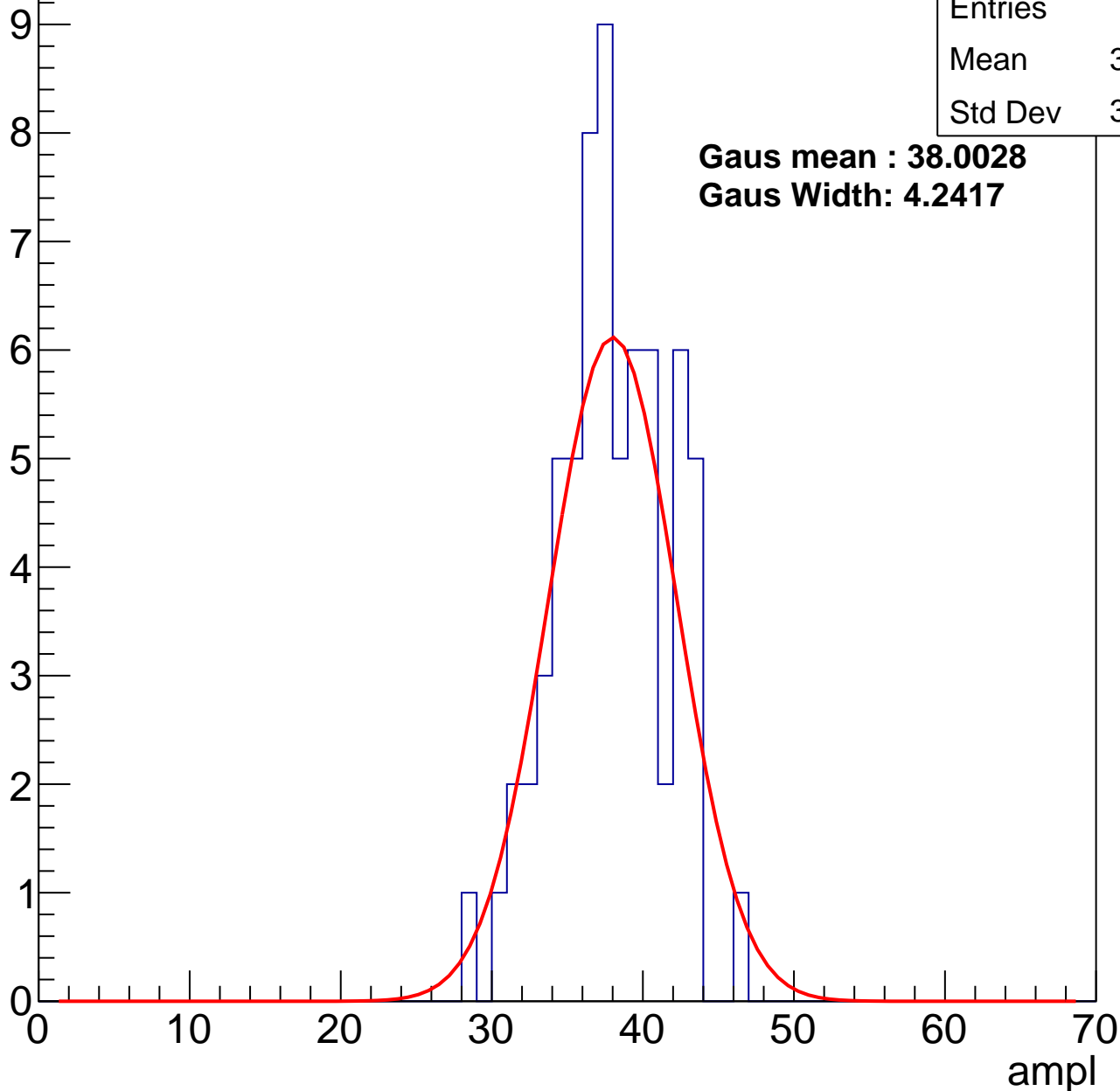
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.43
Std Dev	3.637

Gaus mean : 38.0028

Gaus Width: 4.2417



B1L101S, U4-ch79, adc2

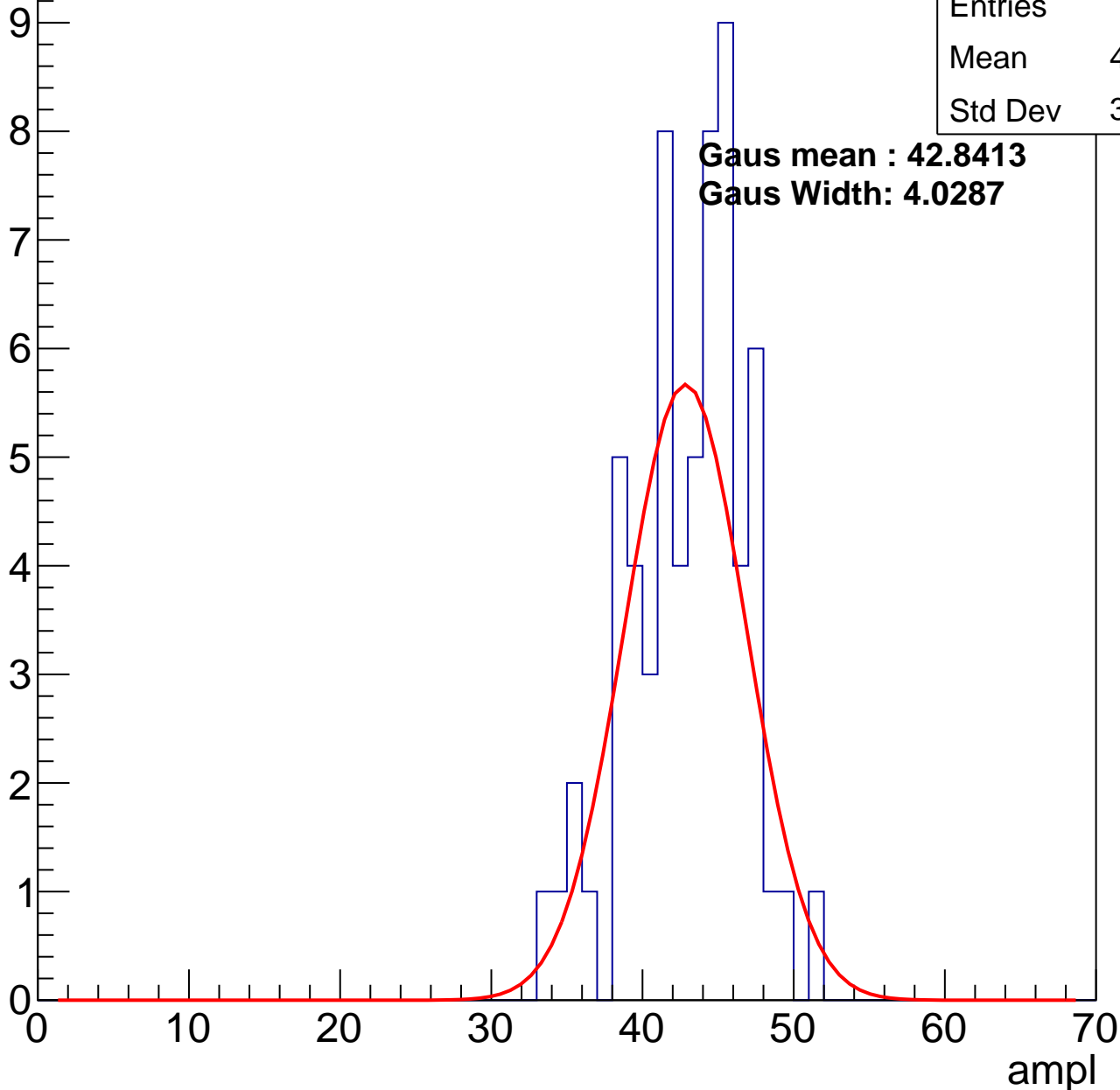
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.52
Std Dev	3.742

Gaus mean : 42.8413

Gaus Width: 4.0287

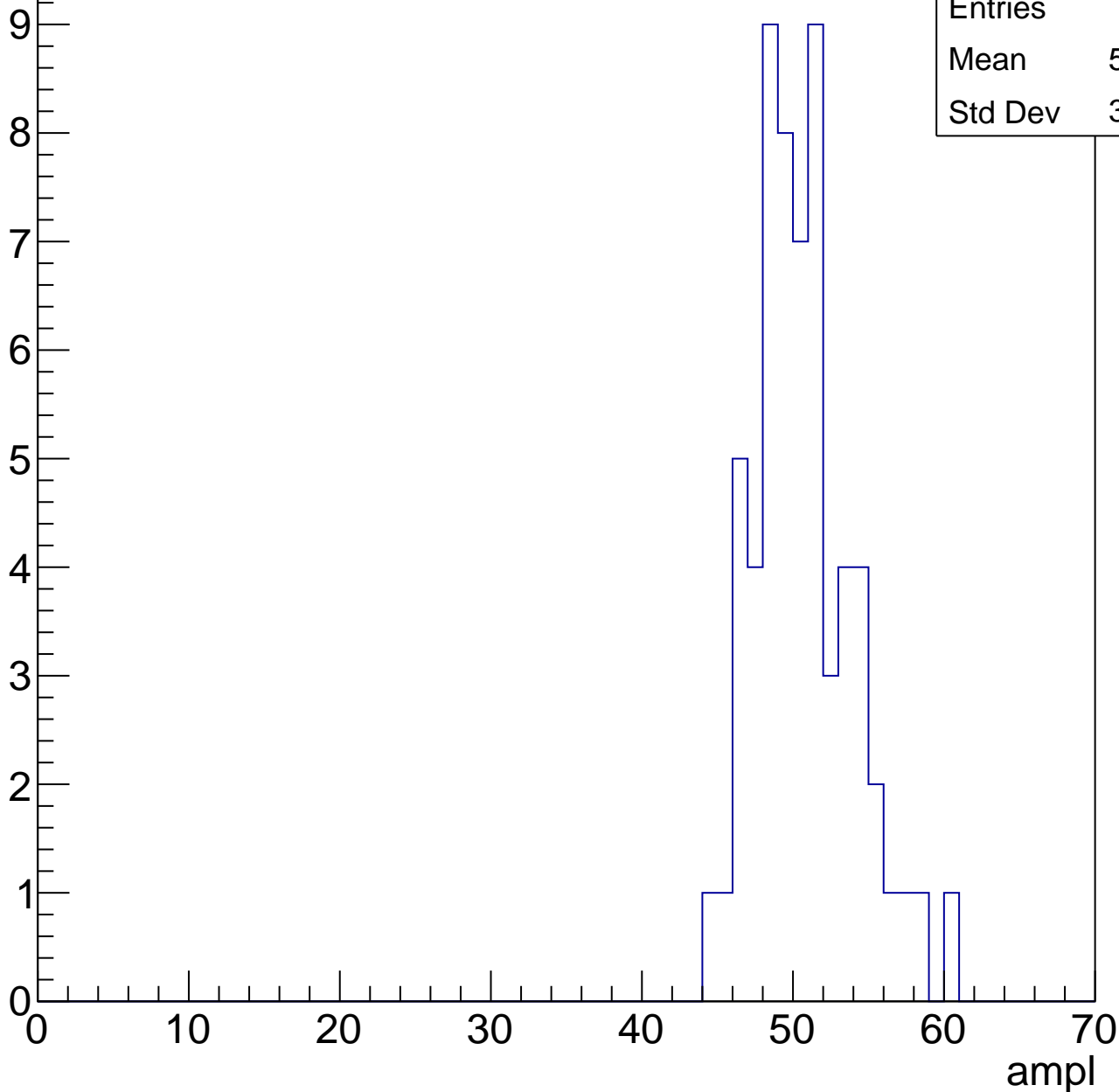


B1L101S, U4-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

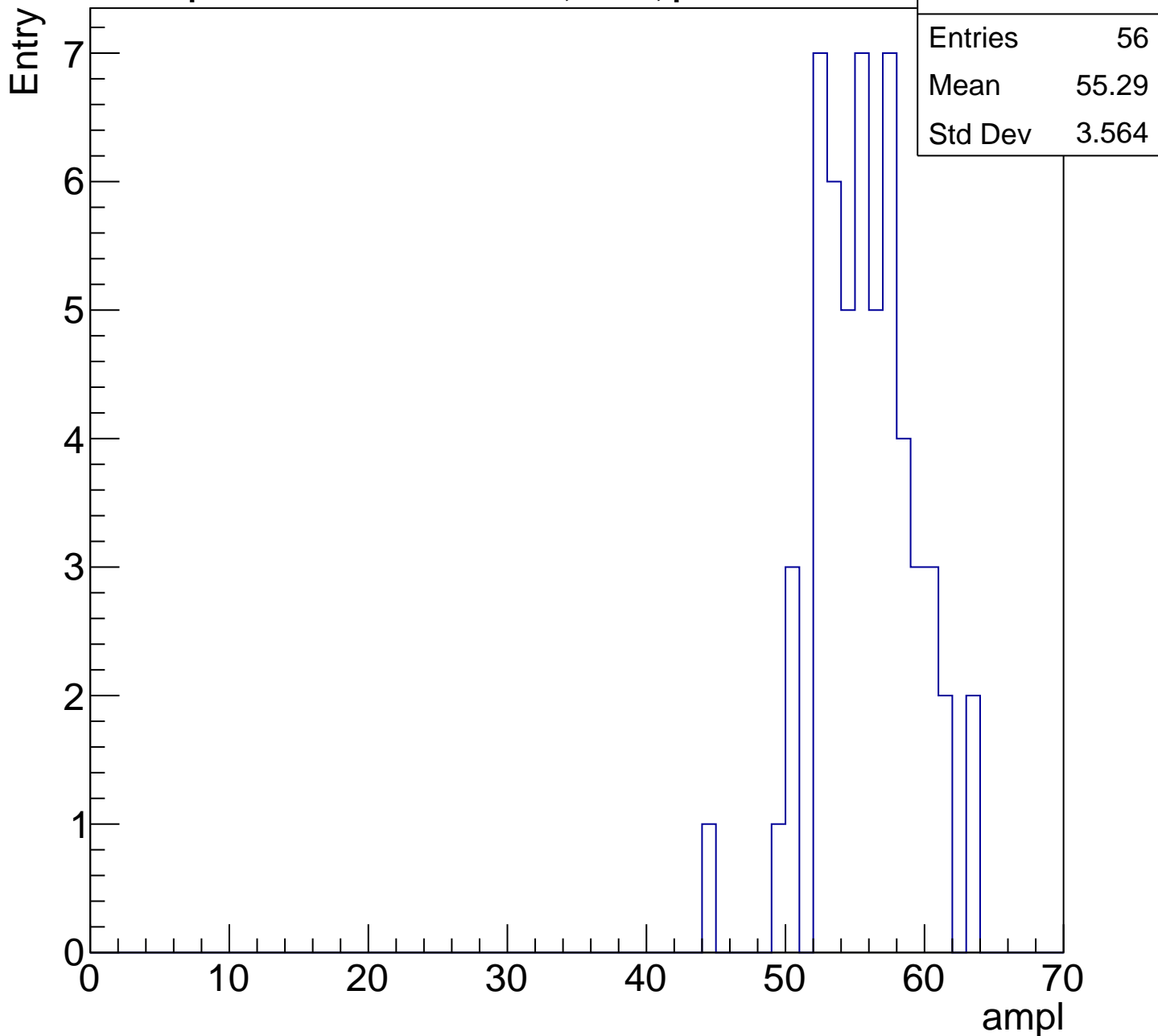
Entry

Entries	61
Mean	50.25
Std Dev	3.232



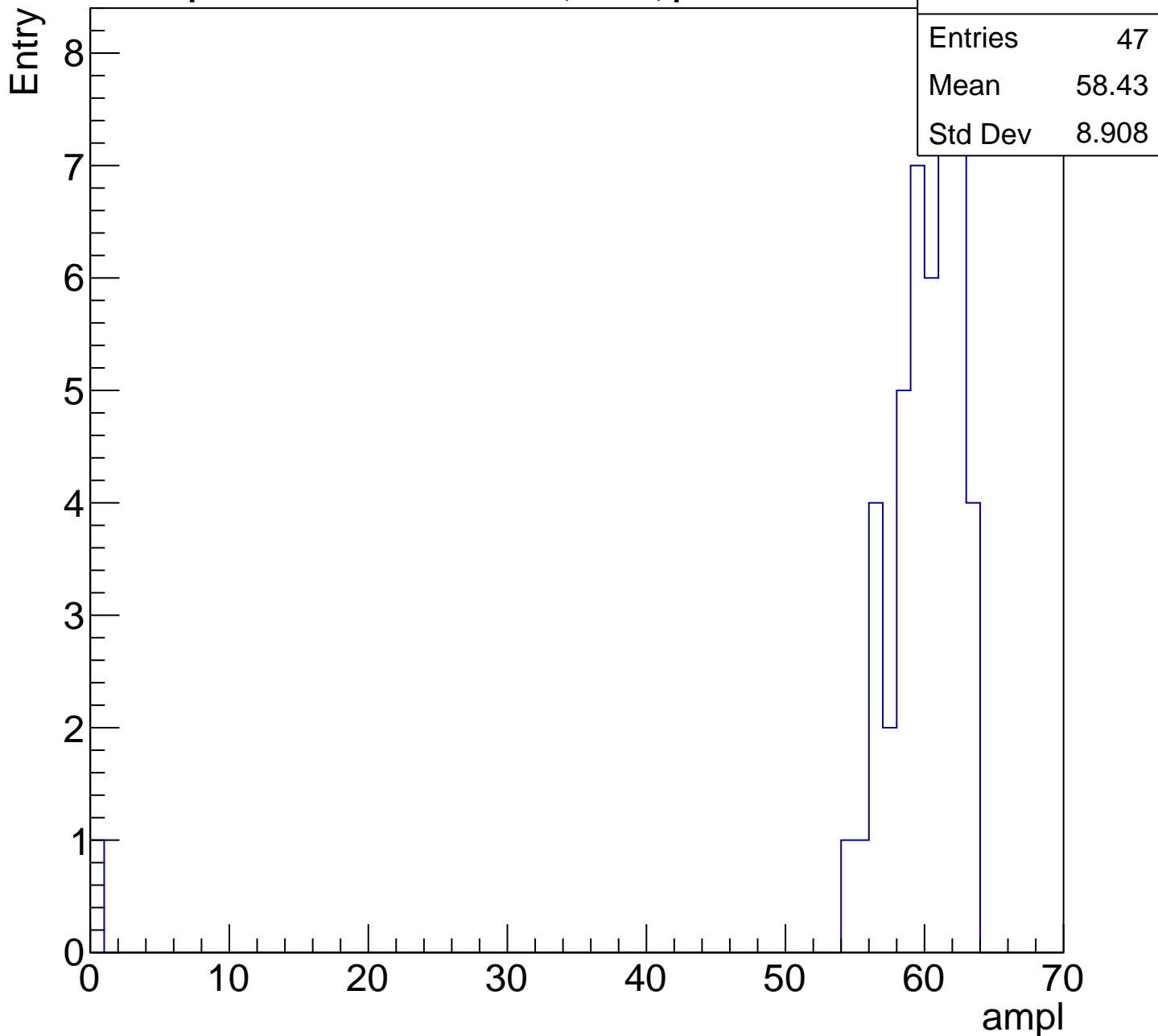
B1L101S, U4-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch79, adc5

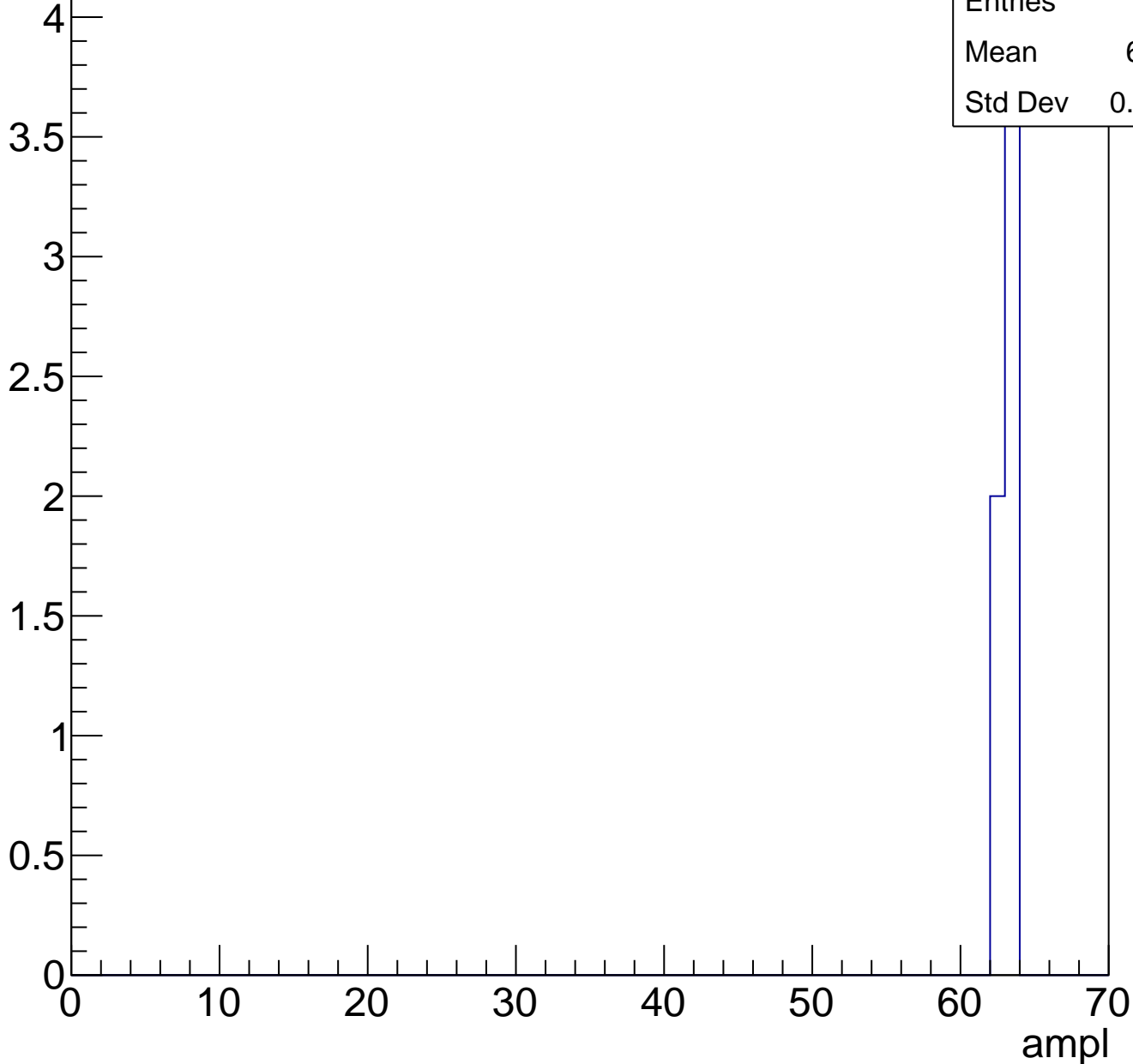
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

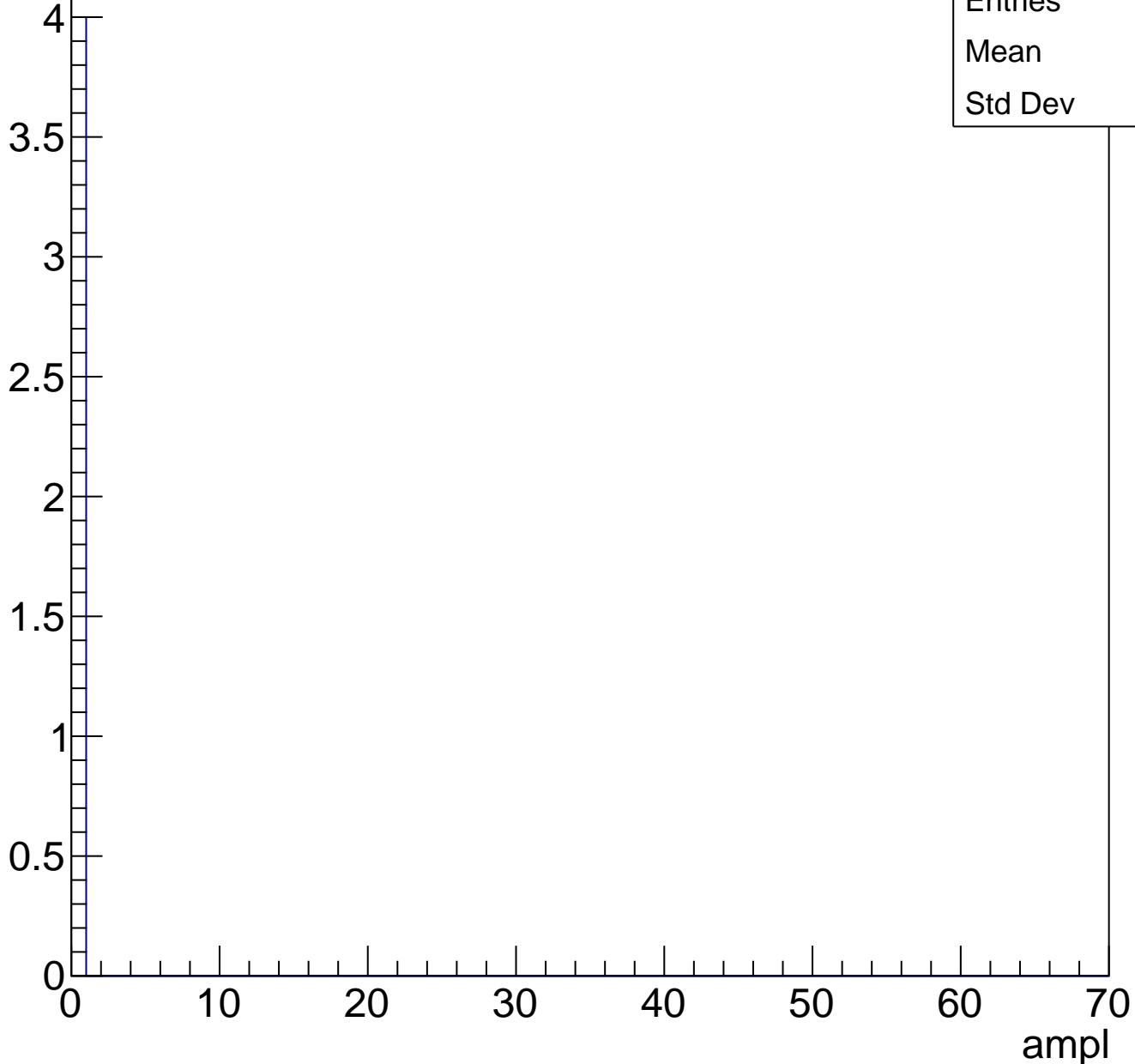
Entry



B1L101S, U4-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	0
Std Dev	0

B1L101S, U4-ch80, adc0

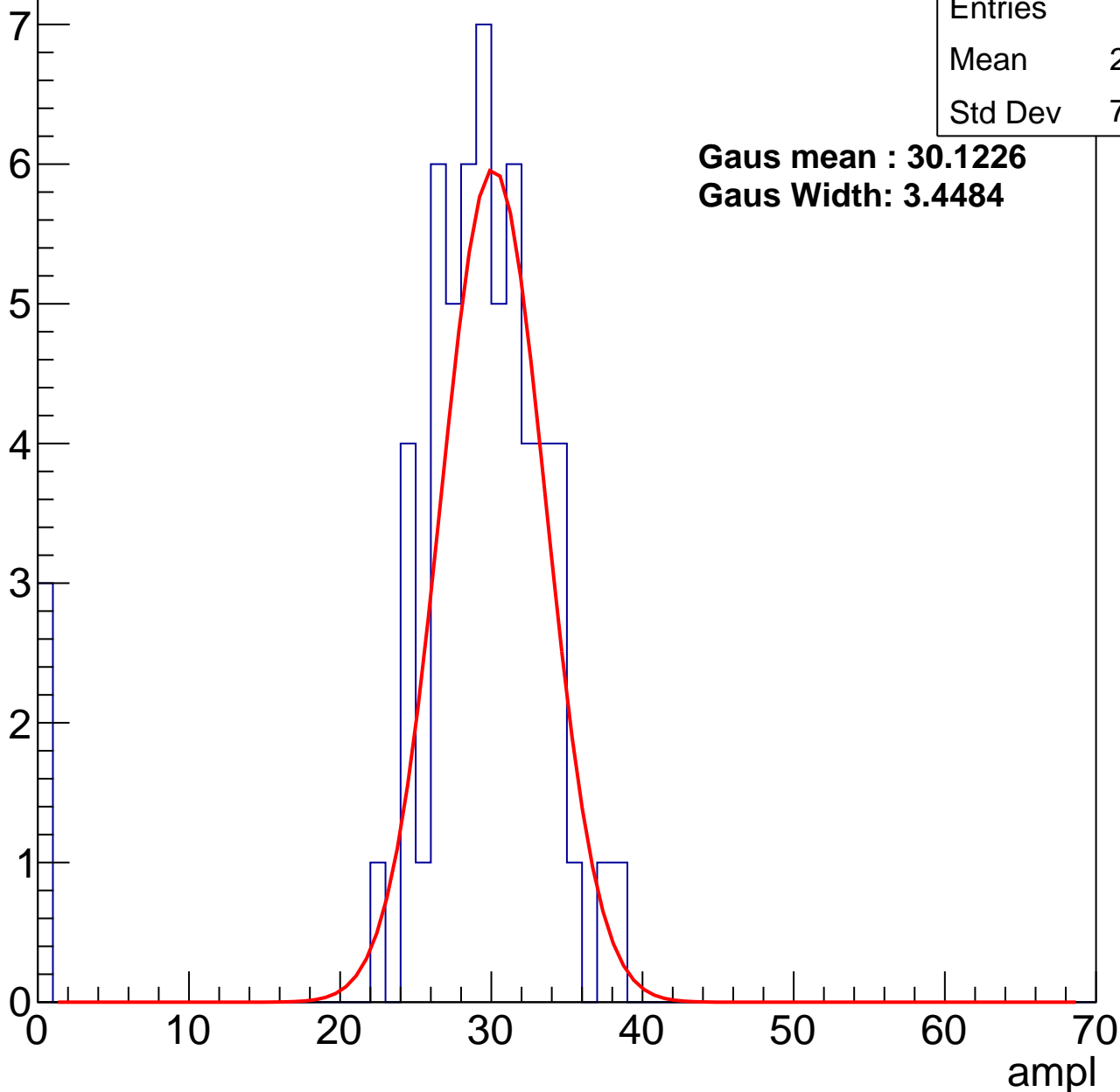
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.92
Std Dev	7.259

Gaus mean : 30.1226

Gaus Width: 3.4484



B1L101S, U4-ch80, adc1

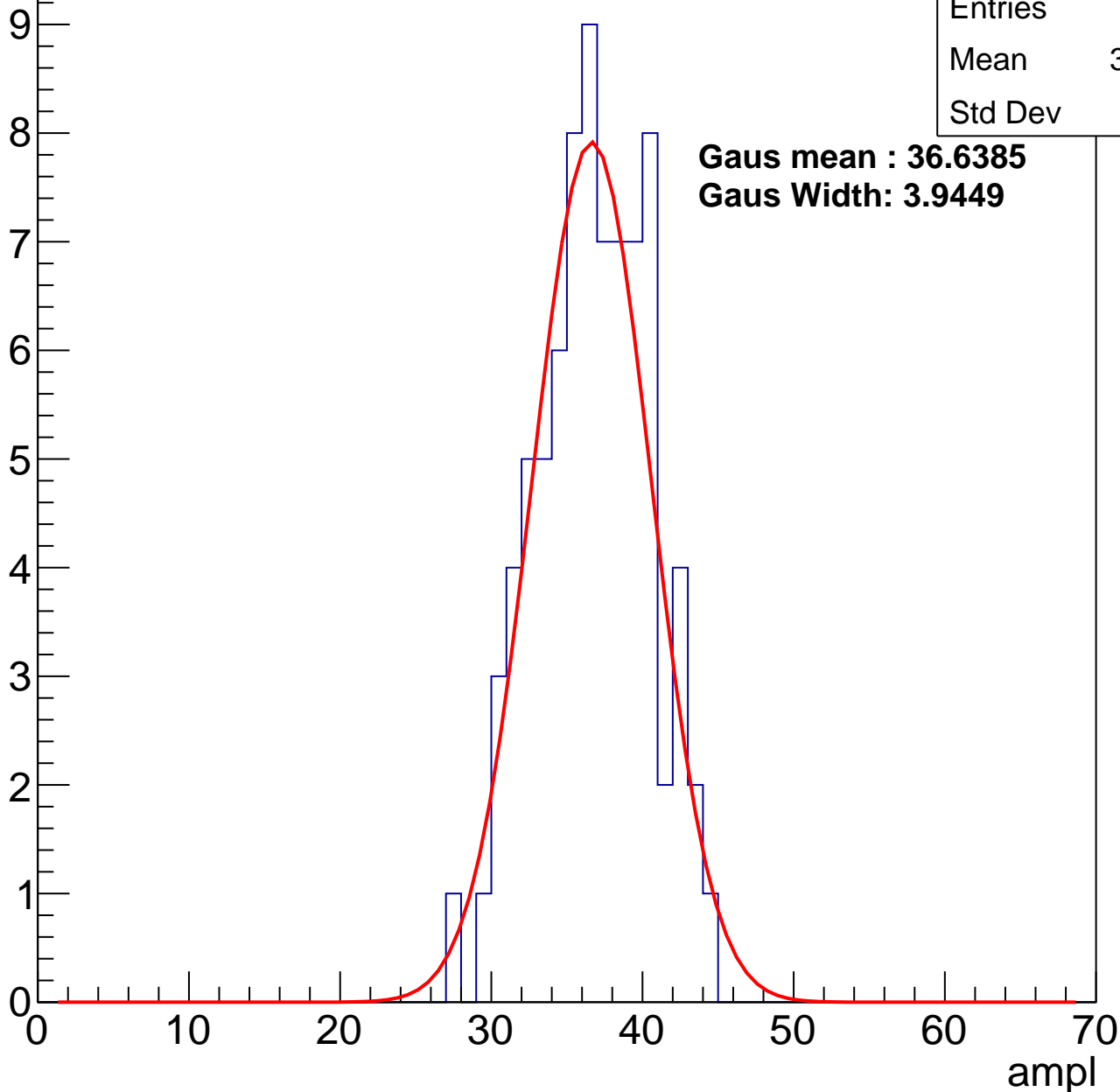
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.26
Std Dev	3.66

Gaus mean : 36.6385

Gaus Width: 3.9449



B1L101S, U4-ch80, adc2

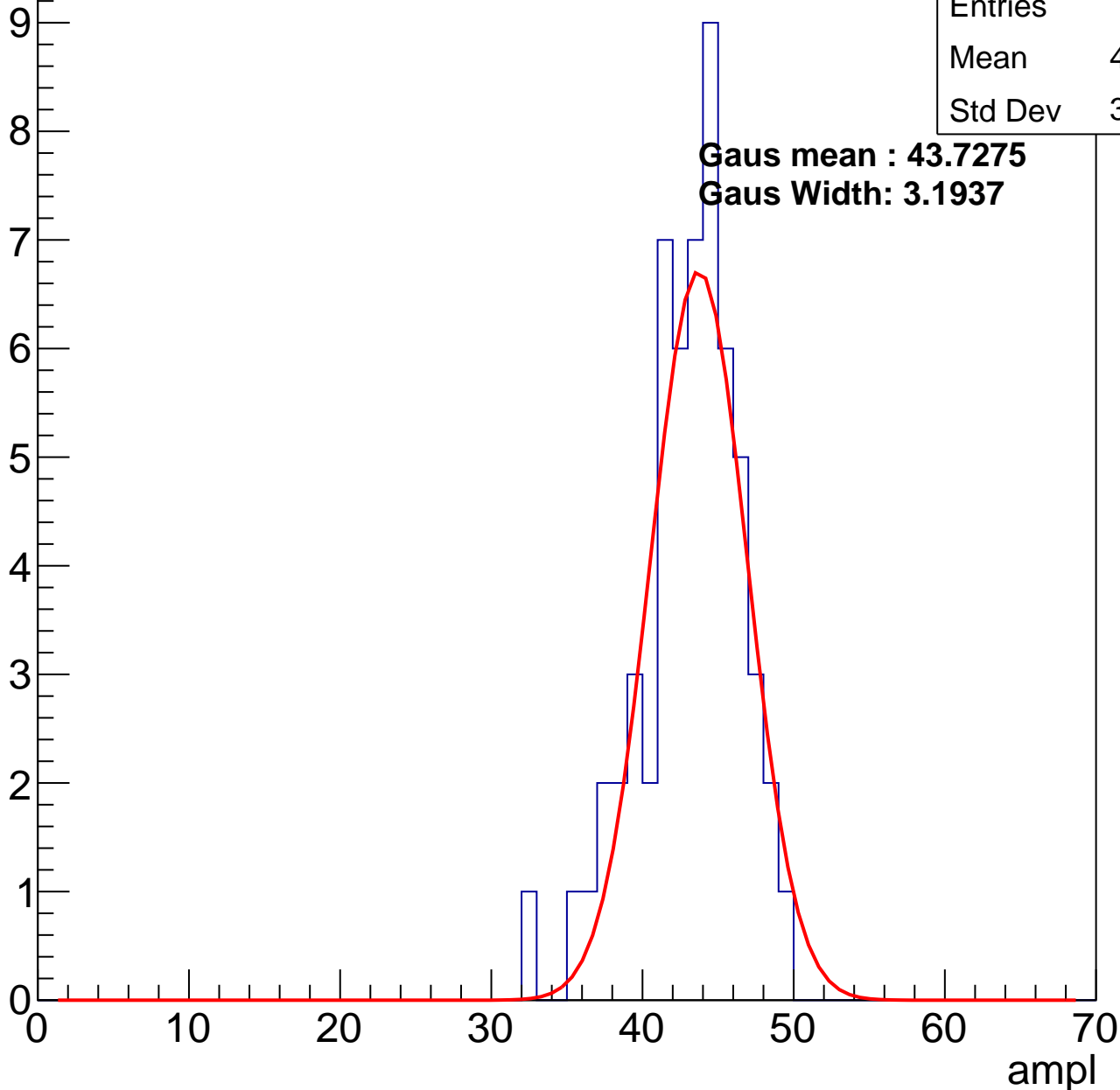
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.62
Std Dev	3.372

Gaus mean : 43.7275

Gaus Width: 3.1937

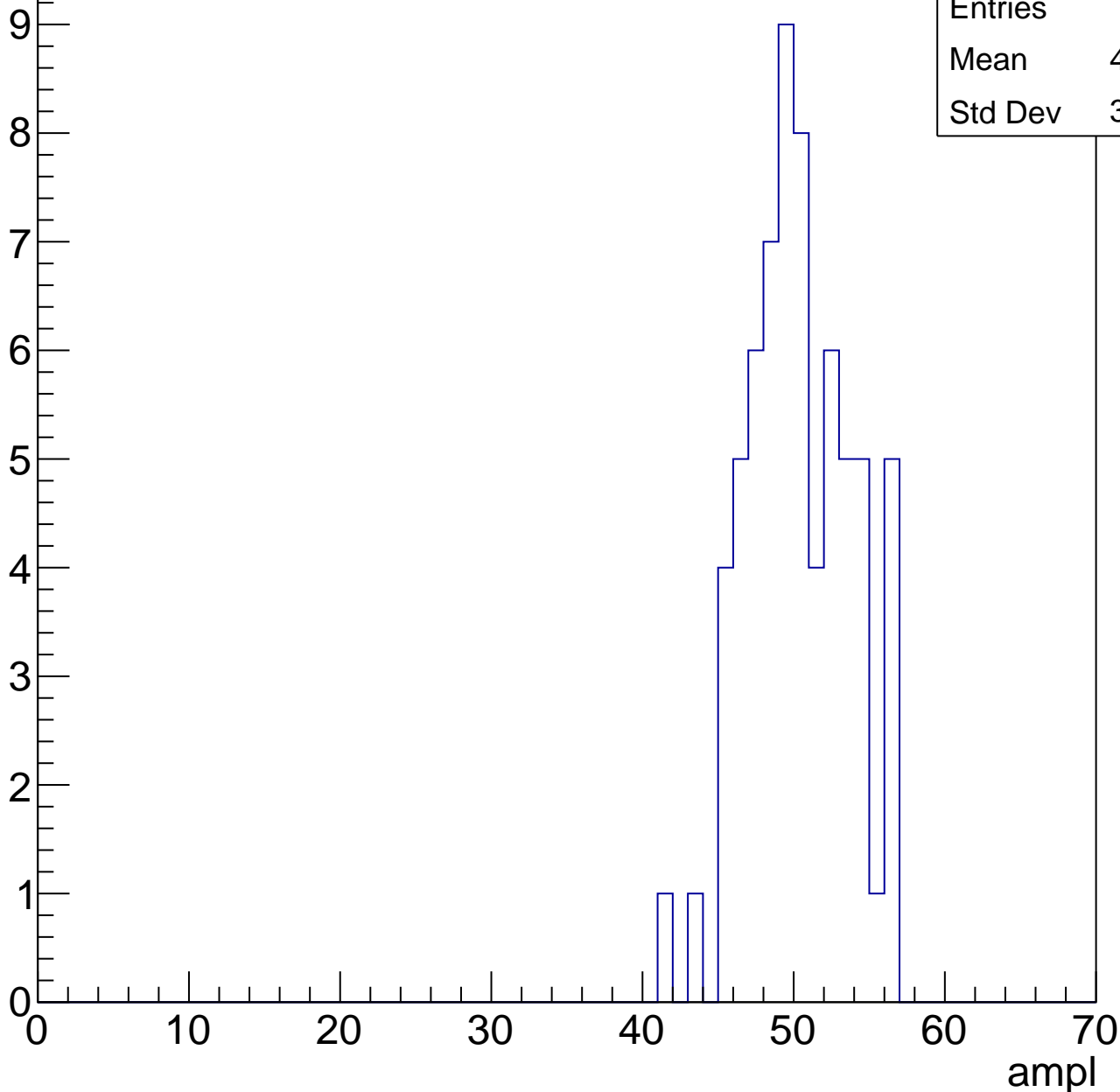


B1L101S, U4-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.84
Std Dev	3.362



B1L101S, U4-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	55.61
Std Dev	3.589

Entry

10

8

6

4

2

0

0

10

20

30

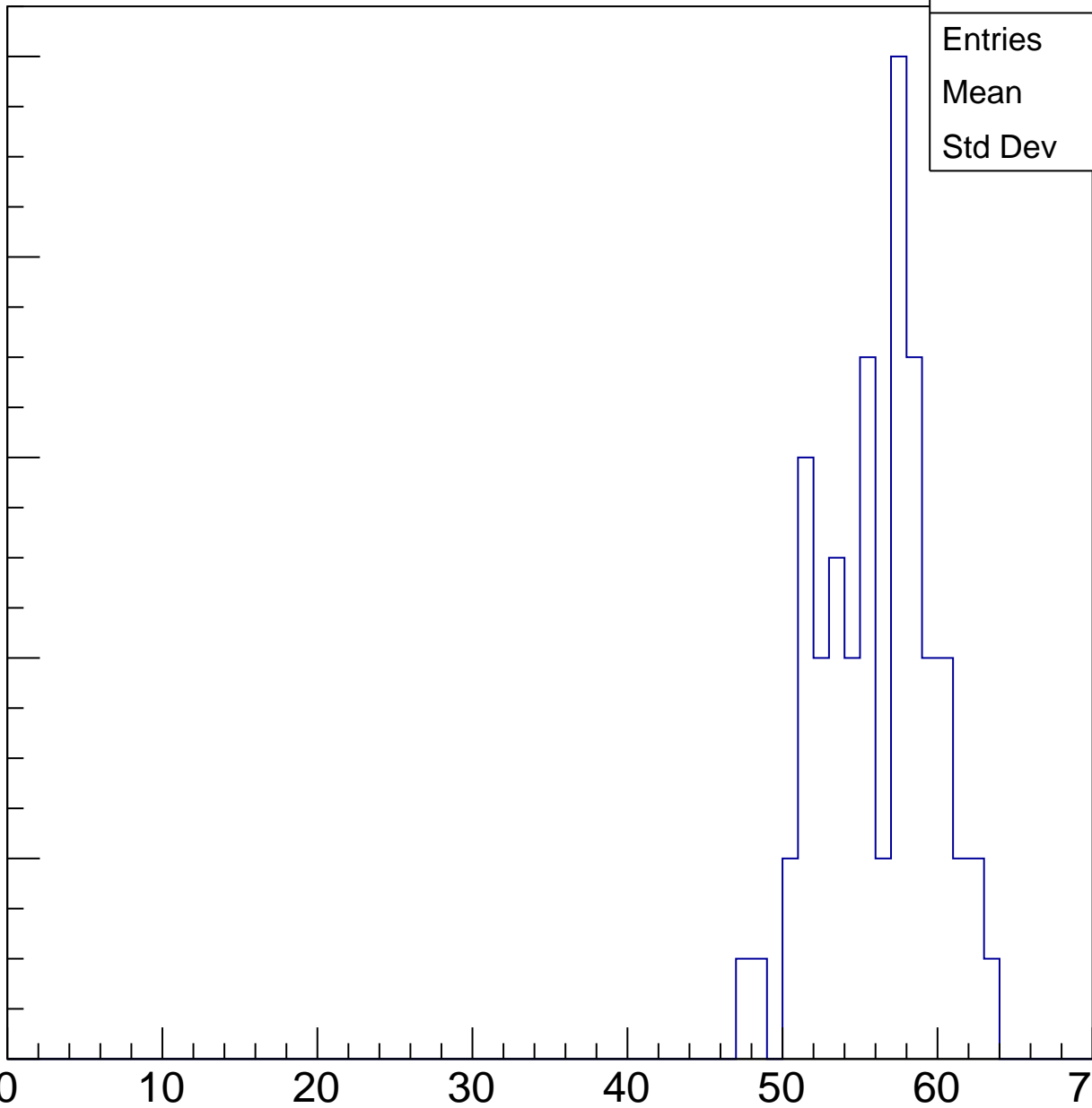
40

50

60

70

ampl



B1L101S, U4-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

59.78

Std Dev

2.418

ampl

0

10

20

30

40

50

60

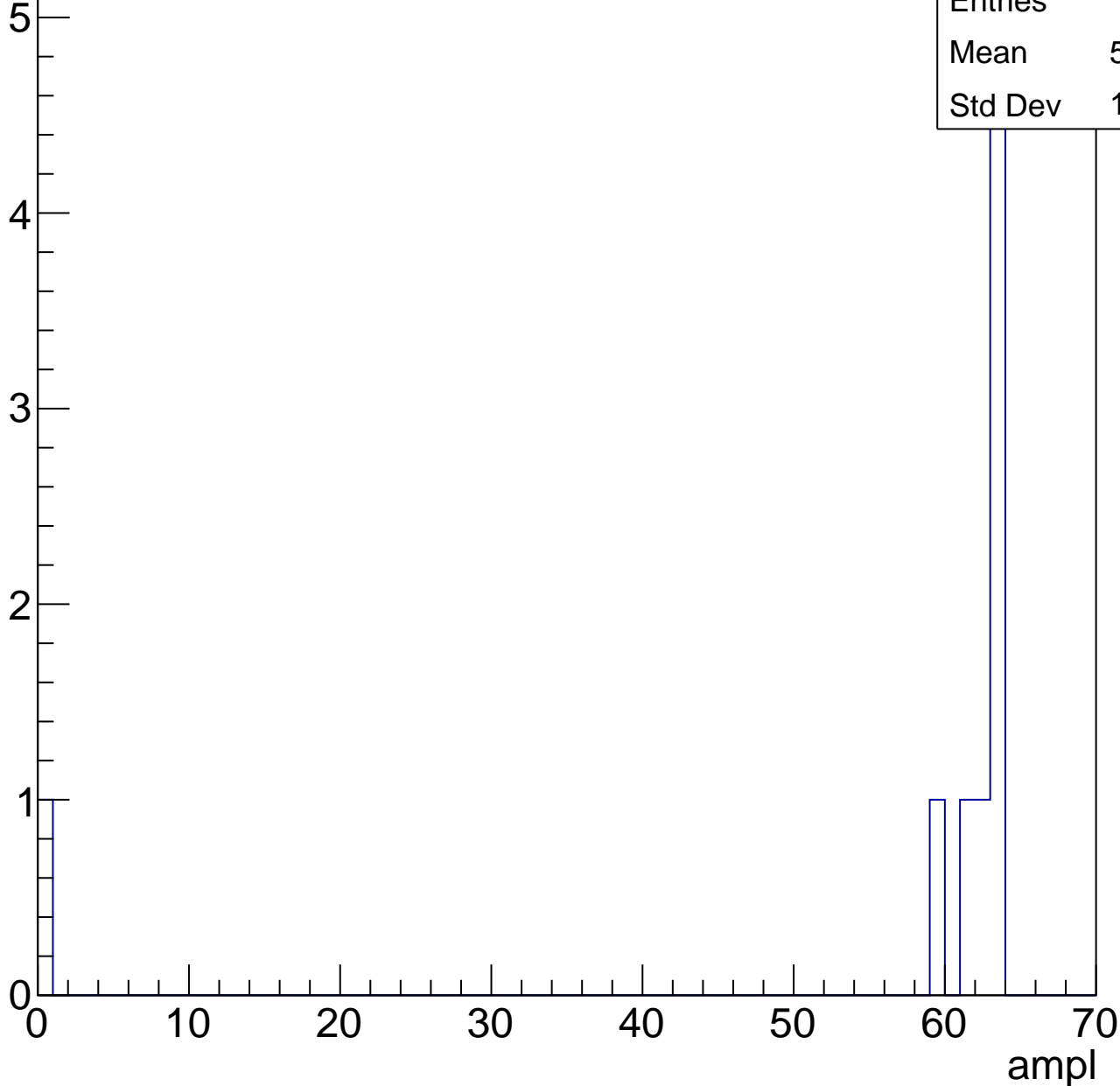
70

B1L101S, U4-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	55.22
Std Dev	19.57



B1L101S, U4-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch81, adc0

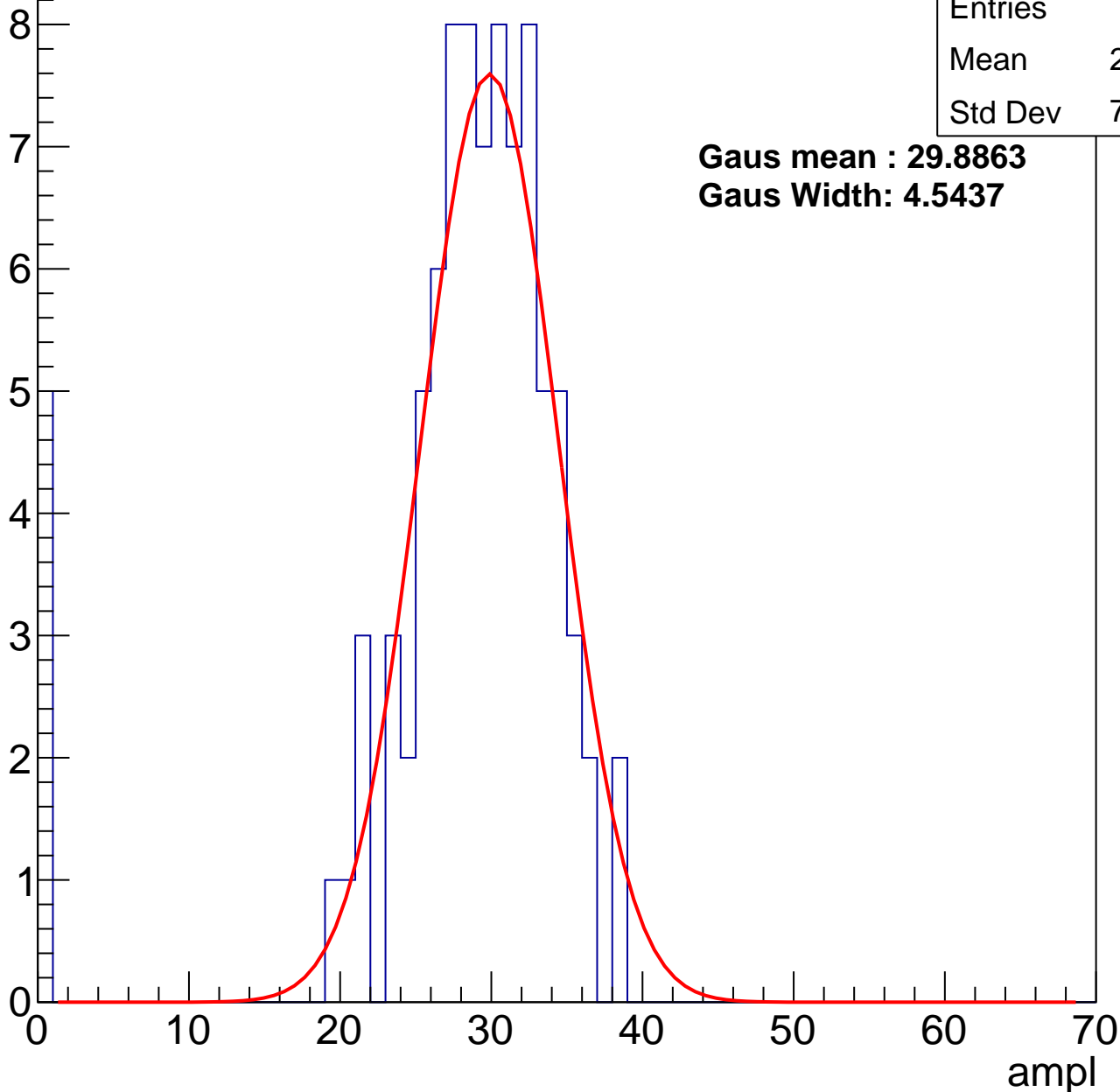
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	27.46
Std Dev	7.775

Gaus mean : 29.8863

Gaus Width: 4.5437



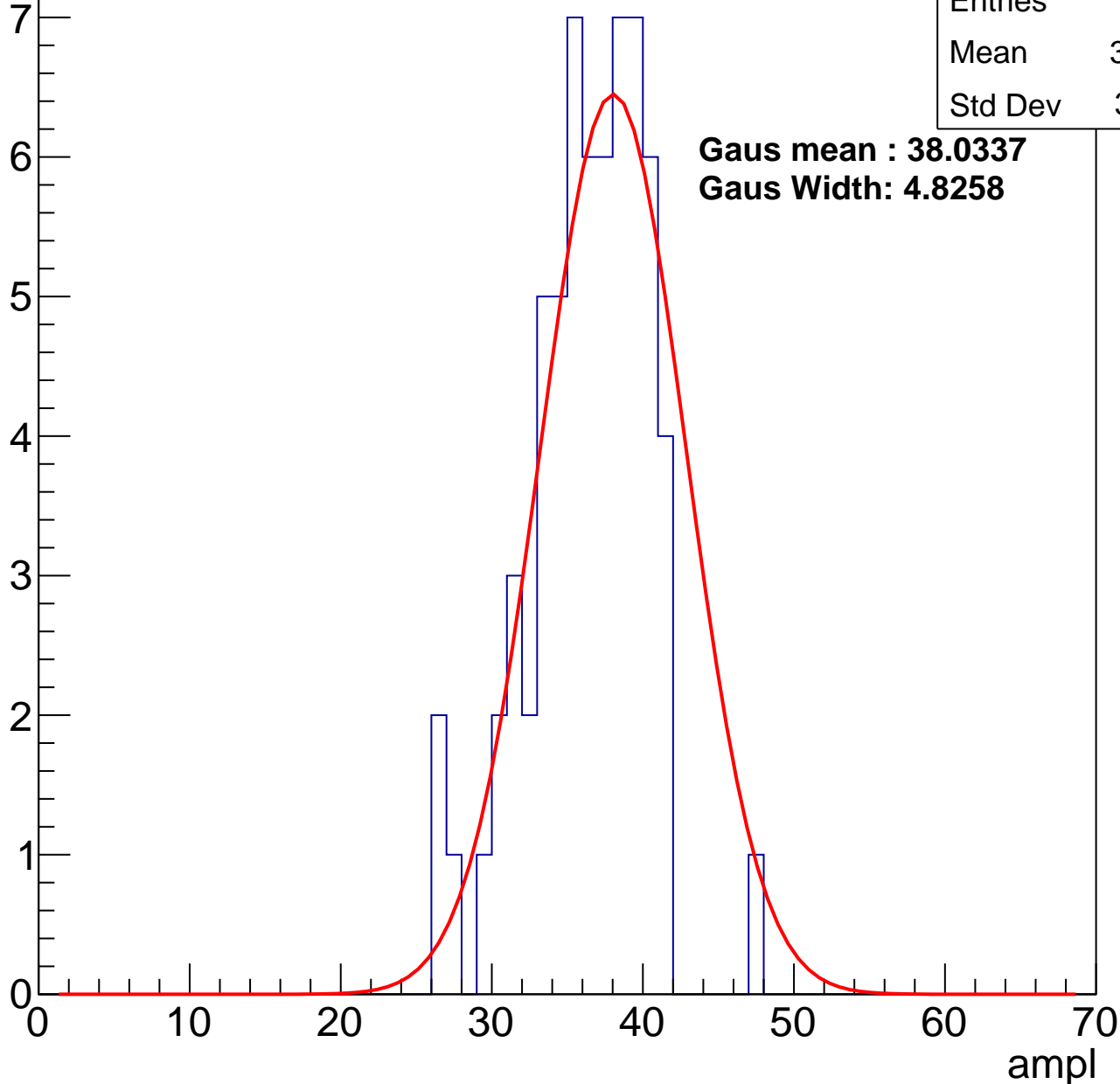
B1L101S, U4-ch81, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	35.89
Std Dev	3.911

Gaus mean : 38.0337
Gaus Width: 4.8258



B1L101S, U4-ch81, adc2

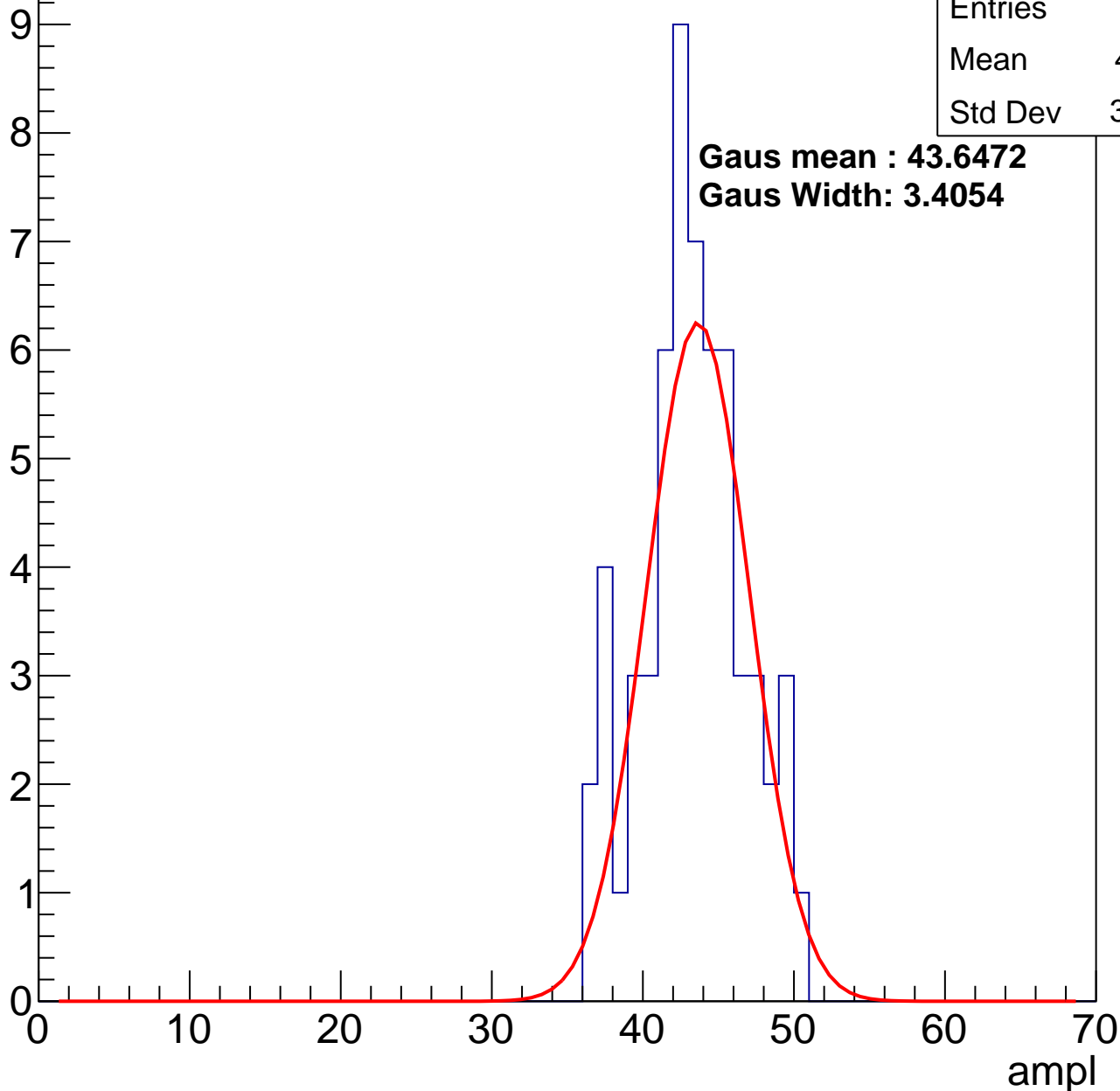
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.81
Std Dev	3.407

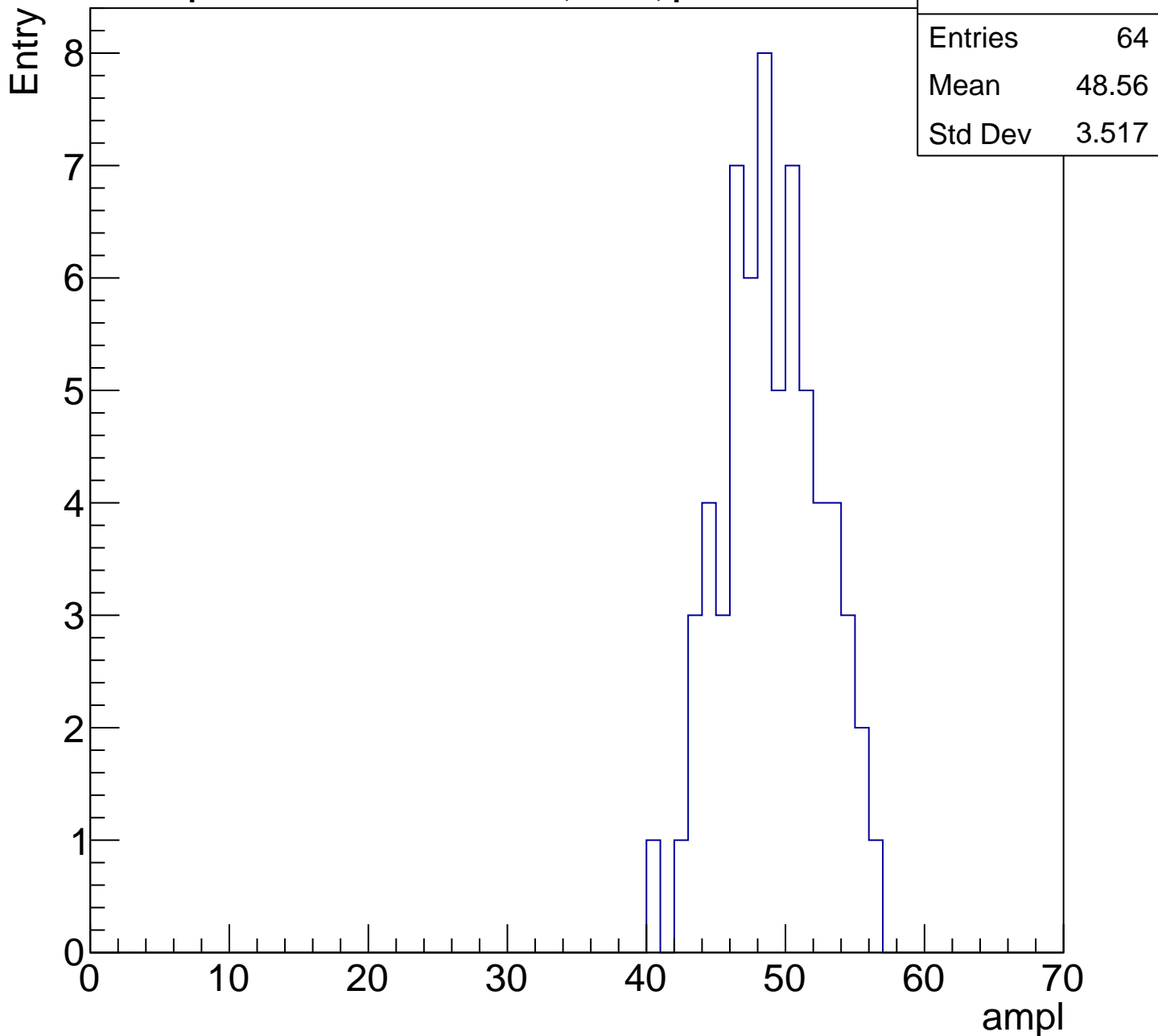
Gaus mean : 43.6472

Gaus Width: 3.4054



B1L101S, U4-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

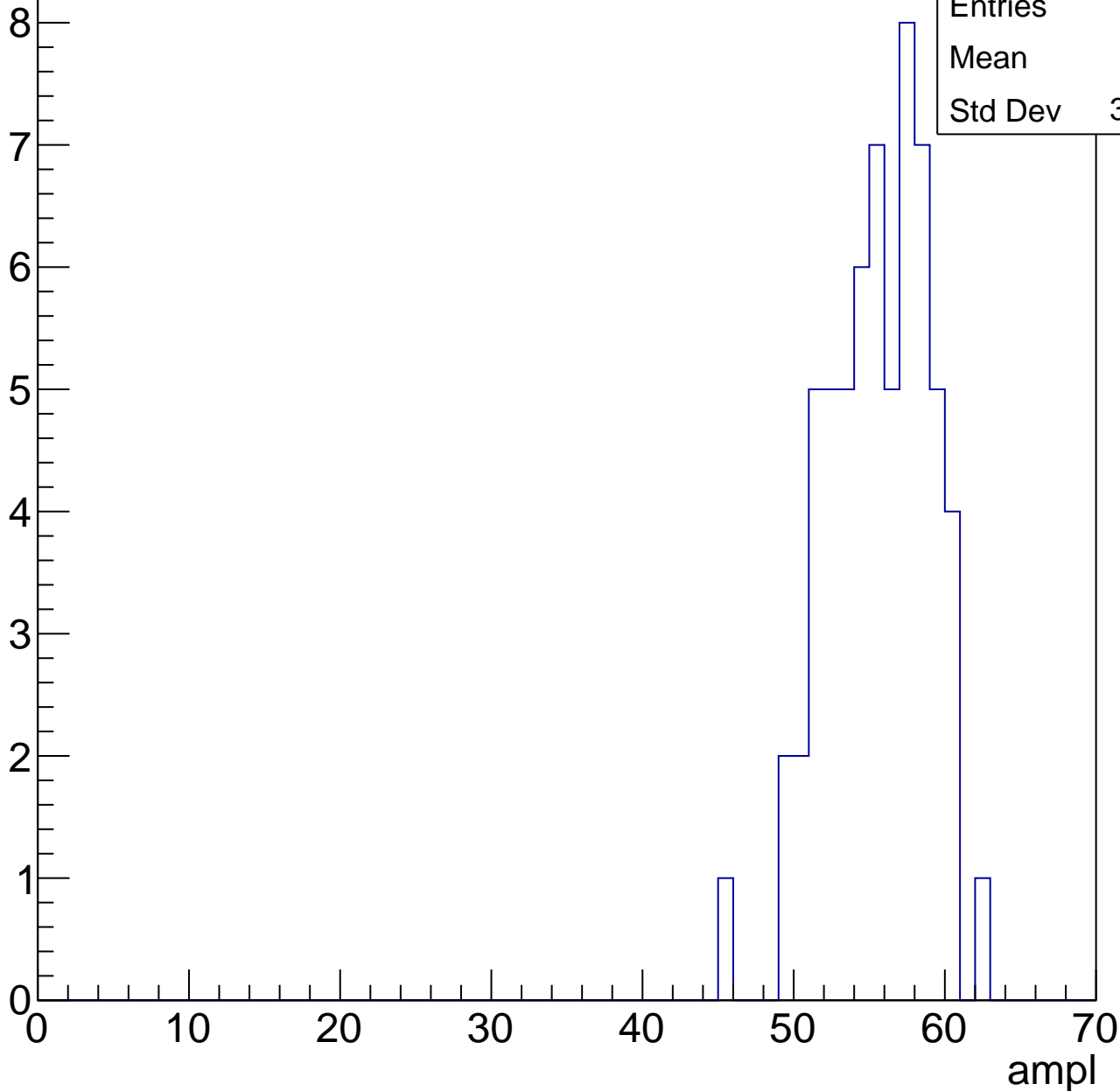


B1L101S, U4-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.1
Std Dev	3.337

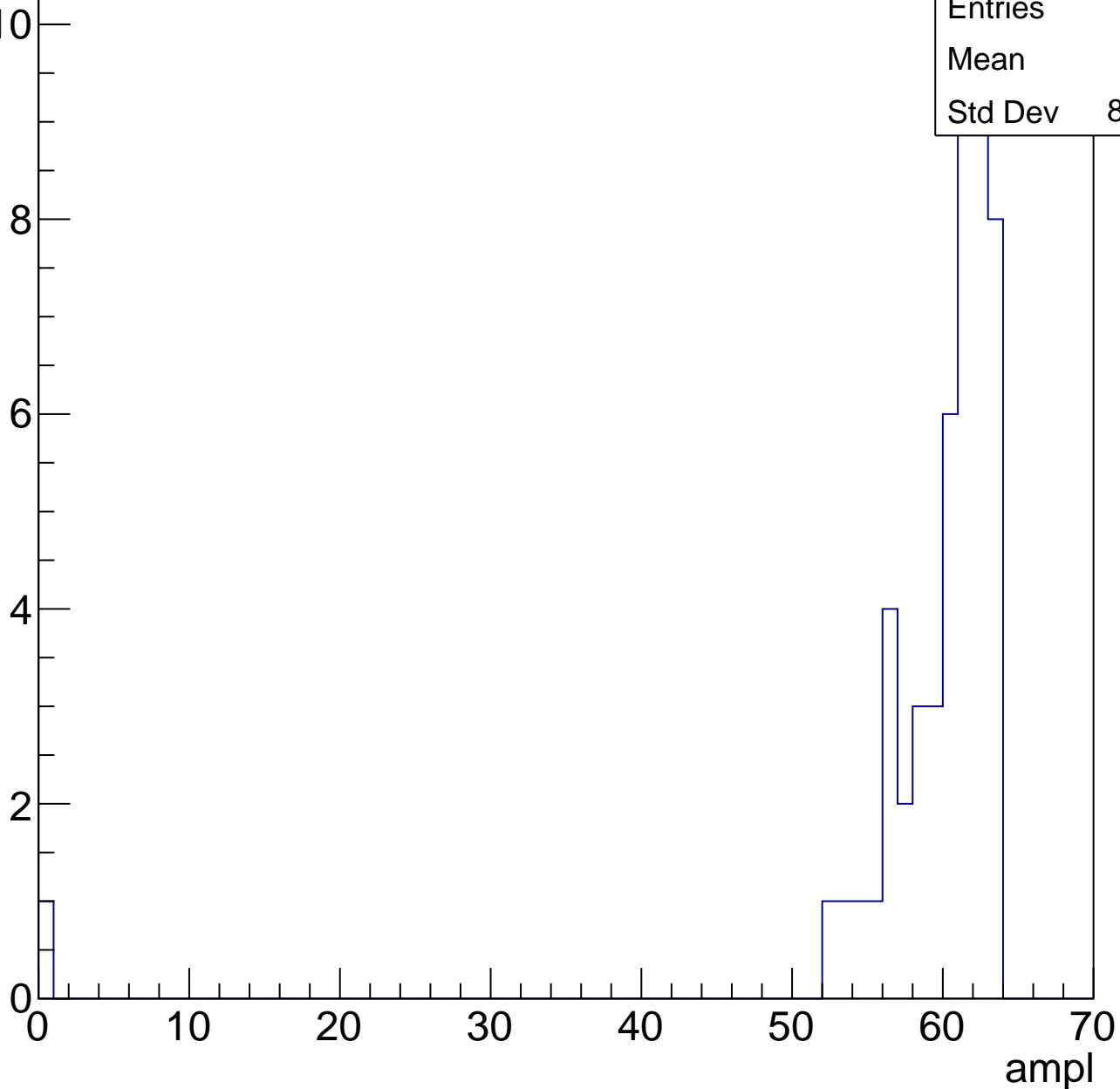


B1L101S, U4-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.7
Std Dev	8.837



B1L101S, U4-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	61
Std Dev	2

B1L101S, U4-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch82, adc0

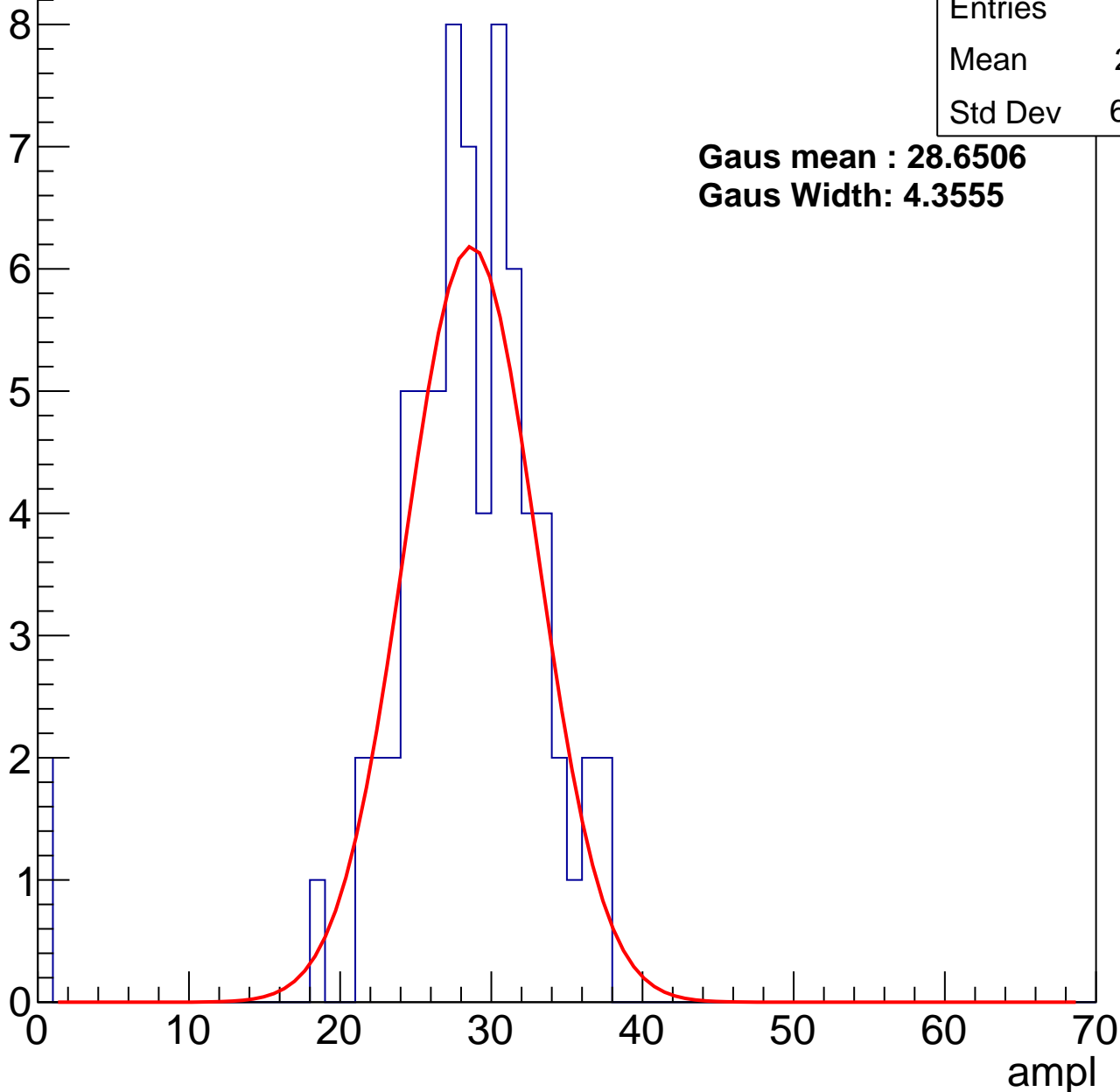
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.61
Std Dev	6.118

Gaus mean : 28.6506

Gaus Width: 4.3555



B1L101S, U4-ch82, adc1

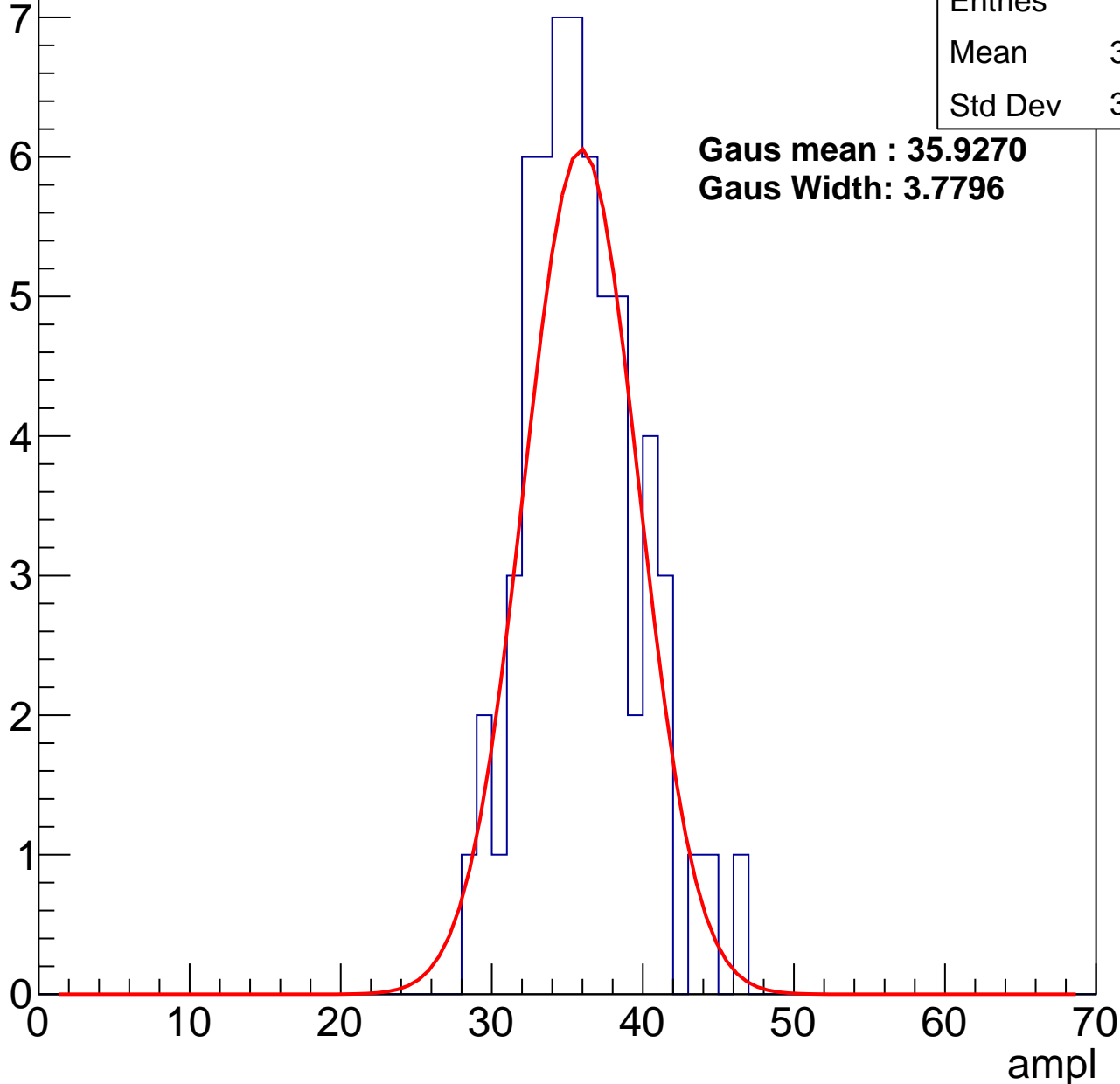
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.52
Std Dev	3.709

Gaus mean : 35.9270

Gaus Width: 3.7796



B1L101S, U4-ch82, adc2

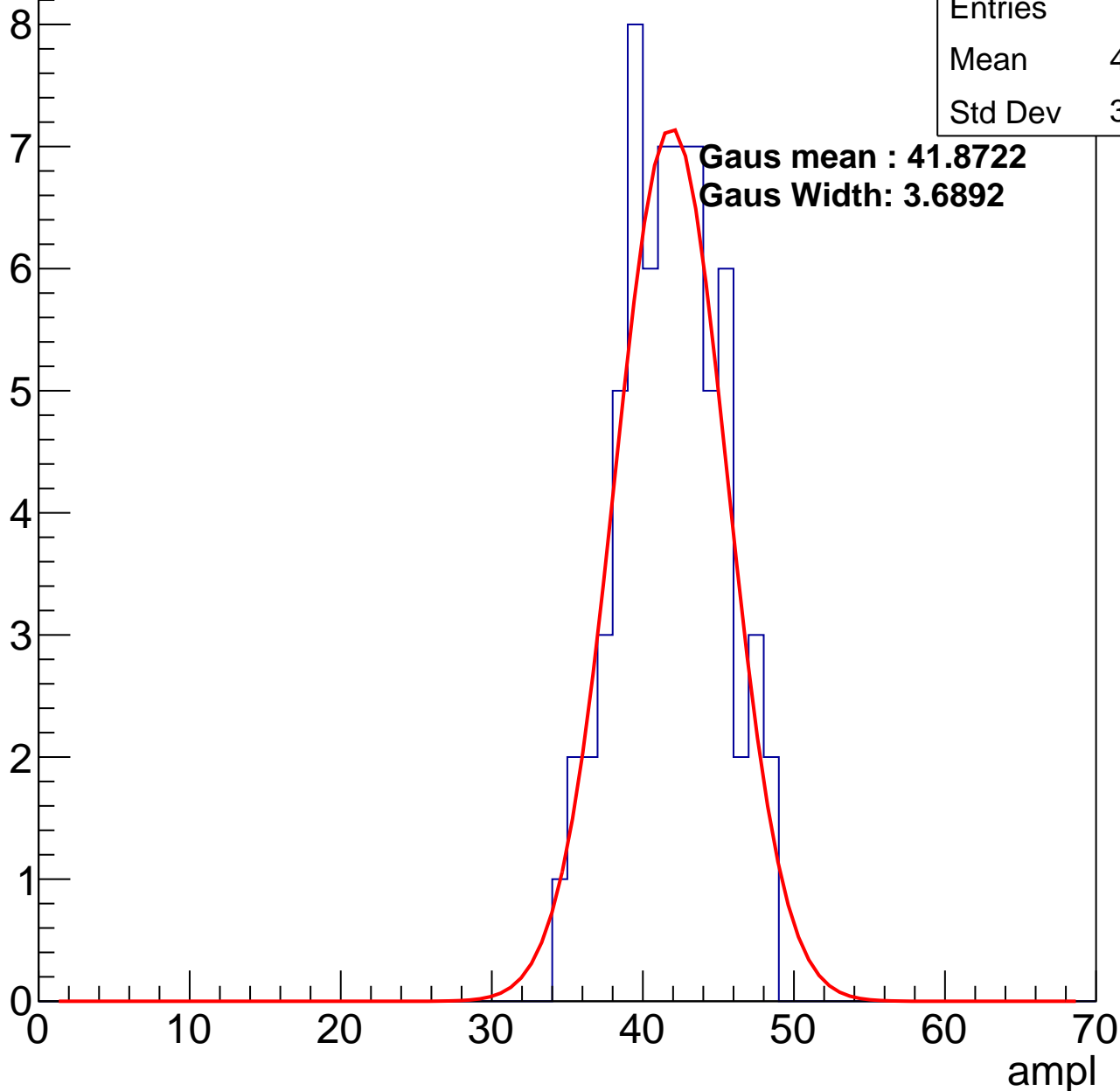
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.36
Std Dev	3.315

Gaus mean : 41.8722

Gaus Width: 3.6892

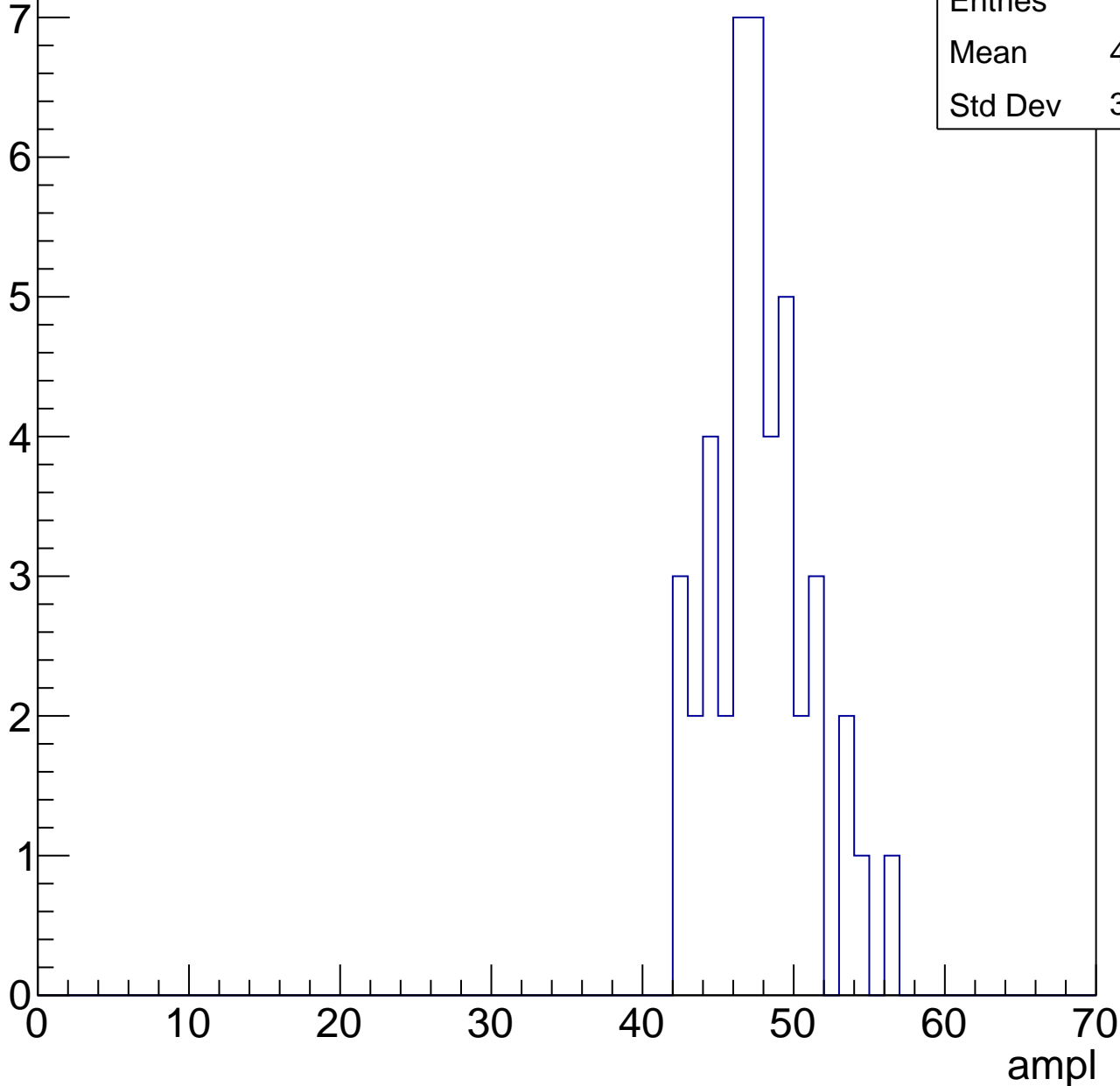


B1L101S, U4-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	47.33
Std Dev	3.219



B1L101S, U4-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

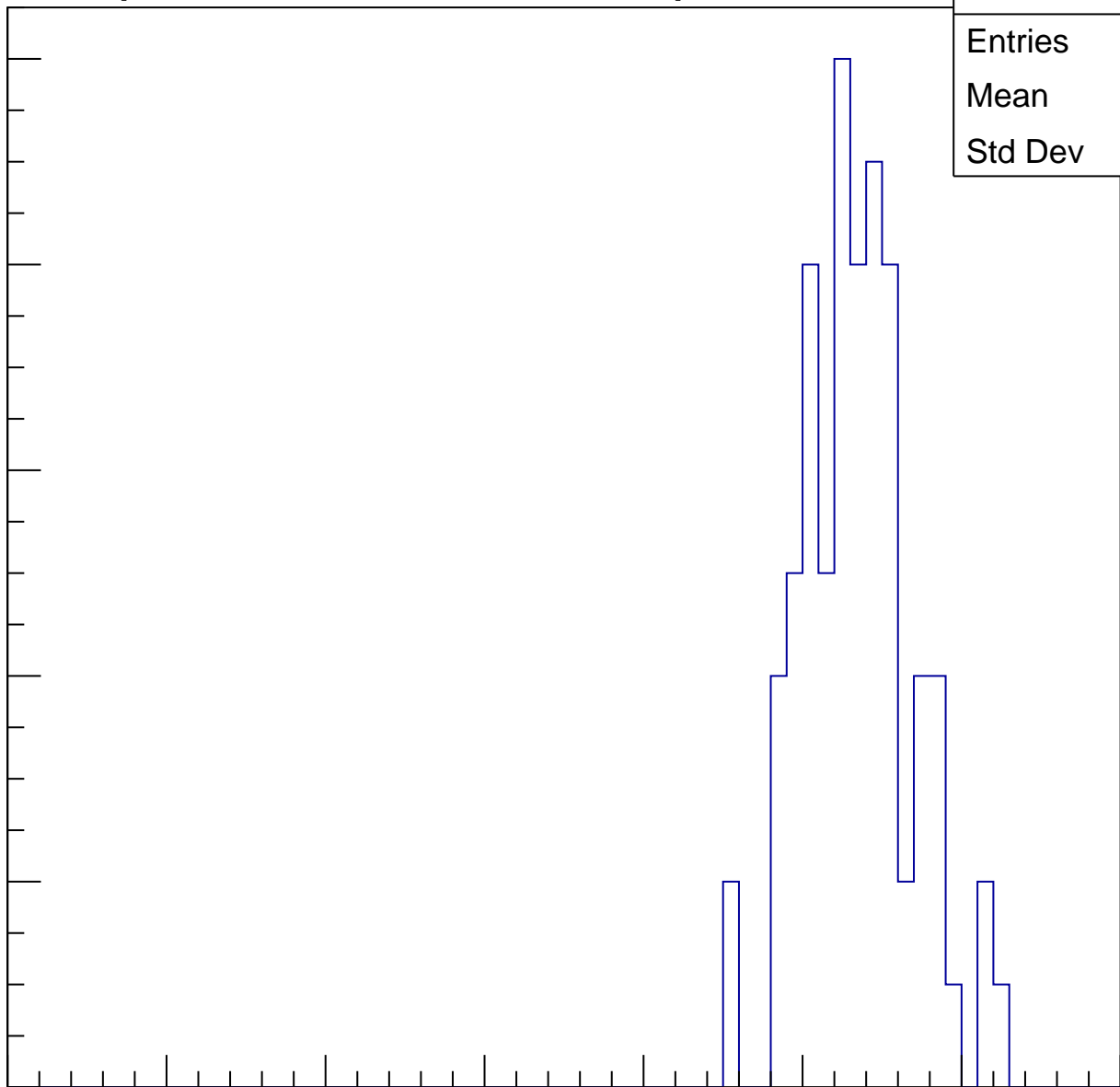
Entries	73
Mean	52.97
Std Dev	3.468

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

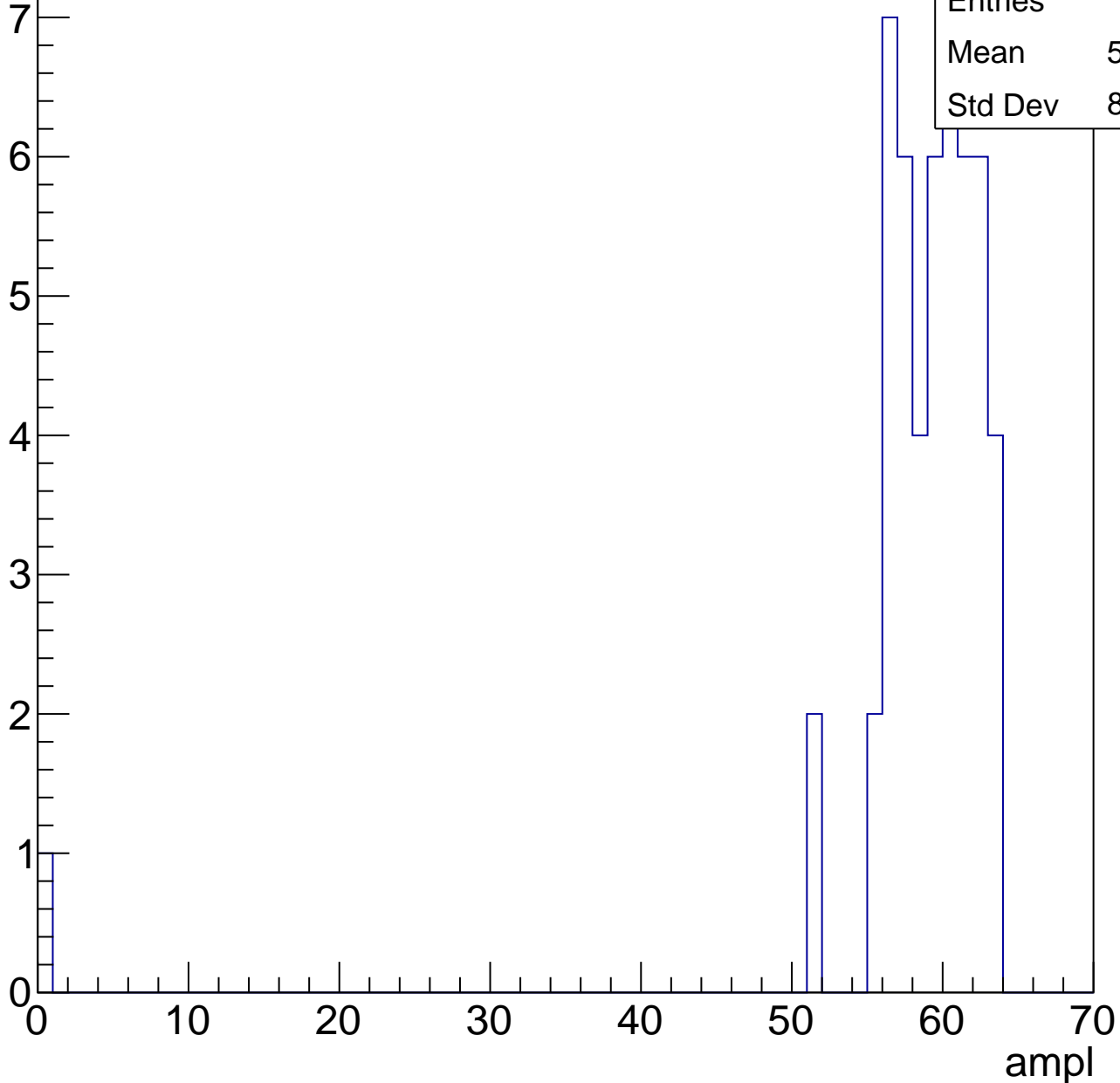


B1L101S, U4-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.69
Std Dev	8.624

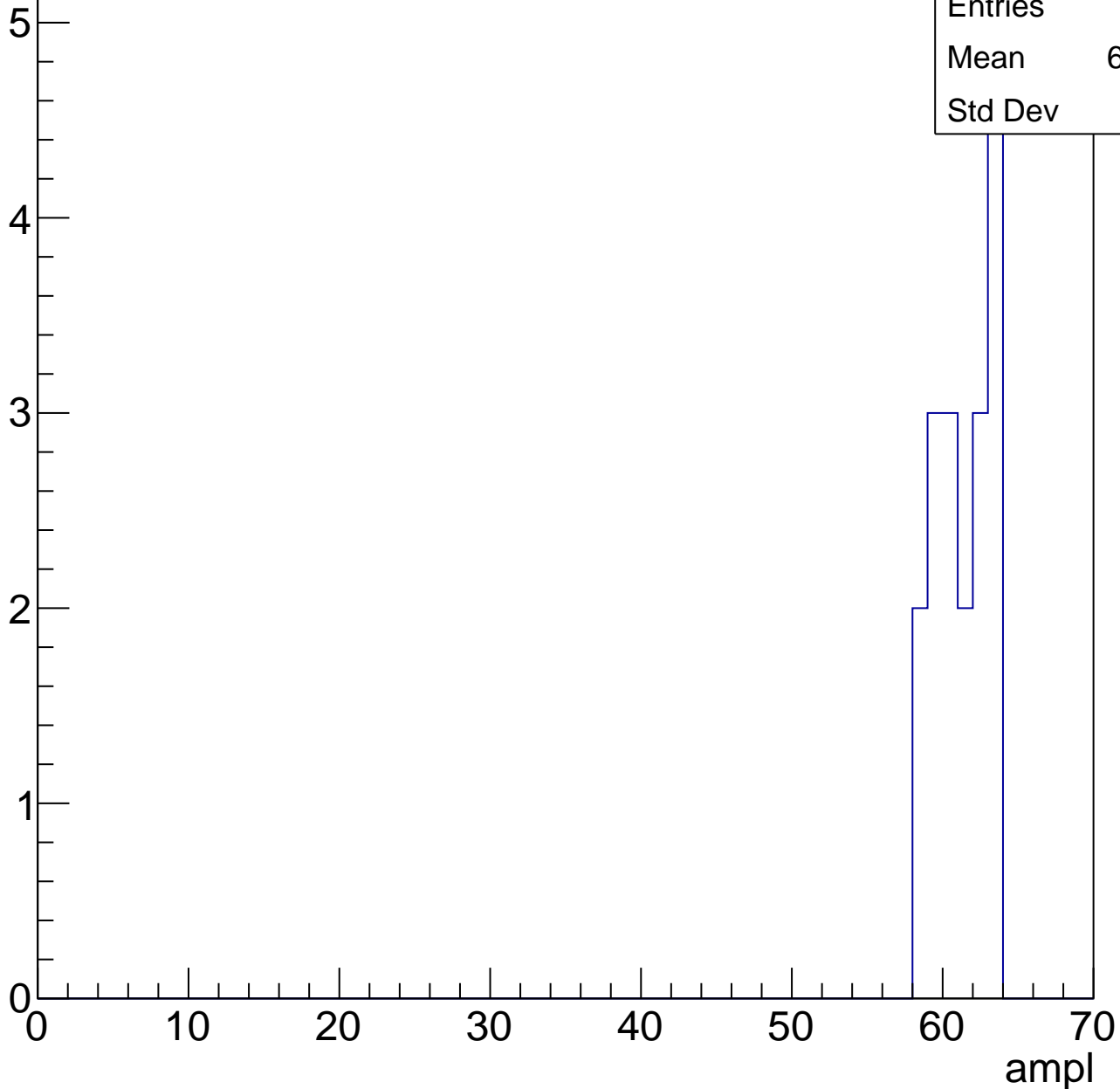


B1L101S, U4-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	60.89
Std Dev	1.76



B1L101S, U4-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch83, adc0

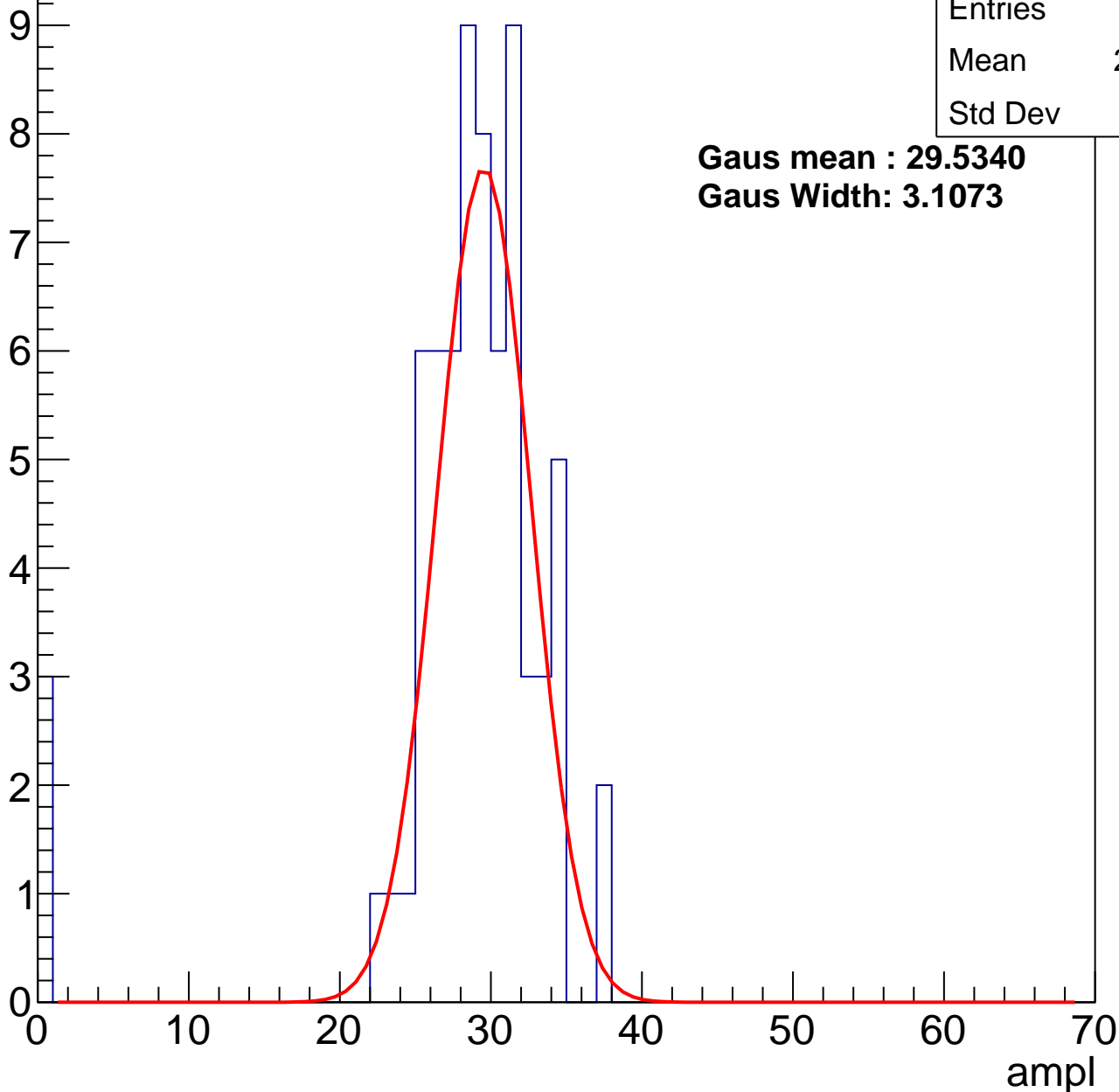
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	27.81
Std Dev	6.69

Gaus mean : 29.5340

Gaus Width: 3.1073



B1L101S, U4-ch83, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	36.13
Std Dev	3.505

Gaus mean : 37.0298

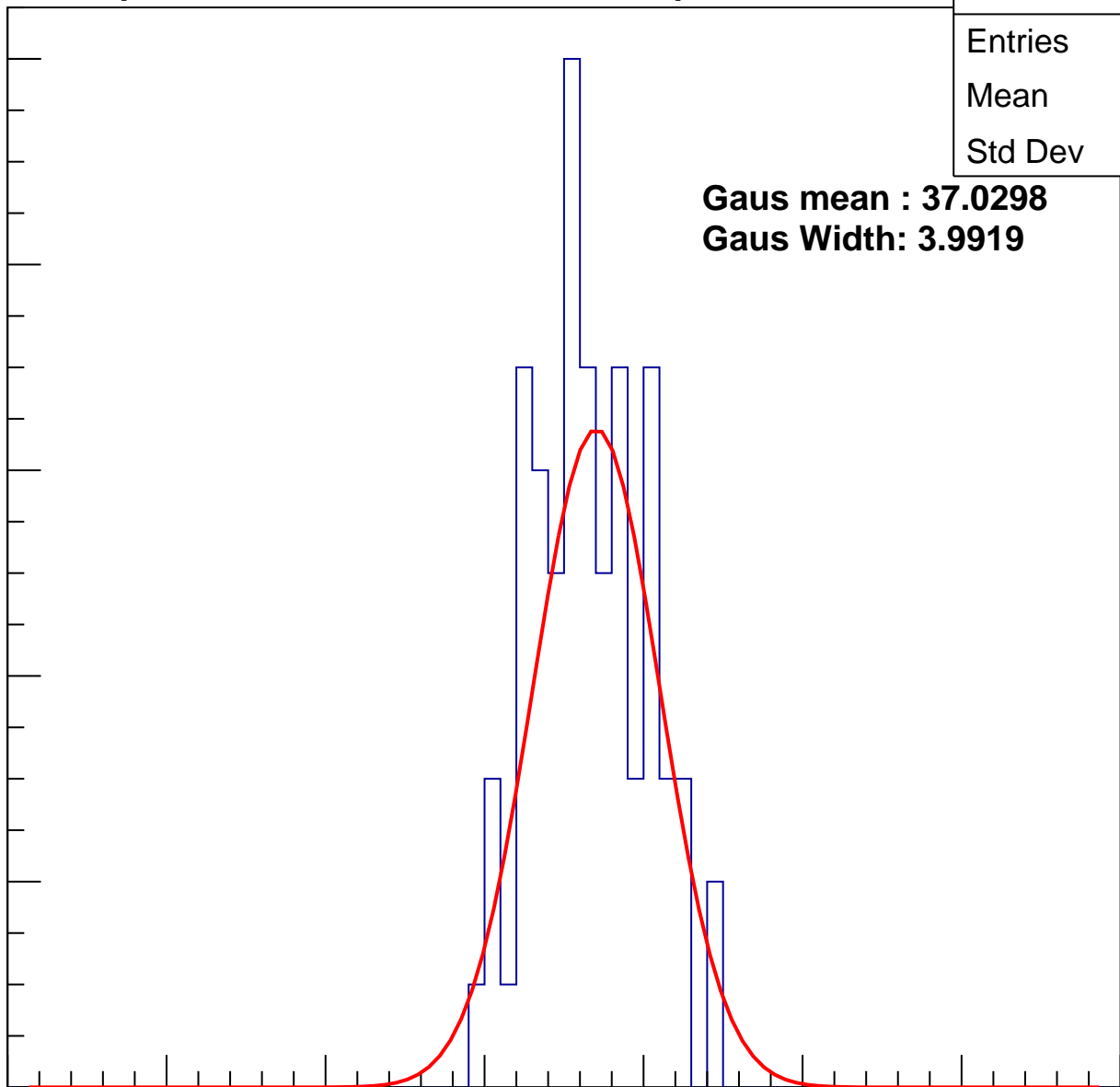
Gaus Width: 3.9919

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch83, adc2

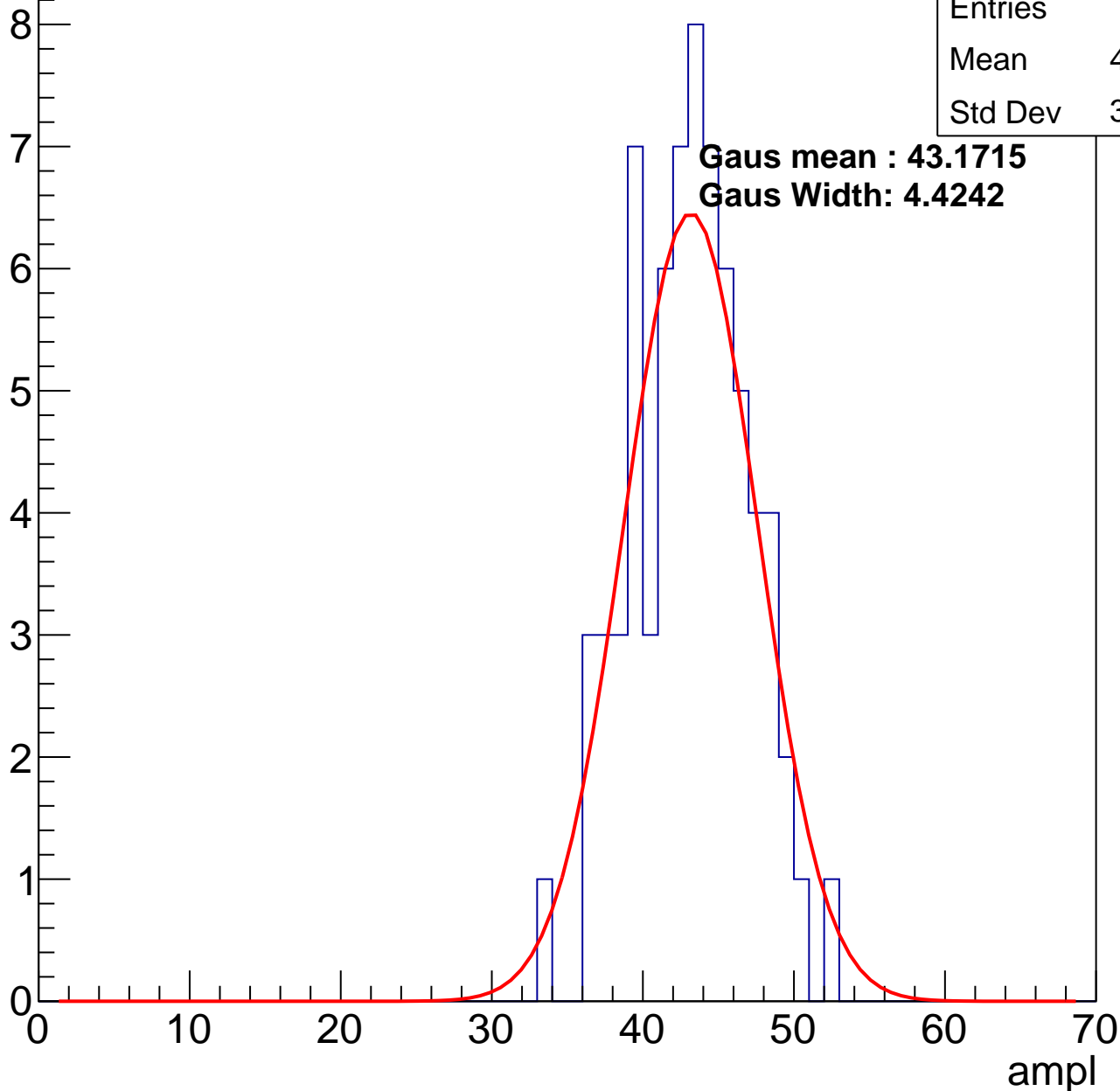
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.69
Std Dev	3.818

Gaus mean : 43.1715

Gaus Width: 4.4242

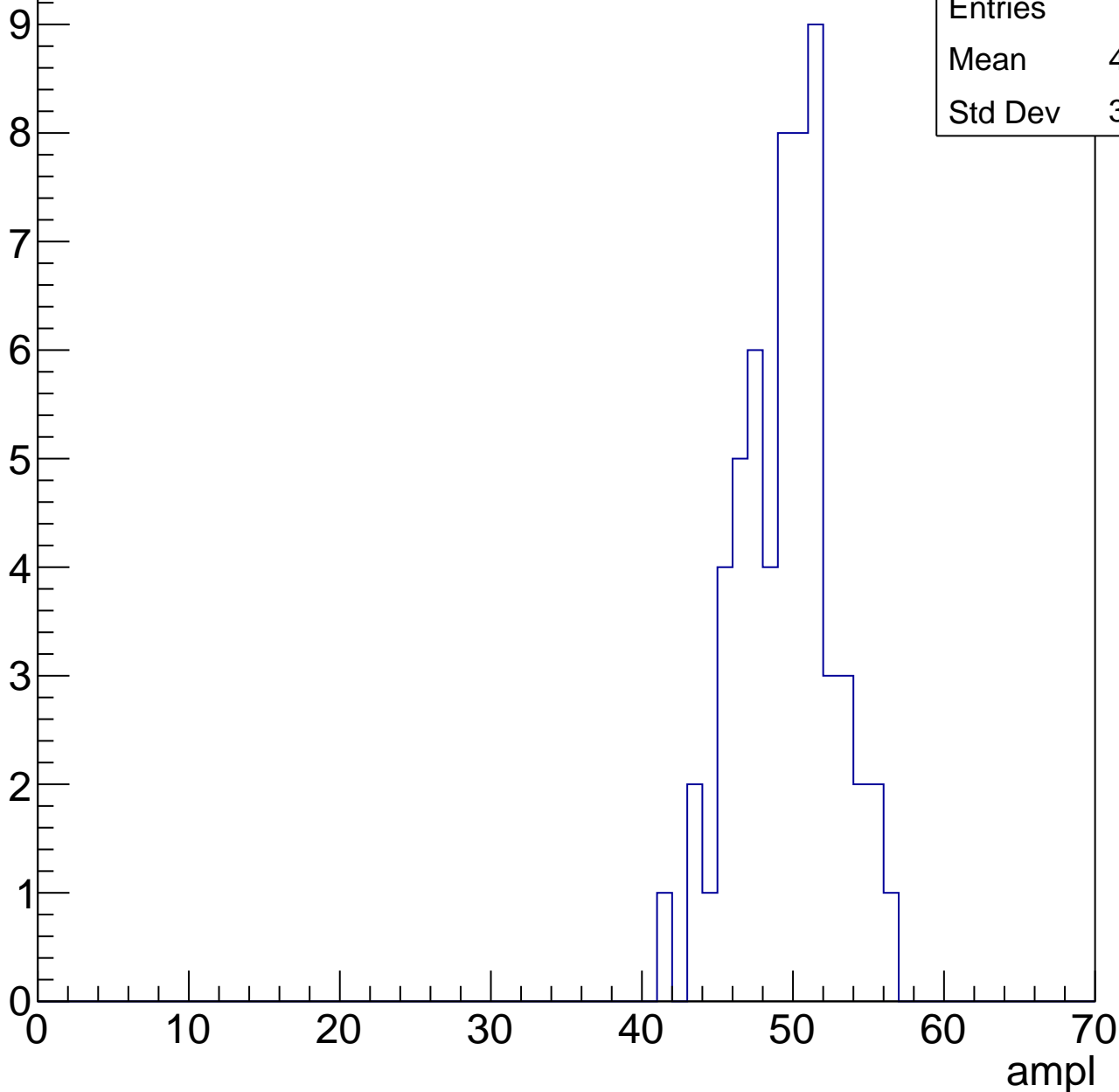


B1L101S, U4-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.07
Std Dev	3.156

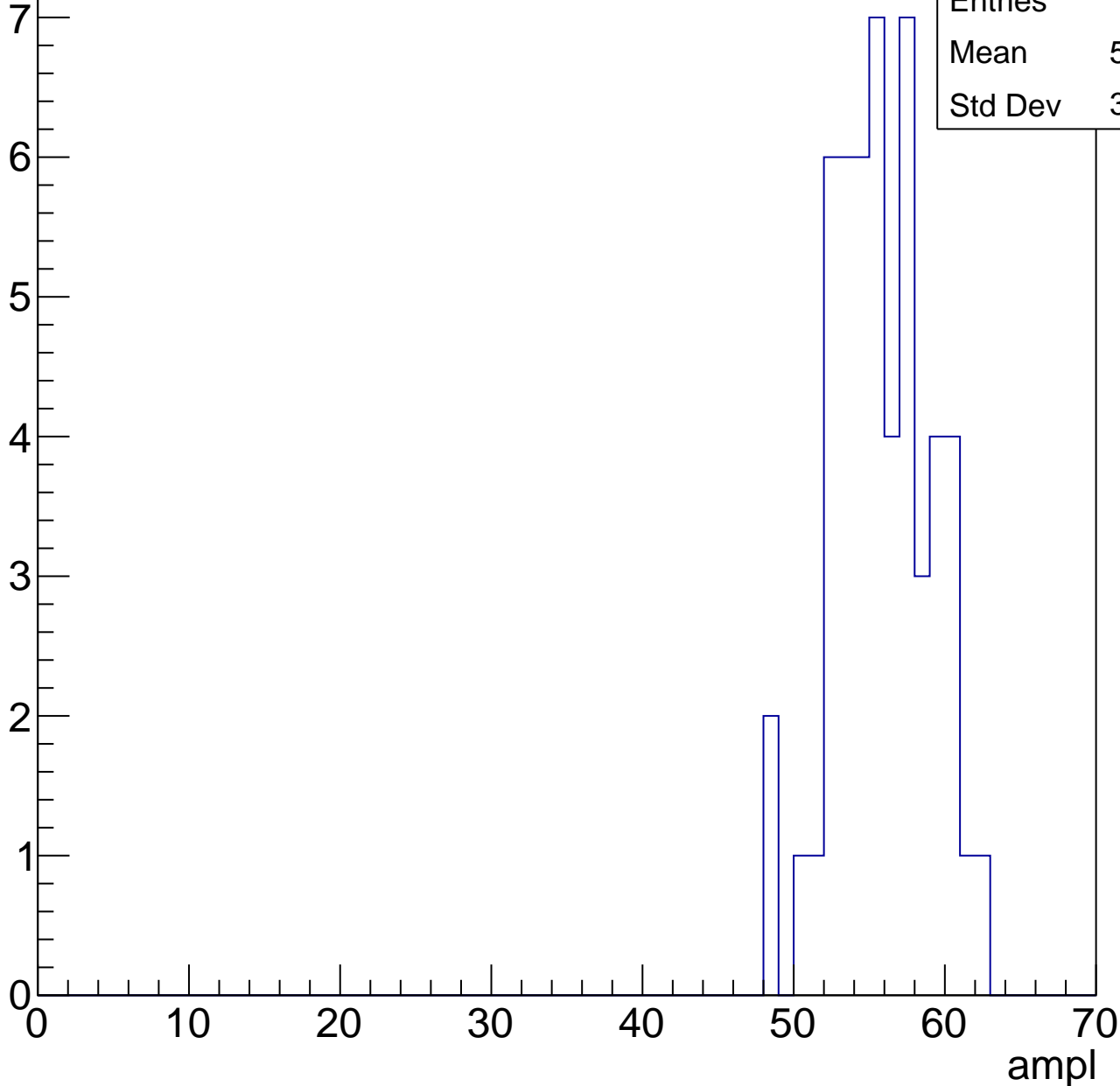


B1L101S, U4-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.32
Std Dev	3.149

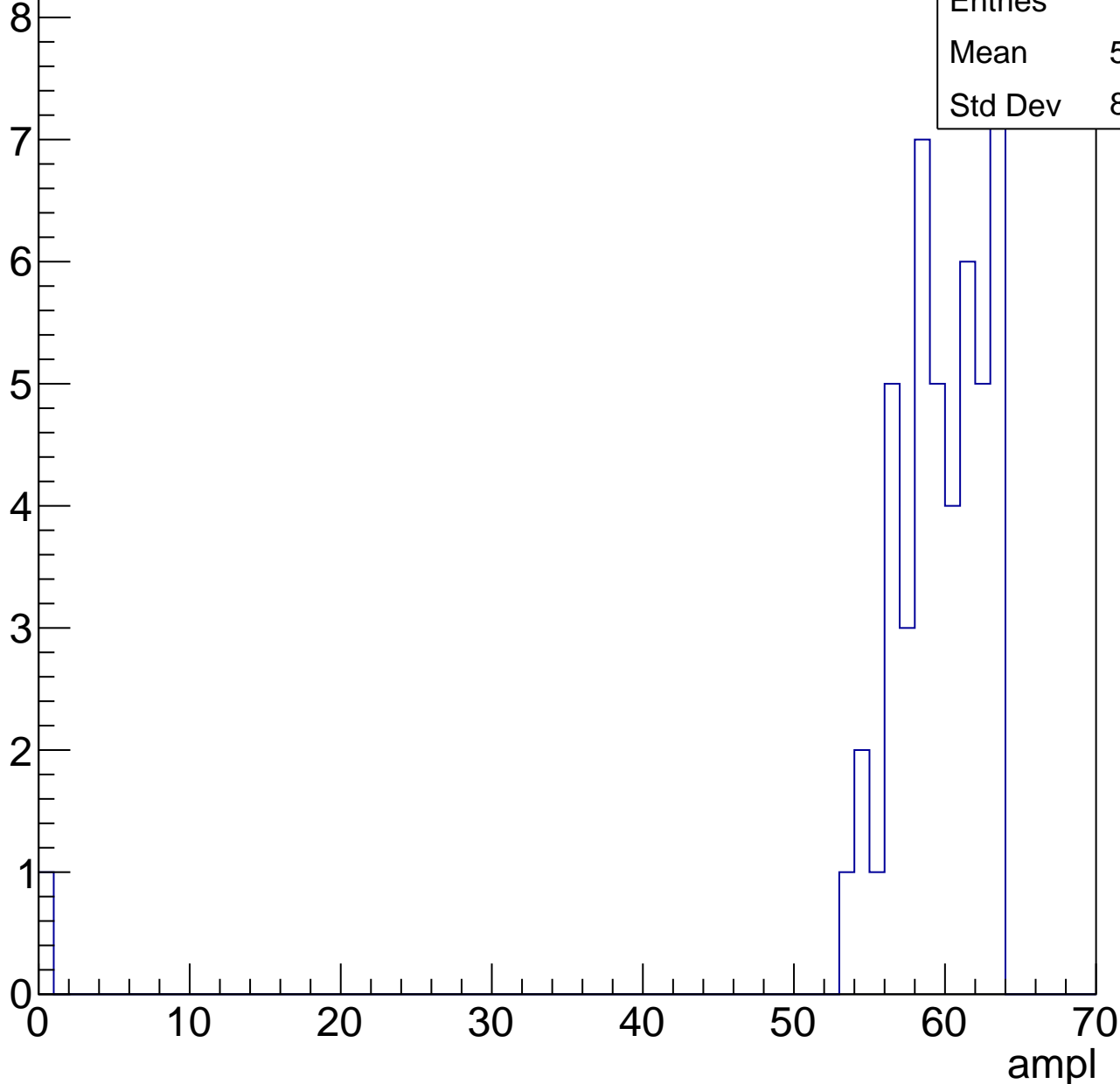


B1L101S, U4-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

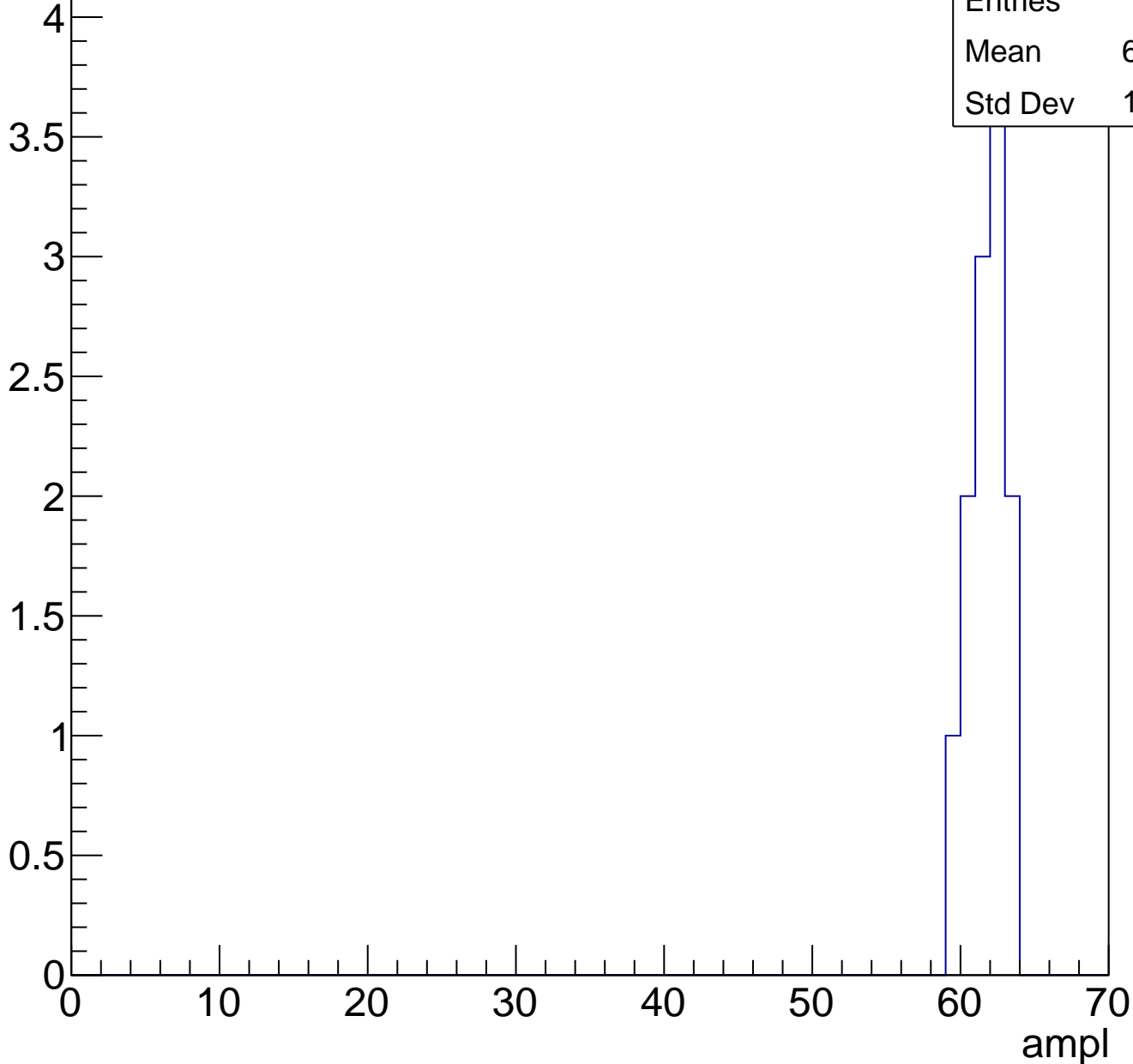
Entries	48
Mean	58.08
Std Dev	8.907



B1L101S, U4-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch84, adc0

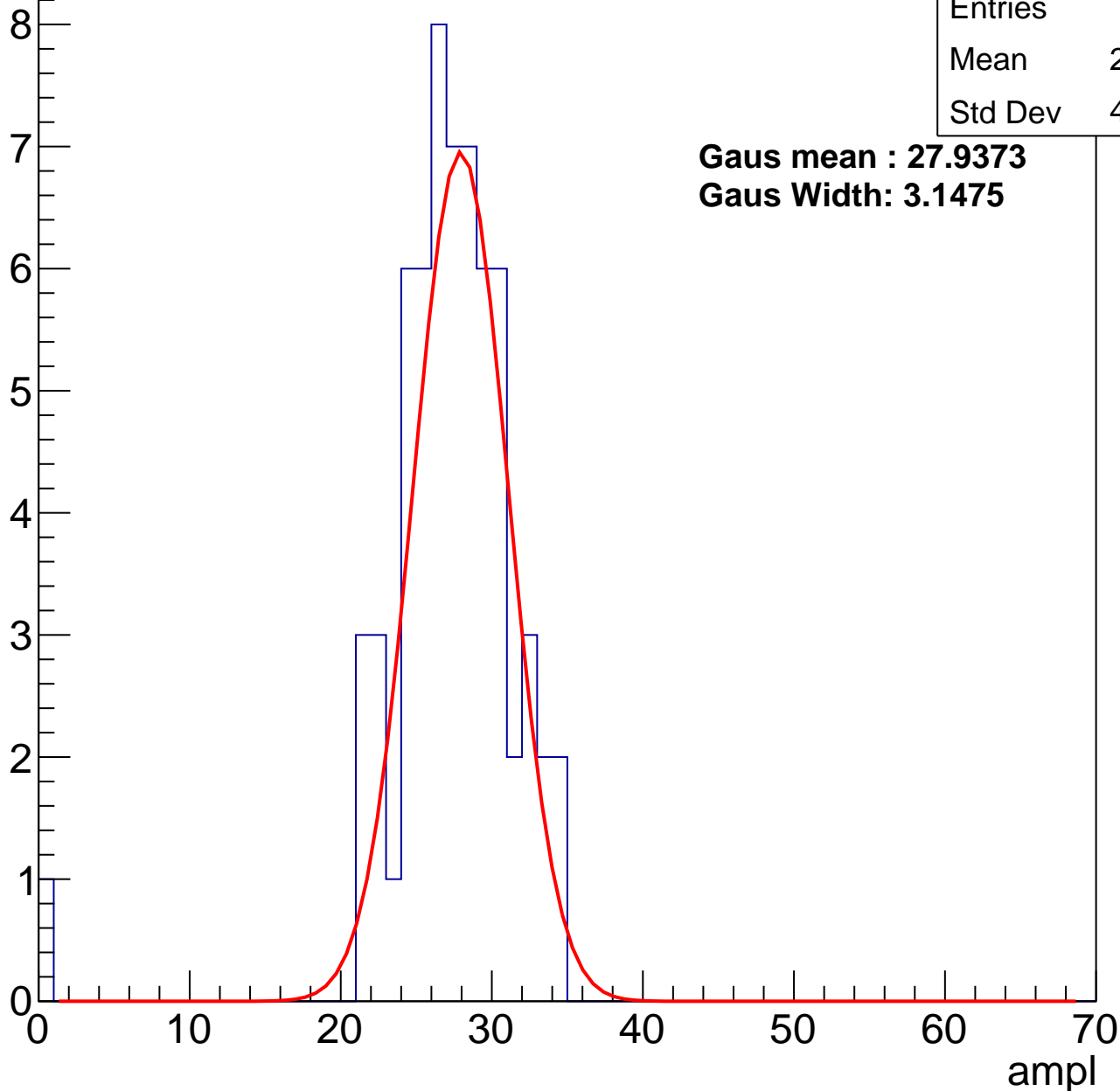
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	26.75
Std Dev	4.667

Gaus mean : 27.9373

Gaus Width: 3.1475



B1L101S, U4-ch84, adc1

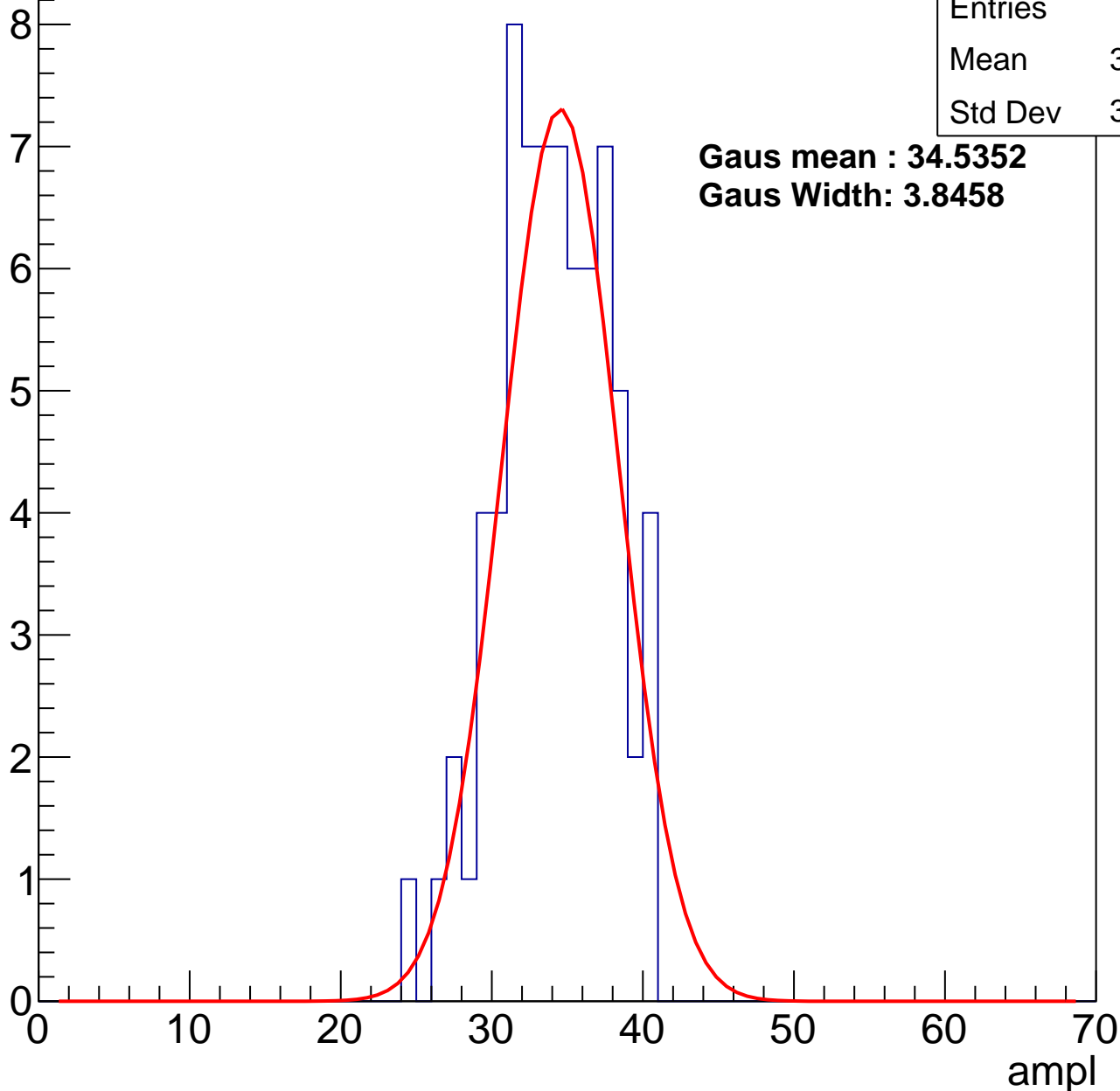
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	33.64
Std Dev	3.587

Gaus mean : 34.5352

Gaus Width: 3.8458



B1L101S, U4-ch84, adc2

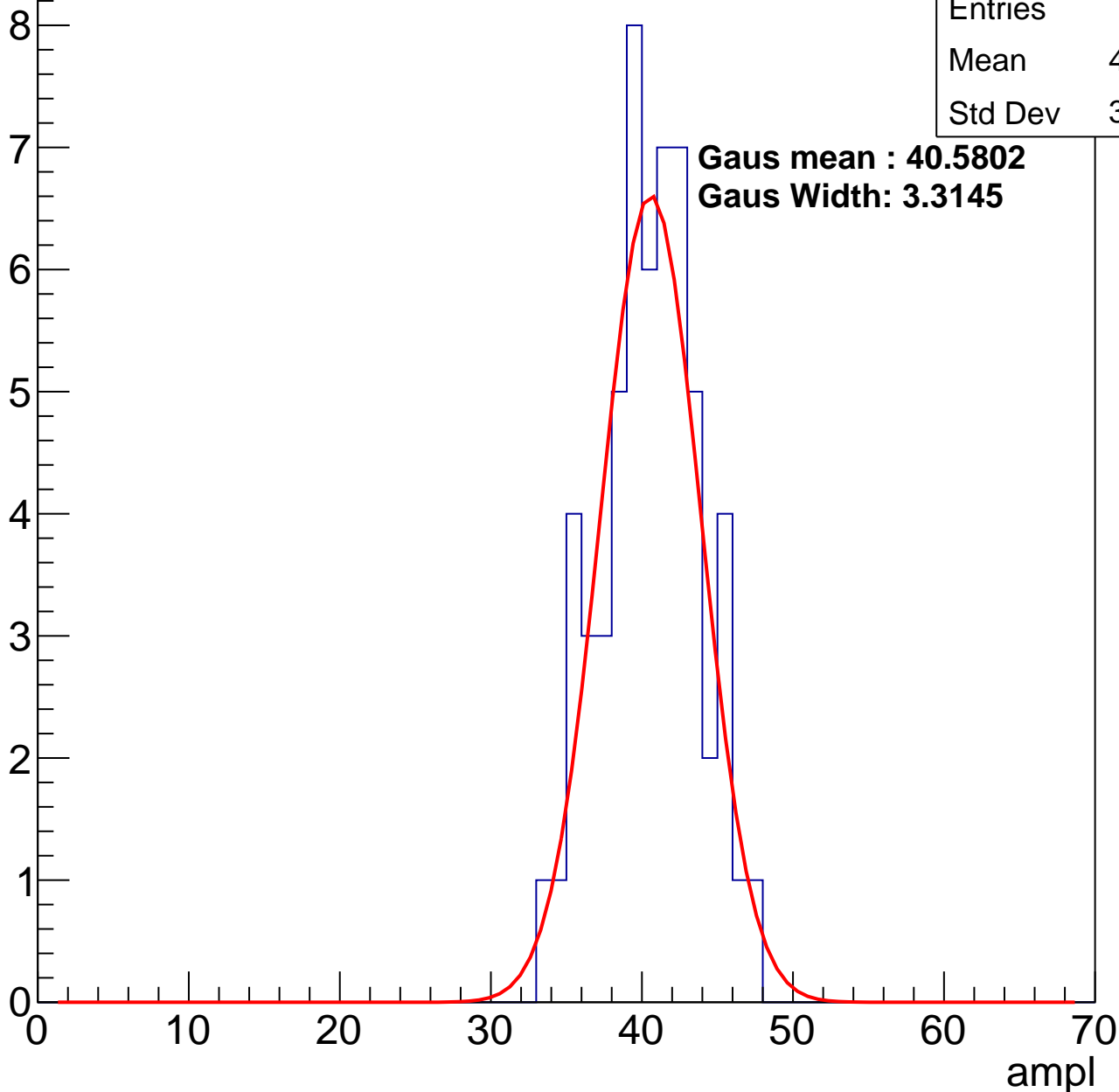
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	40.09
Std Dev	3.175

Gaus mean : 40.5802

Gaus Width: 3.3145



B1L101S, U4-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

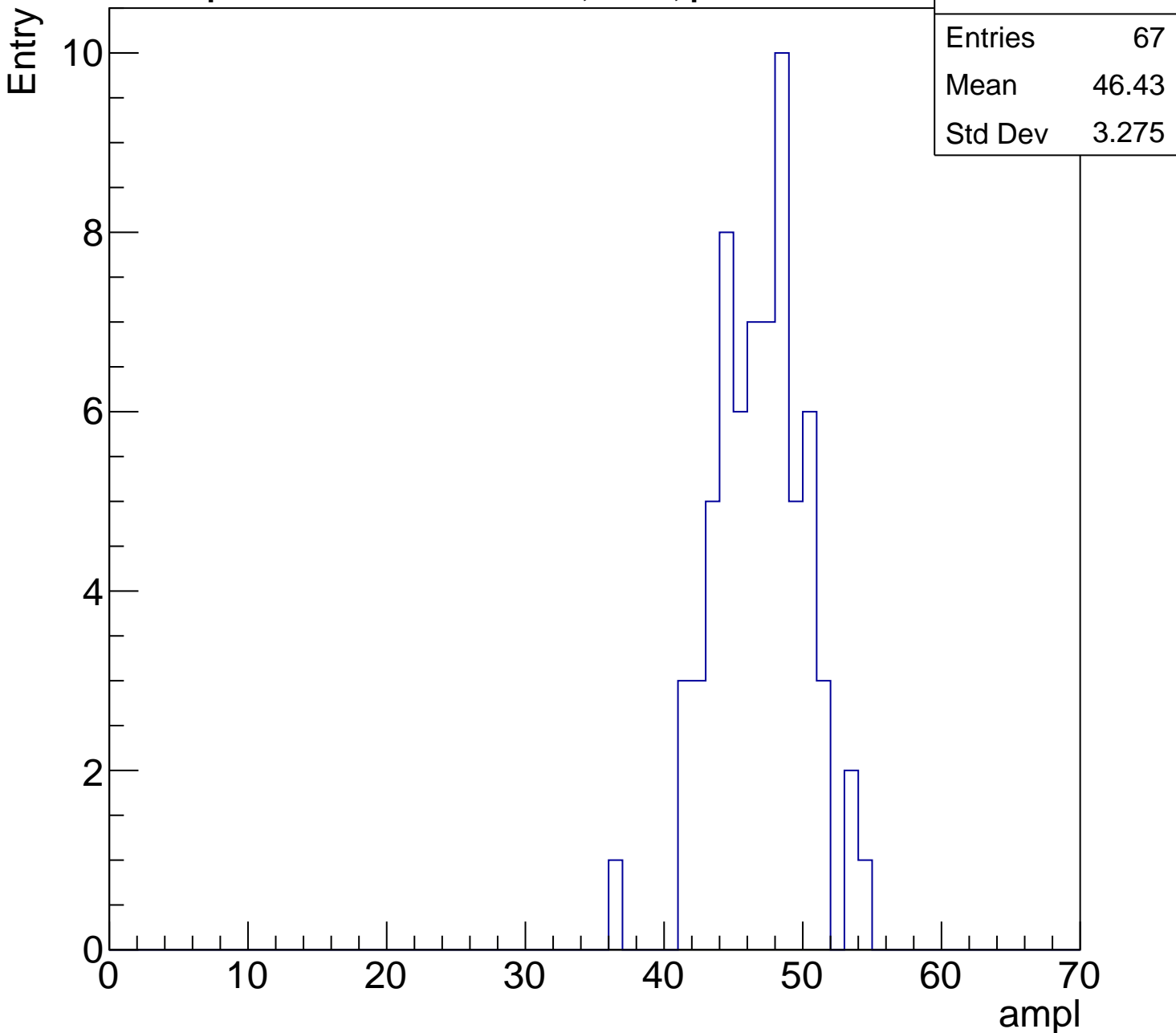
Entries	67
Mean	46.43
Std Dev	3.275

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

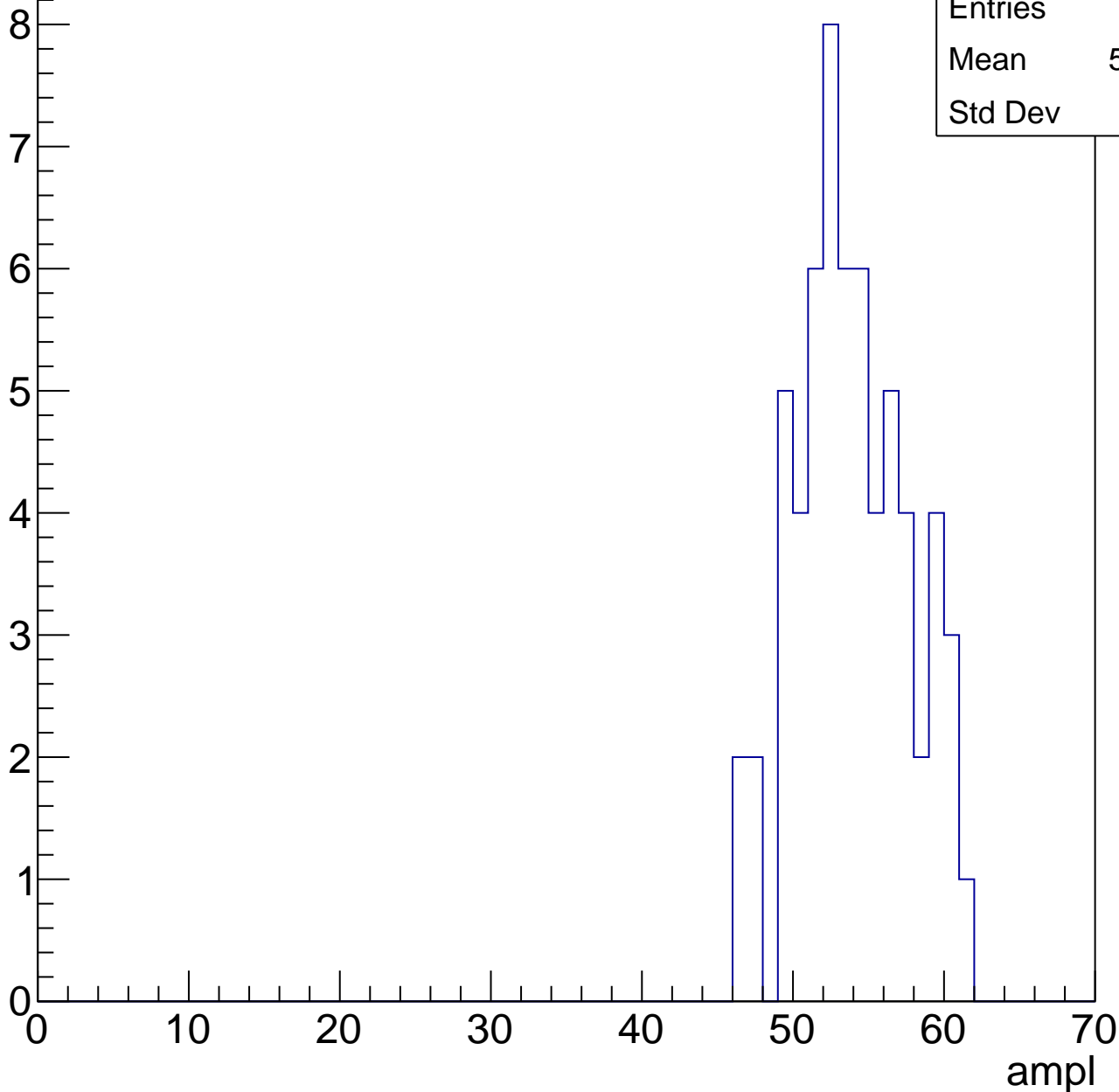


B1L101S, U4-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	53.48
Std Dev	3.68

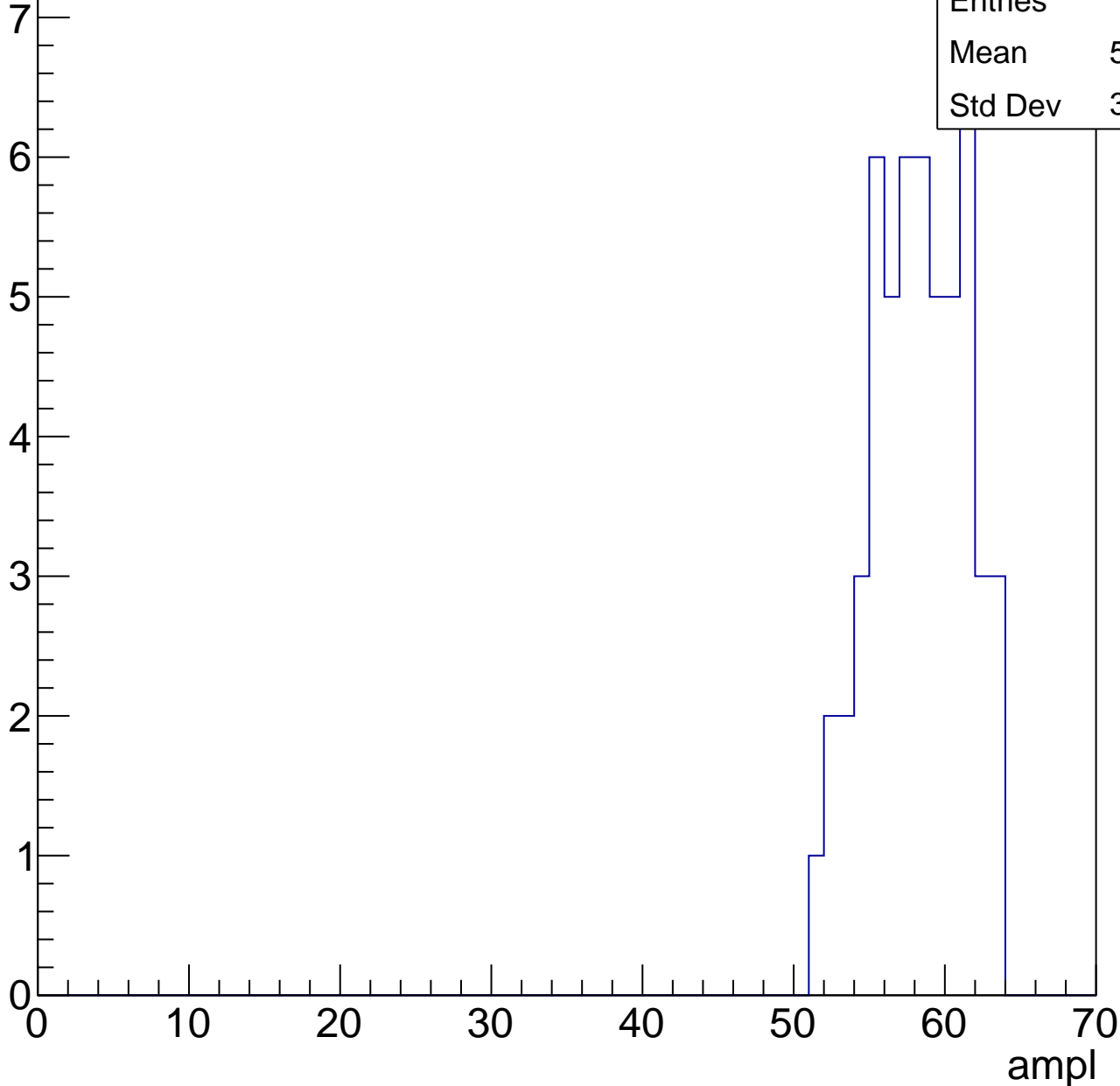


B1L101S, U4-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.78
Std Dev	3.065

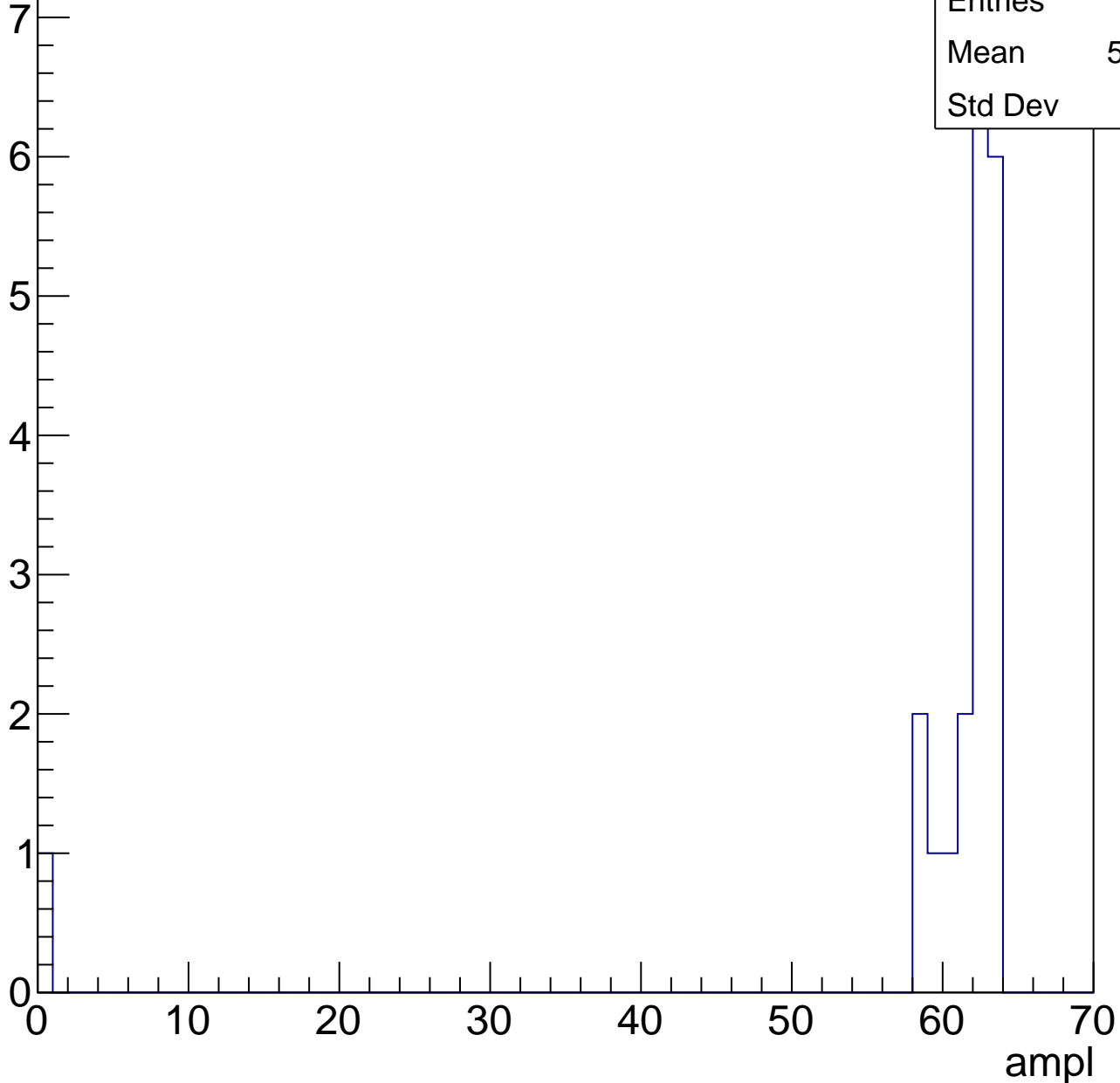


B1L101S, U4-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

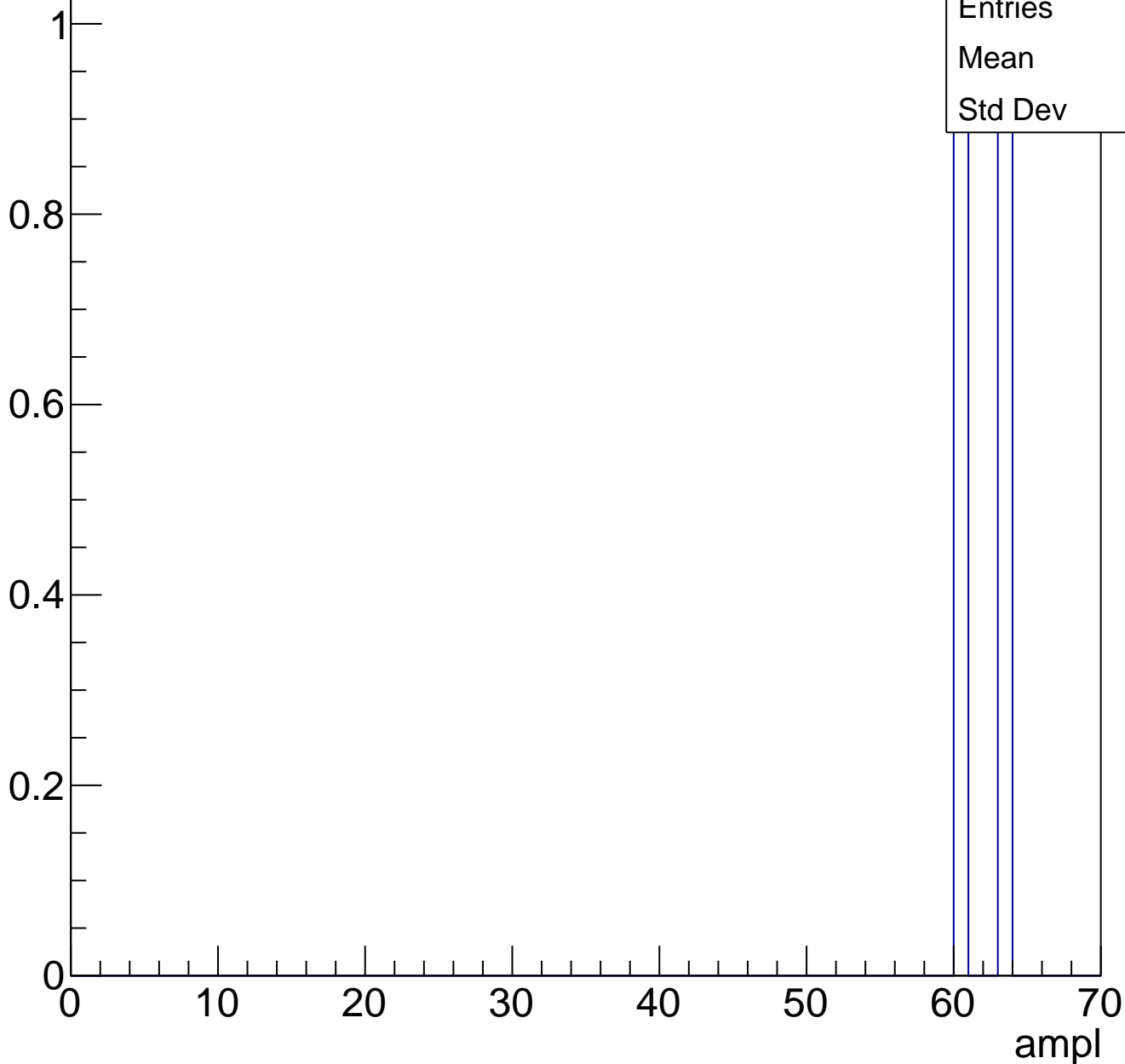
Entries	20
Mean	58.45
Std Dev	13.5



B1L101S, U4-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch85, adc0

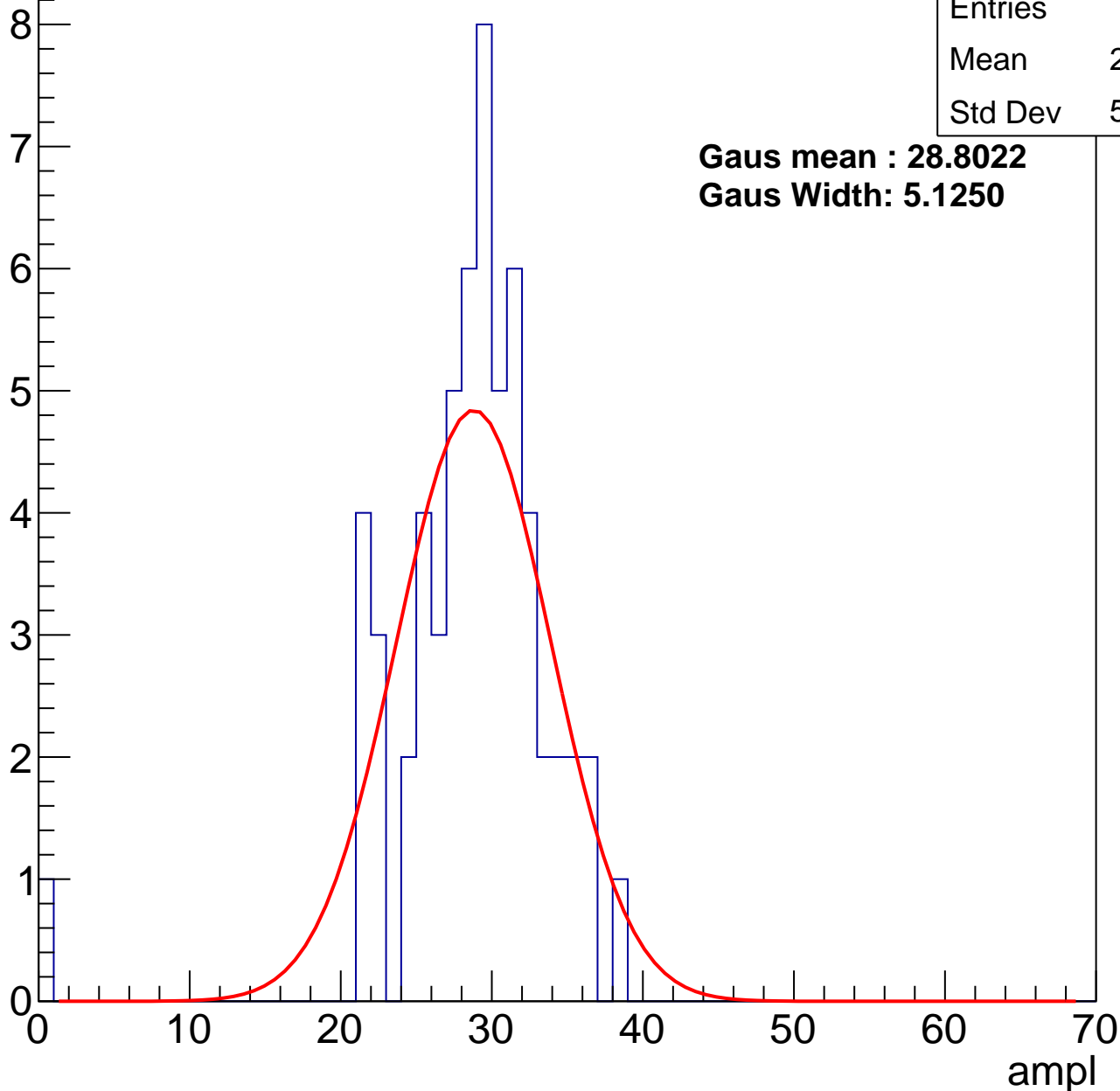
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.15
Std Dev	5.425

Gaus mean : 28.8022

Gaus Width: 5.1250



B1L101S, U4-ch85, adc1

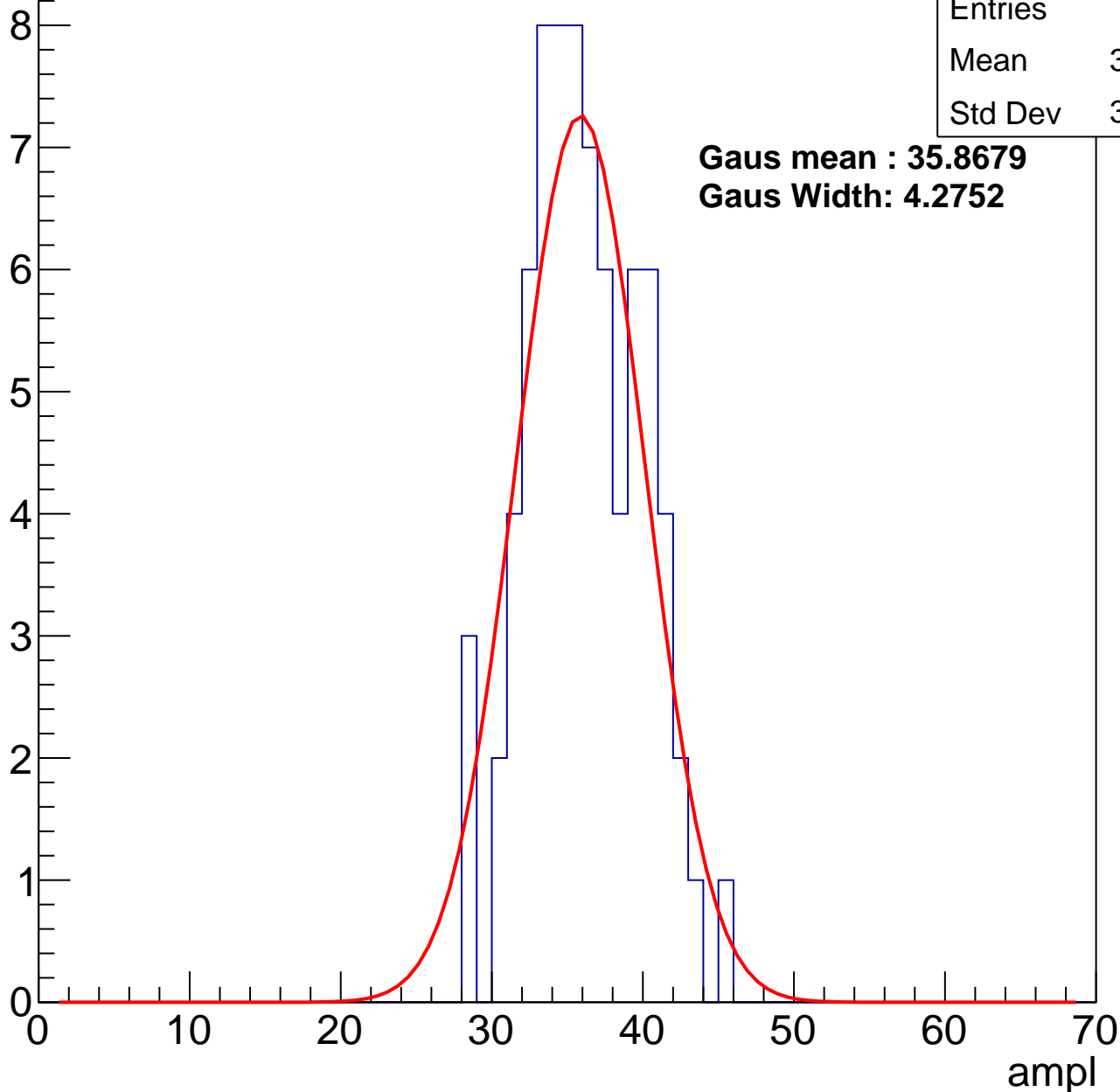
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.68
Std Dev	3.696

Gaus mean : 35.8679

Gaus Width: 4.2752



B1L101S, U4-ch85, adc2

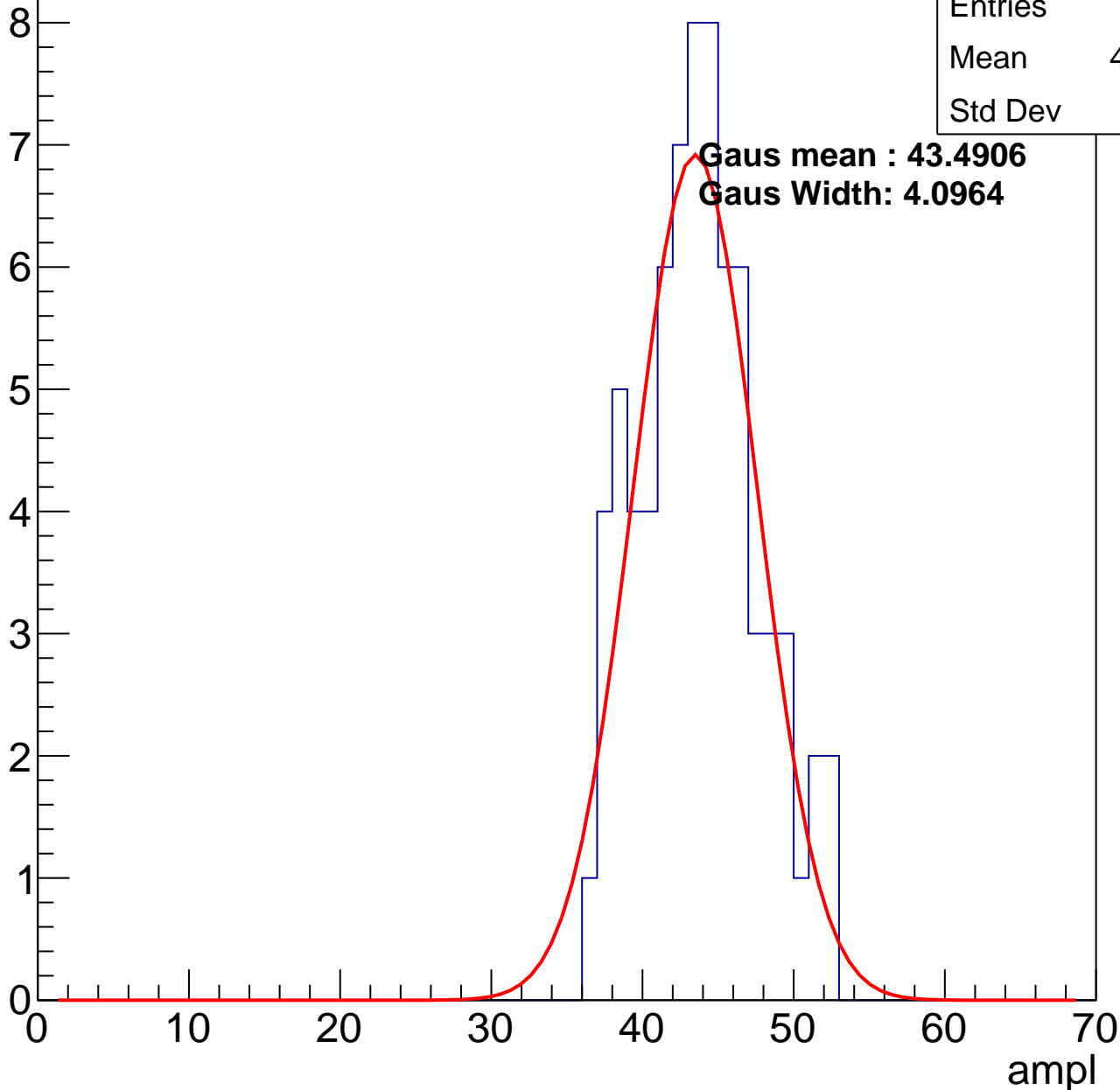
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.29
Std Dev	3.88

Gaus mean : 43.4906

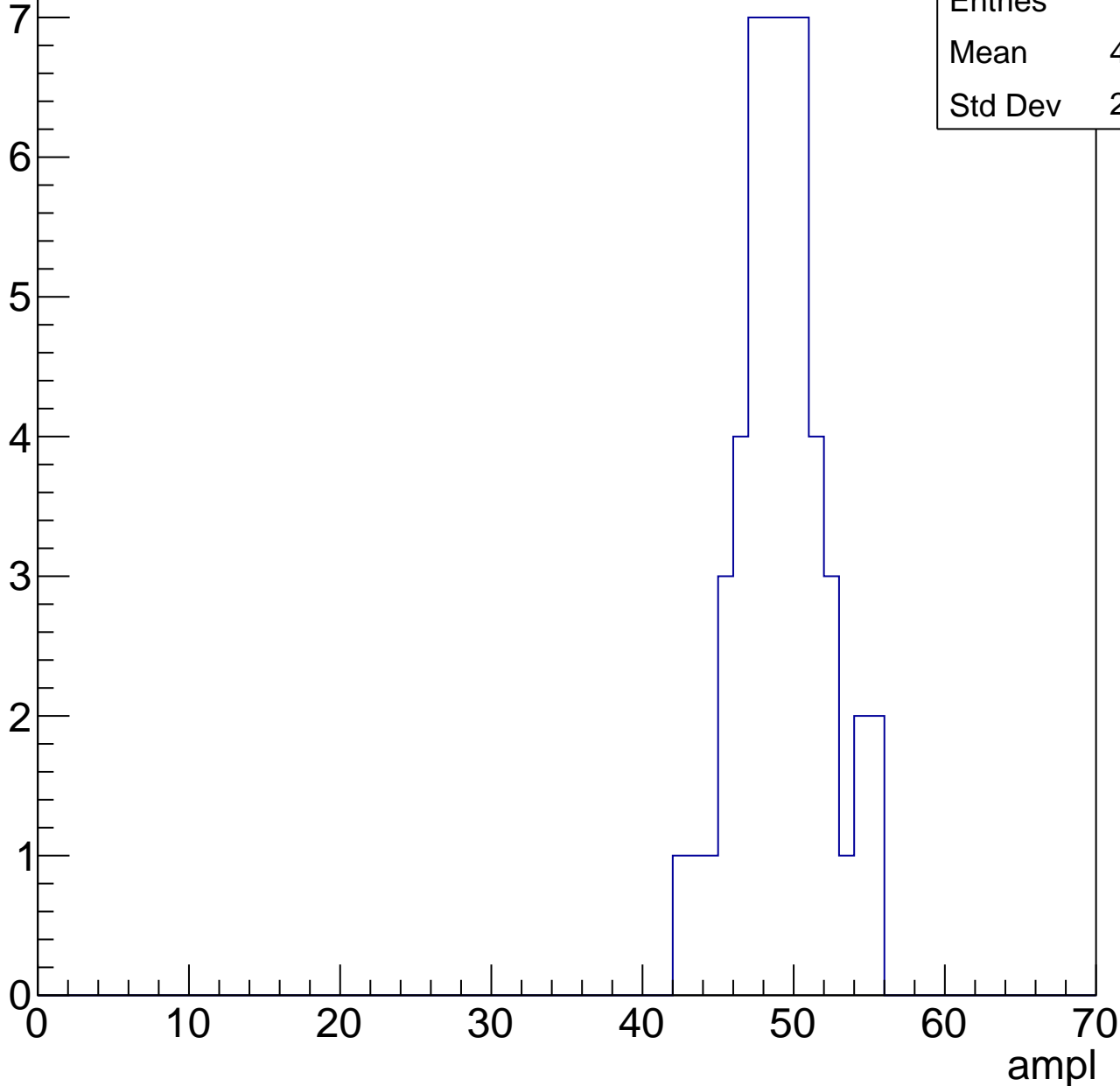
Gaus Width: 4.0964



B1L101S, U4-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	50
Mean	48.74
Std Dev	2.876

B1L101S, U4-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

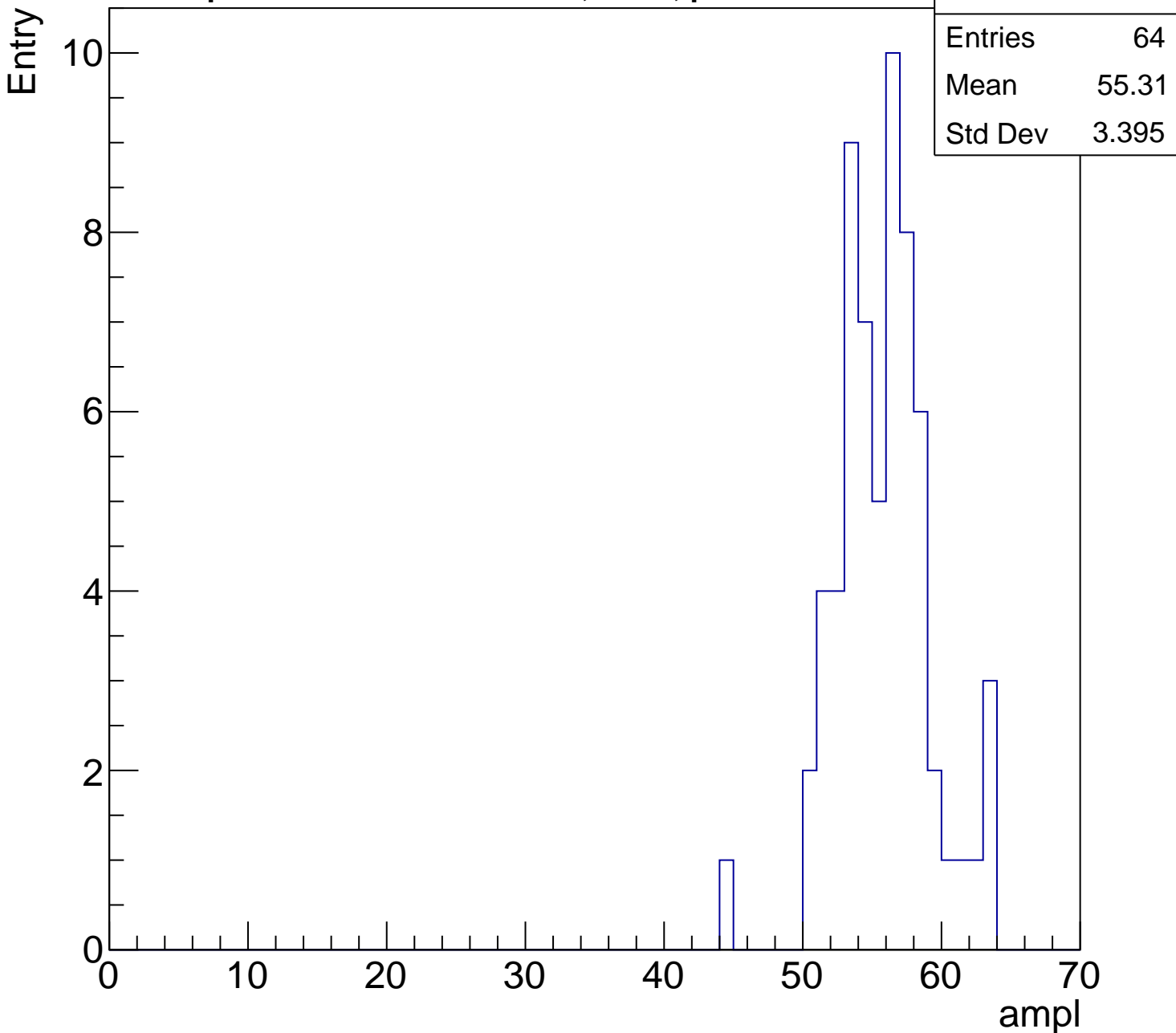
Entries	64
Mean	55.31
Std Dev	3.395

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

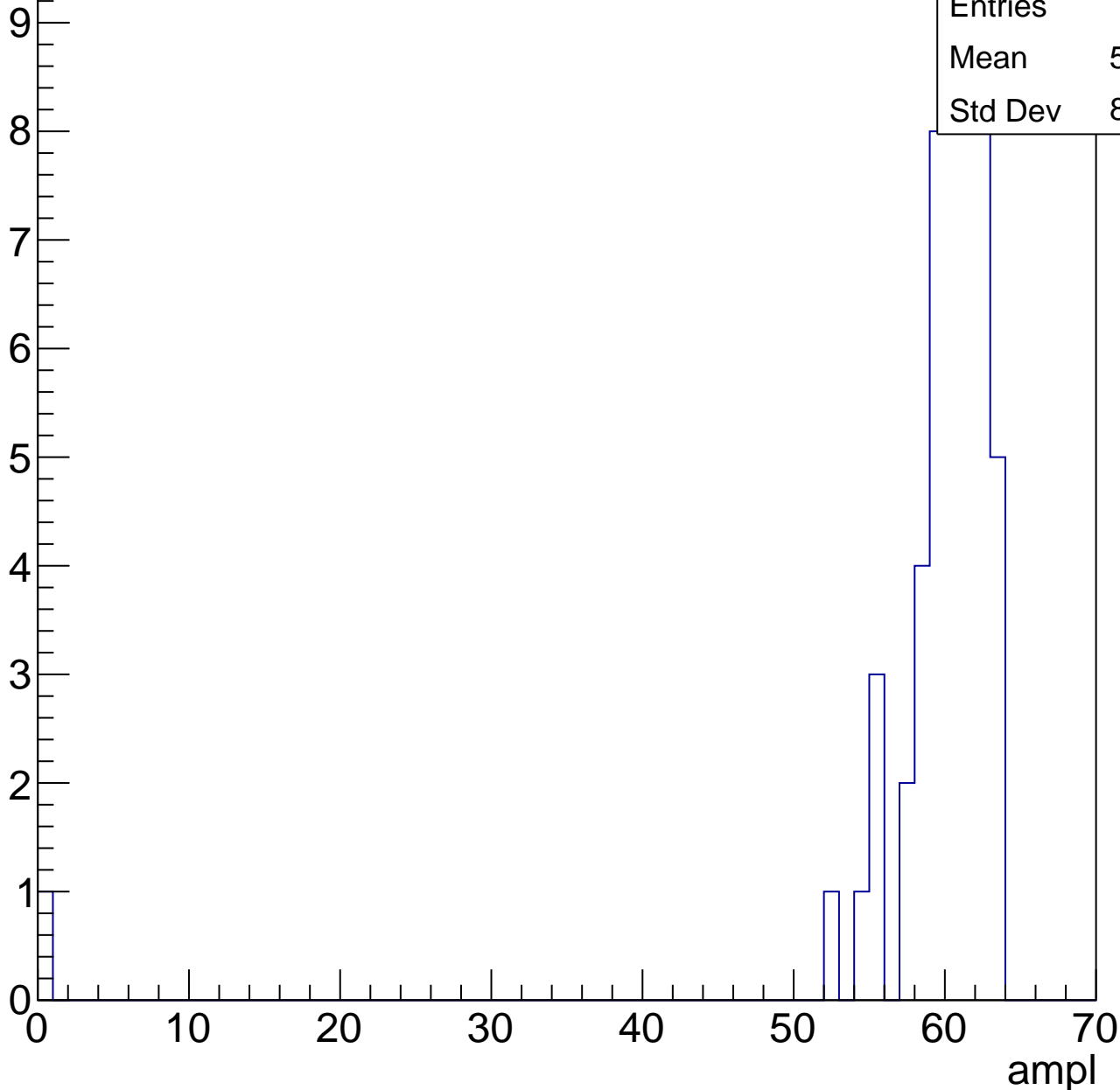


B1L101S, U4-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	58.67
Std Dev	8.566



B1L101S, U4-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U4-ch86, adc0

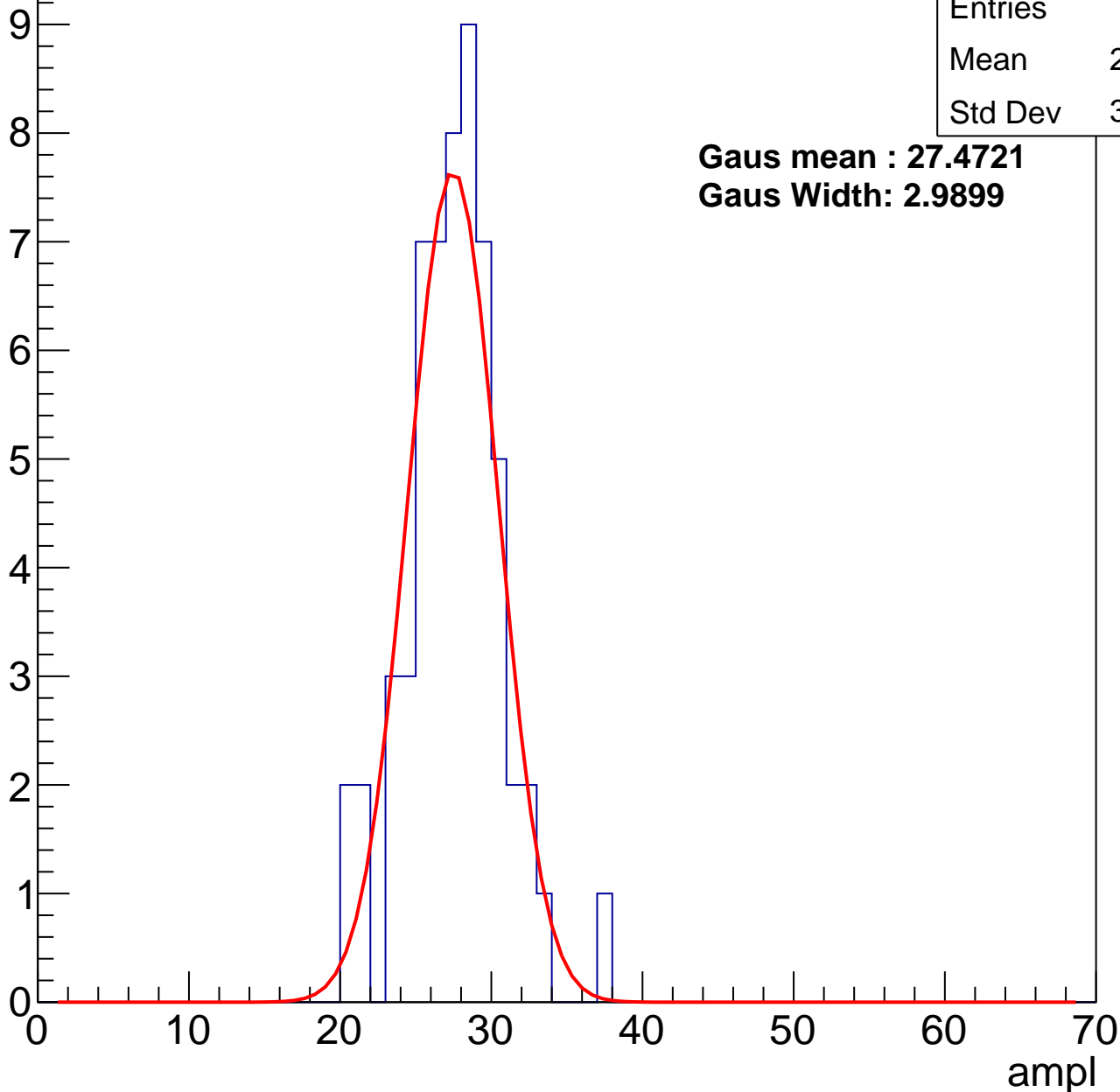
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.07
Std Dev	3.135

Gaus mean : 27.4721

Gaus Width: 2.9899



B1L101S, U4-ch86, adc1

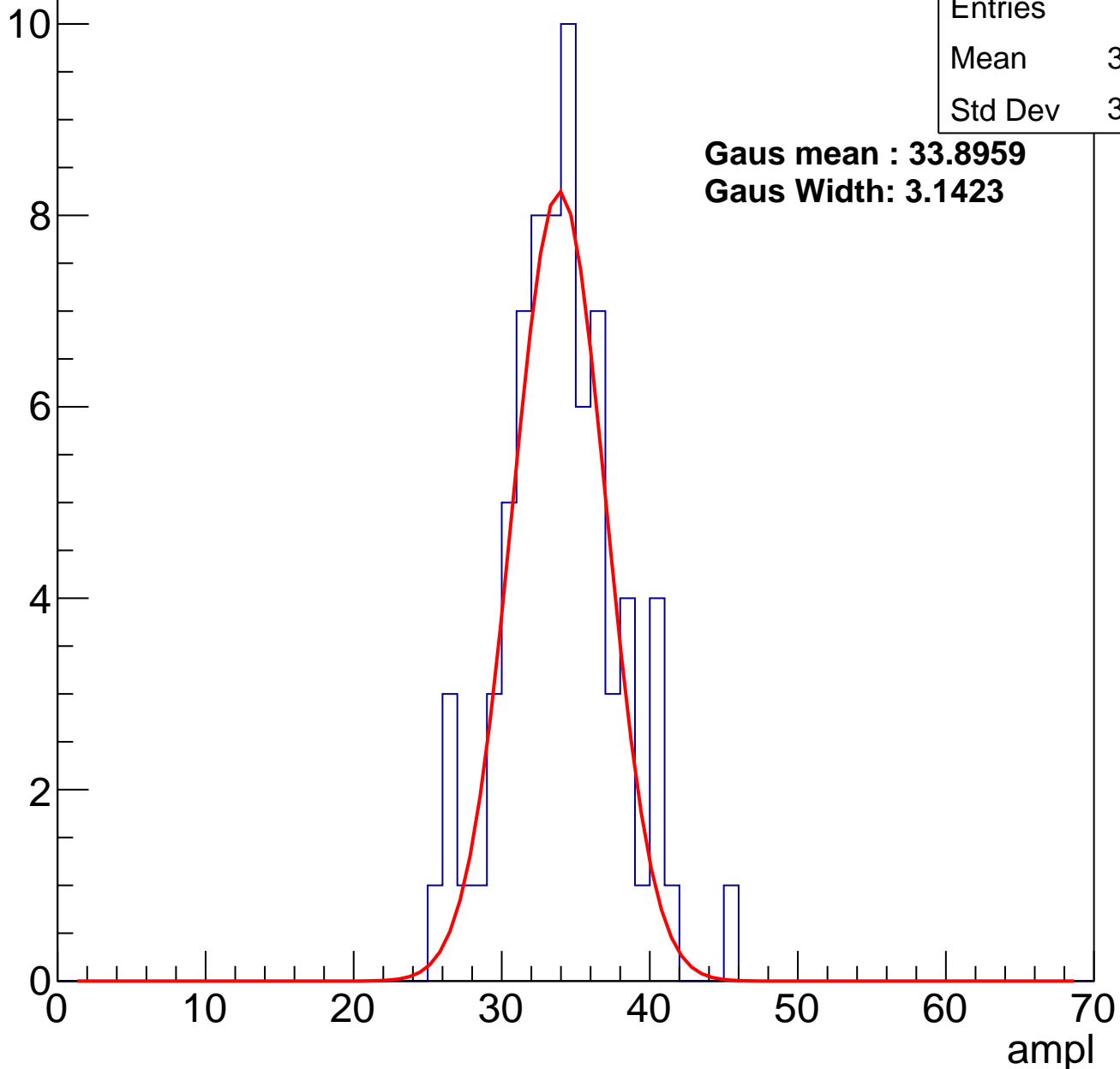
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	33.54
Std Dev	3.793

Gaus mean : 33.8959

Gaus Width: 3.1423

Entry



B1L101S, U4-ch86, adc2

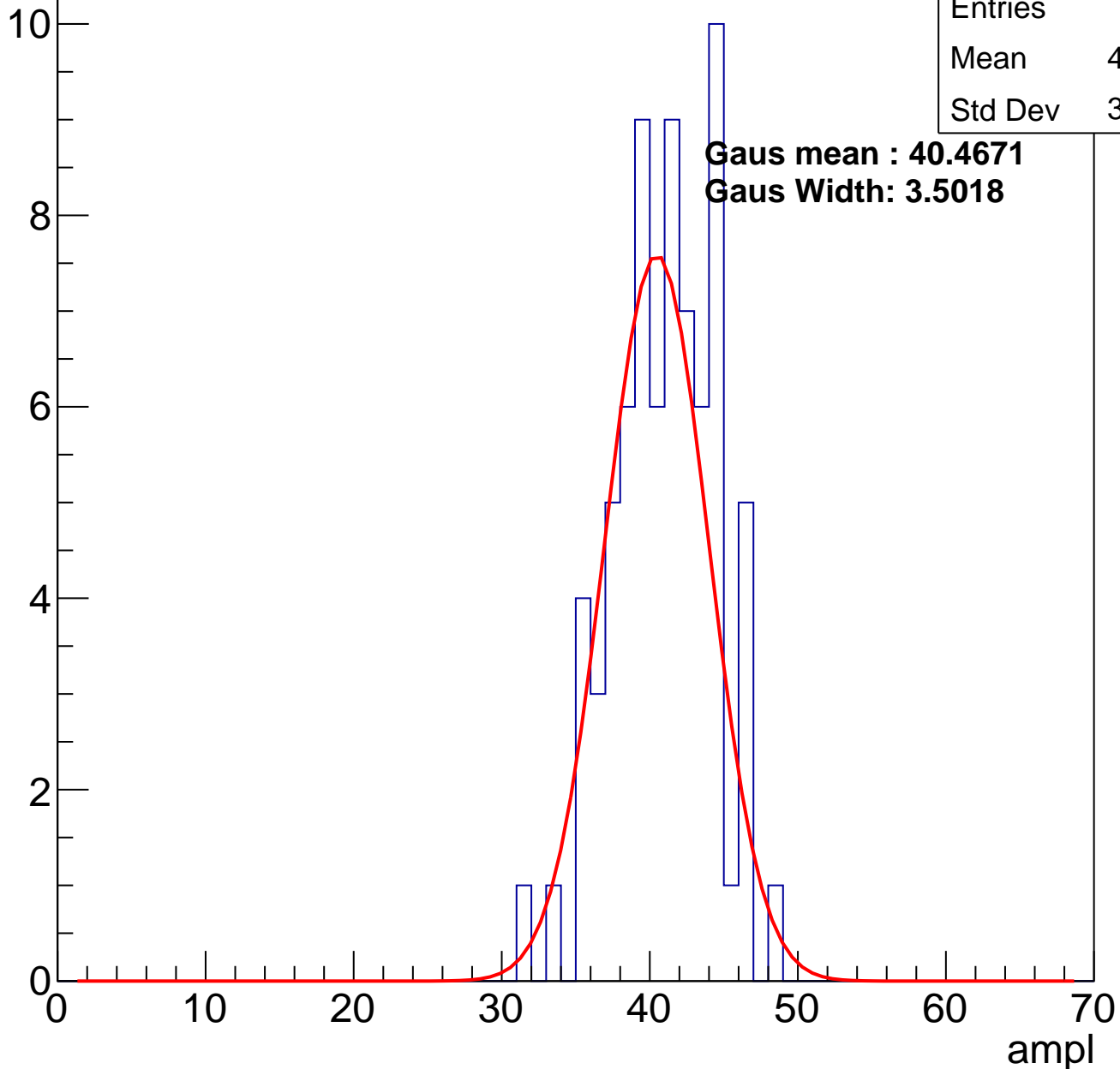
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	40.54
Std Dev	3.398

Gaus mean : 40.4671

Gaus Width: 3.5018

Entry

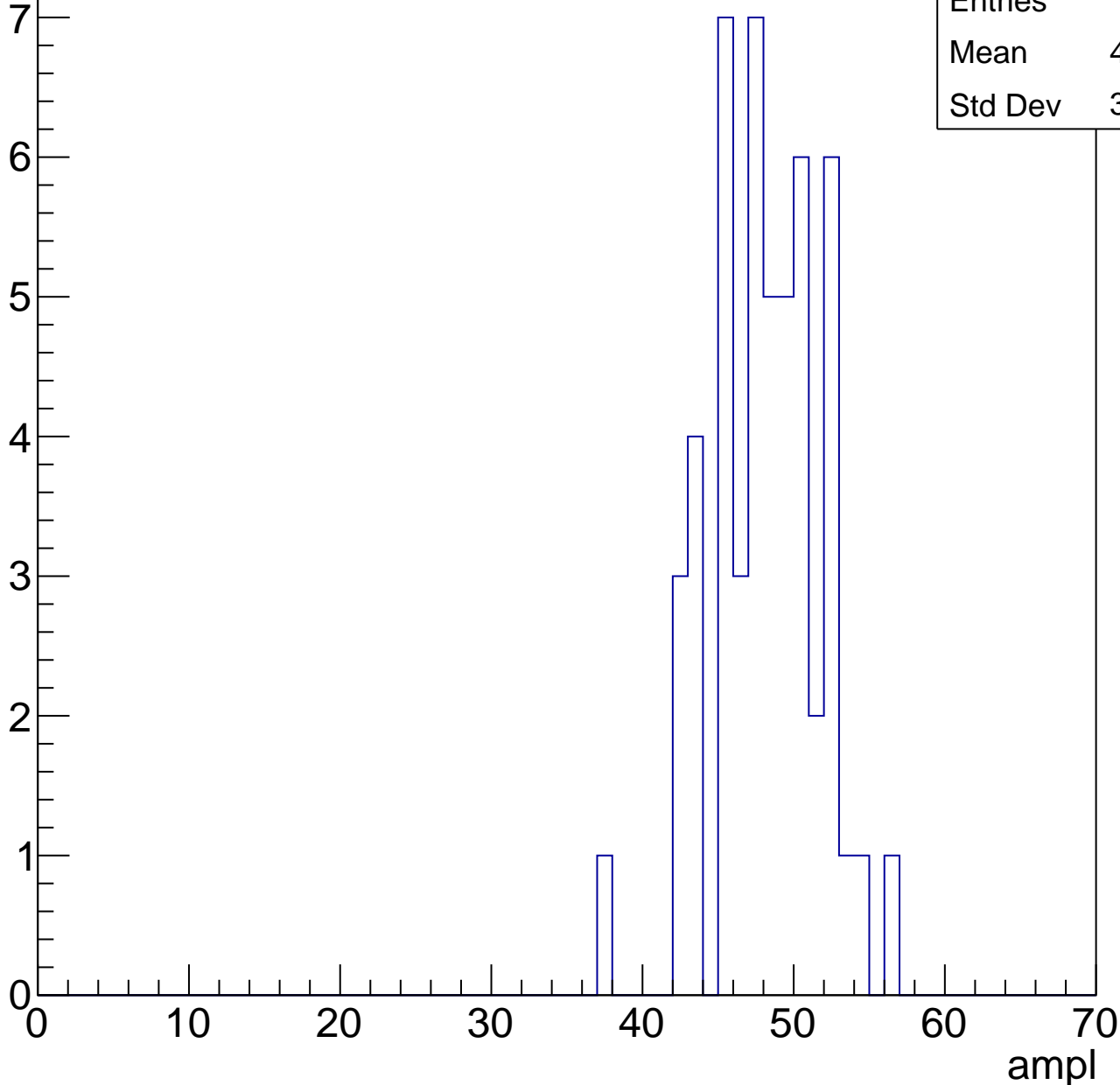


B1L101S, U4-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

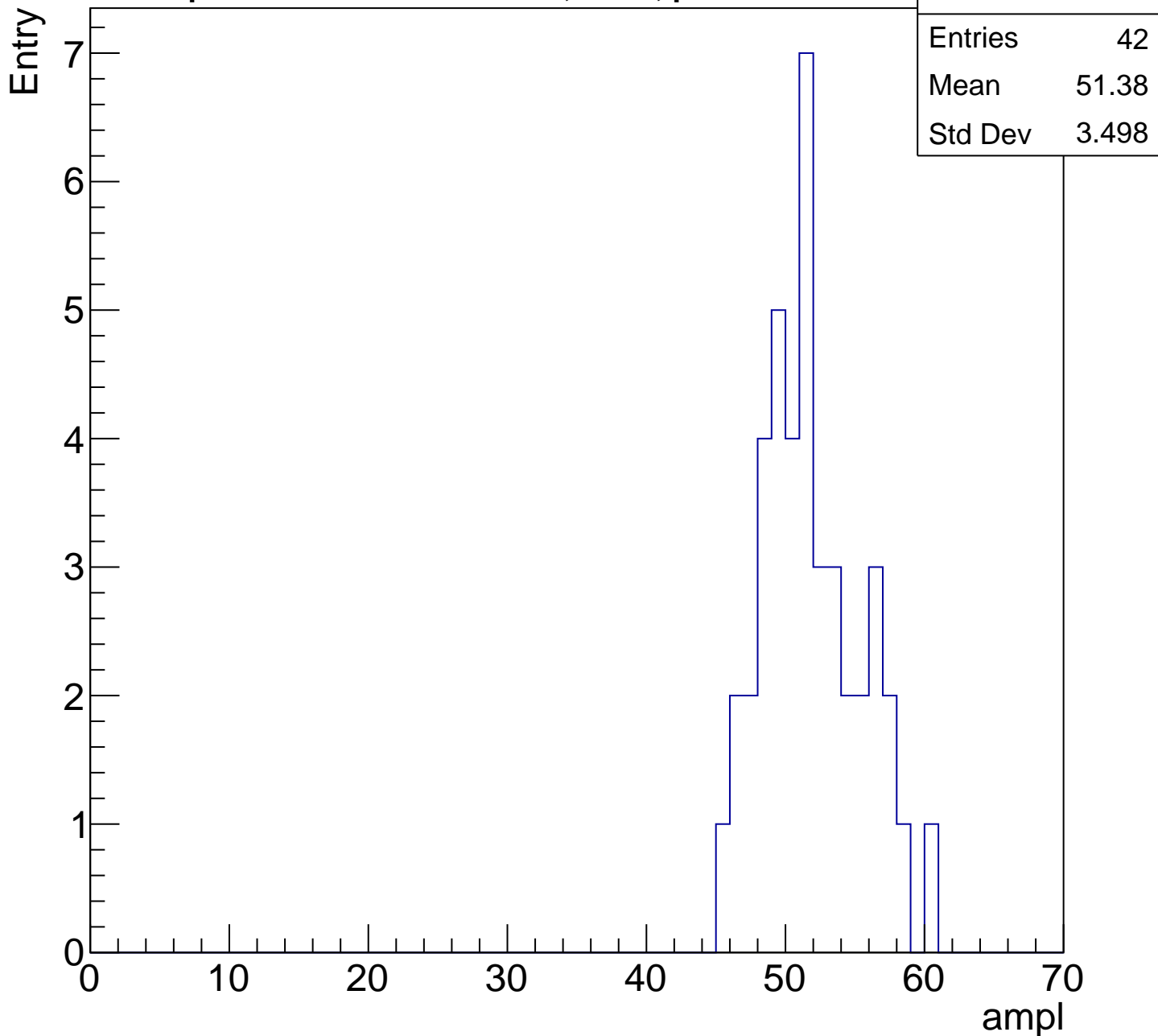
Entry

Entries	52
Mean	47.67
Std Dev	3.599



B1L101S, U4-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

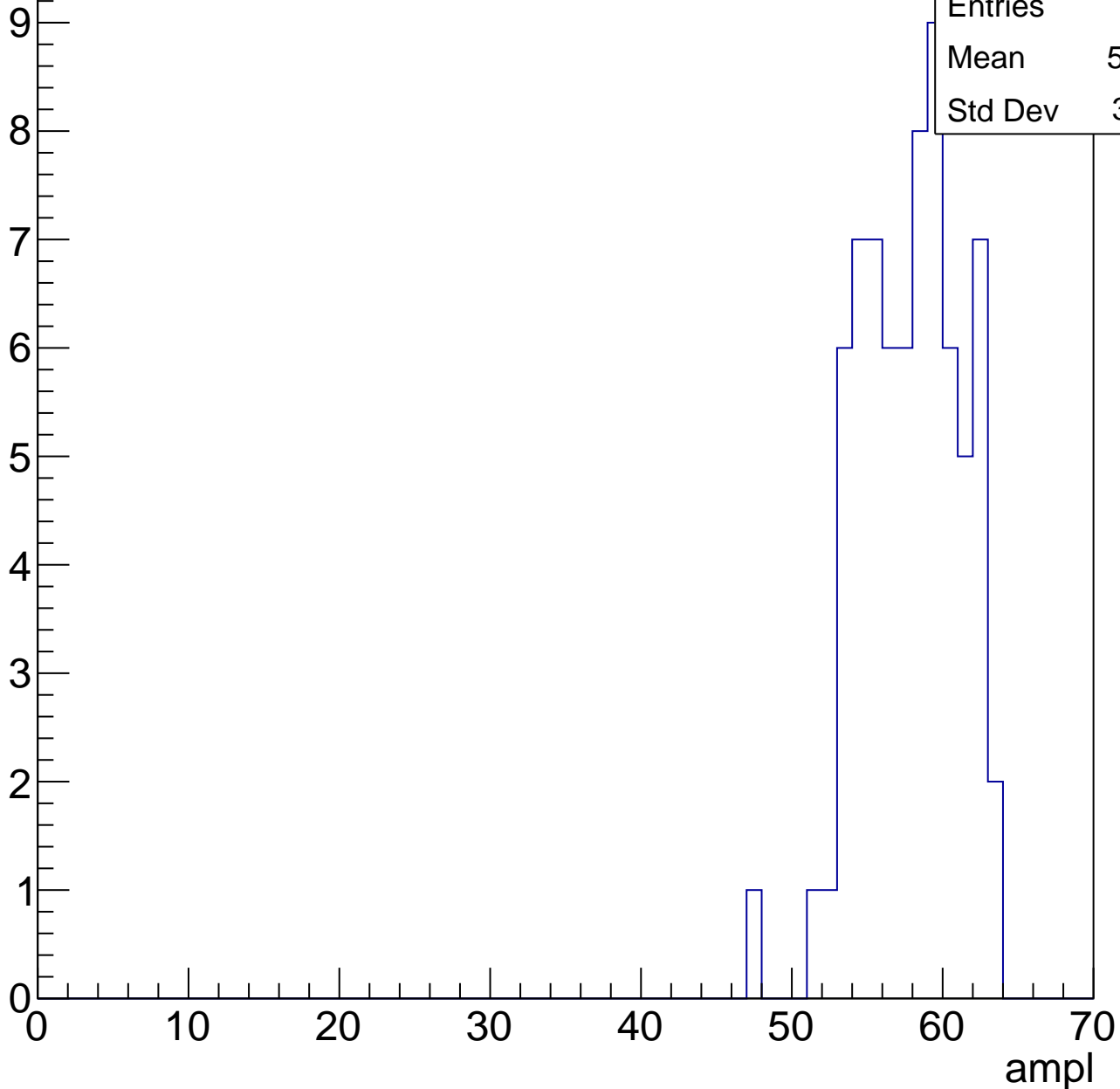


B1L101S, U4-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	57.35
Std Dev	3.271

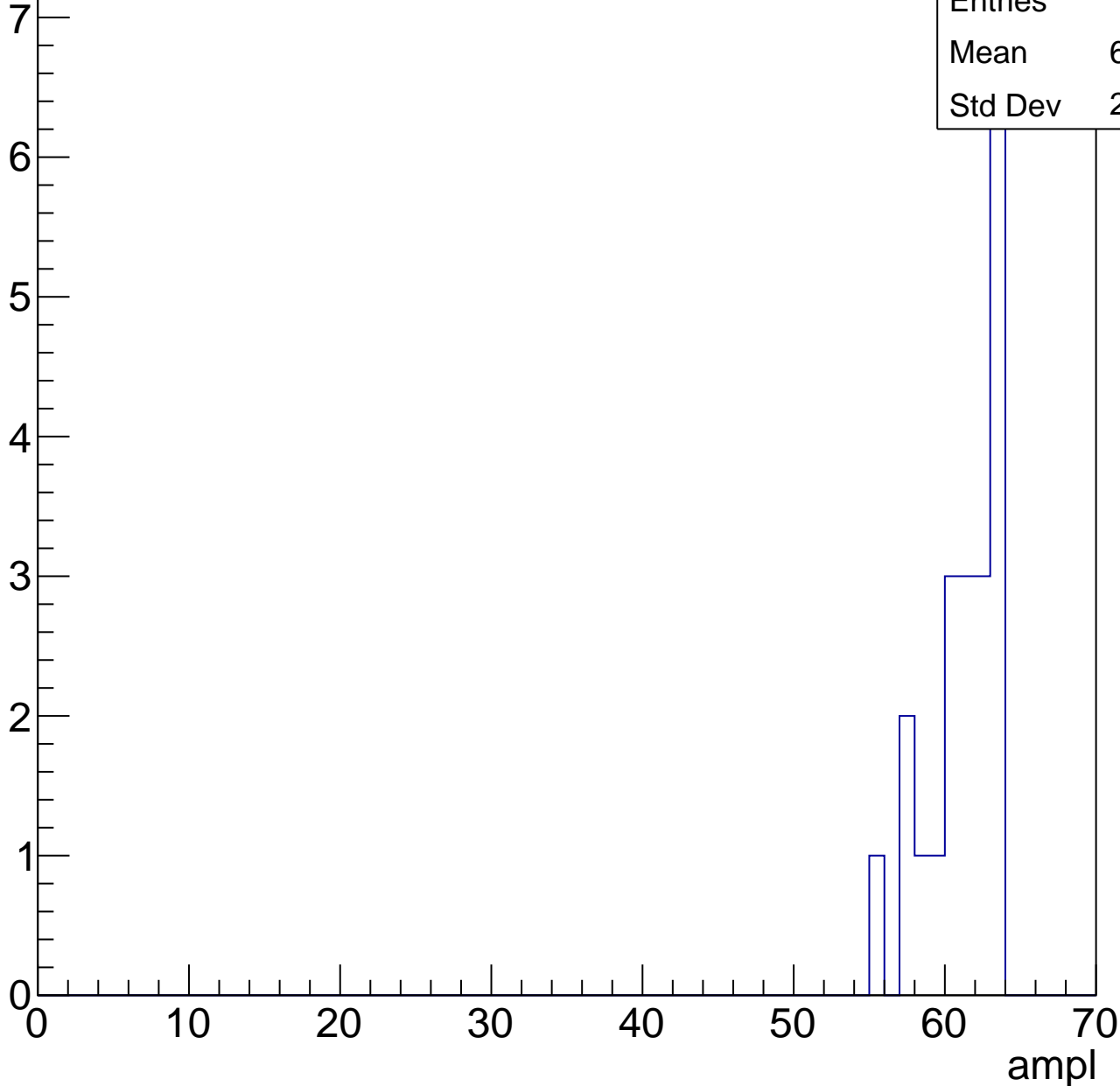


B1L101S, U4-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

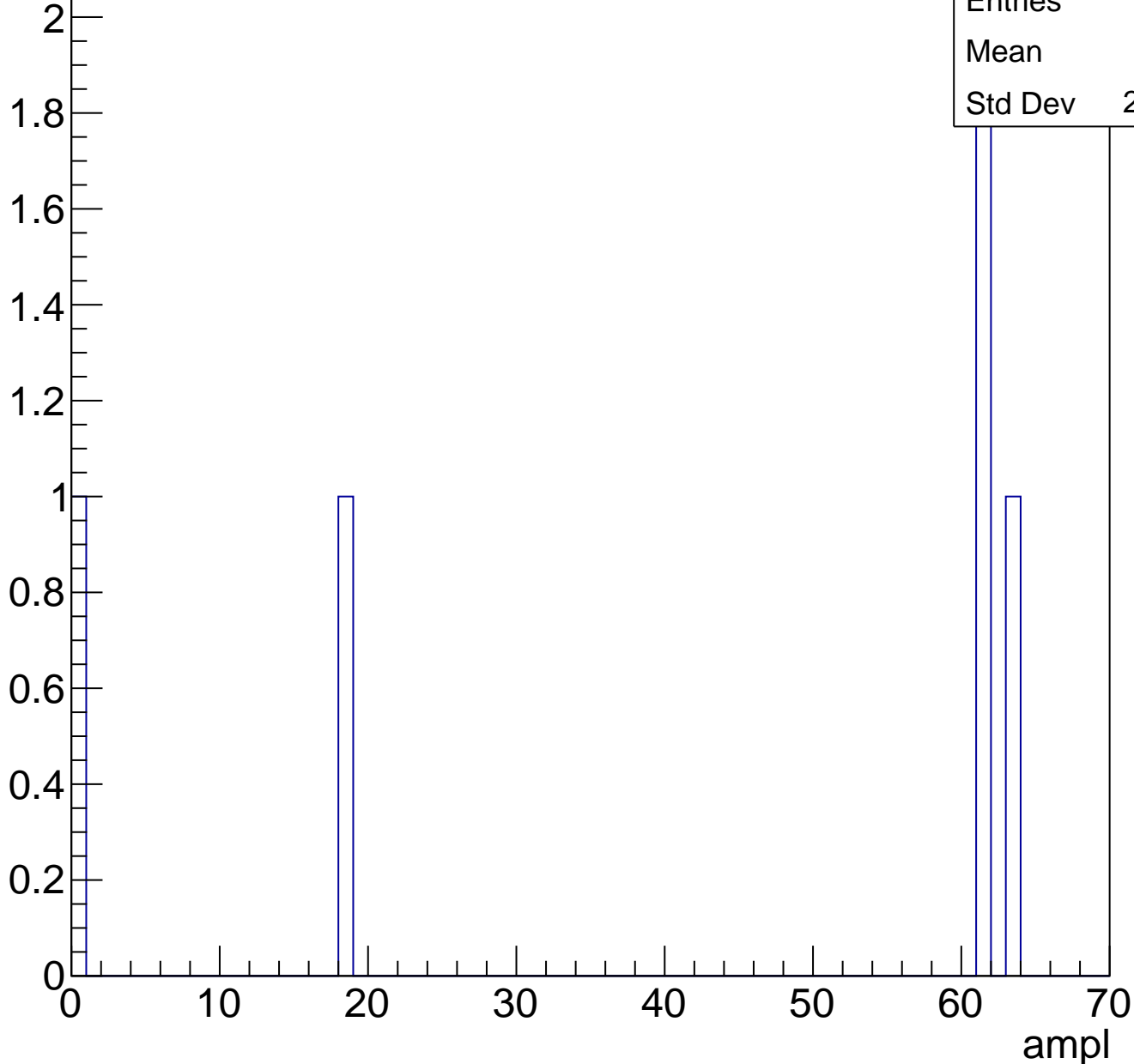
Entries	21
Mean	60.76
Std Dev	2.328



B1L101S, U4-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	40.6
Std Dev	26.43

B1L101S, U4-ch87, adc0

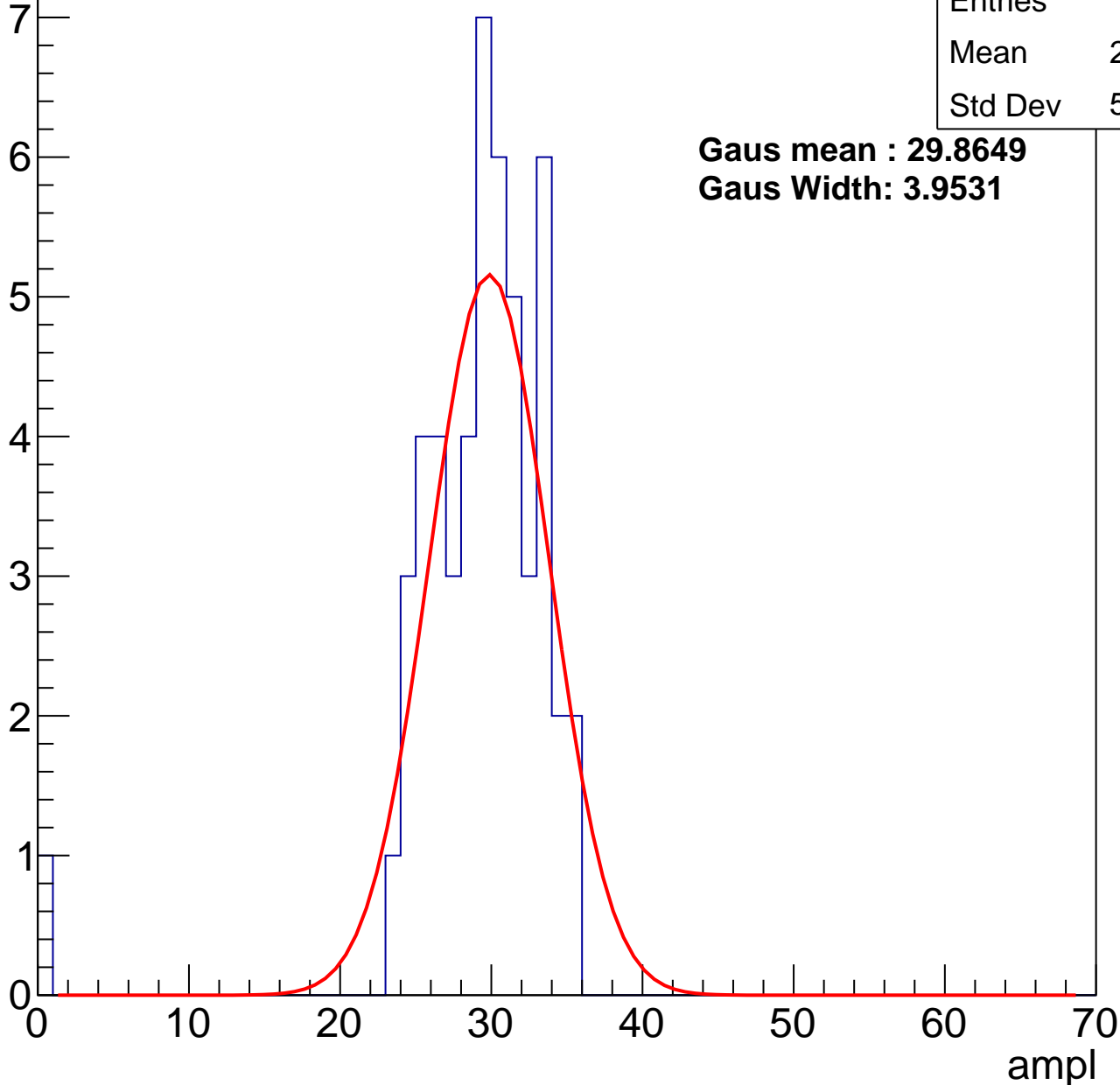
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	28.67
Std Dev	5.113

Gaus mean : 29.8649

Gaus Width: 3.9531



B1L101S, U4-ch87, adc1

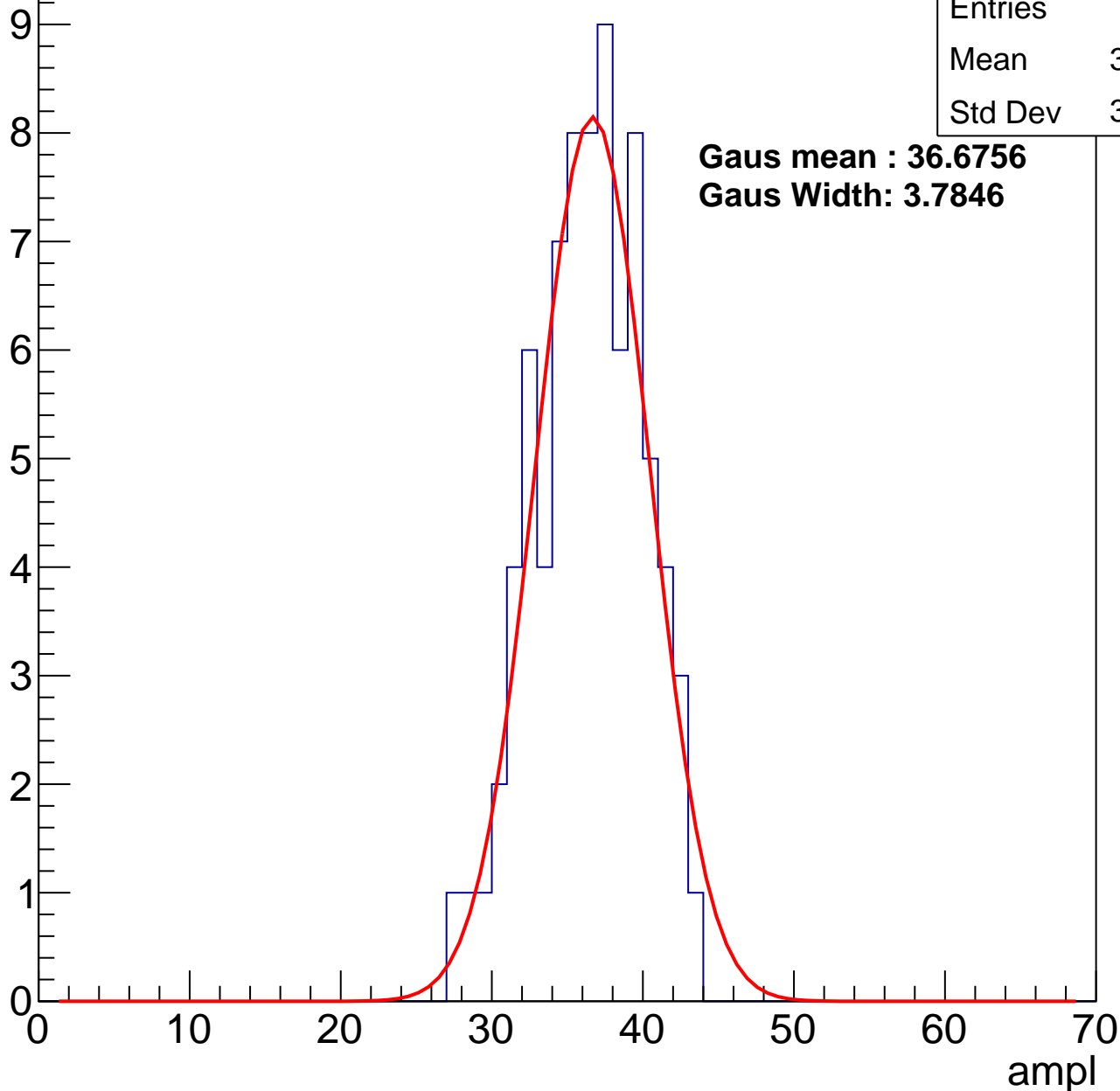
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.95
Std Dev	3.537

Gaus mean : 36.6756

Gaus Width: 3.7846



B1L101S, U4-ch87, adc2

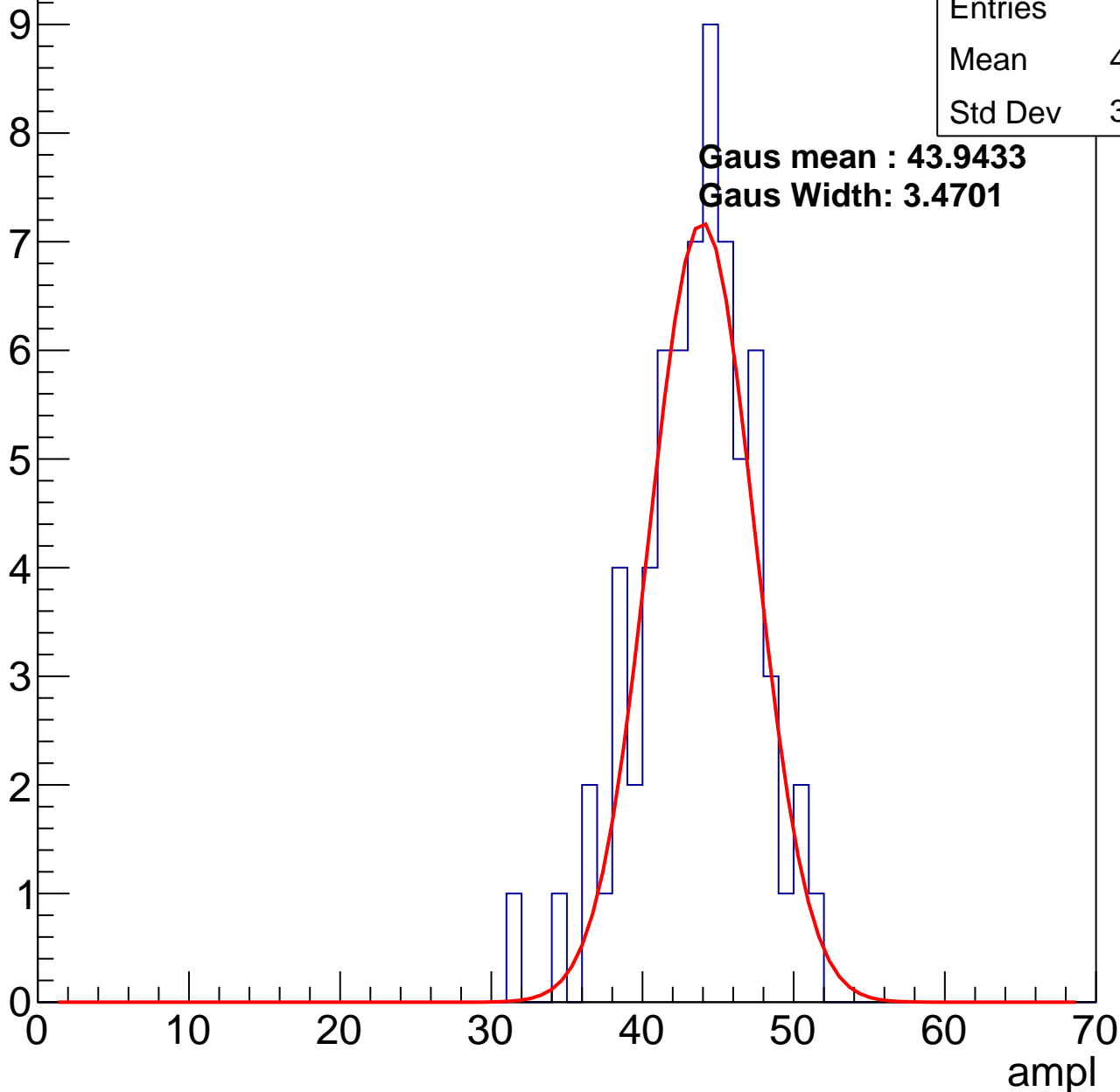
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.09
Std Dev	3.849

Gaus mean : 43.9433

Gaus Width: 3.4701

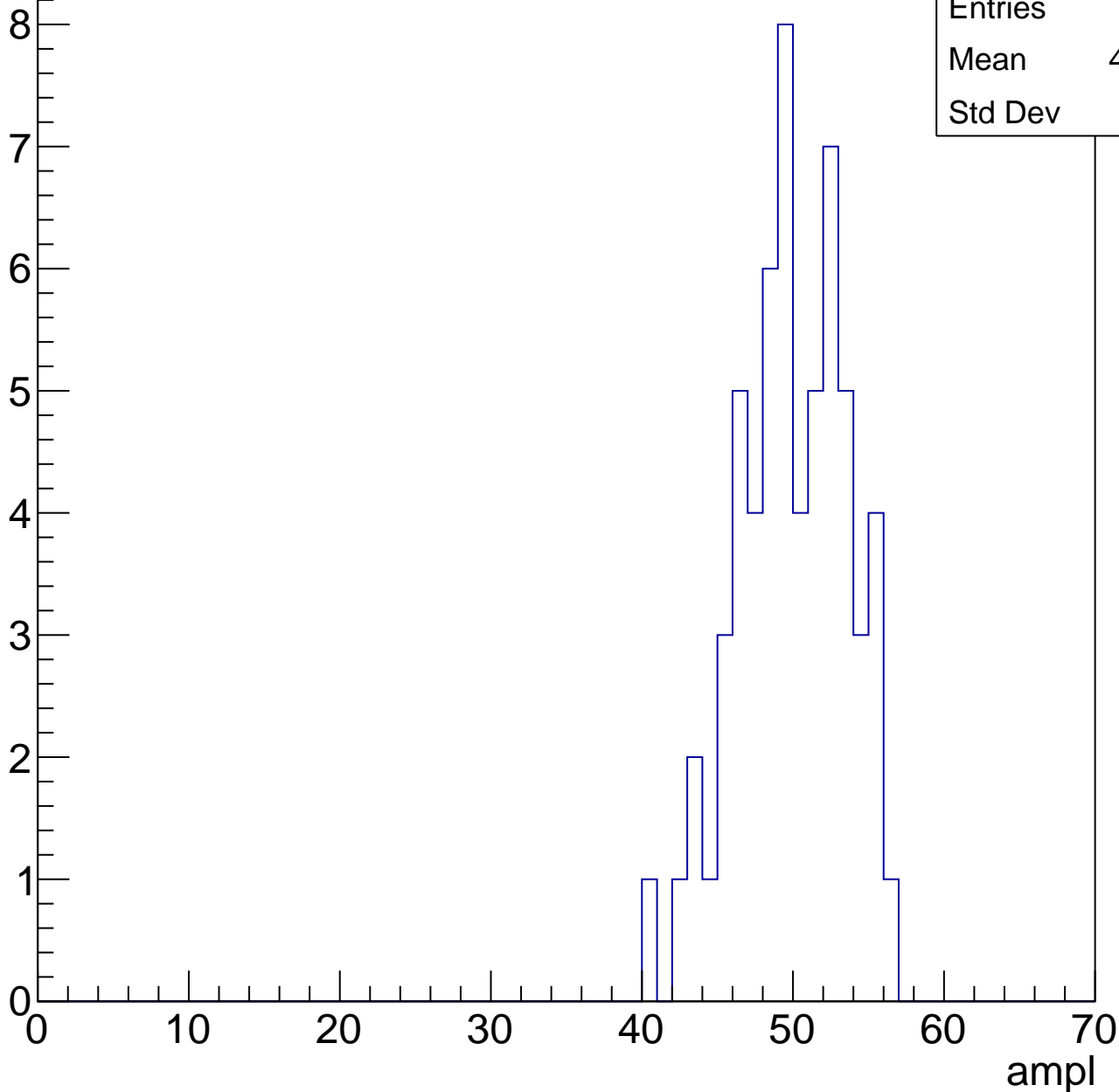


B1L101S, U4-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.45
Std Dev	3.57

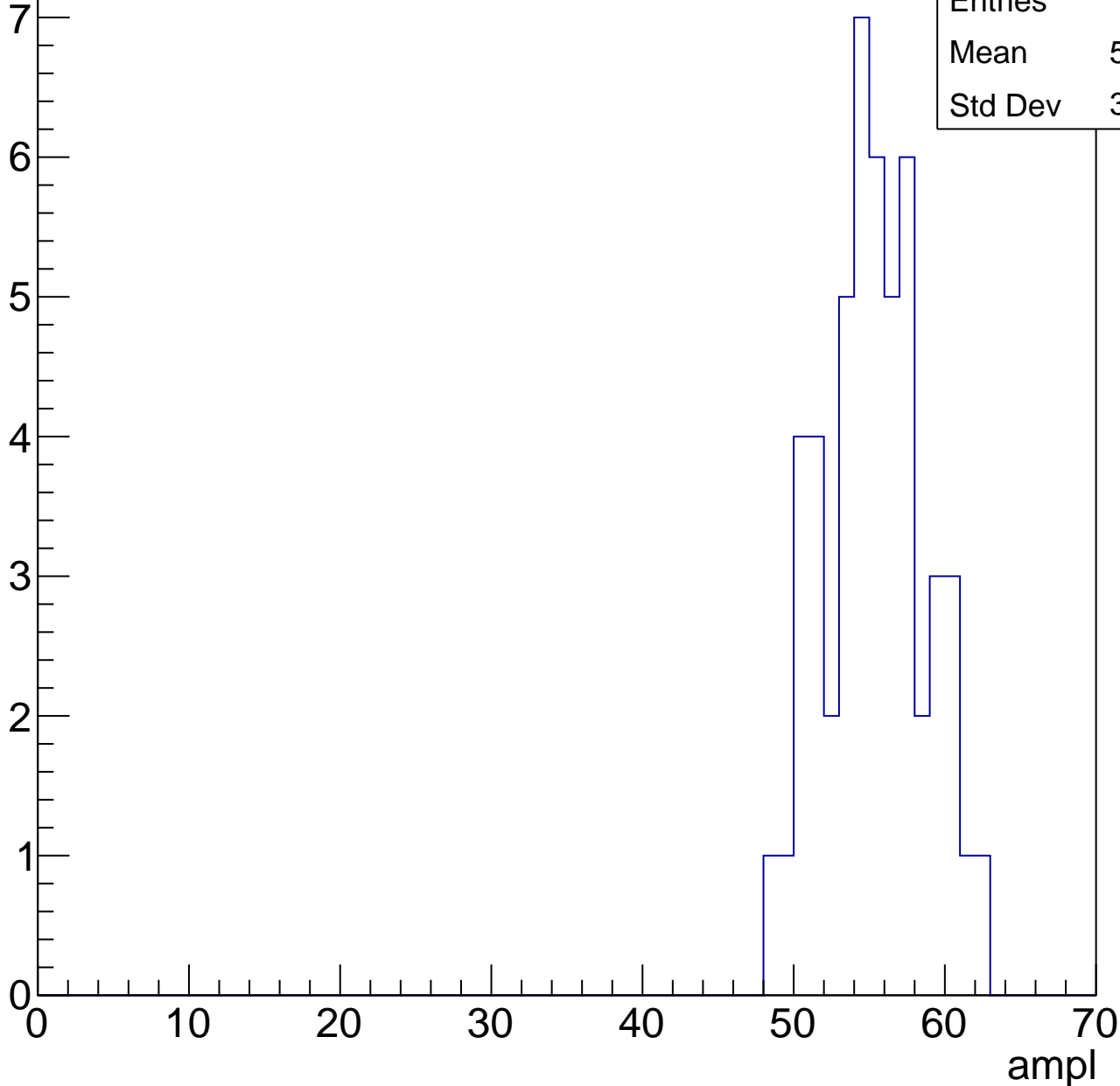


B1L101S, U4-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	54.82
Std Dev	3.276



B1L101S, U4-ch87, adc5

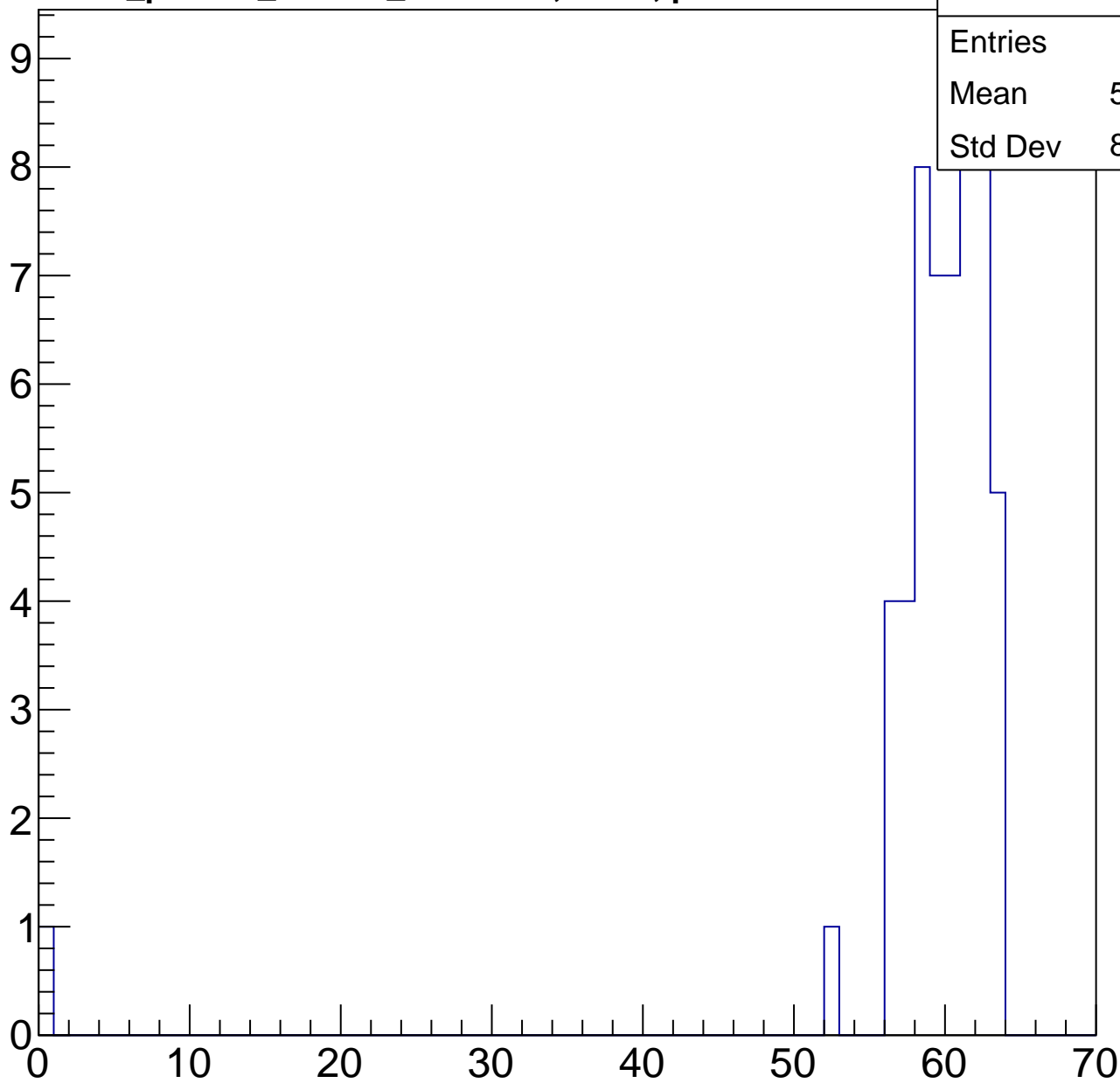
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.54
Std Dev	8.359

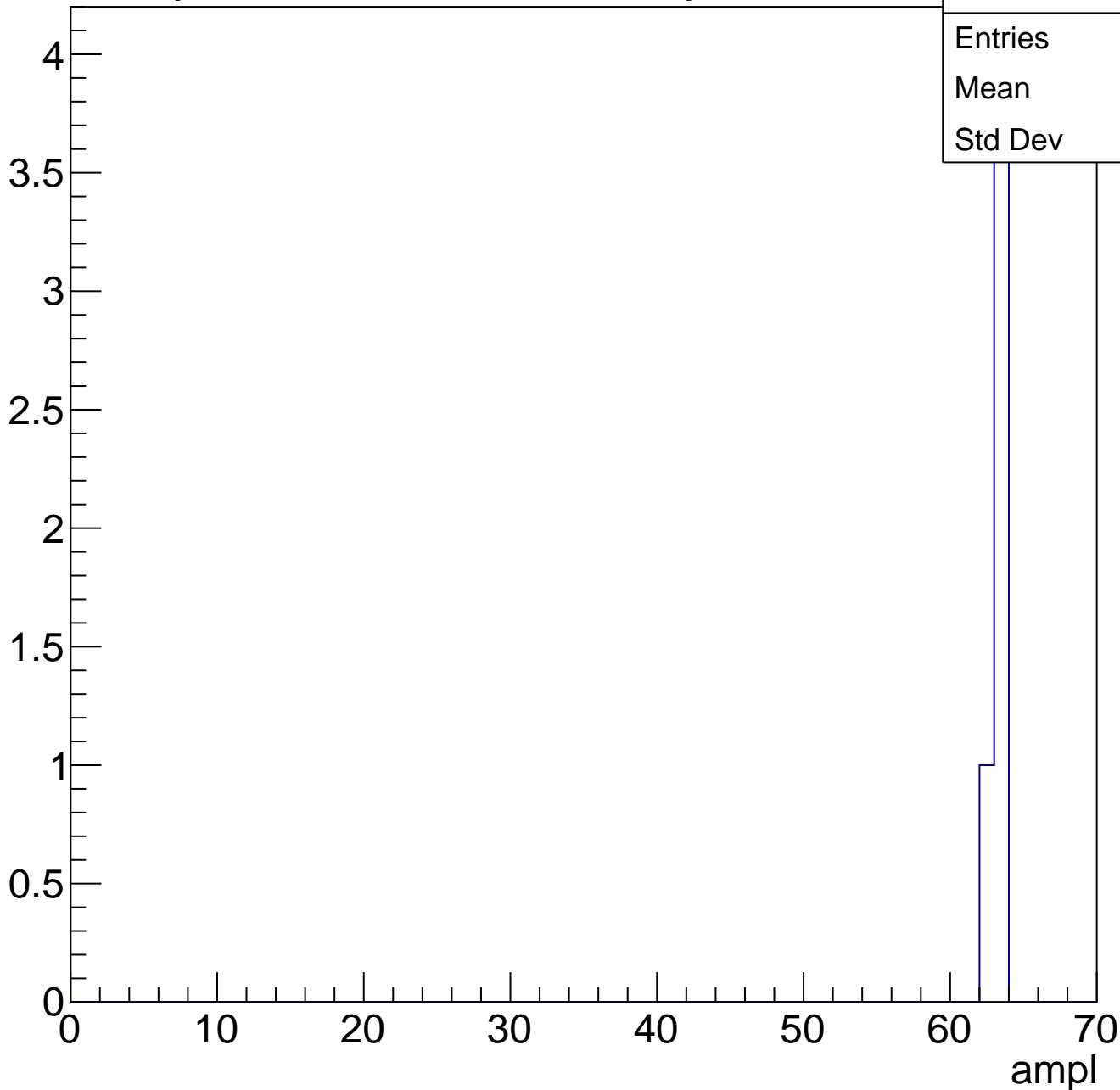
ampl



B1L101S, U4-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

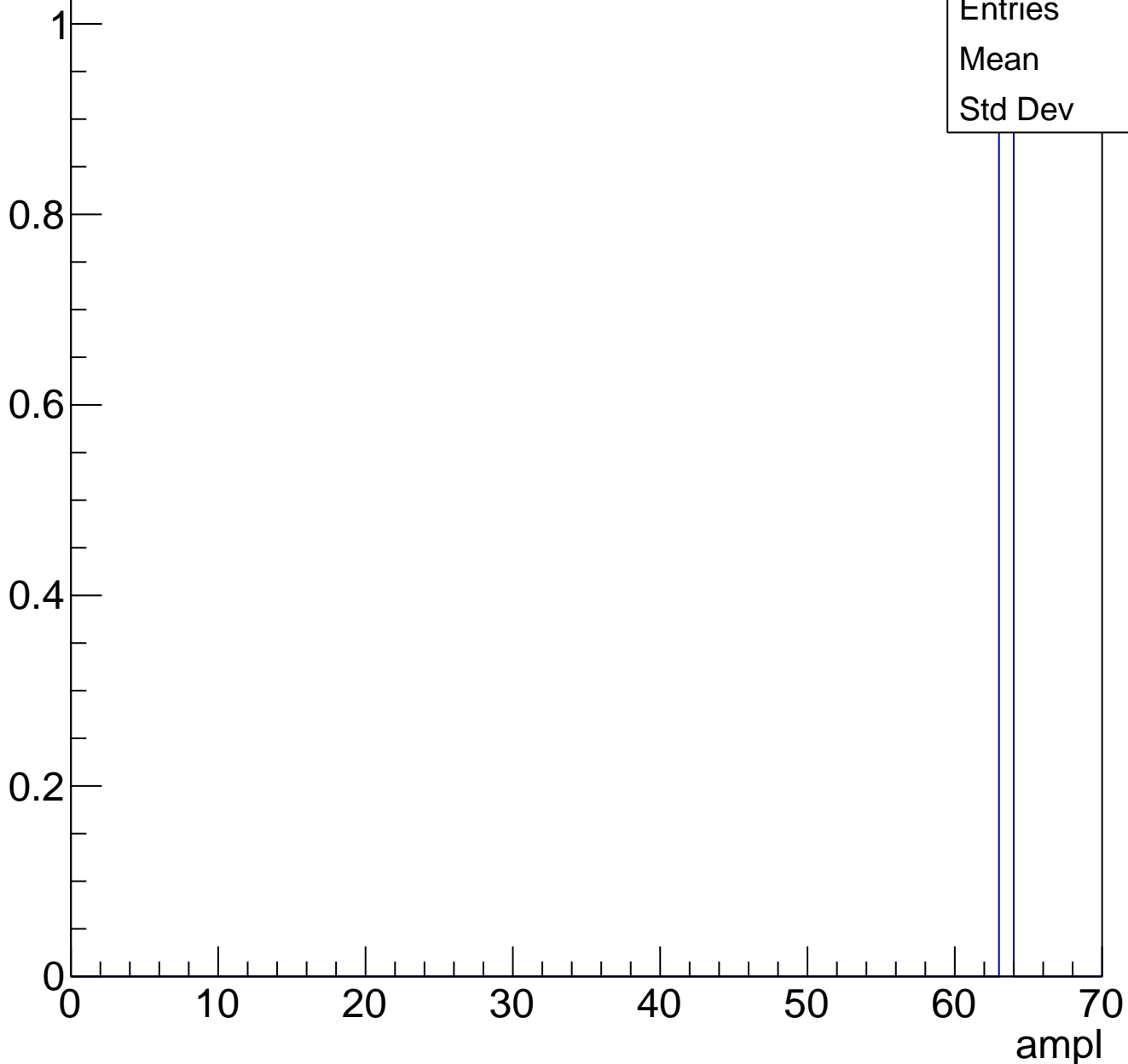
Entry



B1L101S, U4-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch88, adc0

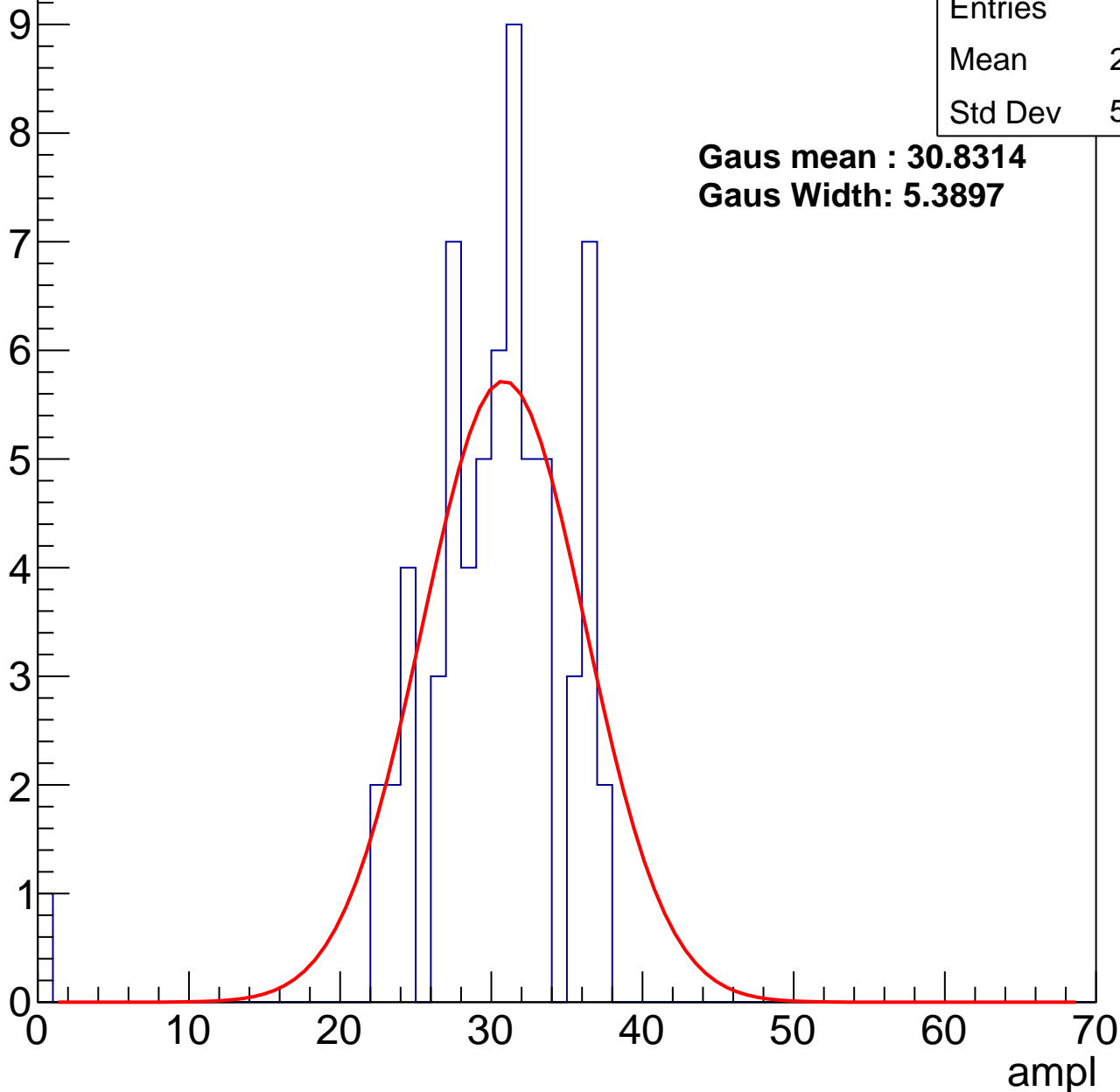
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.62
Std Dev	5.394

Gaus mean : 30.8314

Gaus Width: 5.3897



B1L101S, U4-ch88, adc1

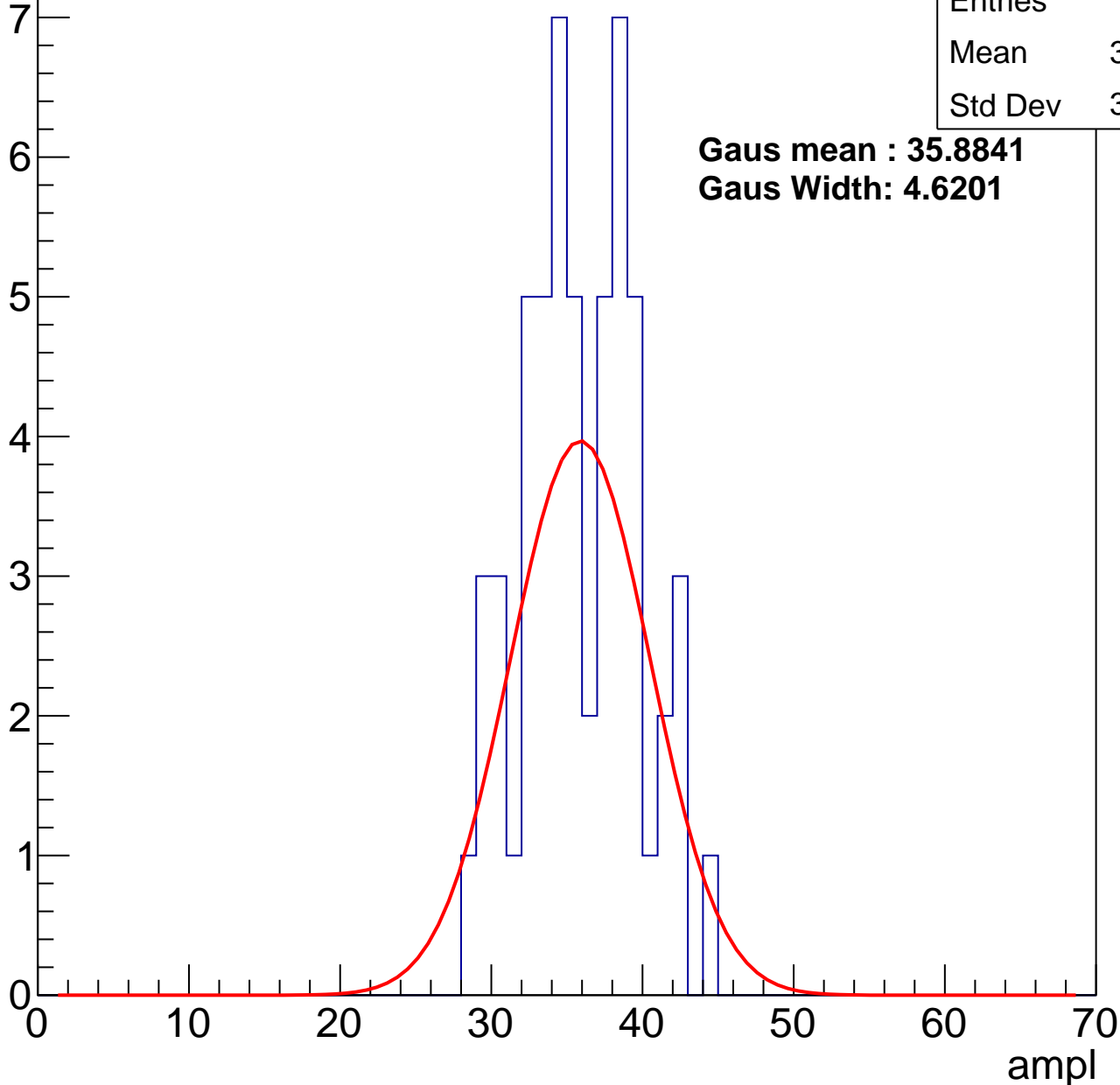
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.43
Std Dev	3.779

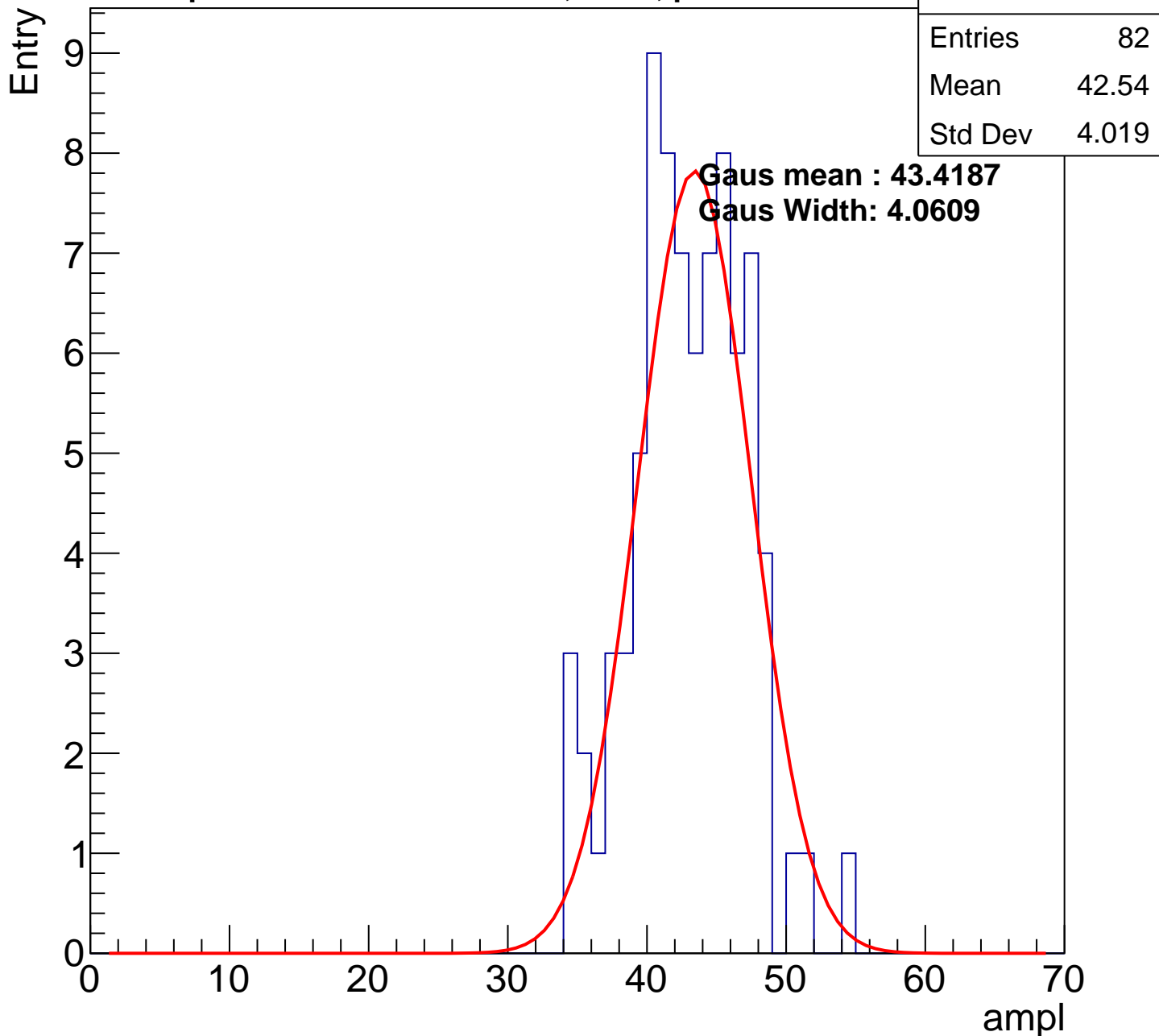
Gaus mean : 35.8841

Gaus Width: 4.6201



B1L101S, U4-ch88, adc2

calib_packv5_042523_0143.root, FC#0, port D2

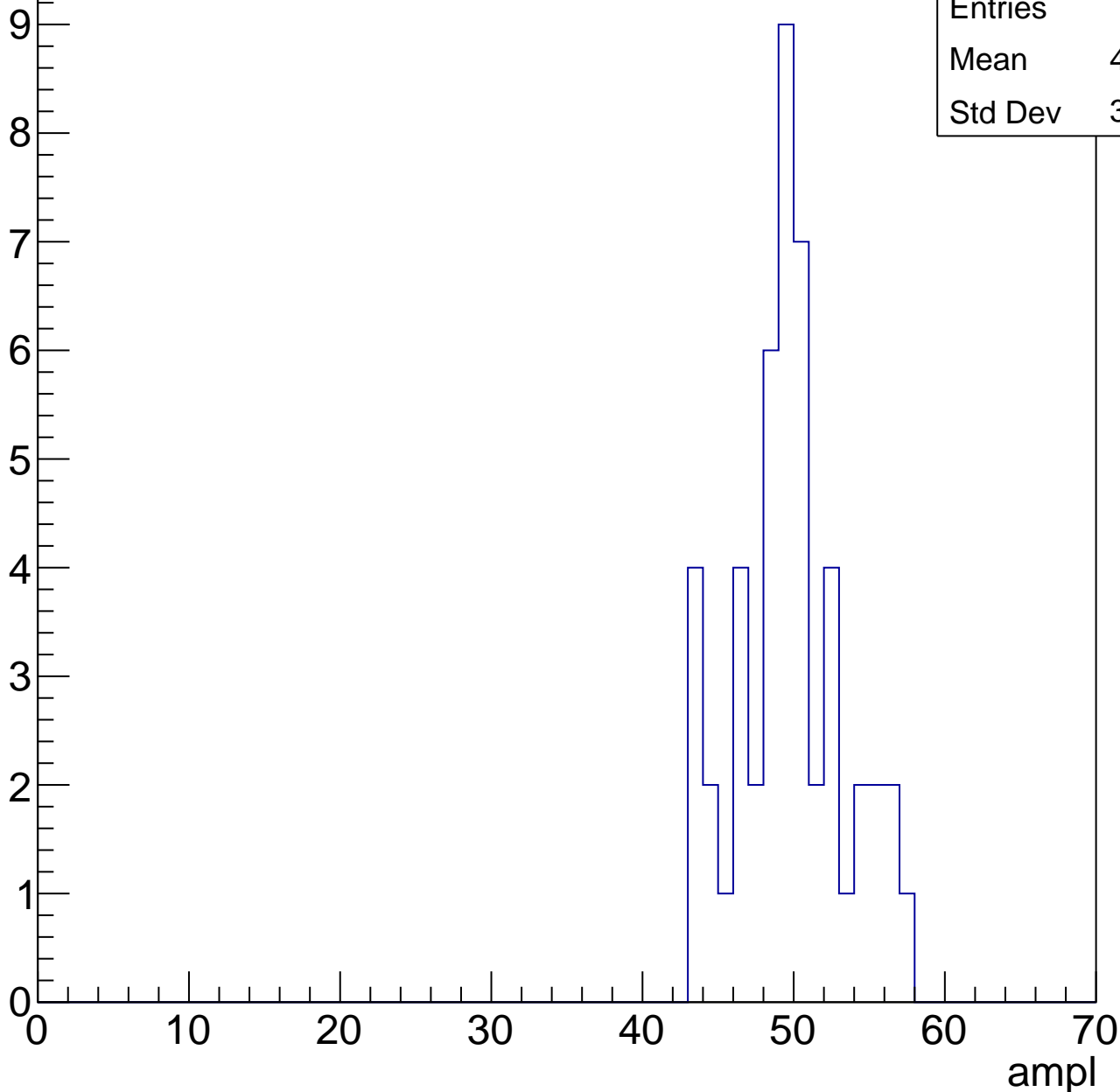


B1L101S, U4-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	49.22
Std Dev	3.524

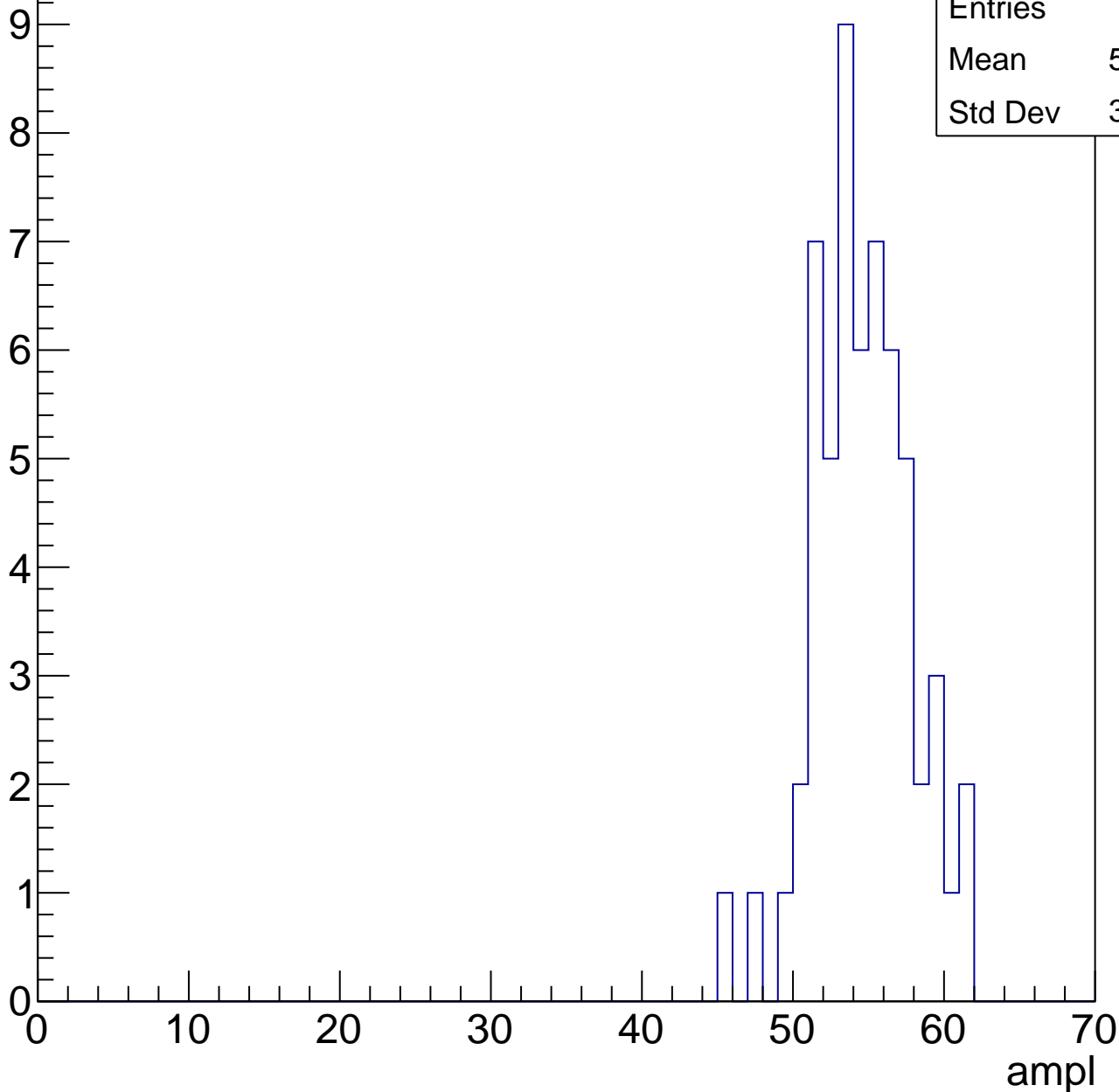


B1L101S, U4-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	54.14
Std Dev	3.203



B1L101S, U4-ch88, adc5

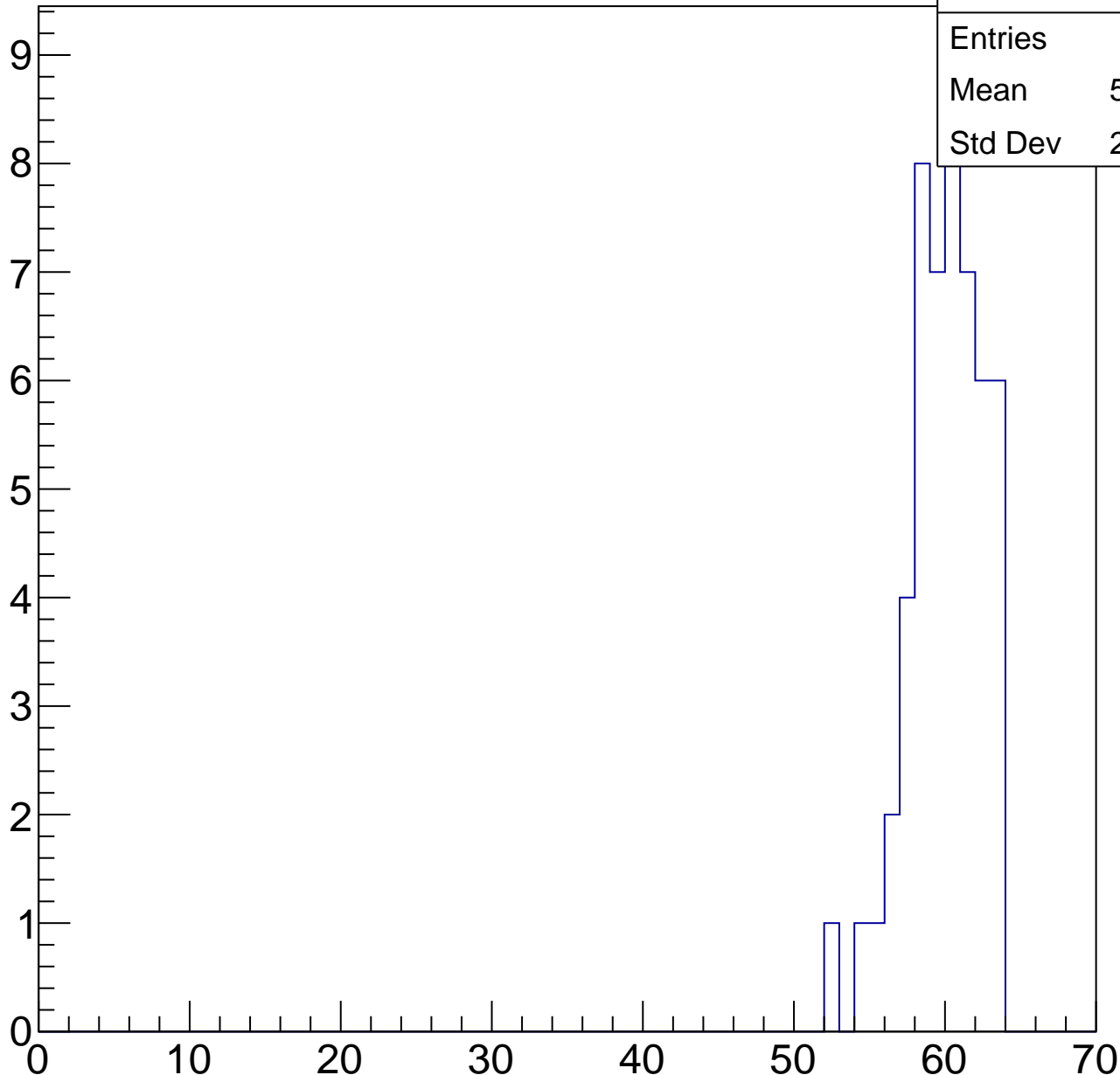
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.52
Std Dev	2.422

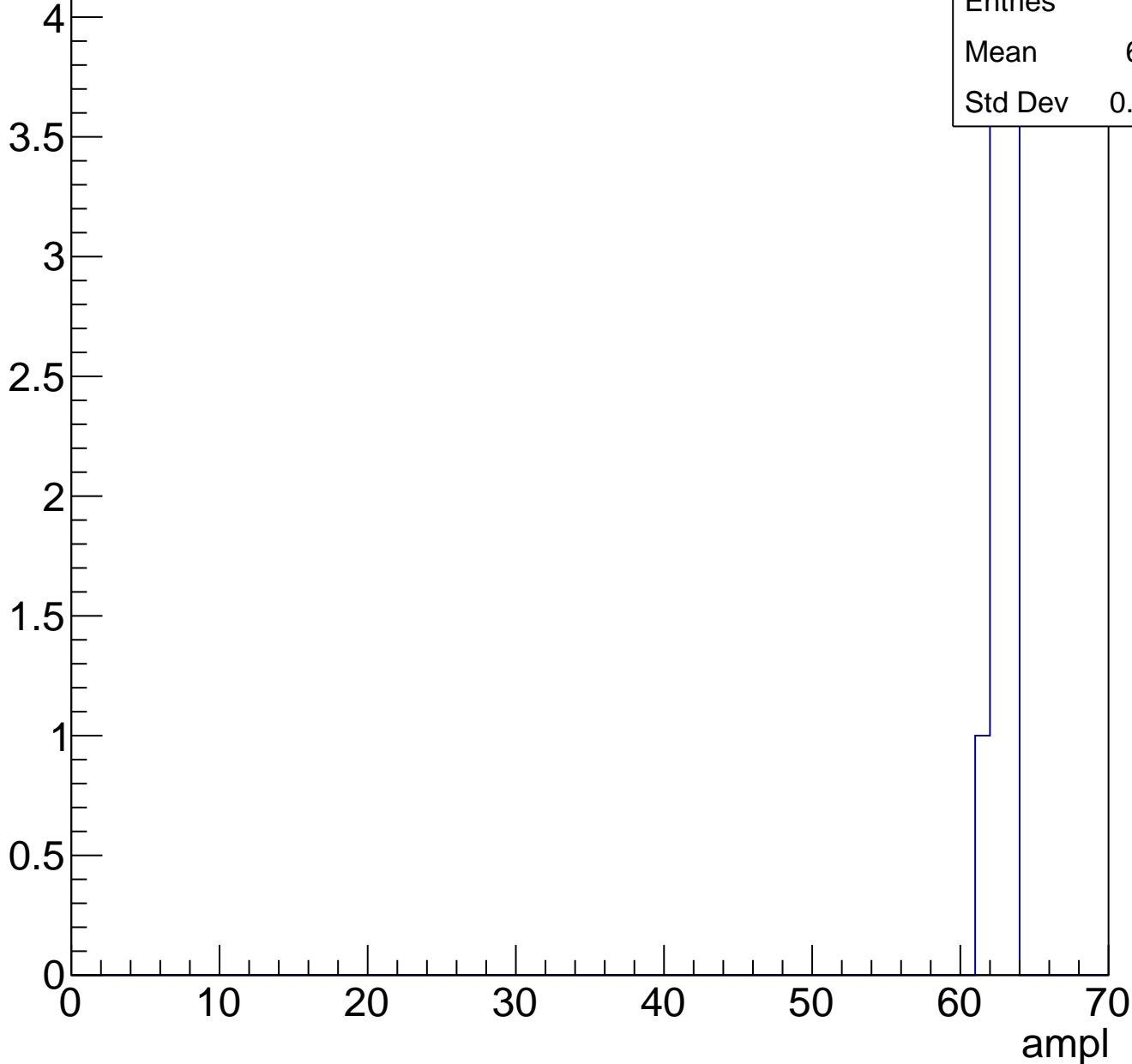
ampl



B1L101S, U4-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch89, adc0

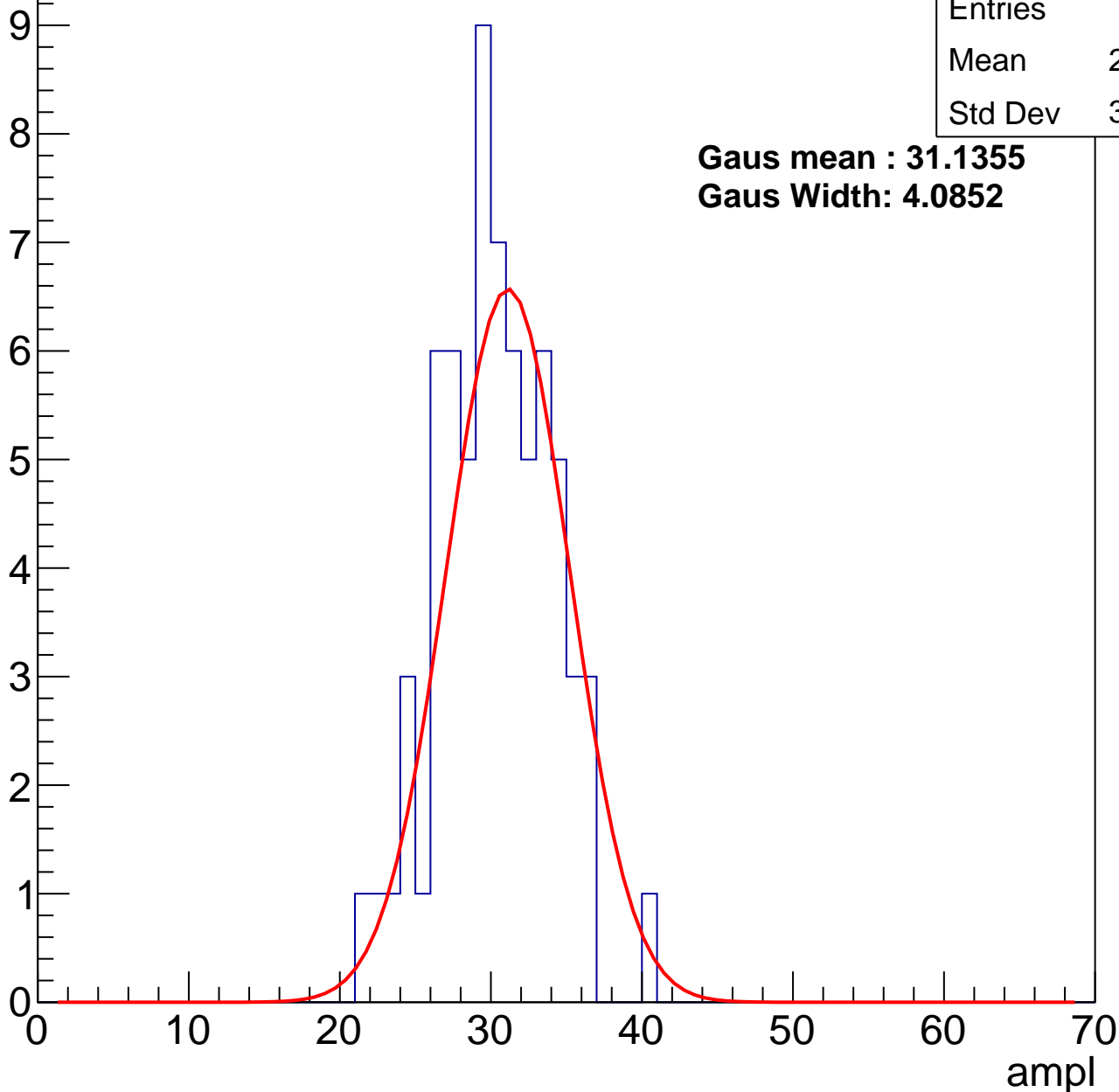
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.84
Std Dev	3.705

Gaus mean : 31.1355

Gaus Width: 4.0852



B1L101S, U4-ch89, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	36.89
Std Dev	3.714

Gaus mean : 37.5721

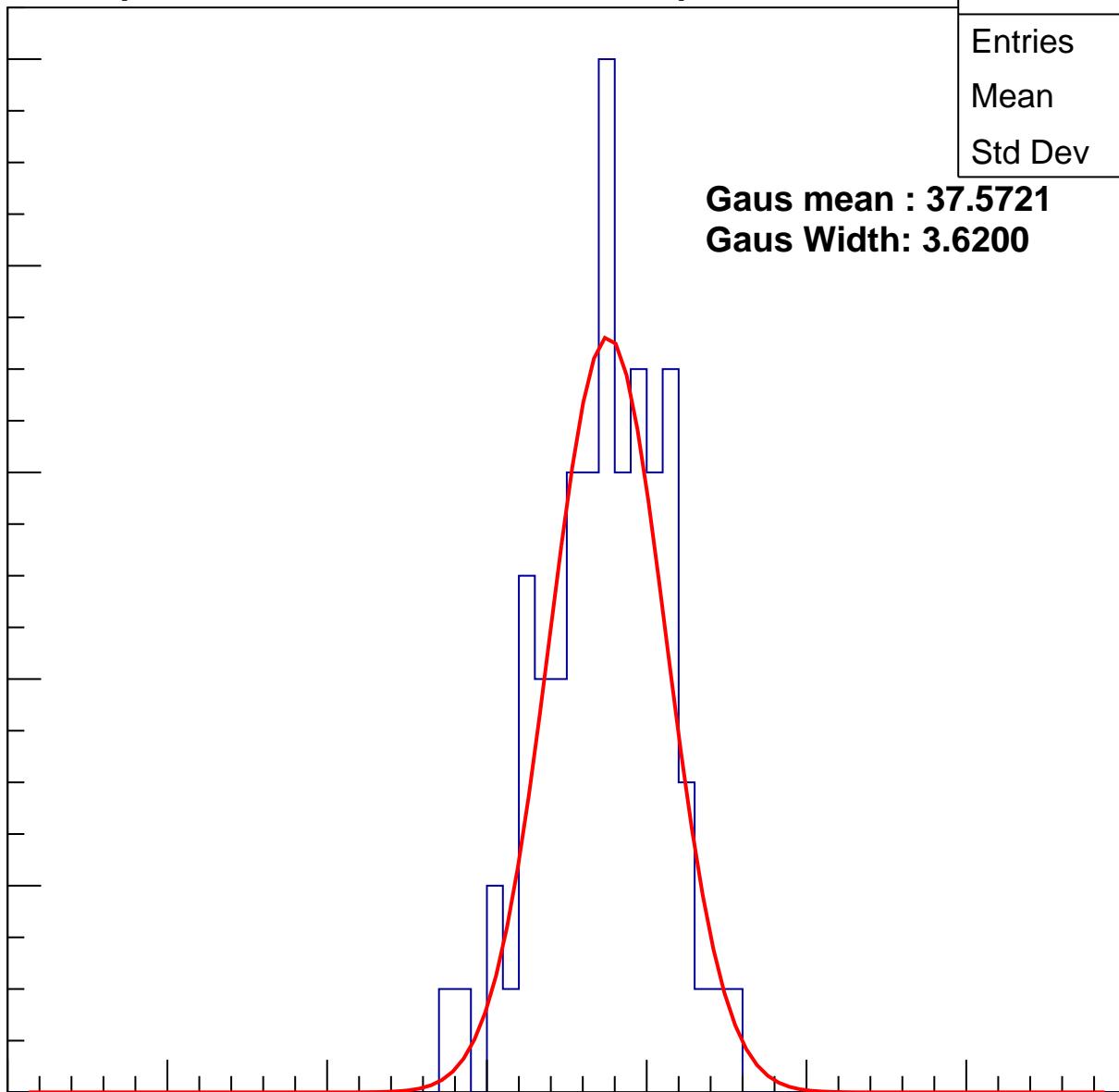
Gaus Width: 3.6200

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch89, adc2

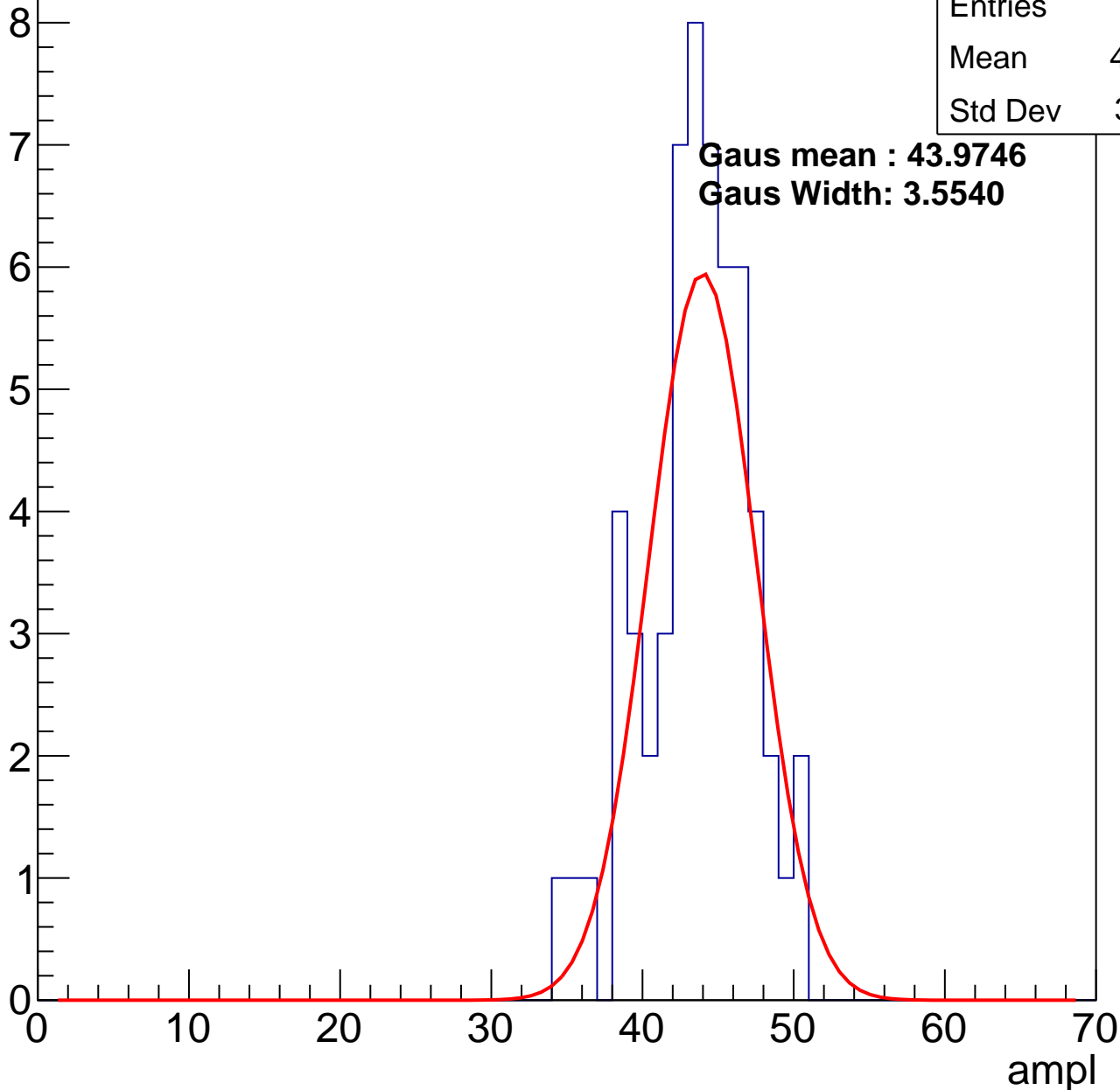
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.14
Std Dev	3.501

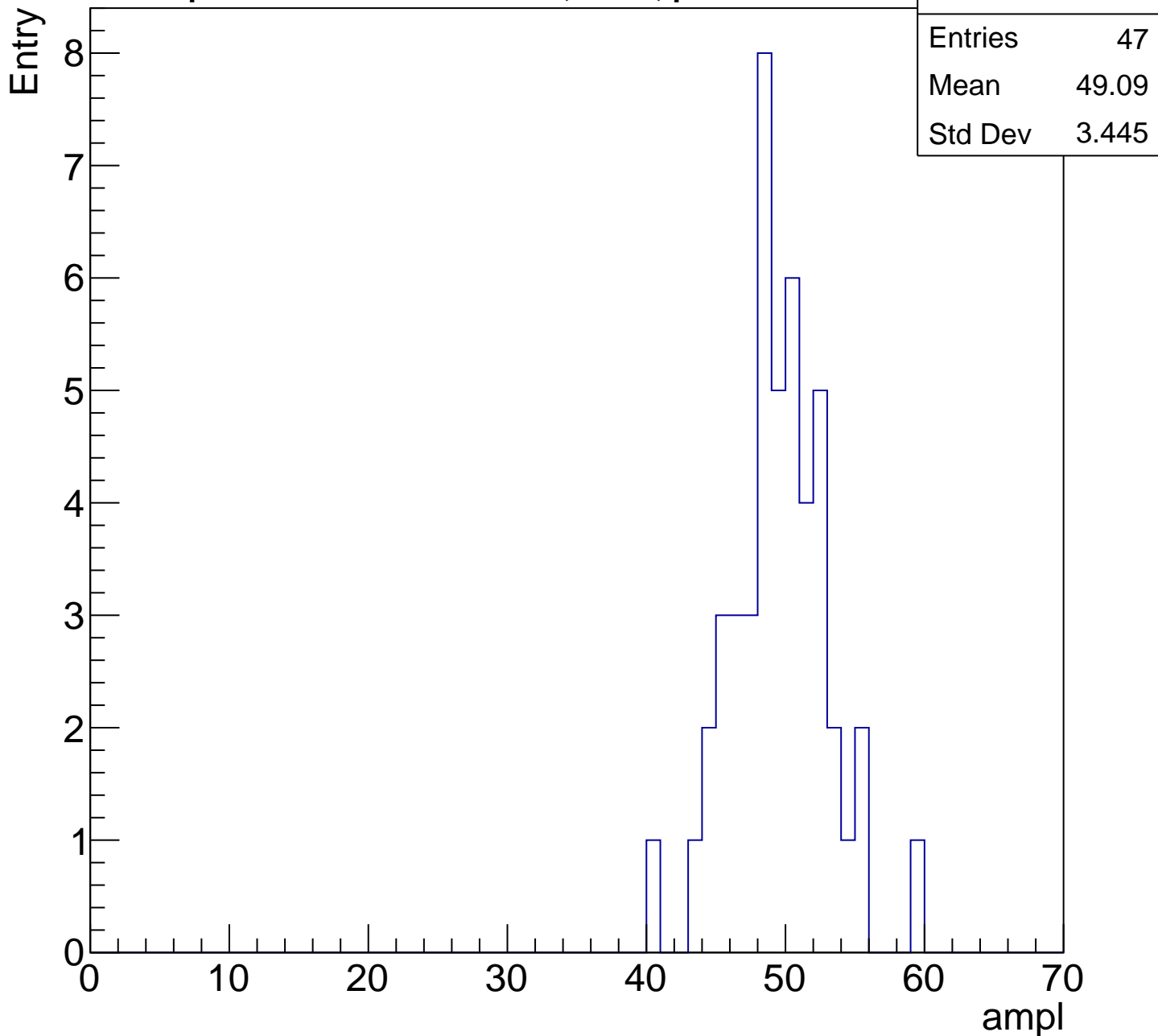
Gaus mean : 43.9746

Gaus Width: 3.5540



B1L101S, U4-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

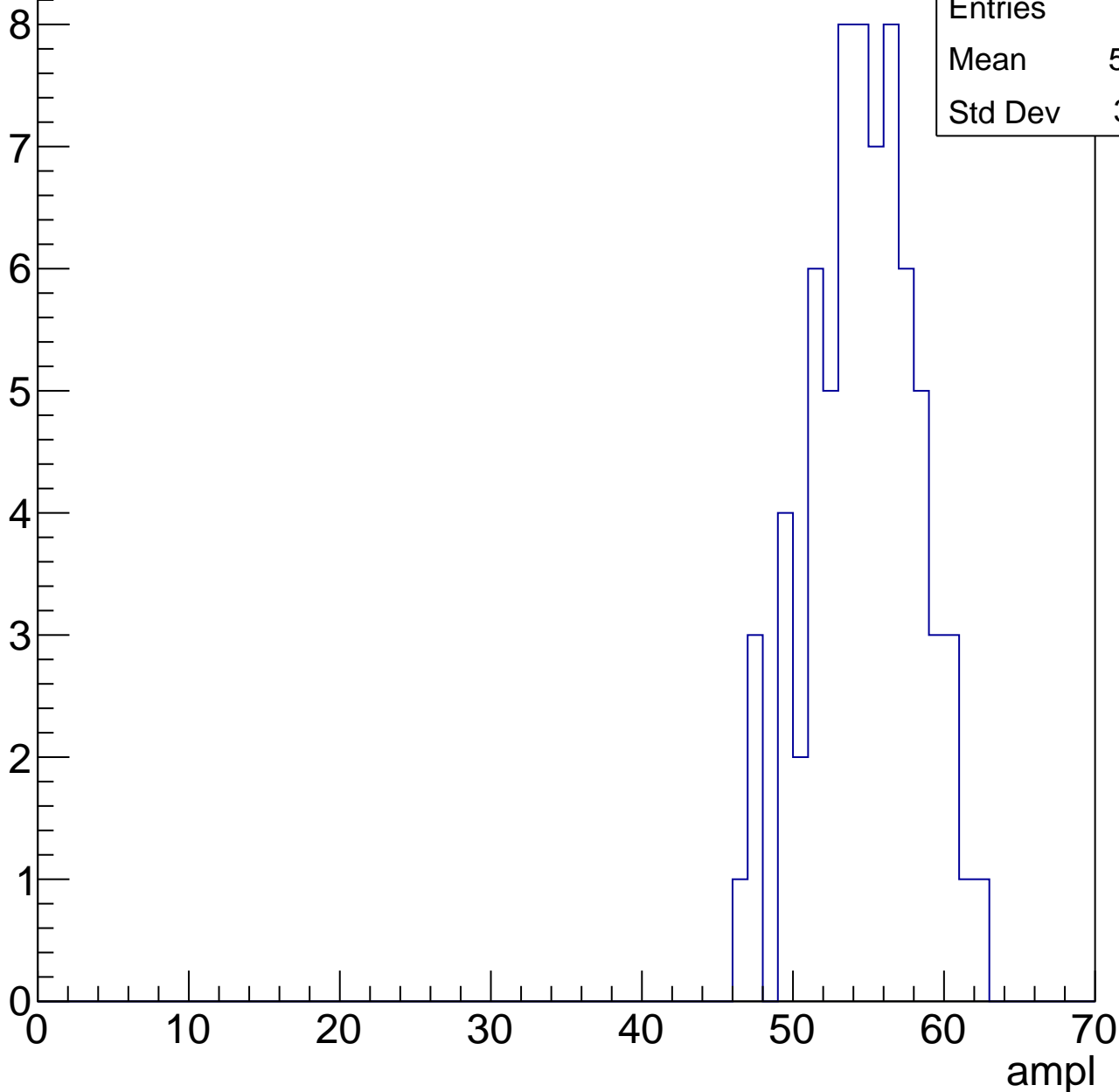


B1L101S, U4-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	54.23
Std Dev	3.541

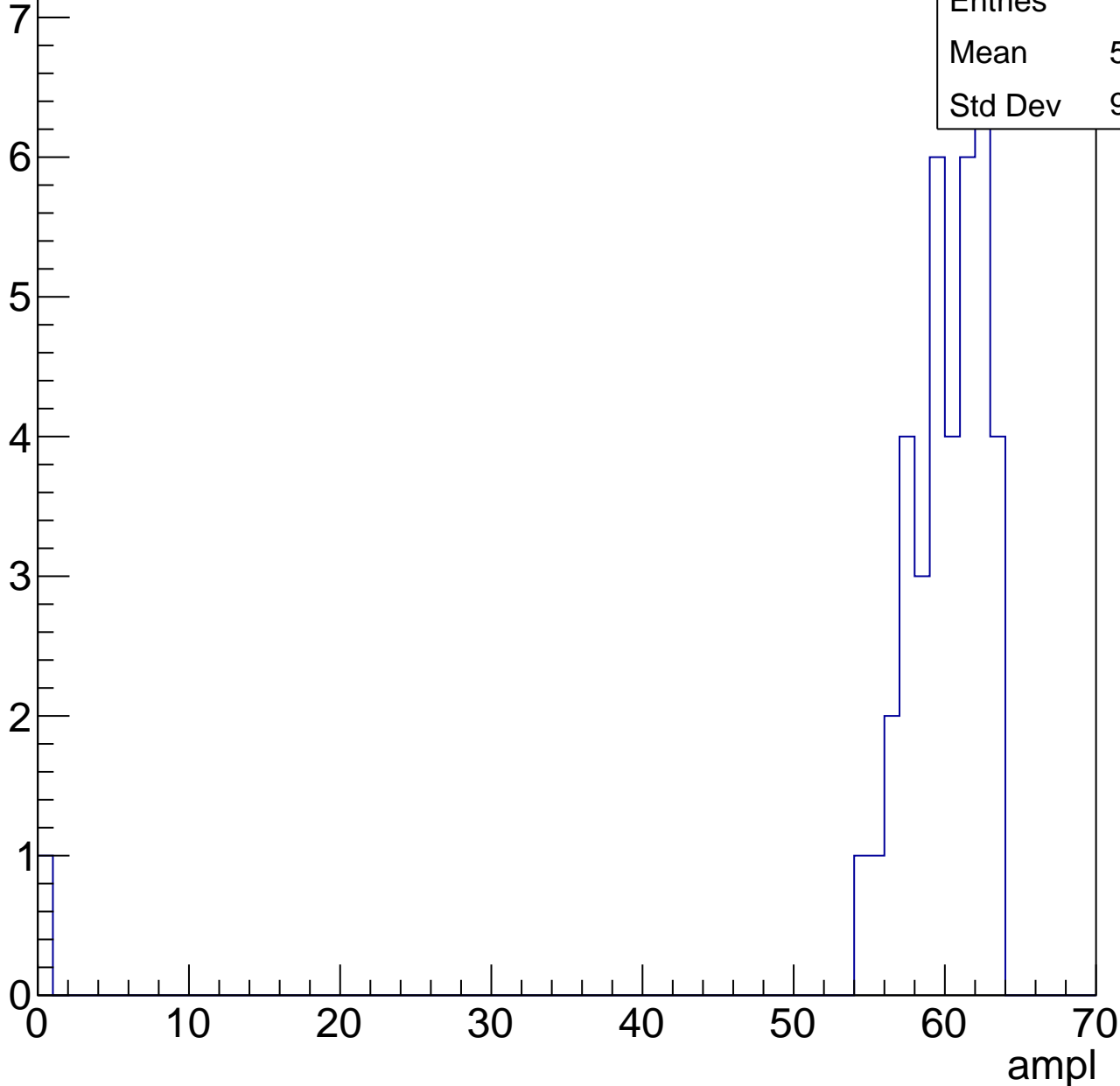


B1L101S, U4-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

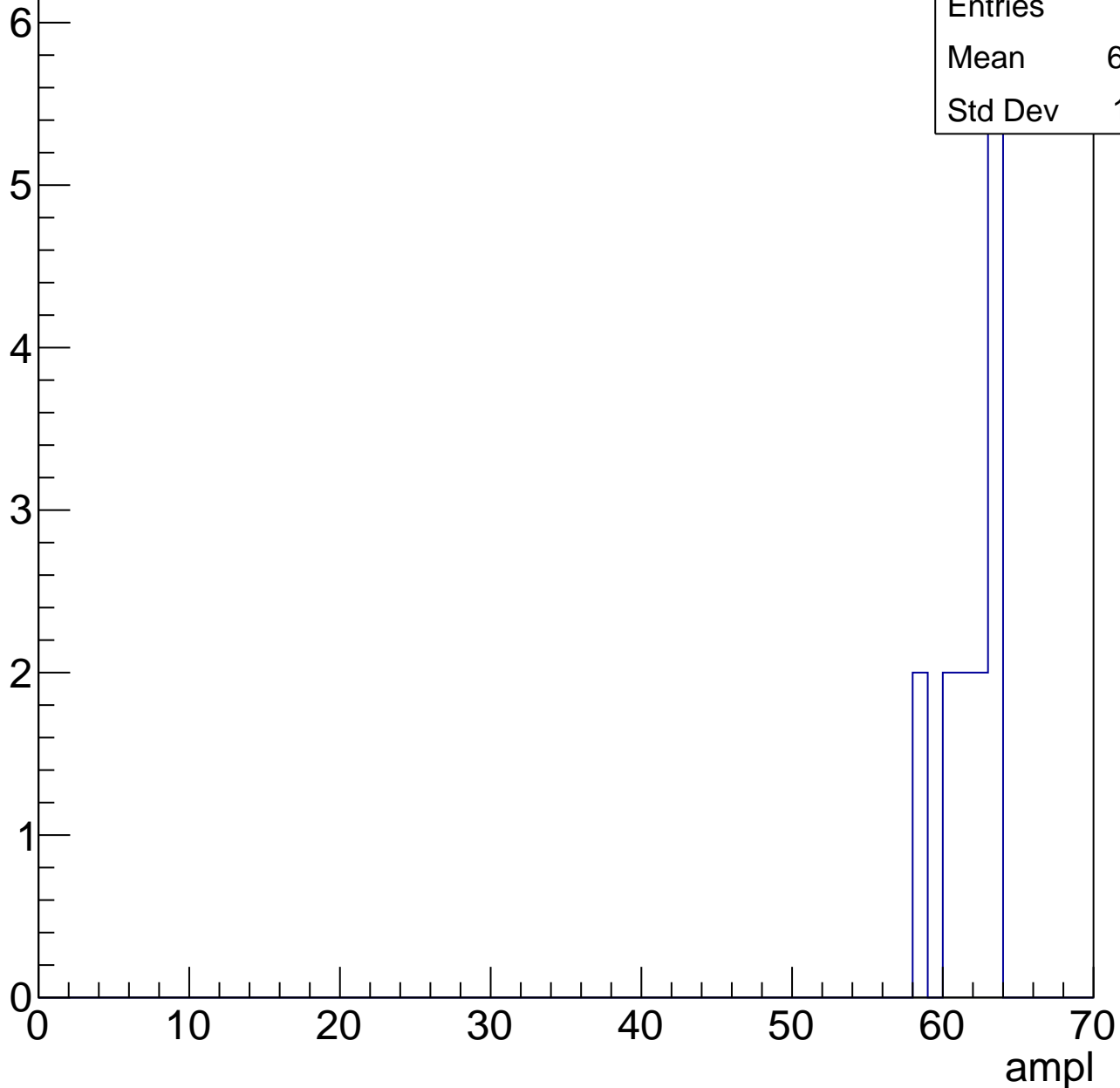
Entries	39
Mean	58.18
Std Dev	9.724



B1L101S, U4-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

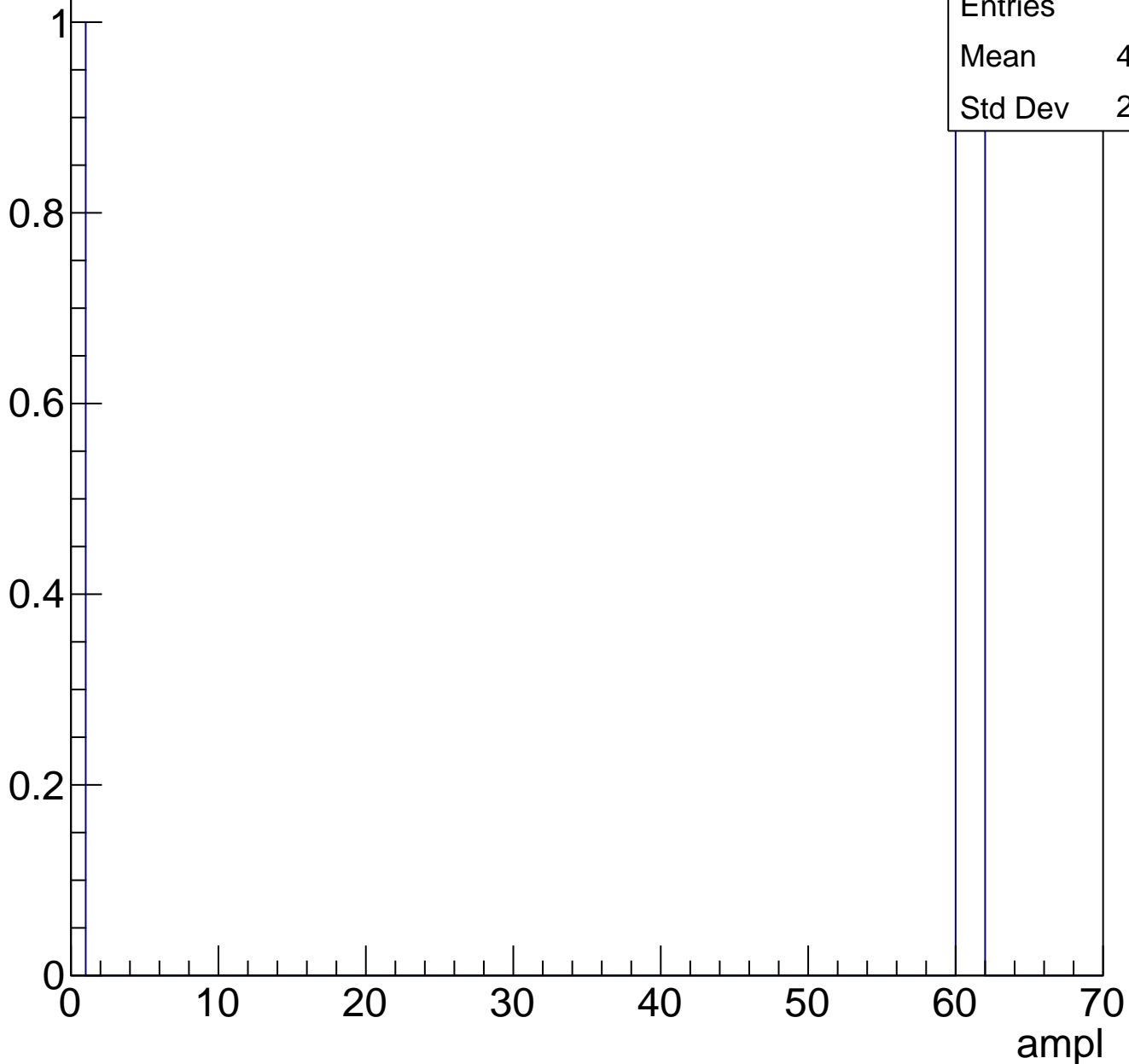
Entry



B1L101S, U4-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch90, adc0

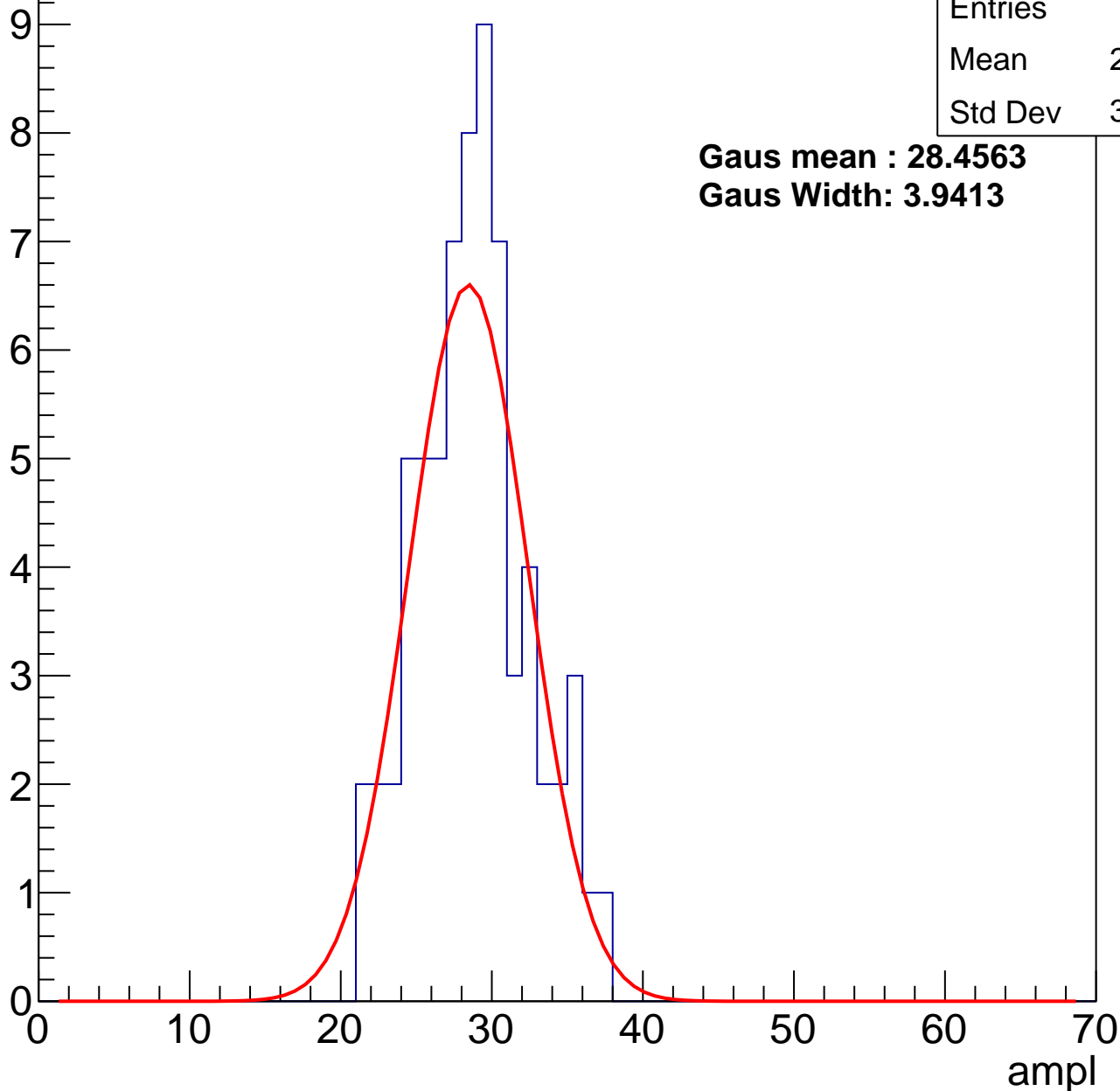
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.29
Std Dev	3.654

Gaus mean : 28.4563

Gaus Width: 3.9413



B1L101S, U4-ch90, adc1

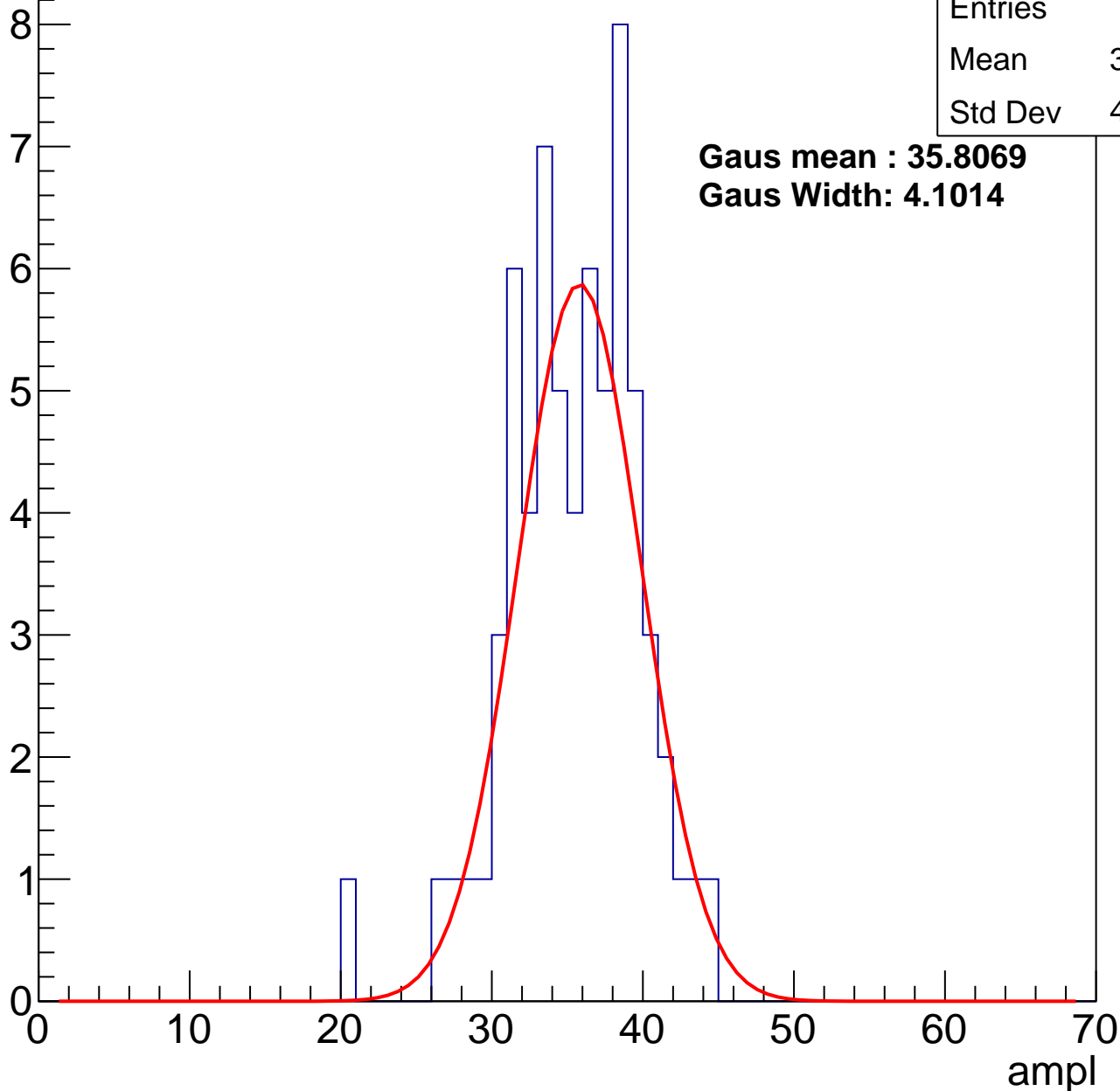
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.94
Std Dev	4.288

Gaus mean : 35.8069

Gaus Width: 4.1014



B1L101S, U4-ch90, adc2

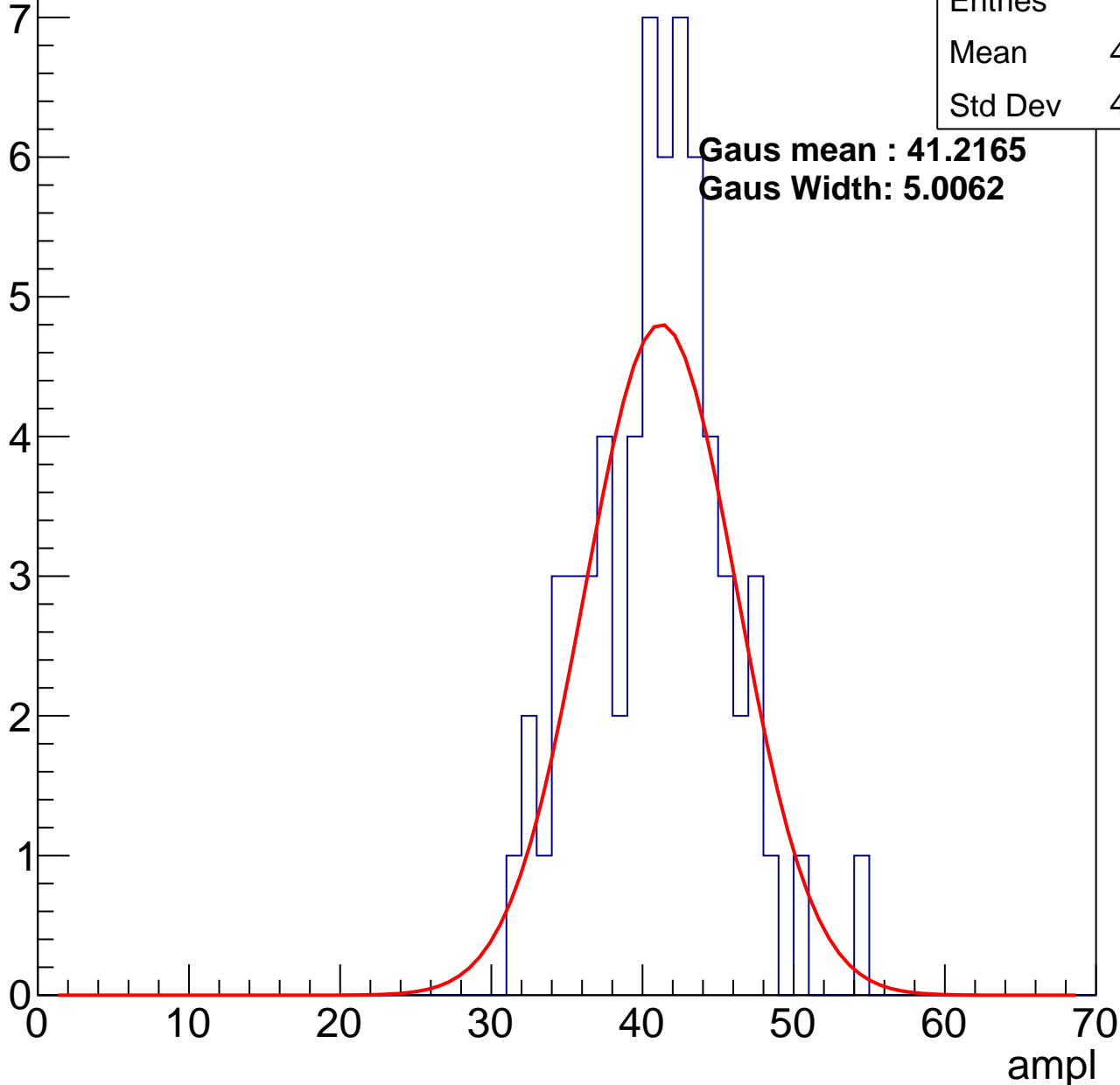
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	40.58
Std Dev	4.544

Gaus mean : 41.2165

Gaus Width: 5.0062

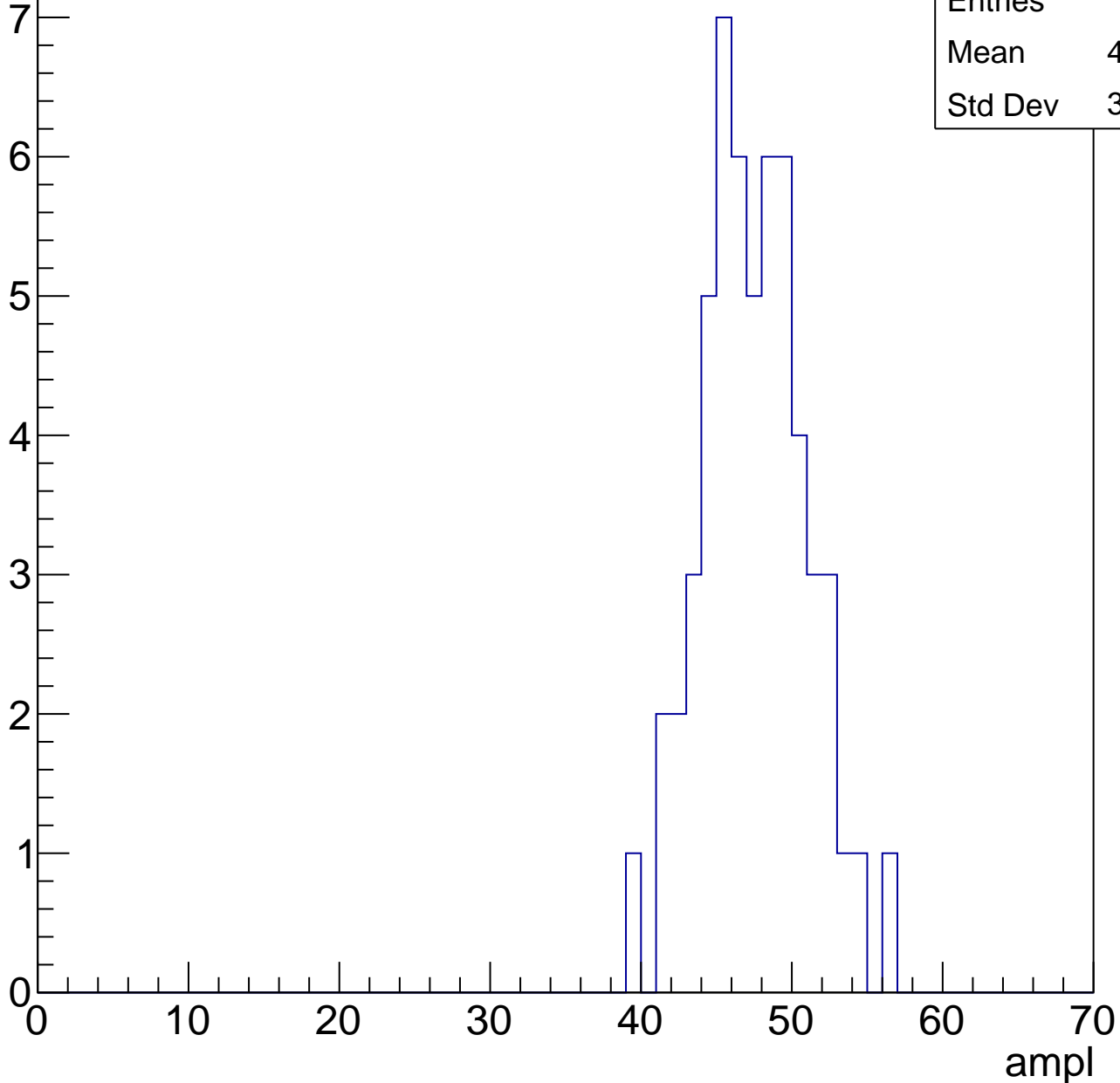


B1L101S, U4-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

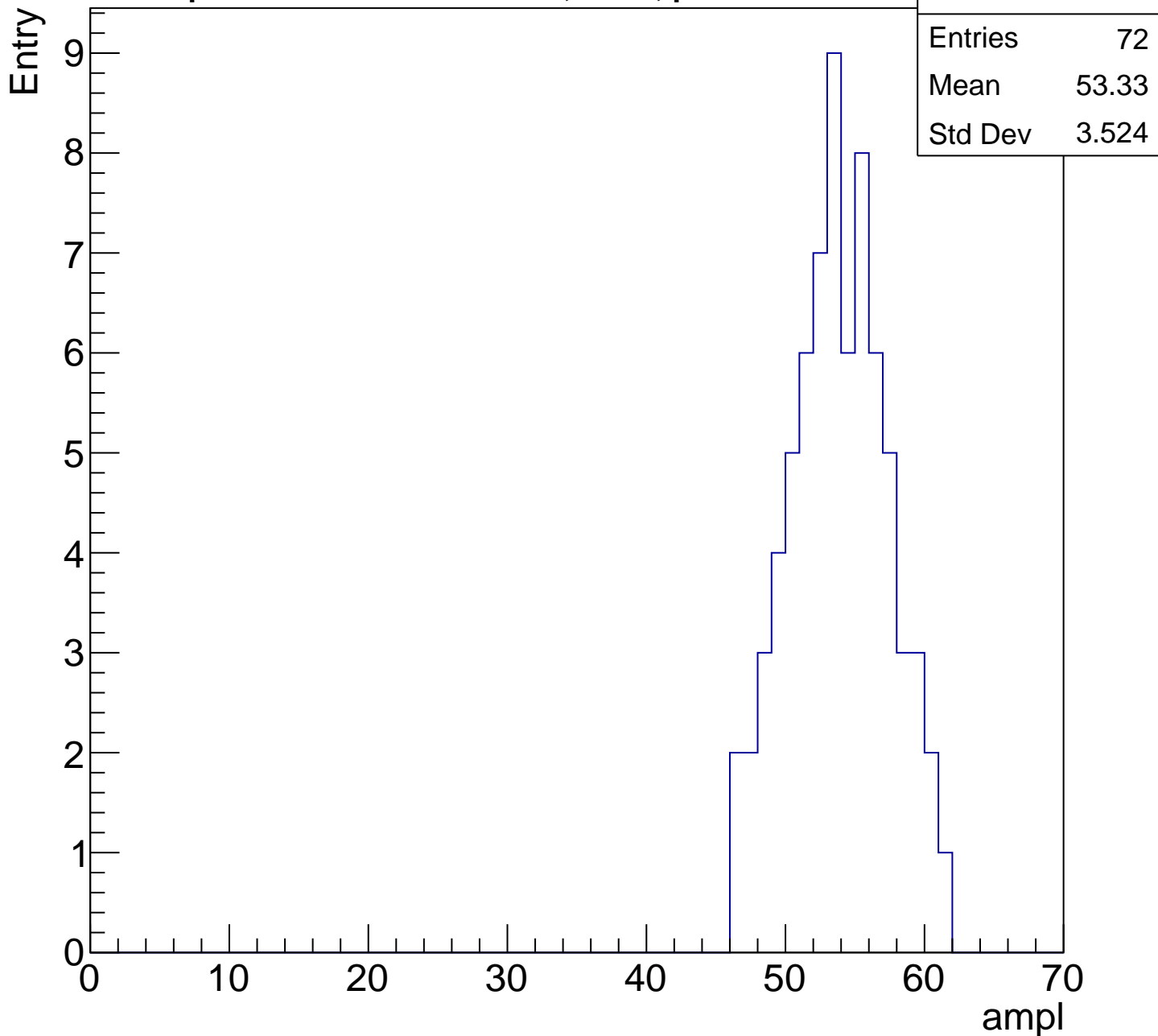
Entry

Entries	56
Mean	47.04
Std Dev	3.454



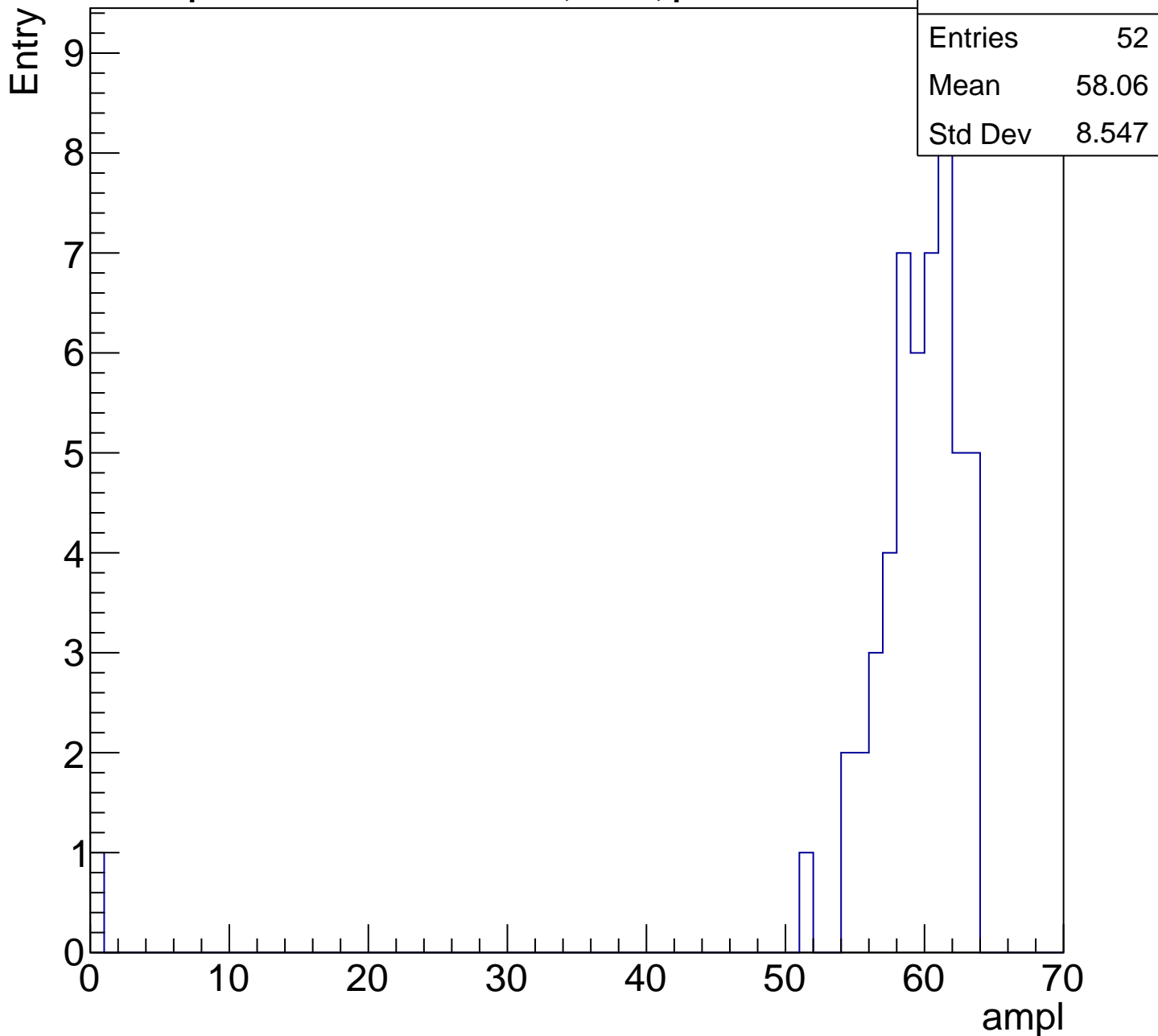
B1L101S, U4-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch90, adc5

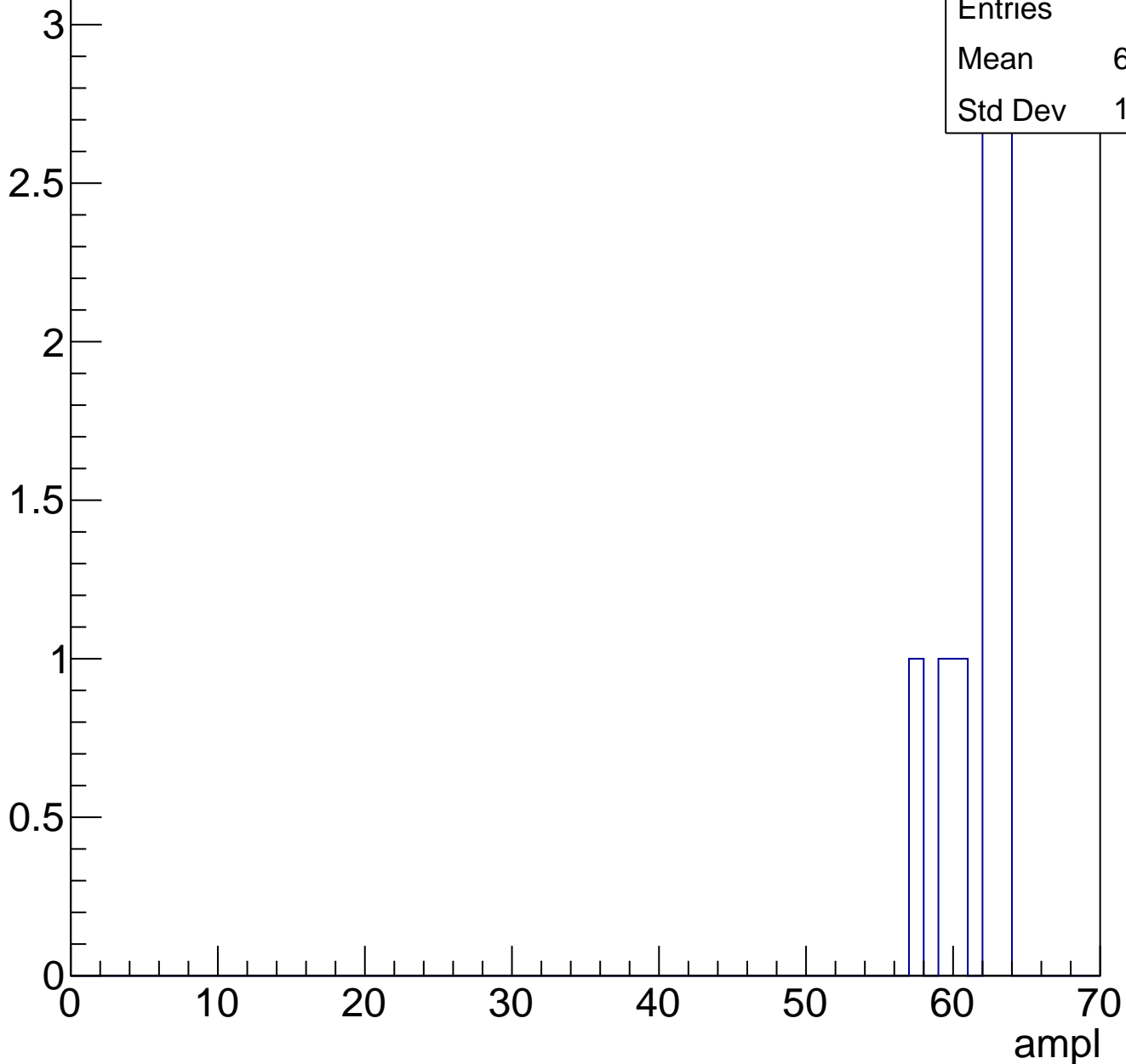
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

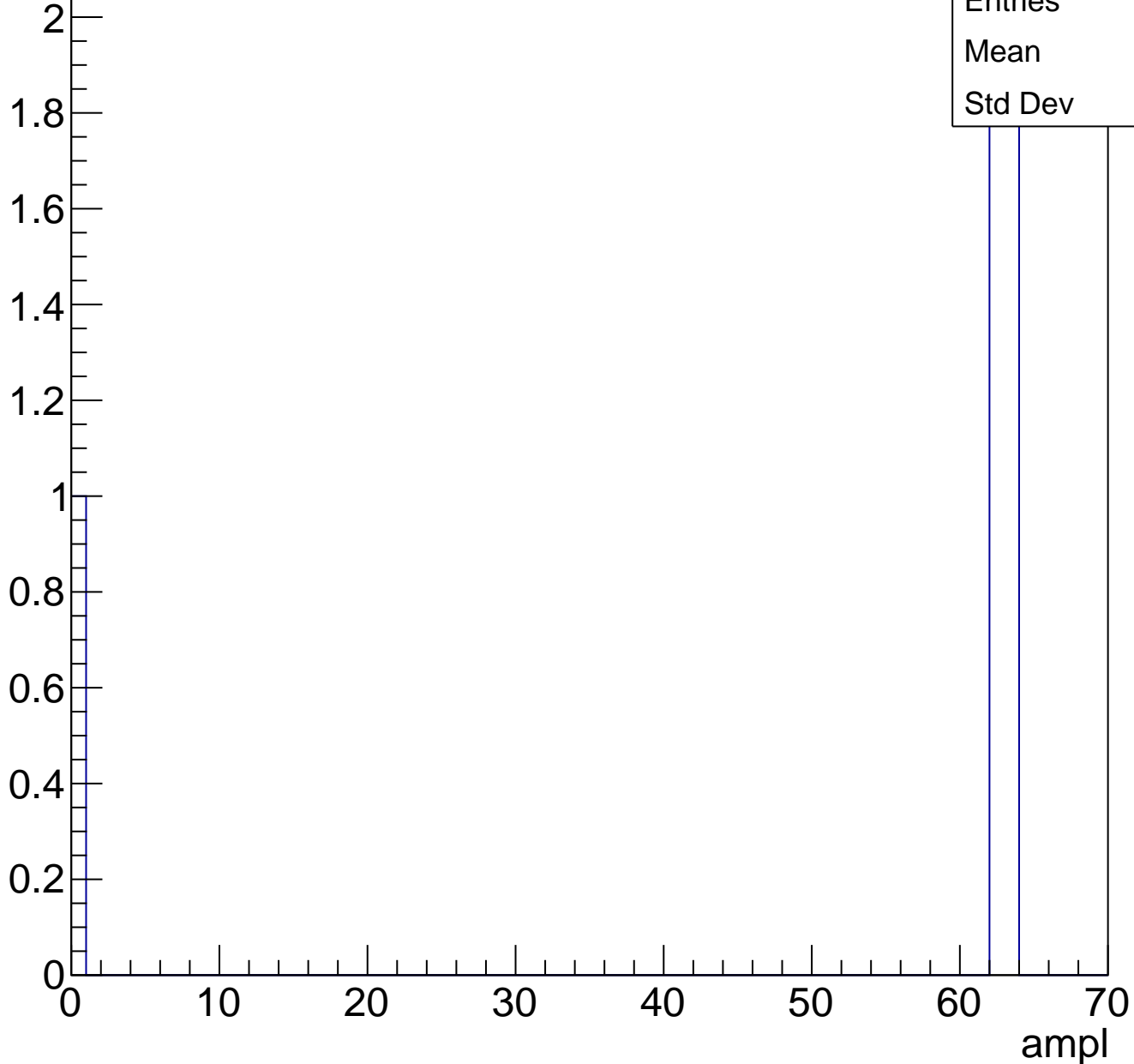
Entry



B1L101S, U4-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch91, adc0

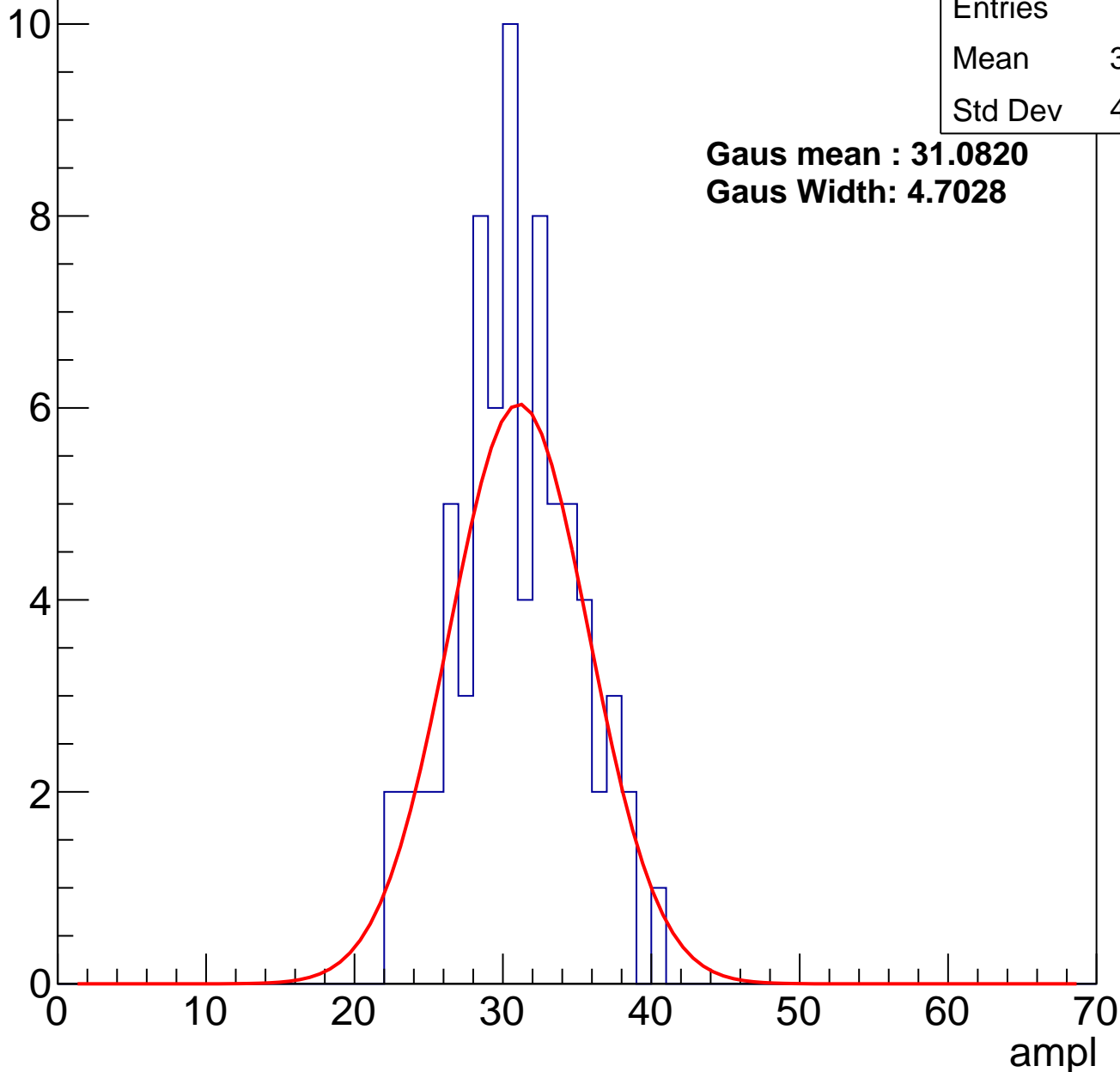
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	30.42
Std Dev	4.007

Gaus mean : 31.0820

Gaus Width: 4.7028

Entry



B1L101S, U4-ch91, adc1

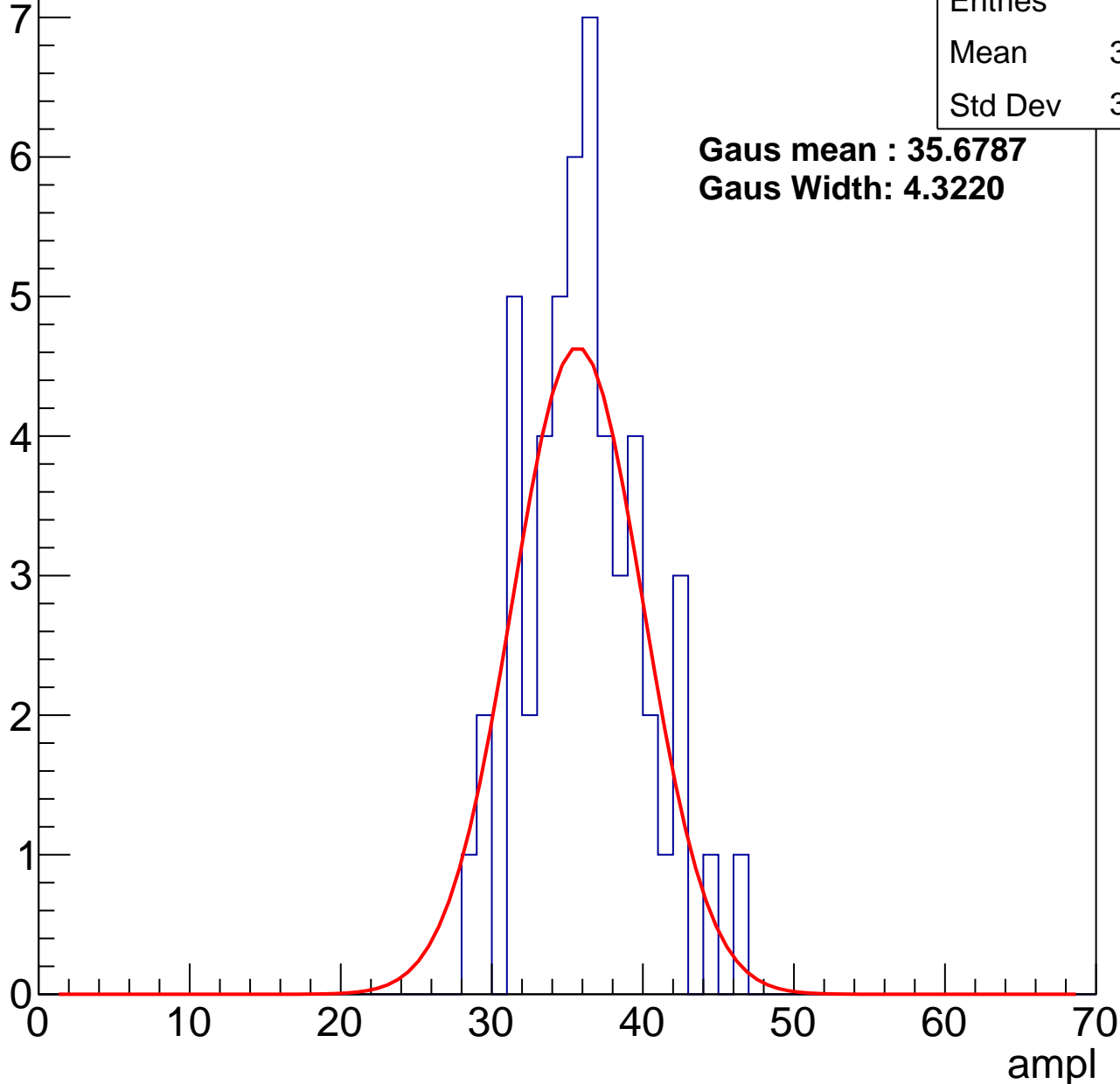
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.76
Std Dev	3.858

Gaus mean : 35.6787

Gaus Width: 4.3220



B1L101S, U4-ch91, adc2

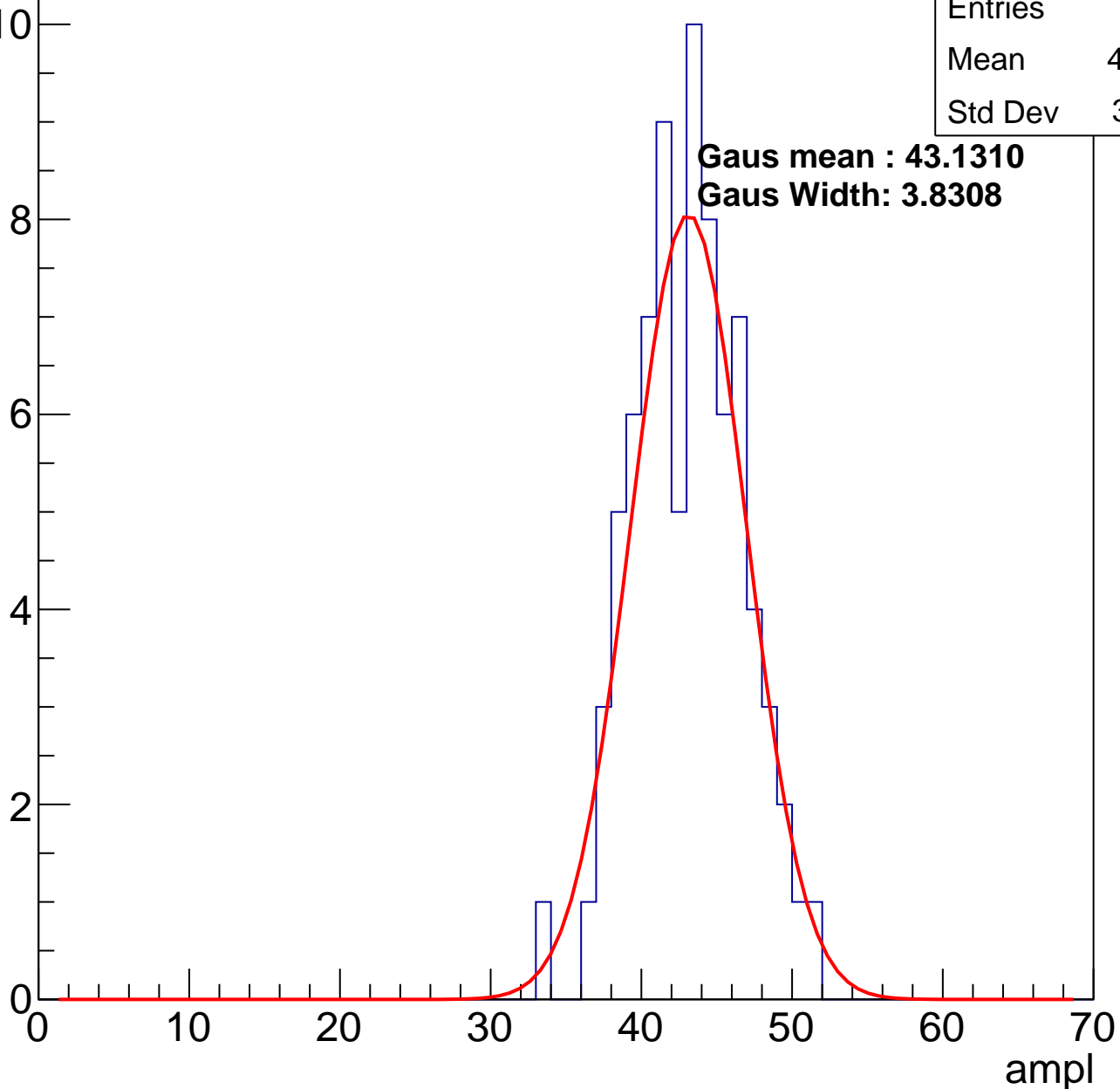
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	42.63
Std Dev	3.541

Gaus mean : 43.1310

Gaus Width: 3.8308

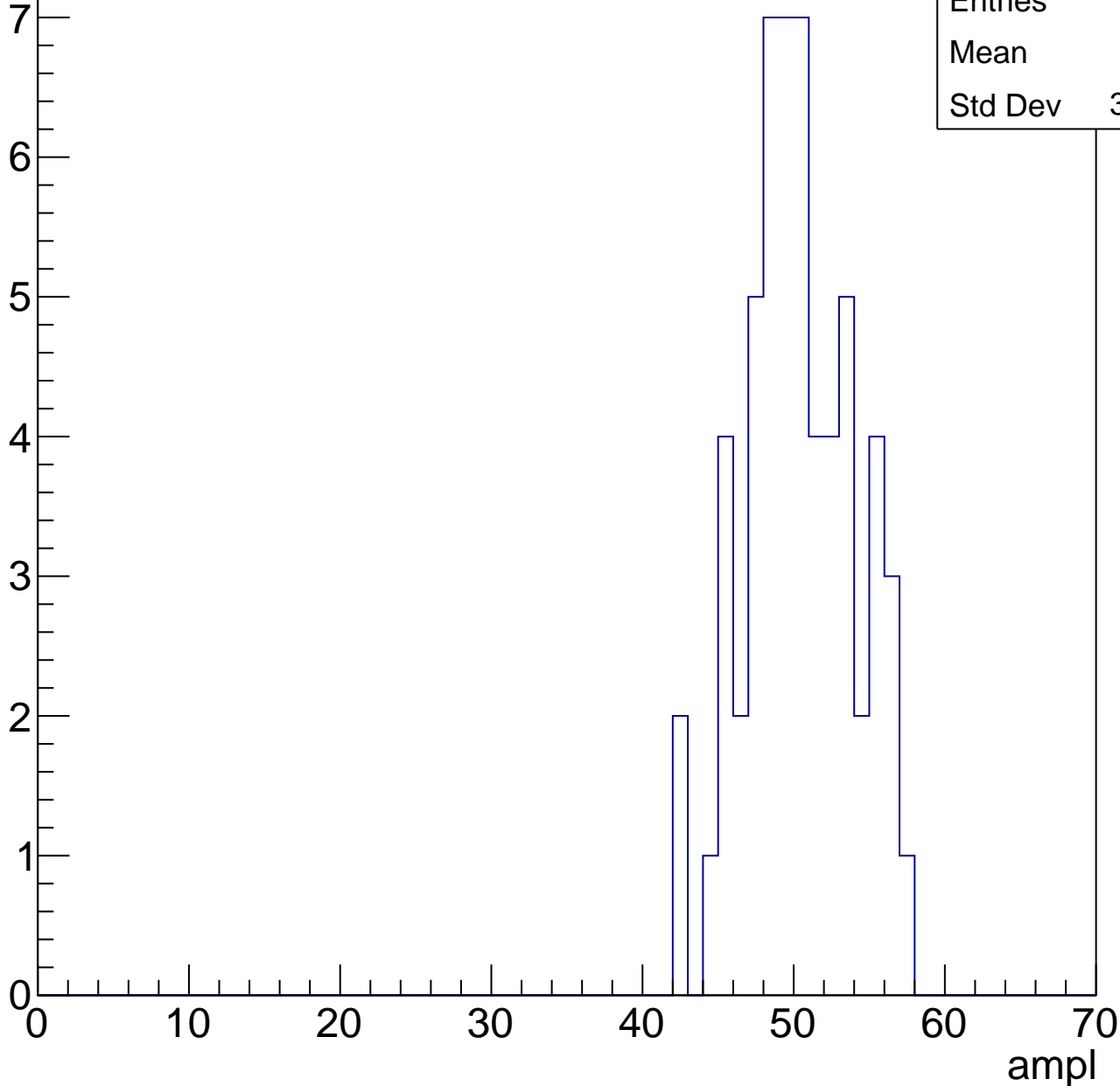


B1L101S, U4-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

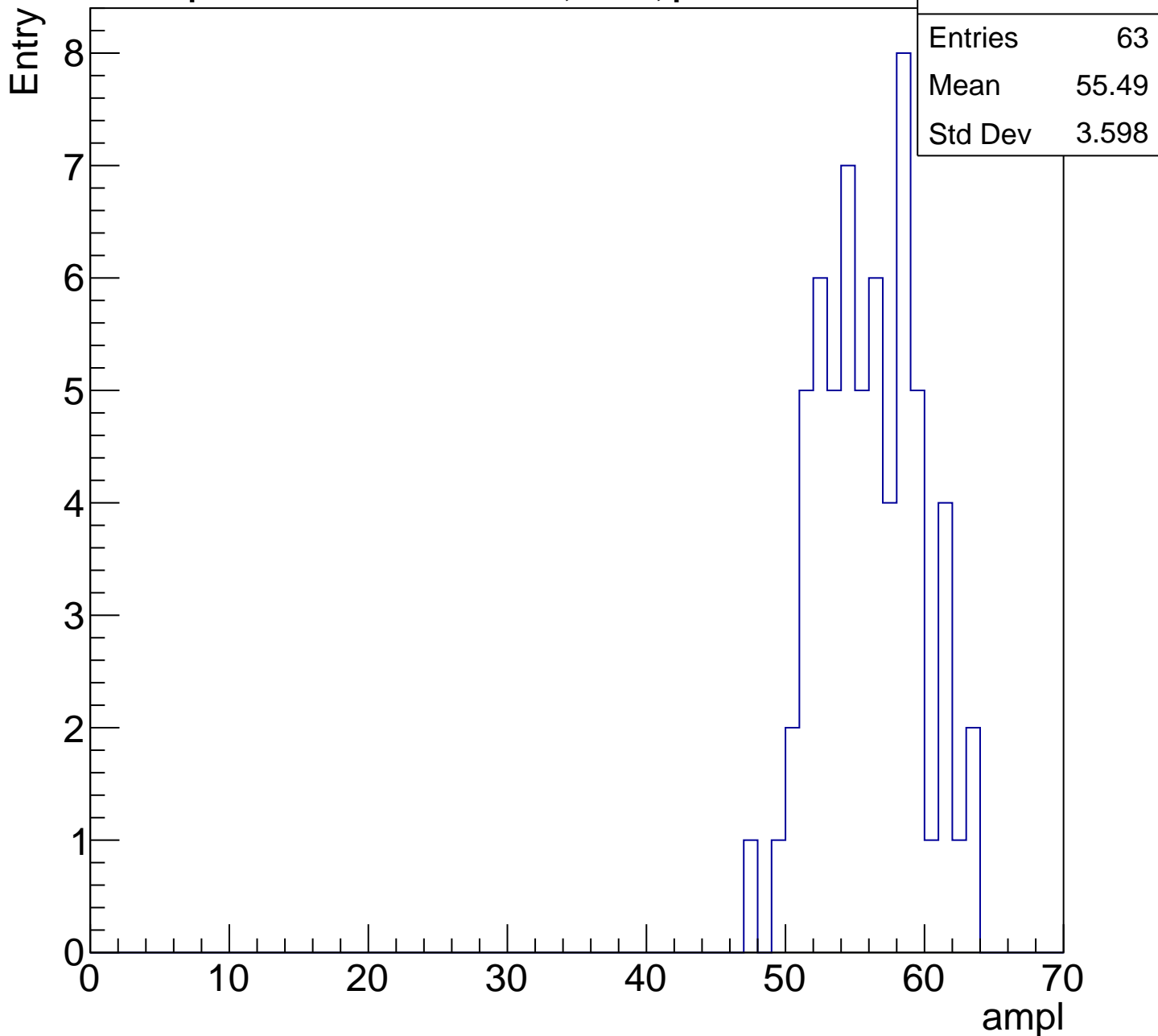
Entry

Entries	58
Mean	49.9
Std Dev	3.546



B1L101S, U4-ch91, adc4

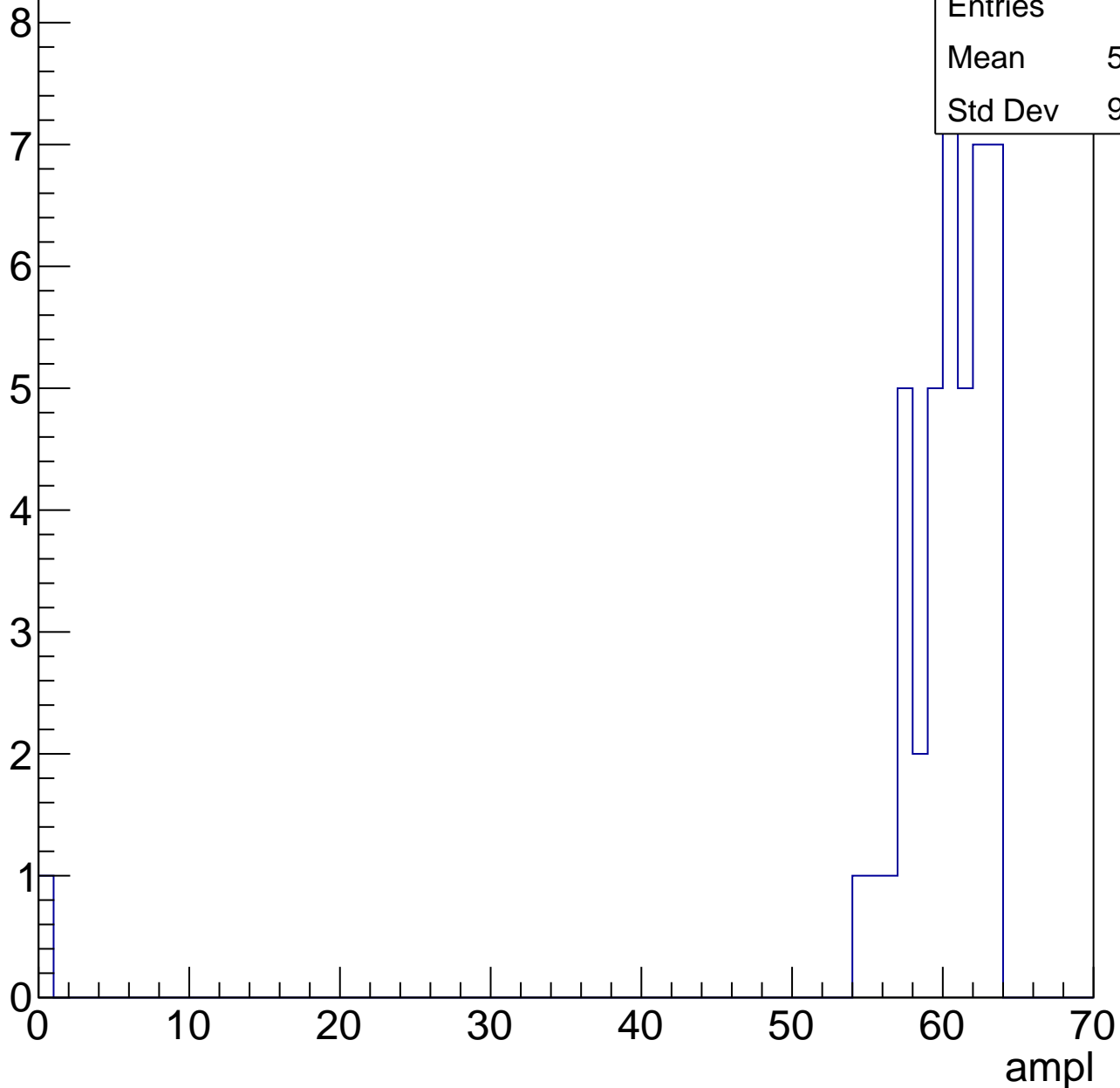
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B1L101S, U4-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch92, adc0

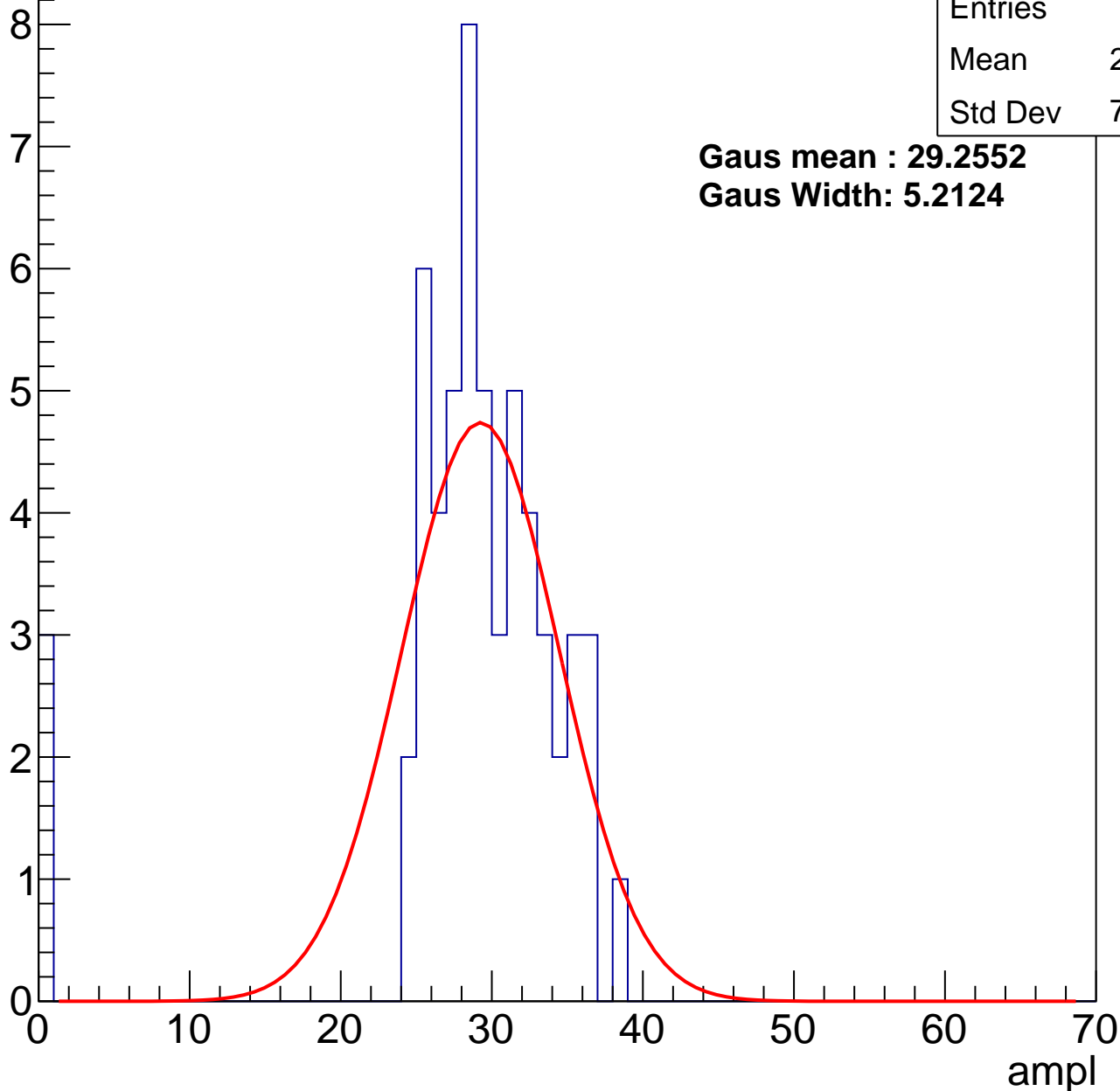
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.02
Std Dev	7.459

Gaus mean : 29.2552

Gaus Width: 5.2124



B1L101S, U4-ch92, adc1

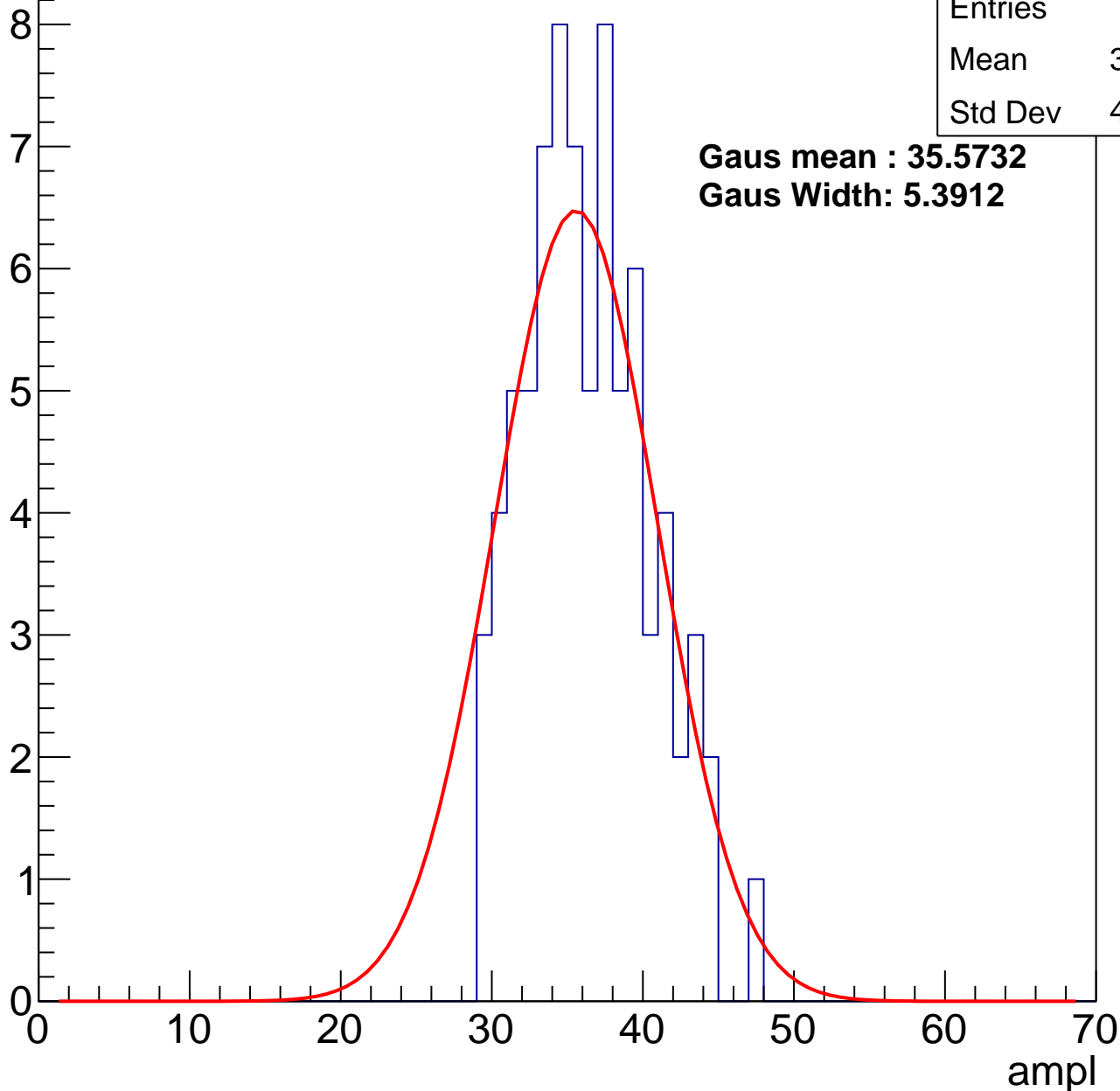
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.92
Std Dev	4.075

Gaus mean : 35.5732

Gaus Width: 5.3912



B1L101S, U4-ch92, adc2

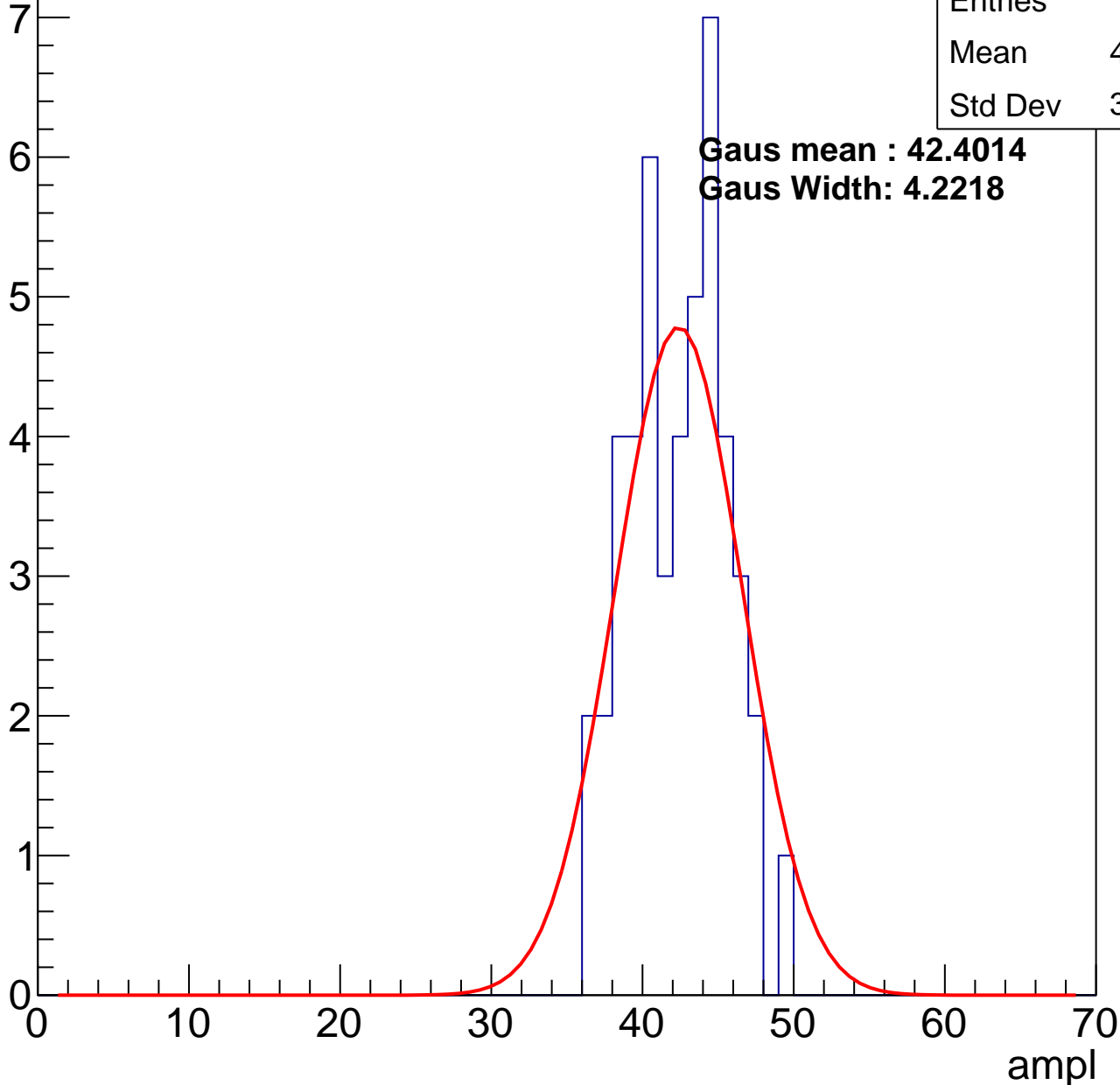
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	41.89
Std Dev	3.144

Gaus mean : 42.4014

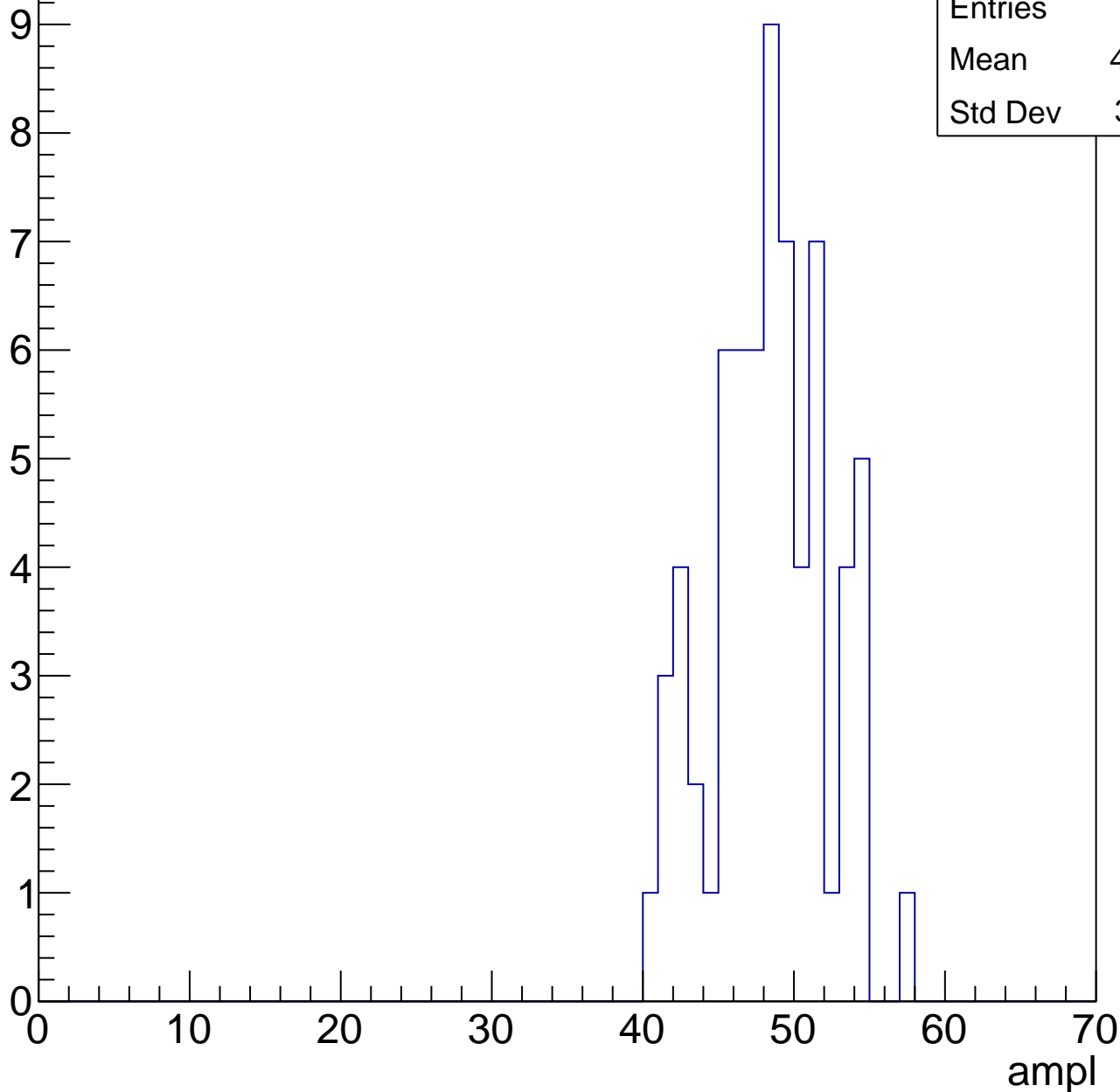
Gaus Width: 4.2218



B1L101S, U4-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

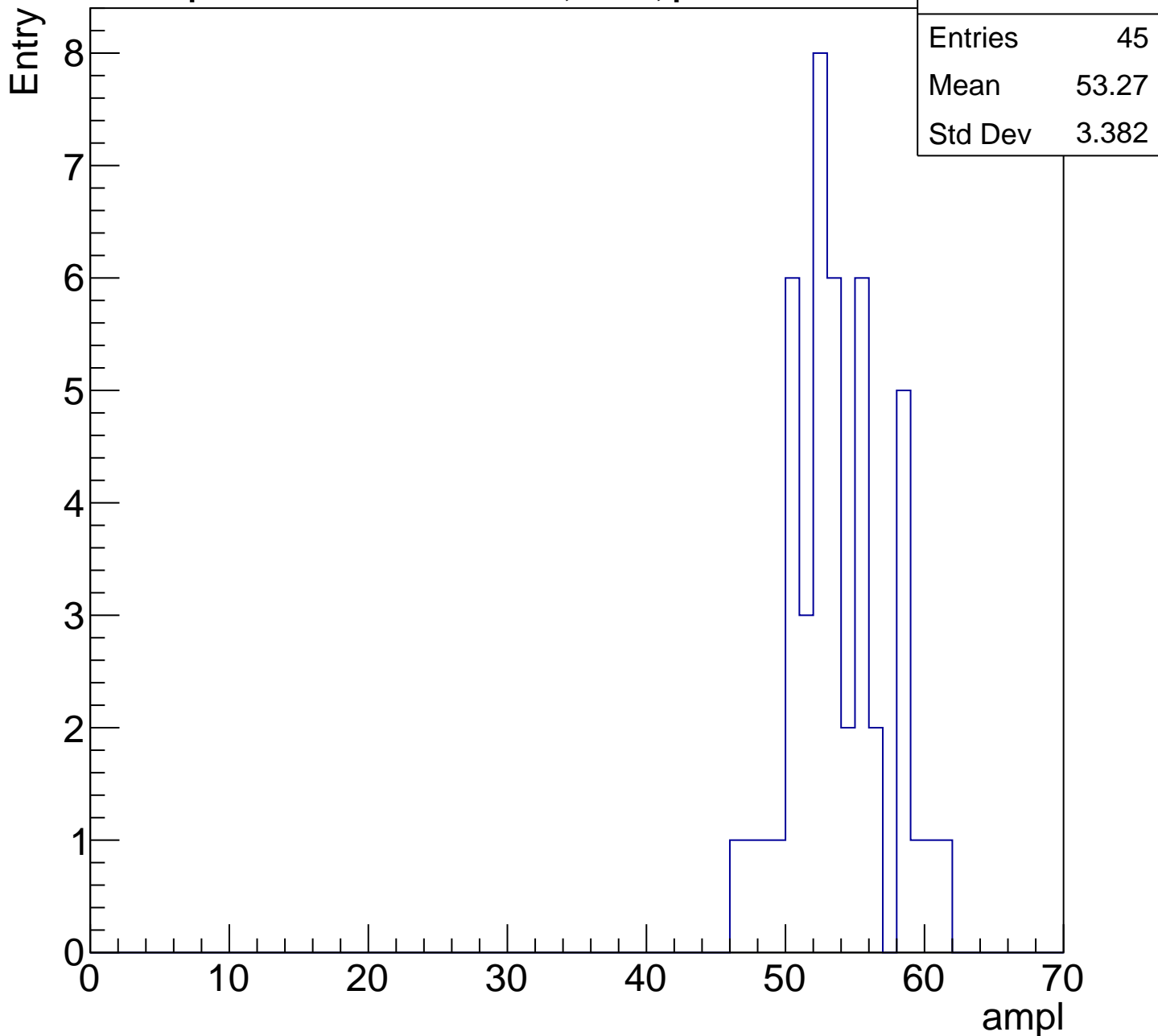
Entry



Entries	67
Mean	47.94
Std Dev	3.801

B1L101S, U4-ch92, adc4

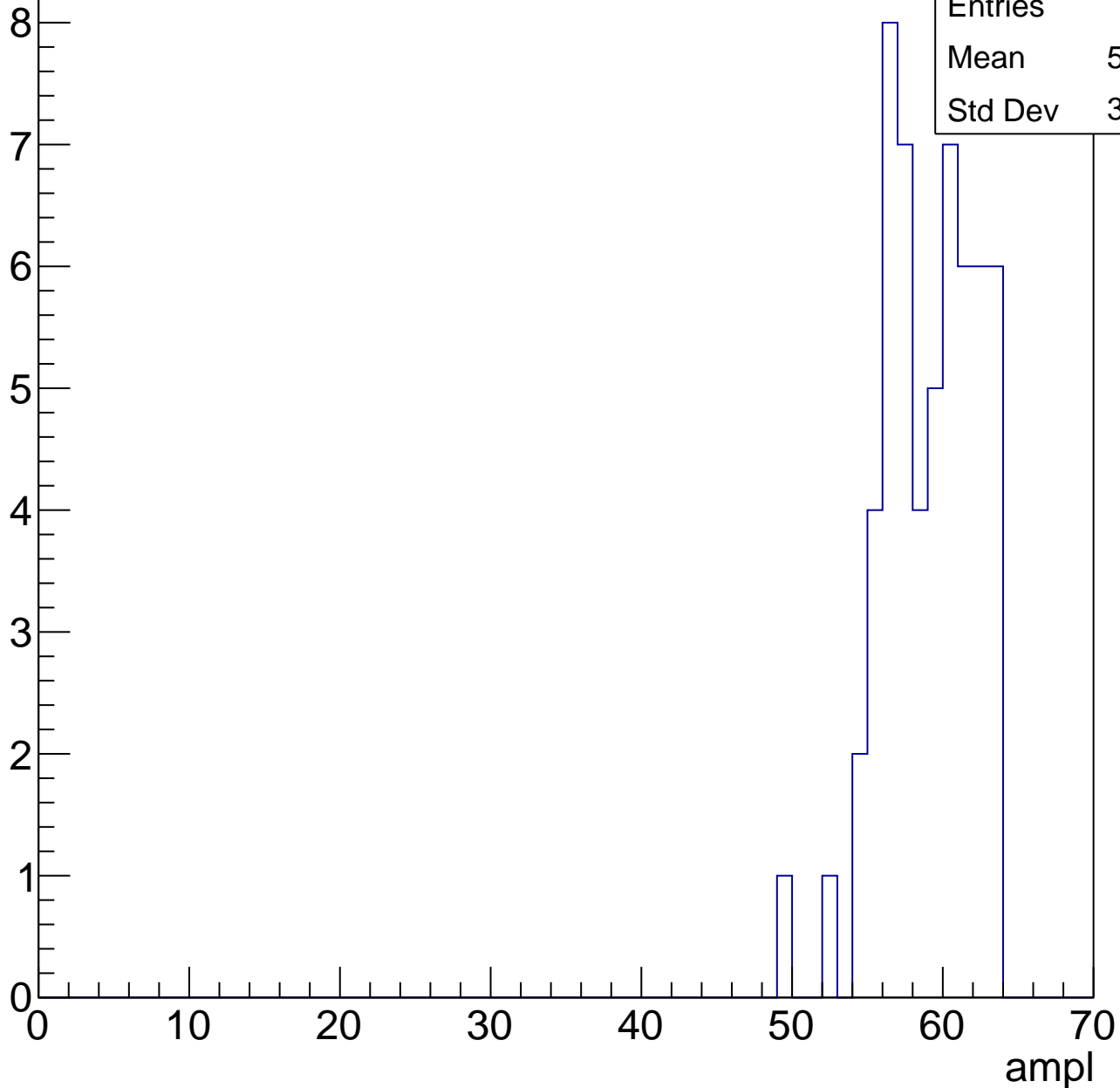
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

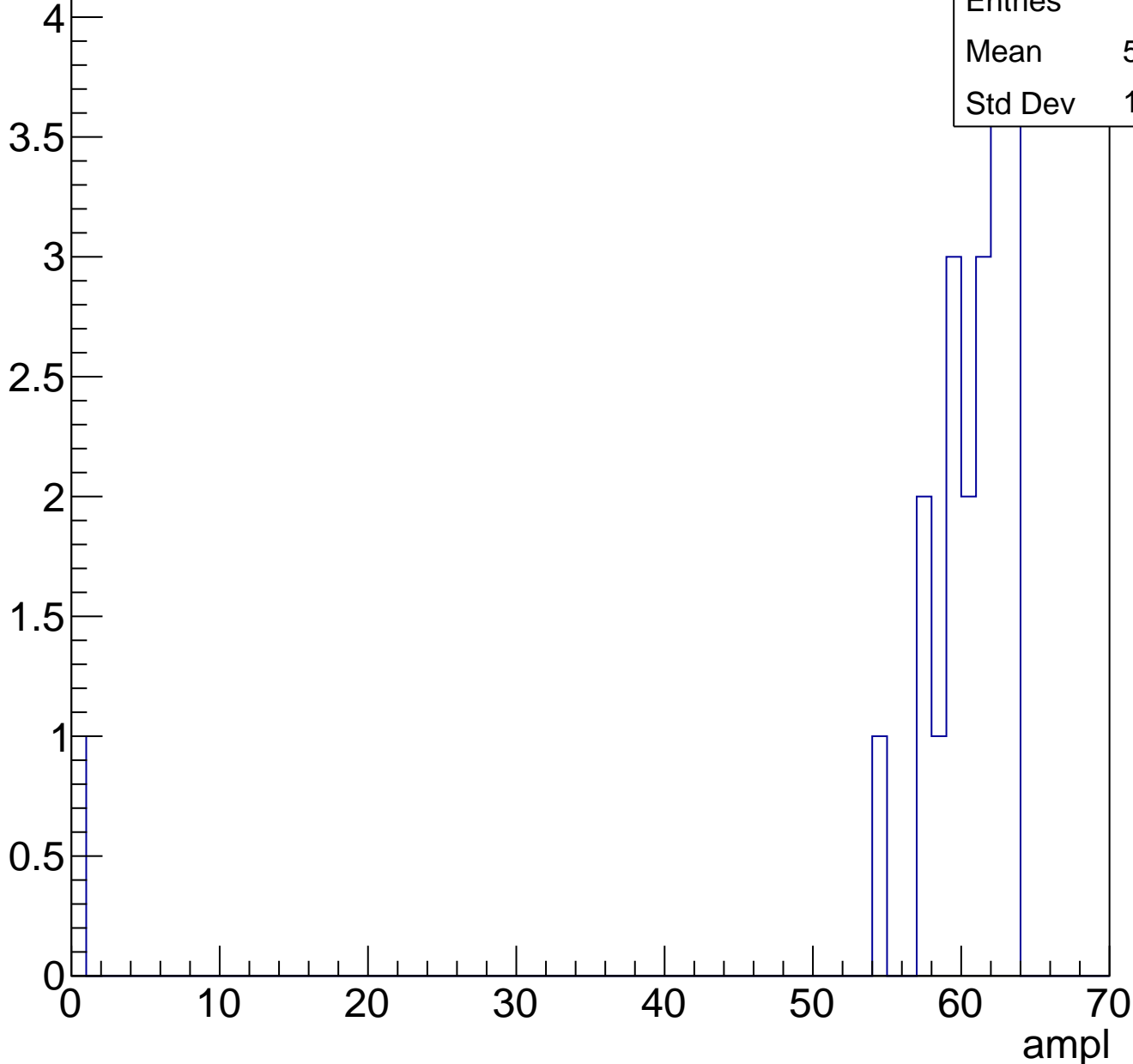


Entries	57
Mean	58.58
Std Dev	3.072

B1L101S, U4-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

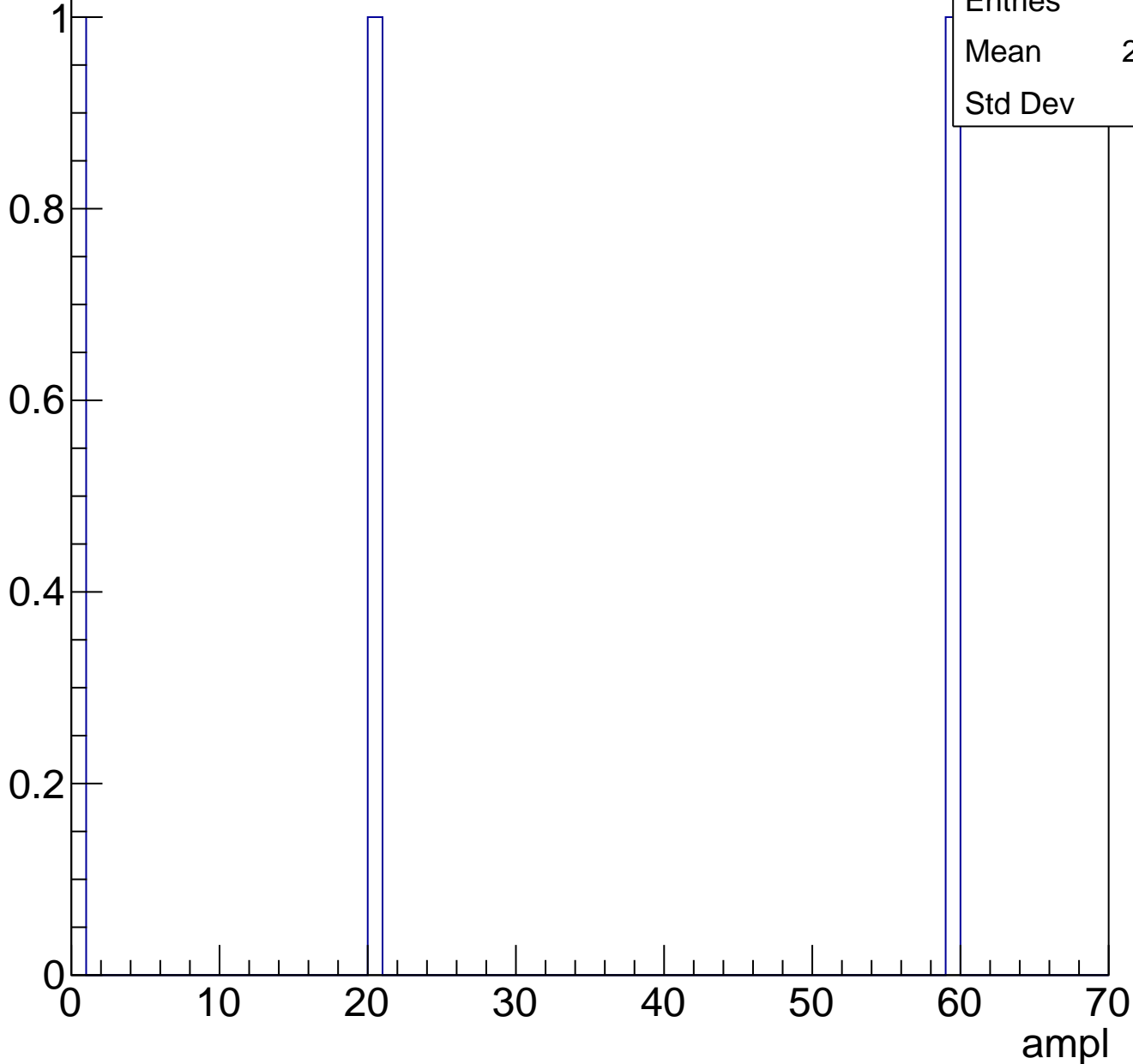
Entry



B1L101S, U4-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch93, adc0

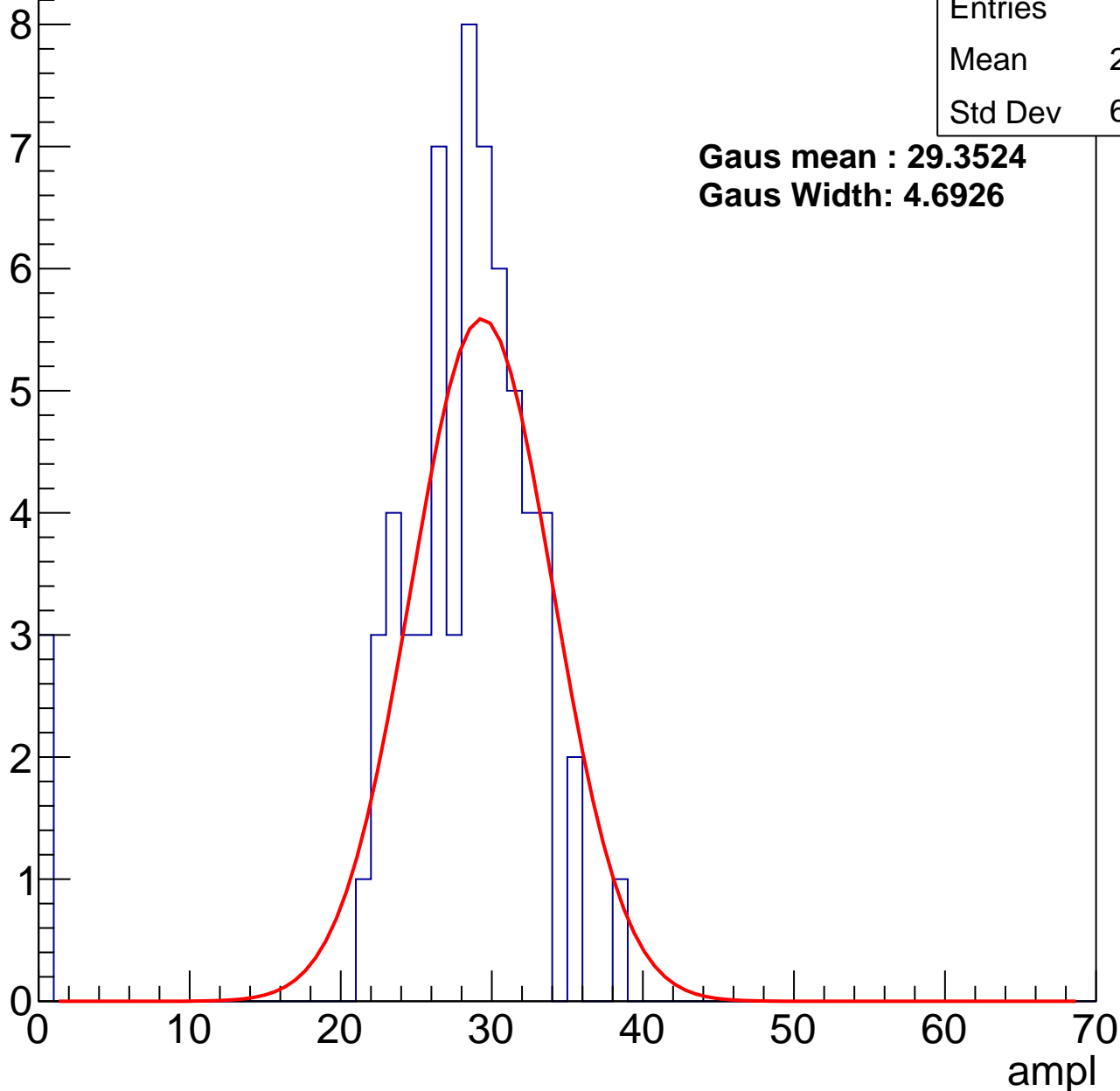
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	26.86
Std Dev	6.926

Gaus mean : 29.3524

Gaus Width: 4.6926



B1L101S, U4-ch93, adc1

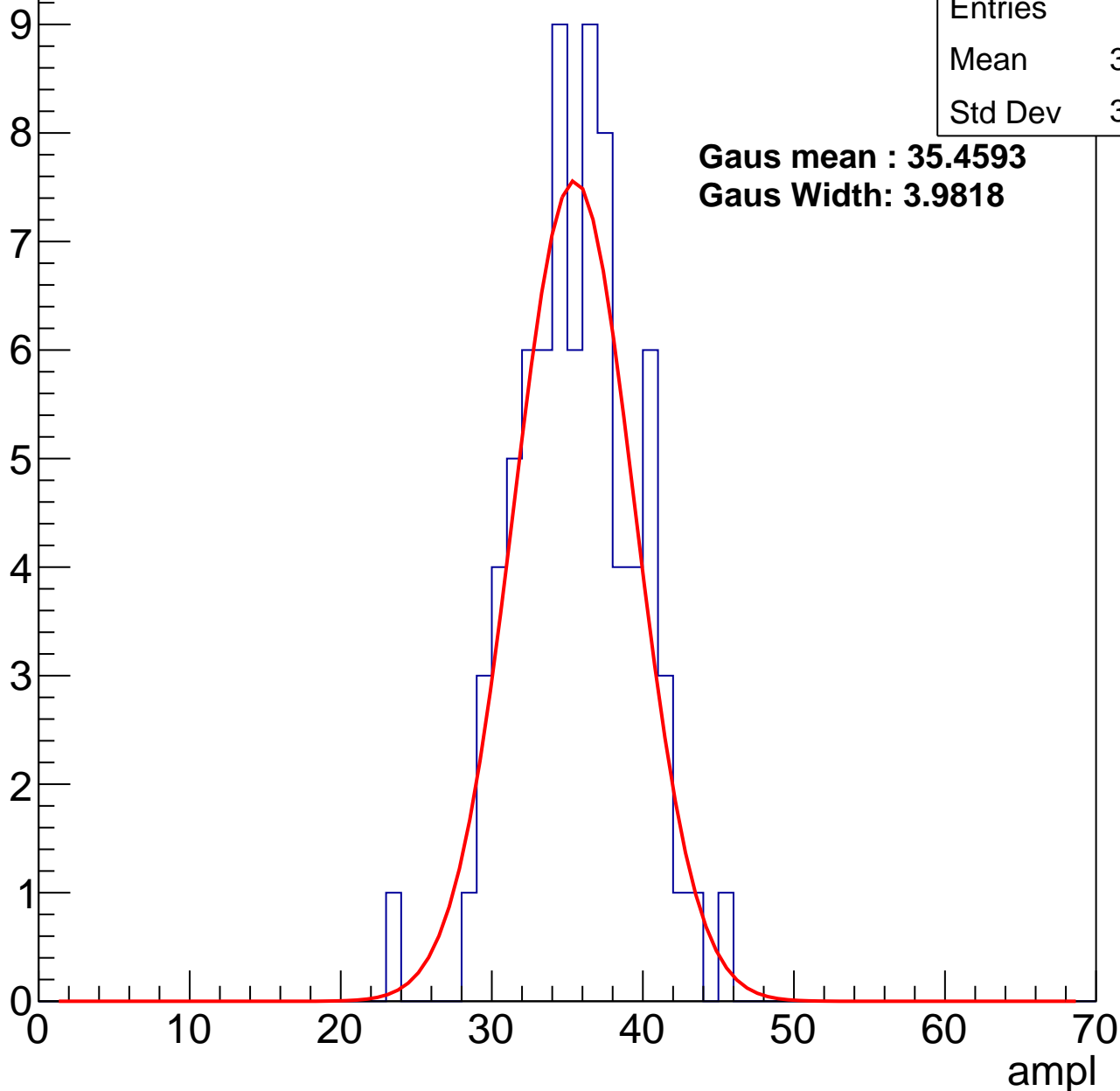
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.13
Std Dev	3.887

Gaus mean : 35.4593

Gaus Width: 3.9818



B1L101S, U4-ch93, adc2

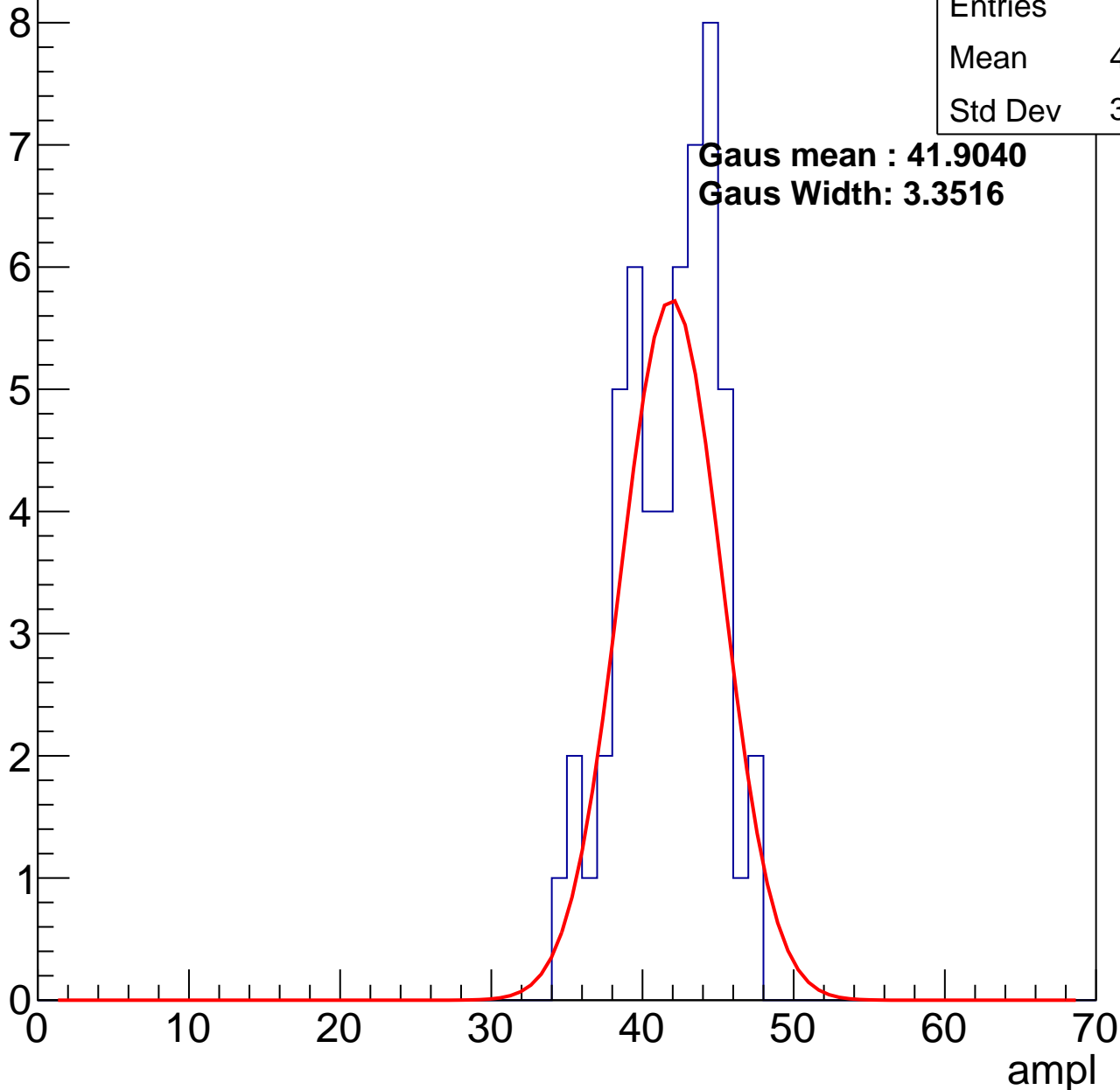
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.33
Std Dev	3.127

Gaus mean : 41.9040

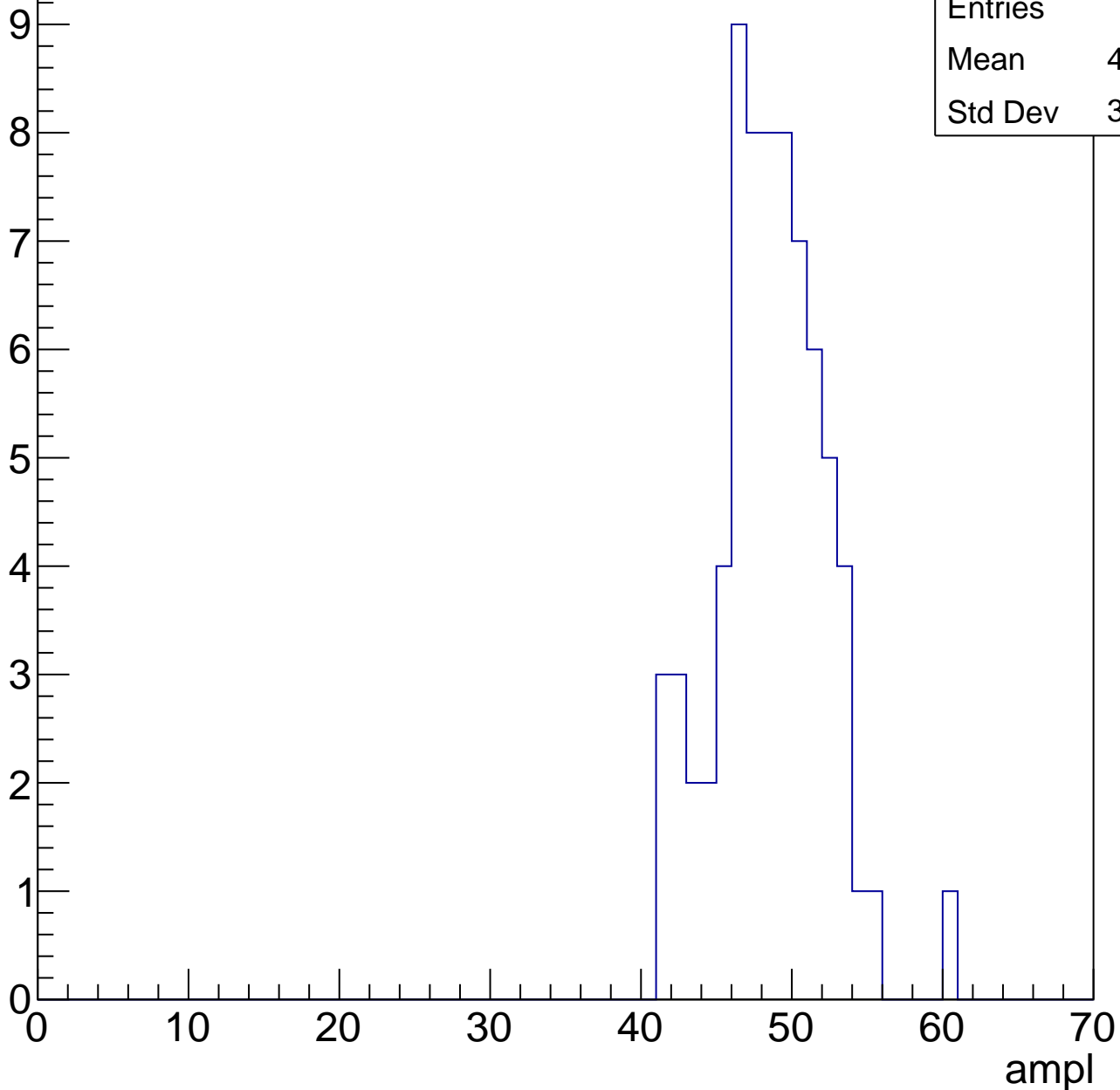
Gaus Width: 3.3516



B1L101S, U4-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

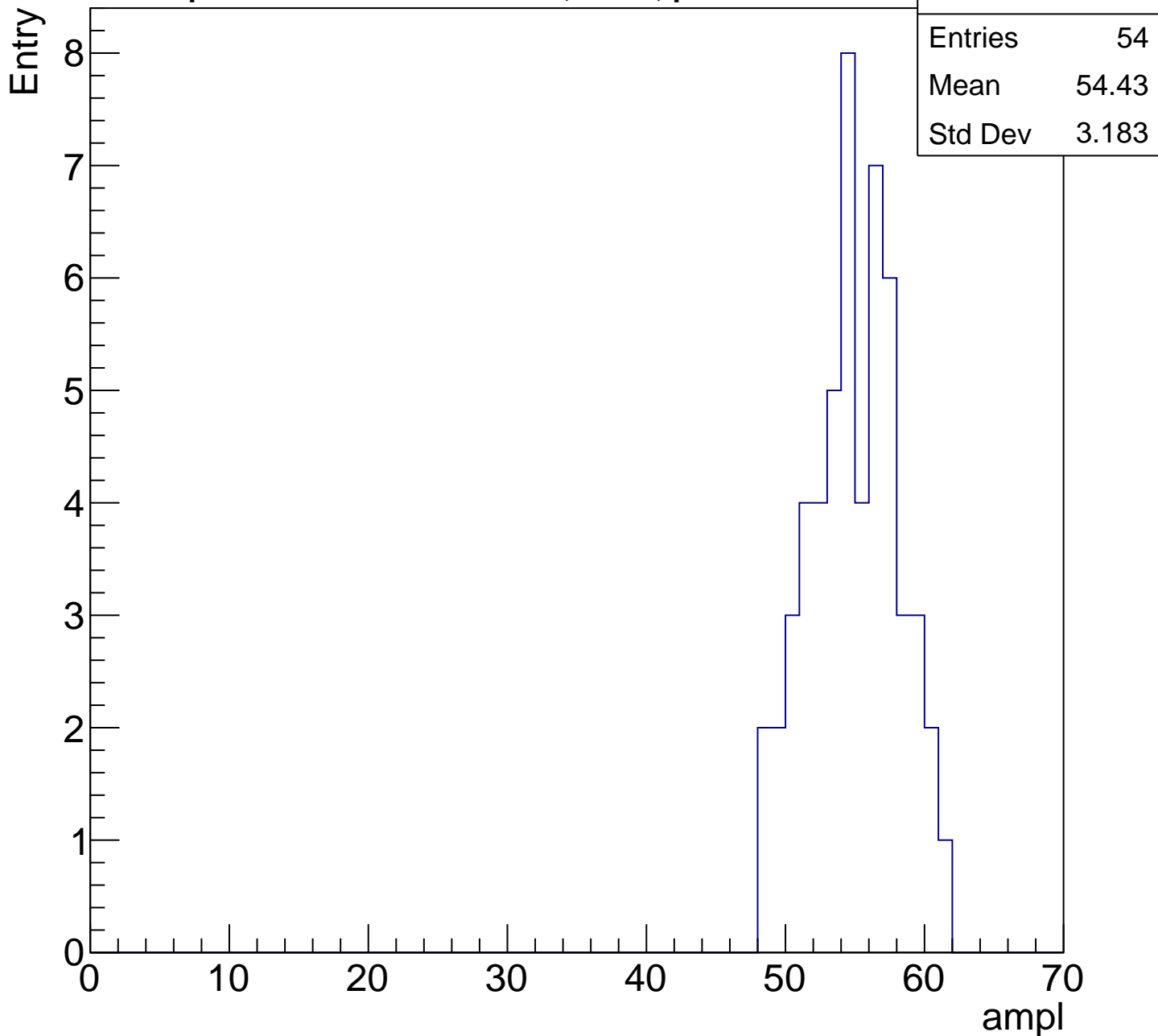
Entry



Entries	72
Mean	48.14
Std Dev	3.564

B1L101S, U4-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

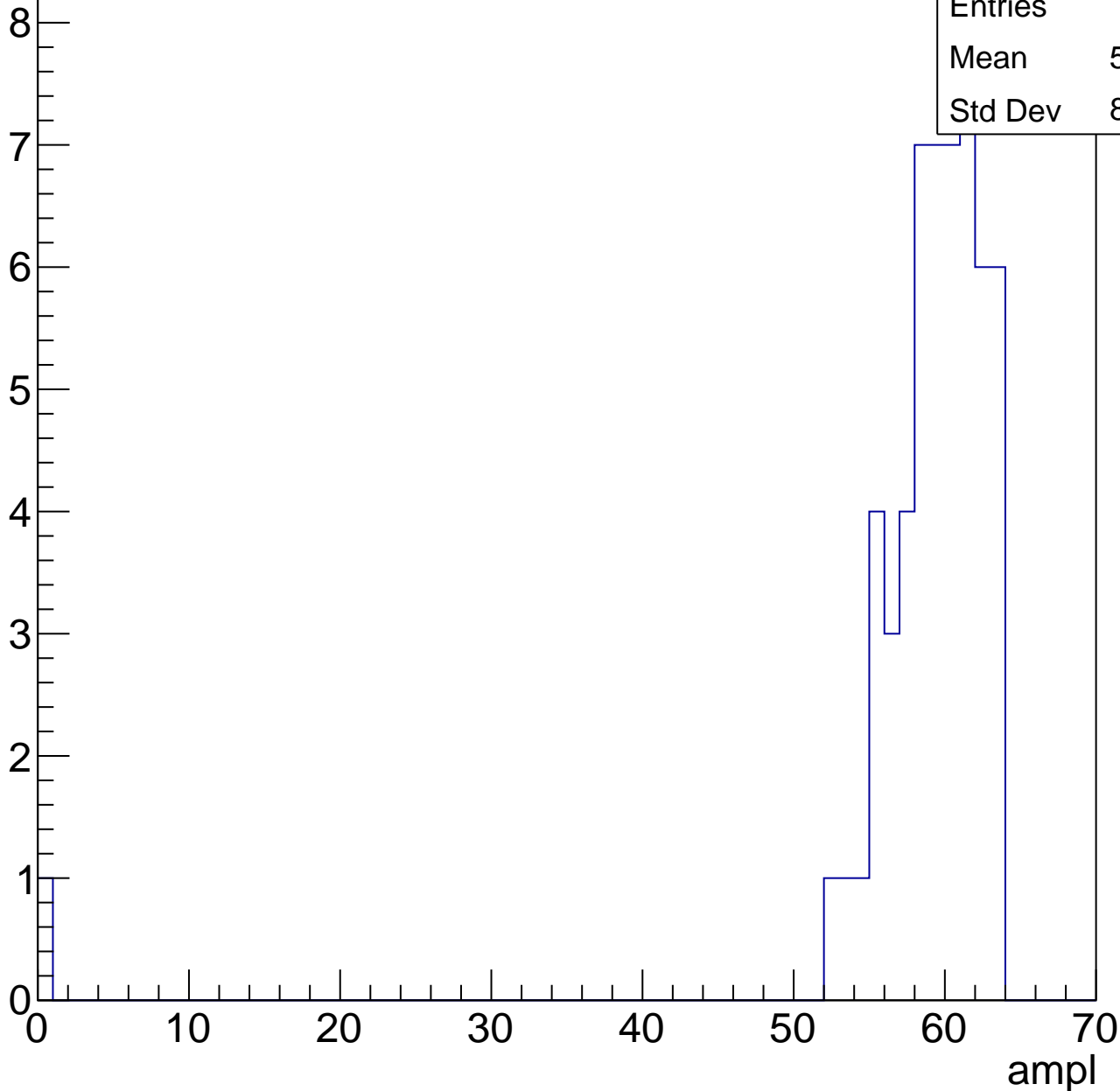


B1L101S, U4-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

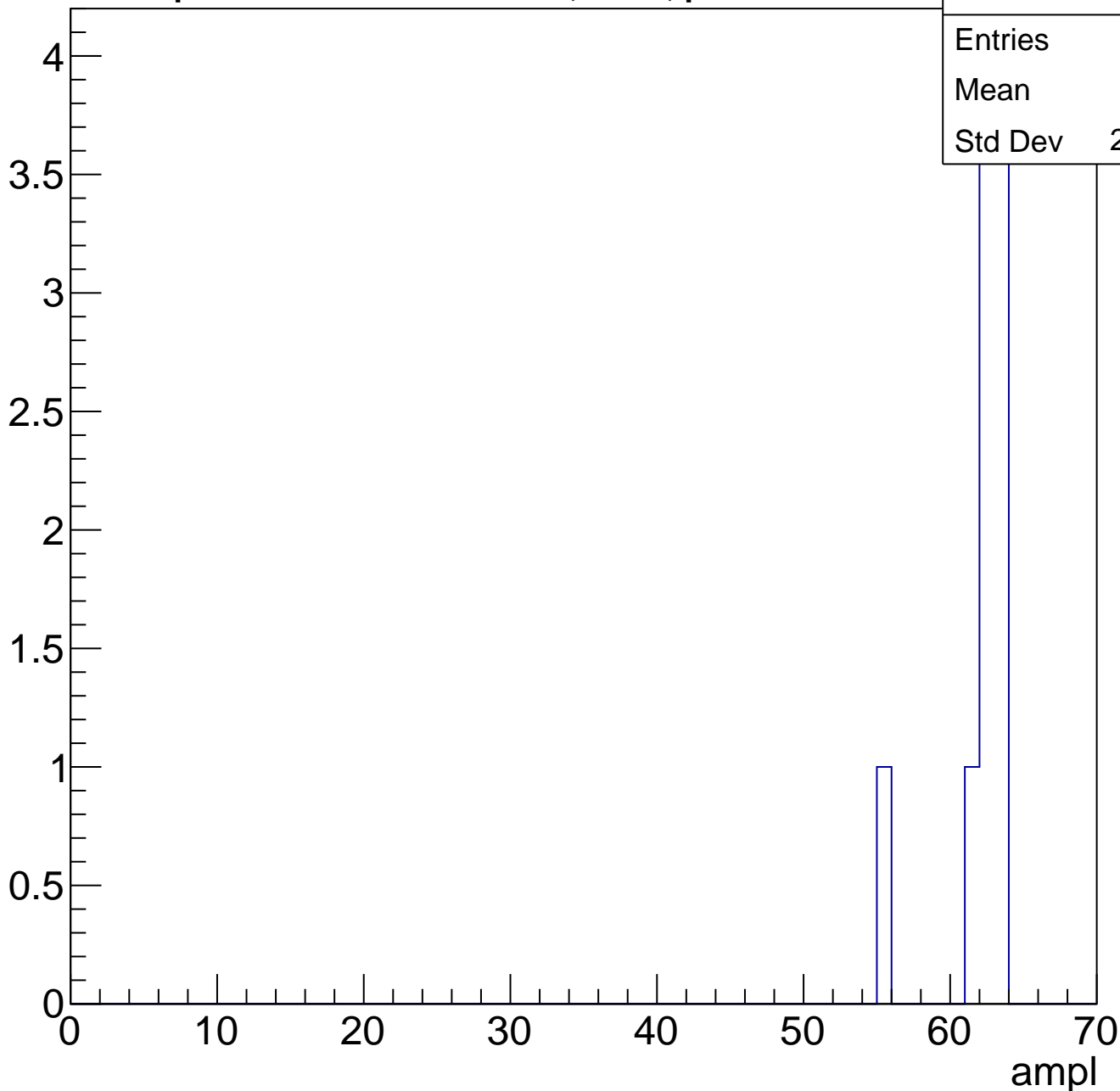
Entries	56
Mean	58.07
Std Dev	8.287



B1L101S, U4-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch94, adc0

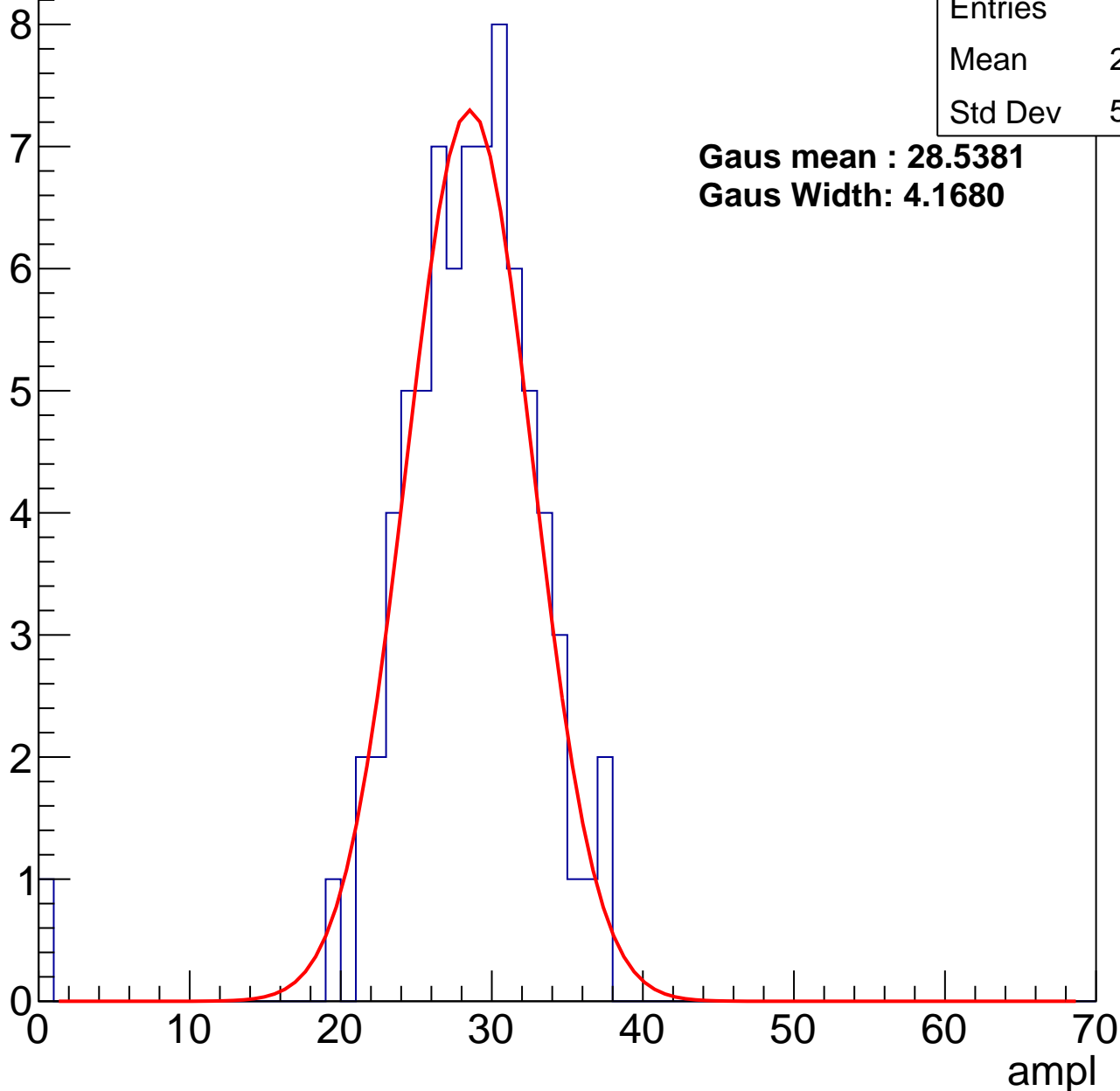
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	27.92
Std Dev	5.032

Gaus mean : 28.5381

Gaus Width: 4.1680



B1L101S, U4-ch94, adc1

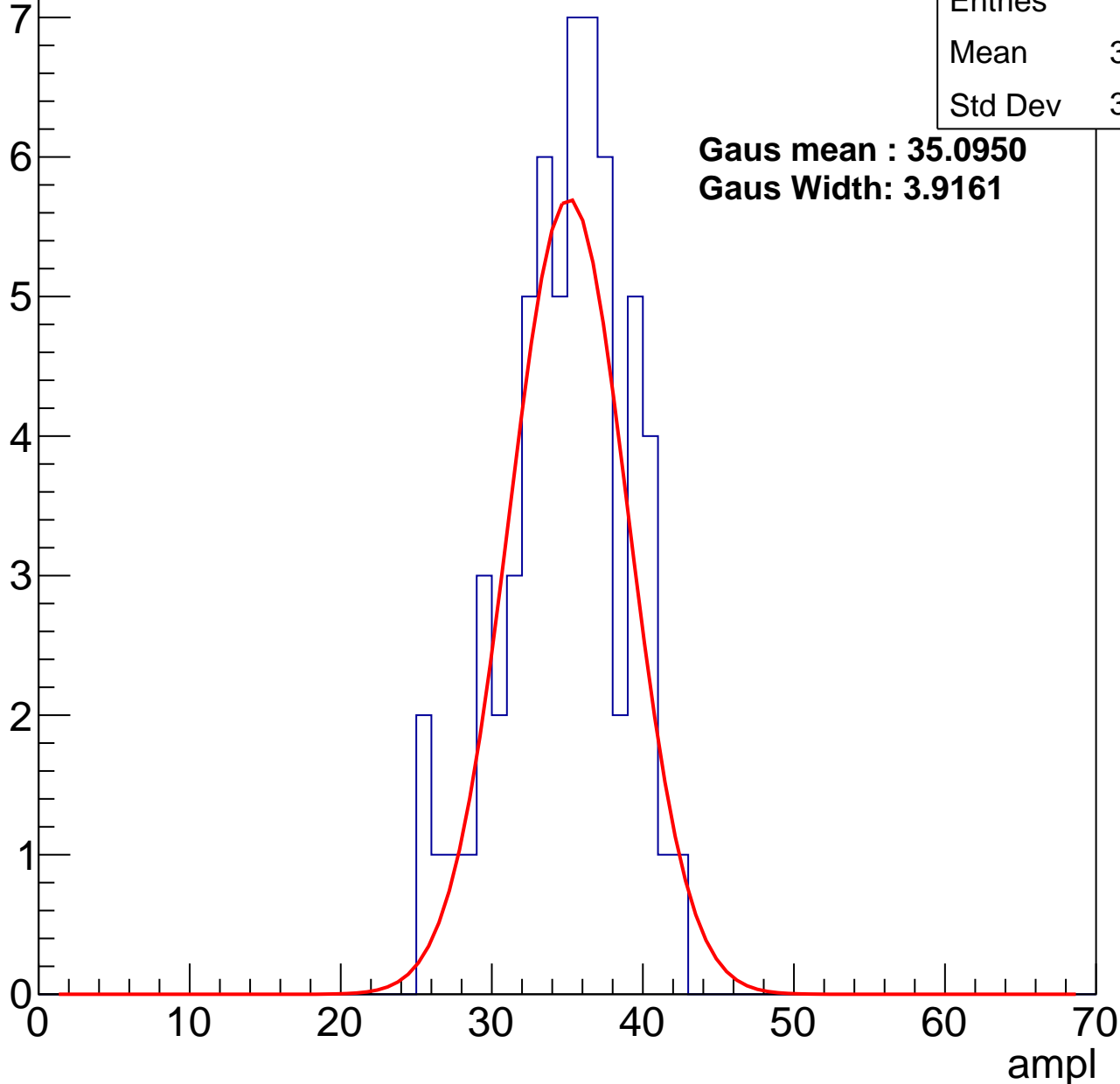
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	34.39
Std Dev	3.945

Gaus mean : 35.0950

Gaus Width: 3.9161



B1L101S, U4-ch94, adc2

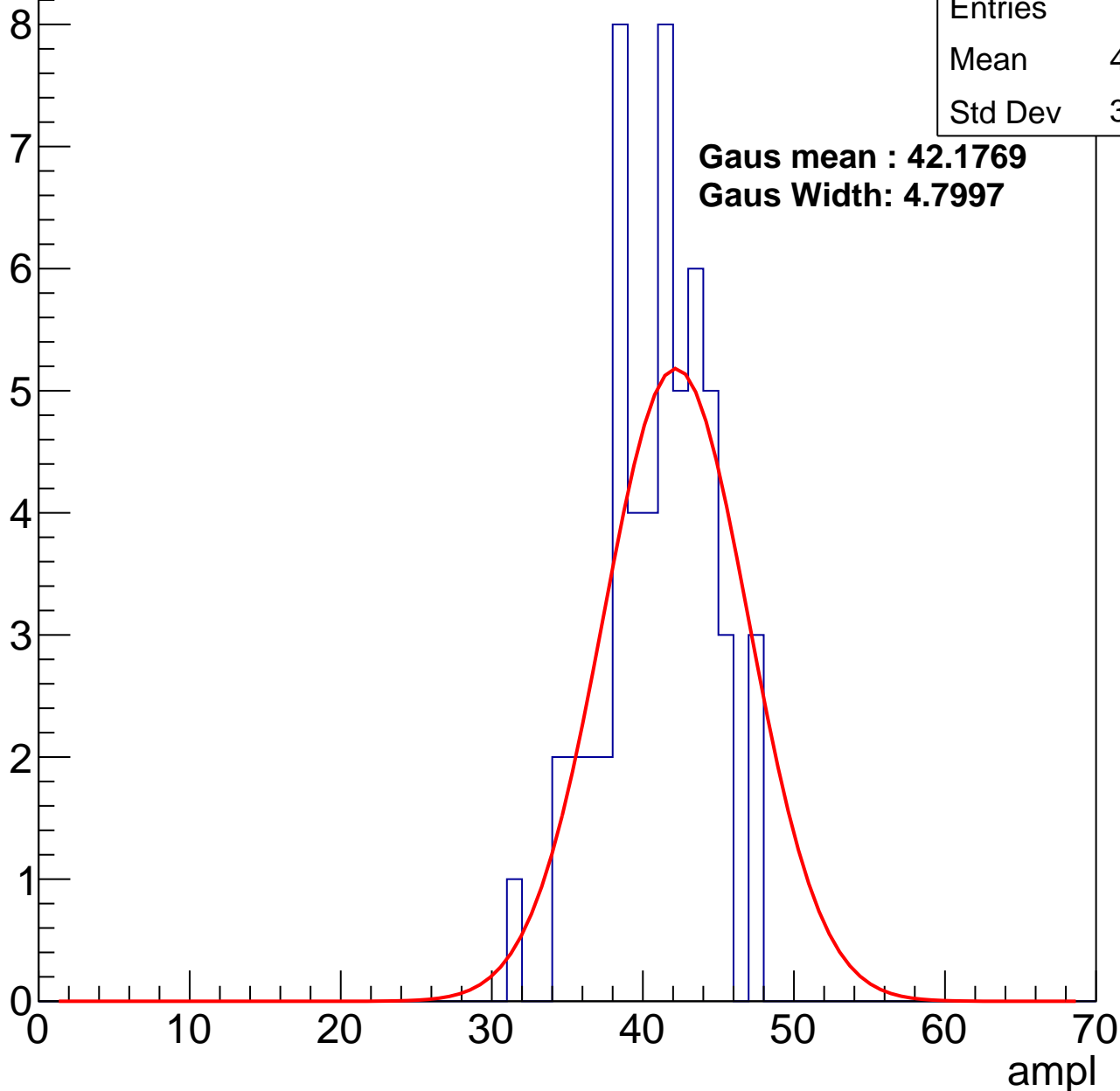
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	40.49
Std Dev	3.463

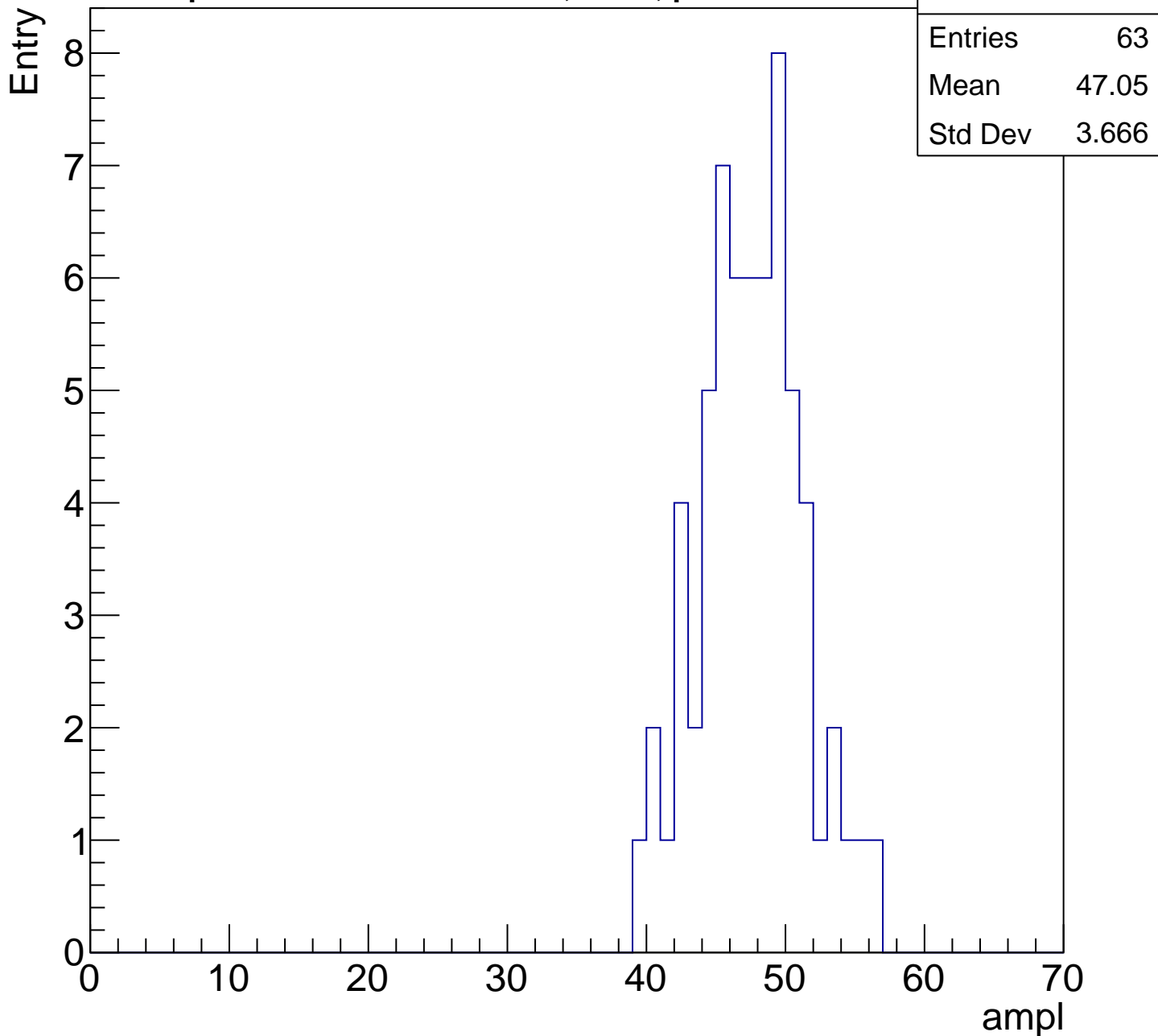
Gaus mean : 42.1769

Gaus Width: 4.7997



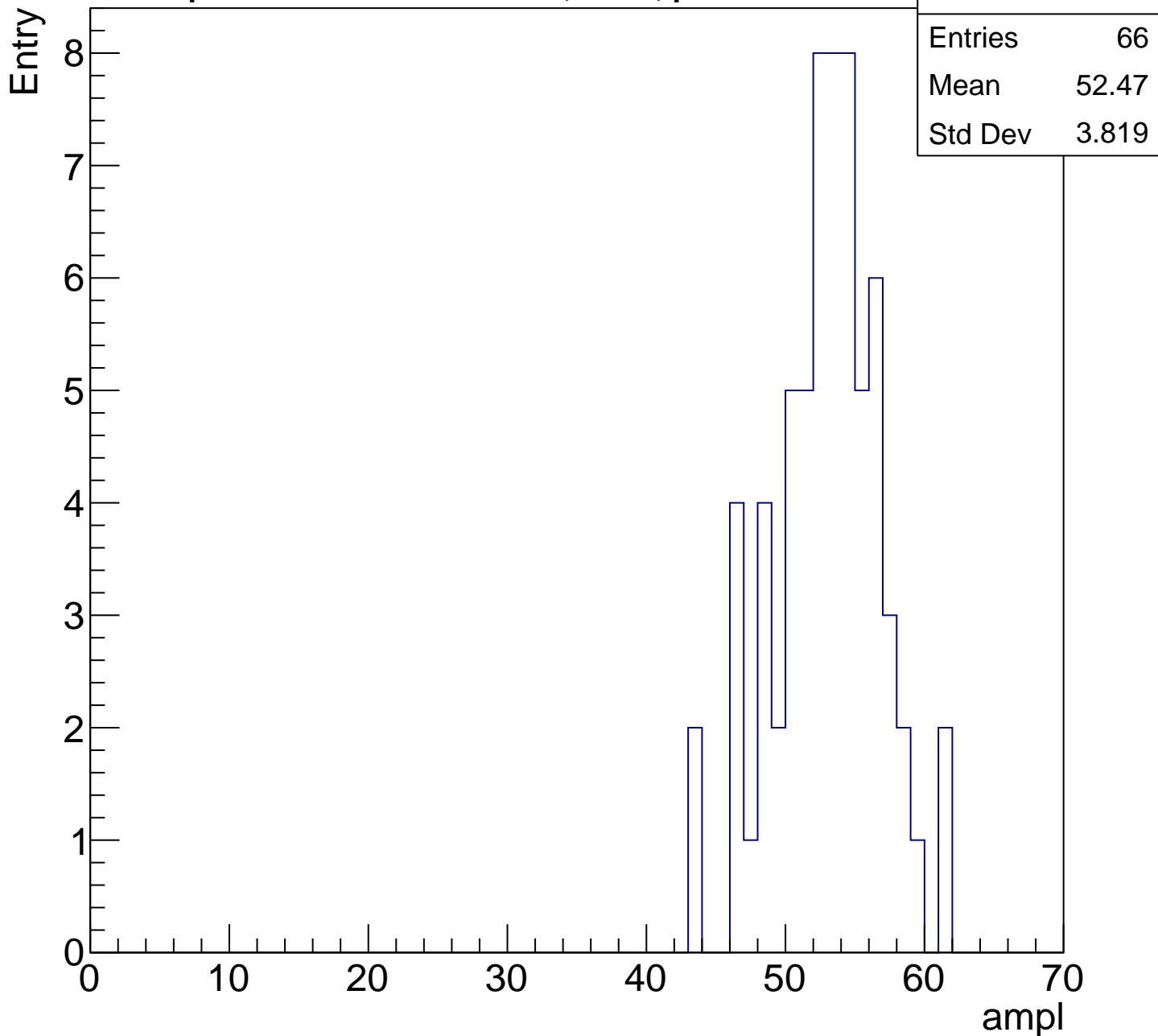
B1L101S, U4-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2



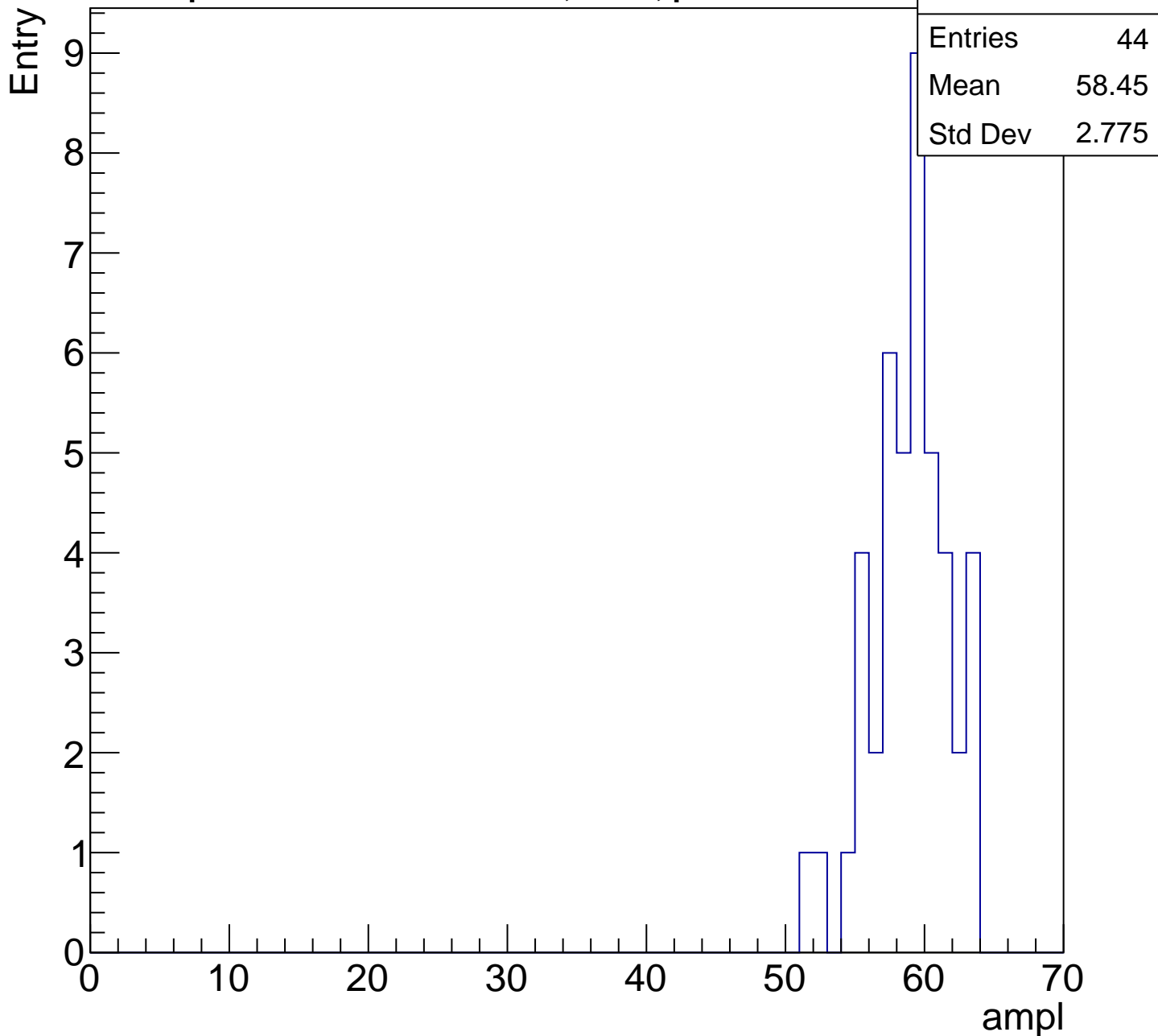
B1L101S, U4-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

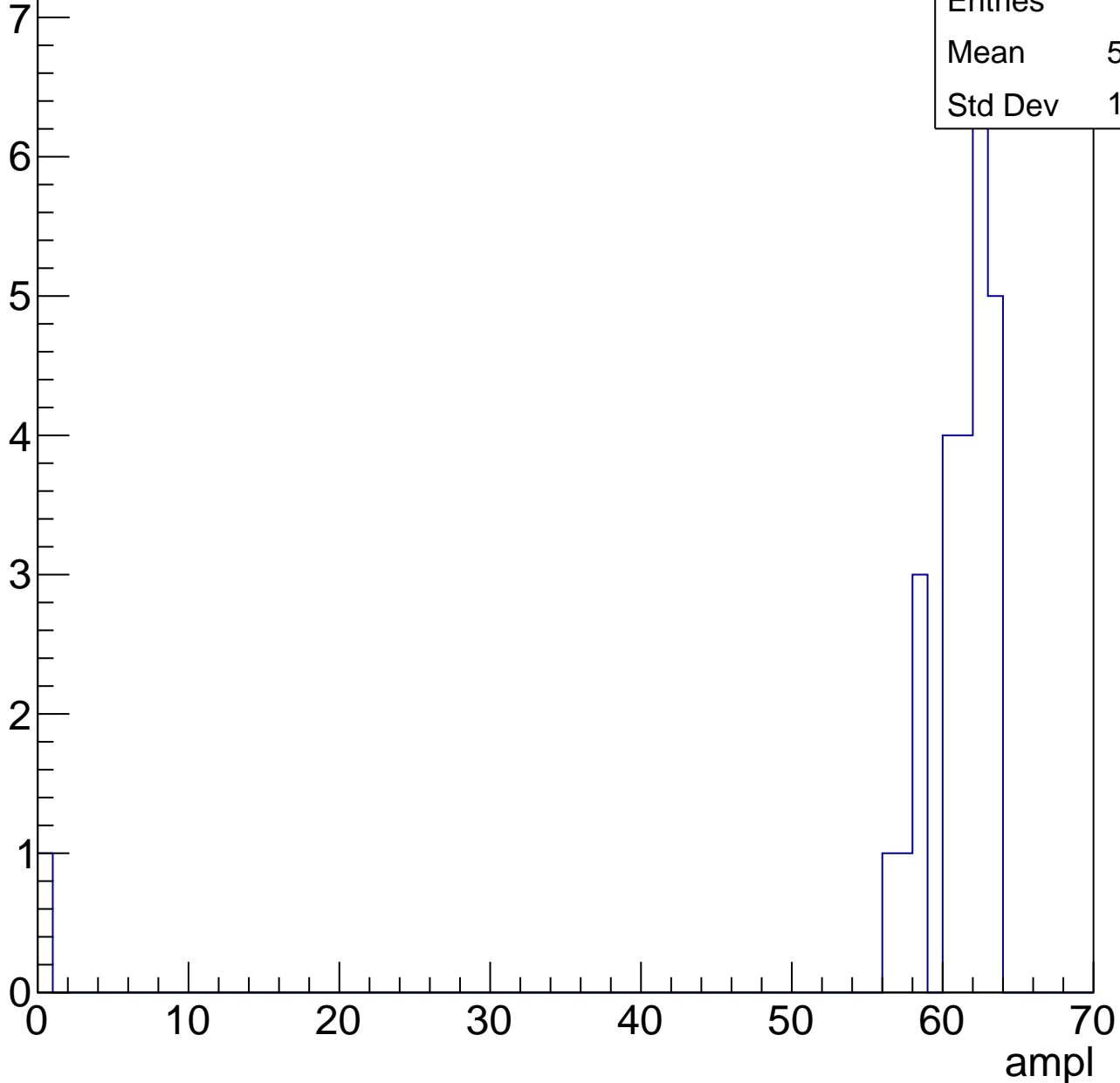


B1L101S, U4-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

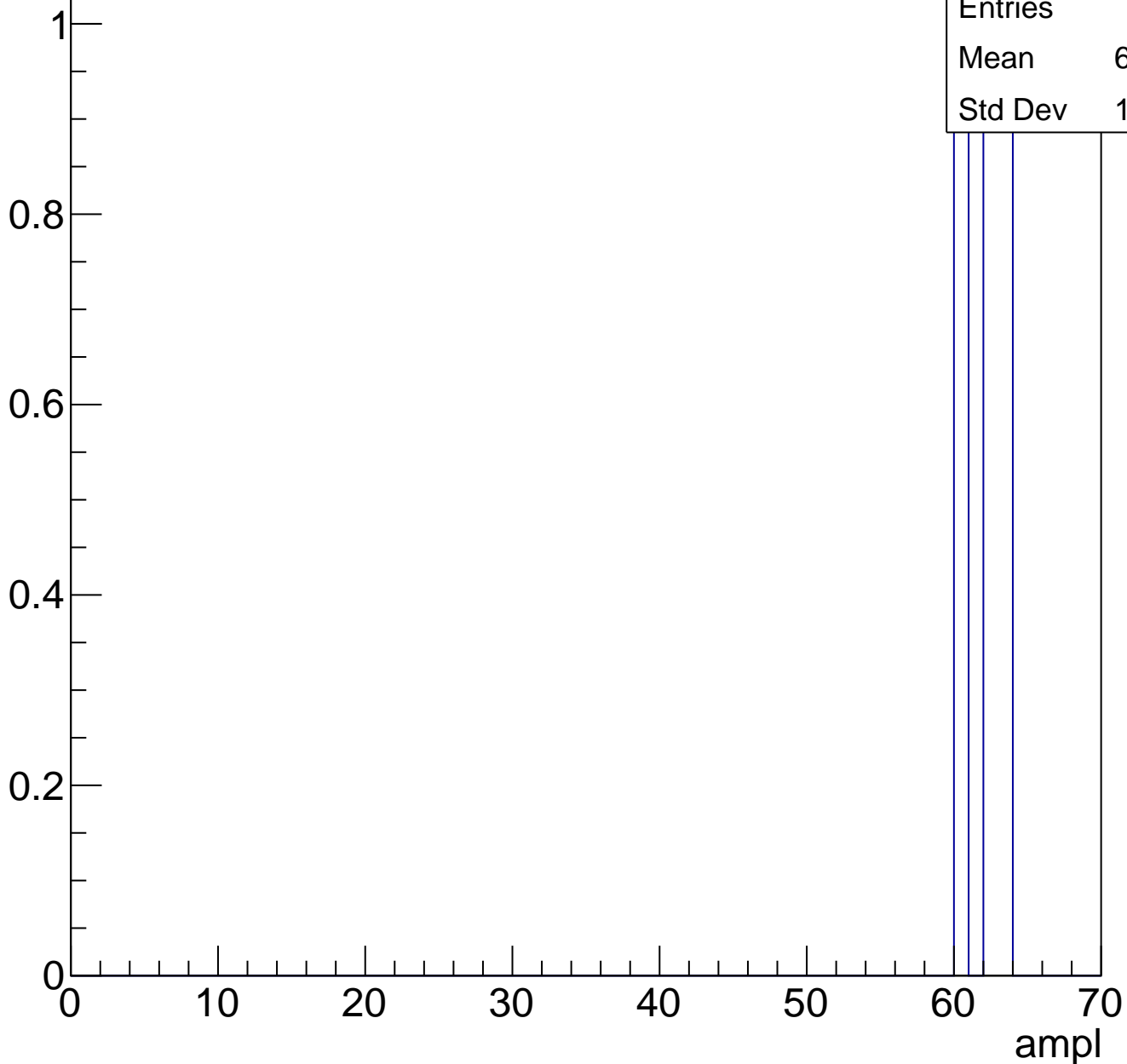
Entries	26
Mean	58.46
Std Dev	11.85



B1L101S, U4-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch95, adc0

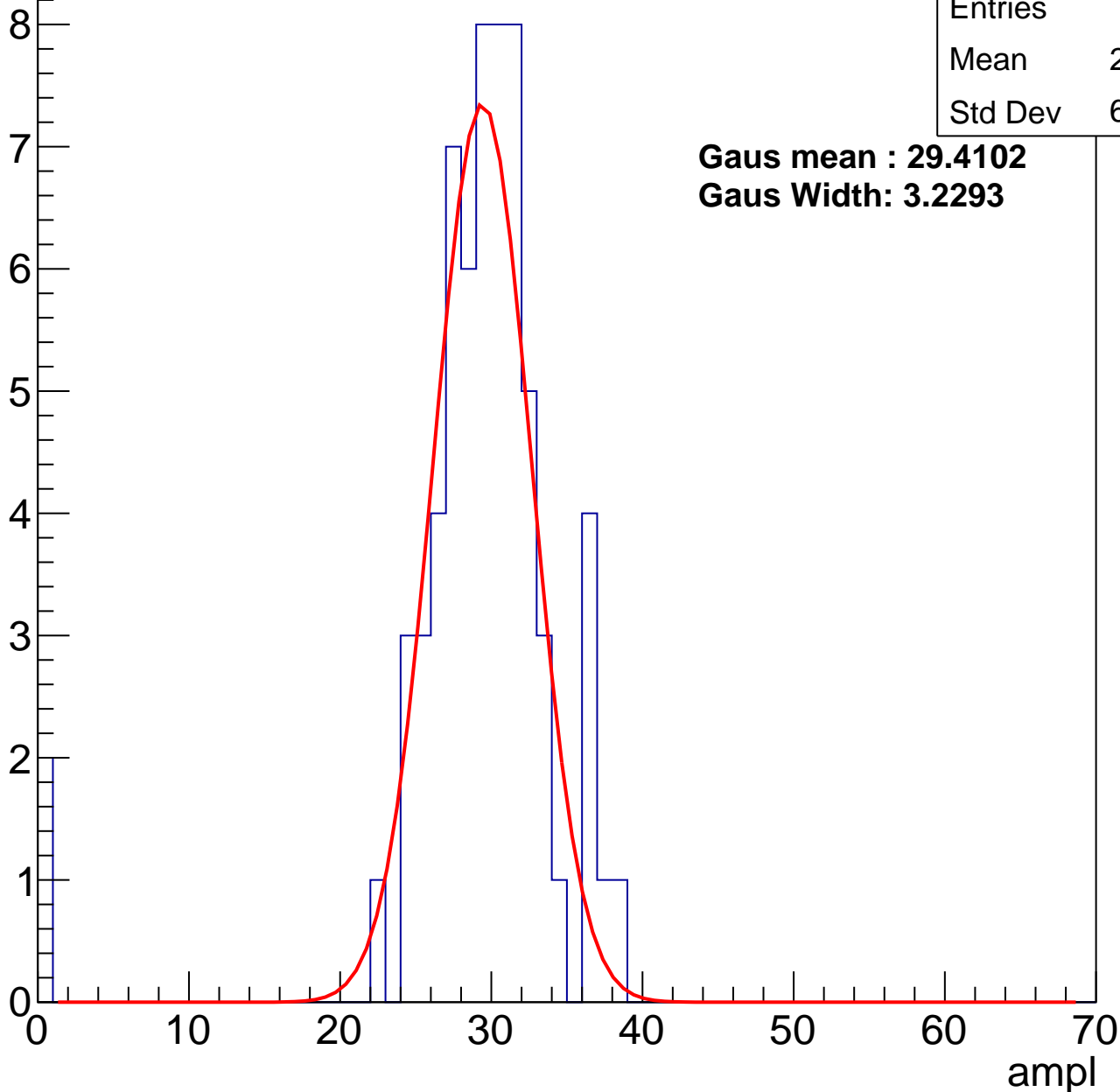
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.65
Std Dev	6.095

Gaus mean : 29.4102

Gaus Width: 3.2293



B1L101S, U4-ch95, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.49
Std Dev	4.244

Gaus mean : 36.0713

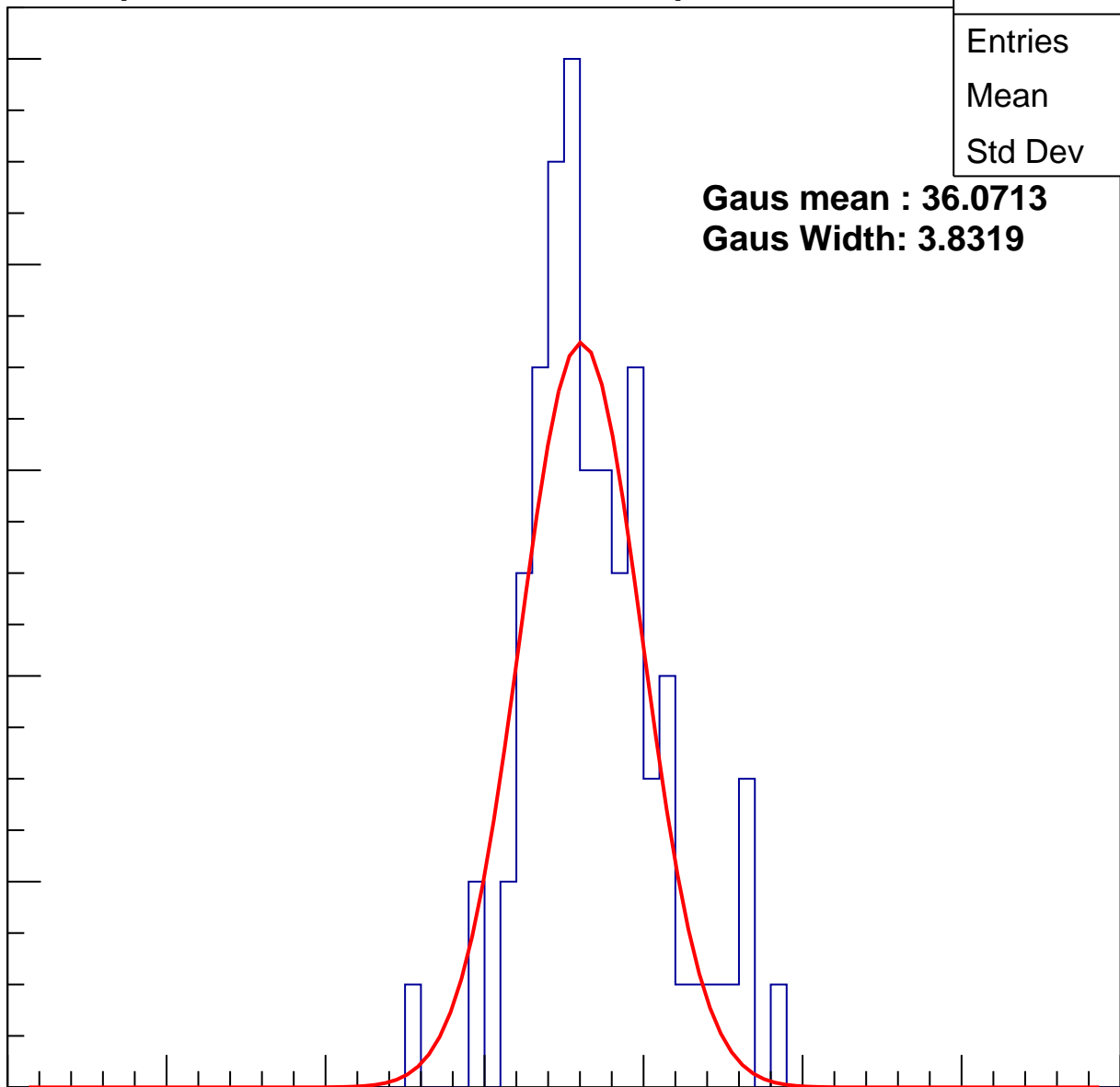
Gaus Width: 3.8319

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch95, adc2

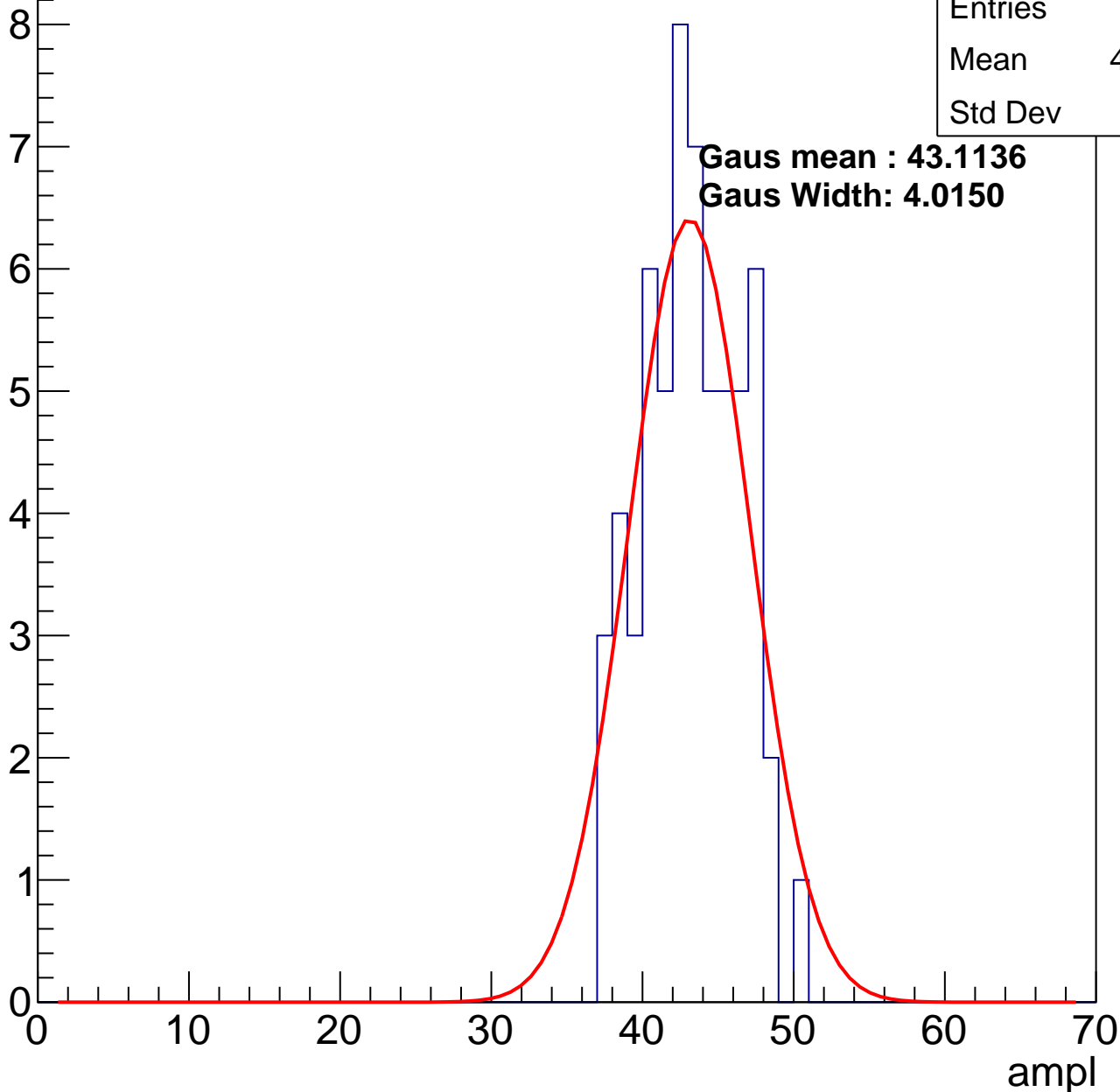
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.75
Std Dev	3.16

Gaus mean : 43.1136

Gaus Width: 4.0150

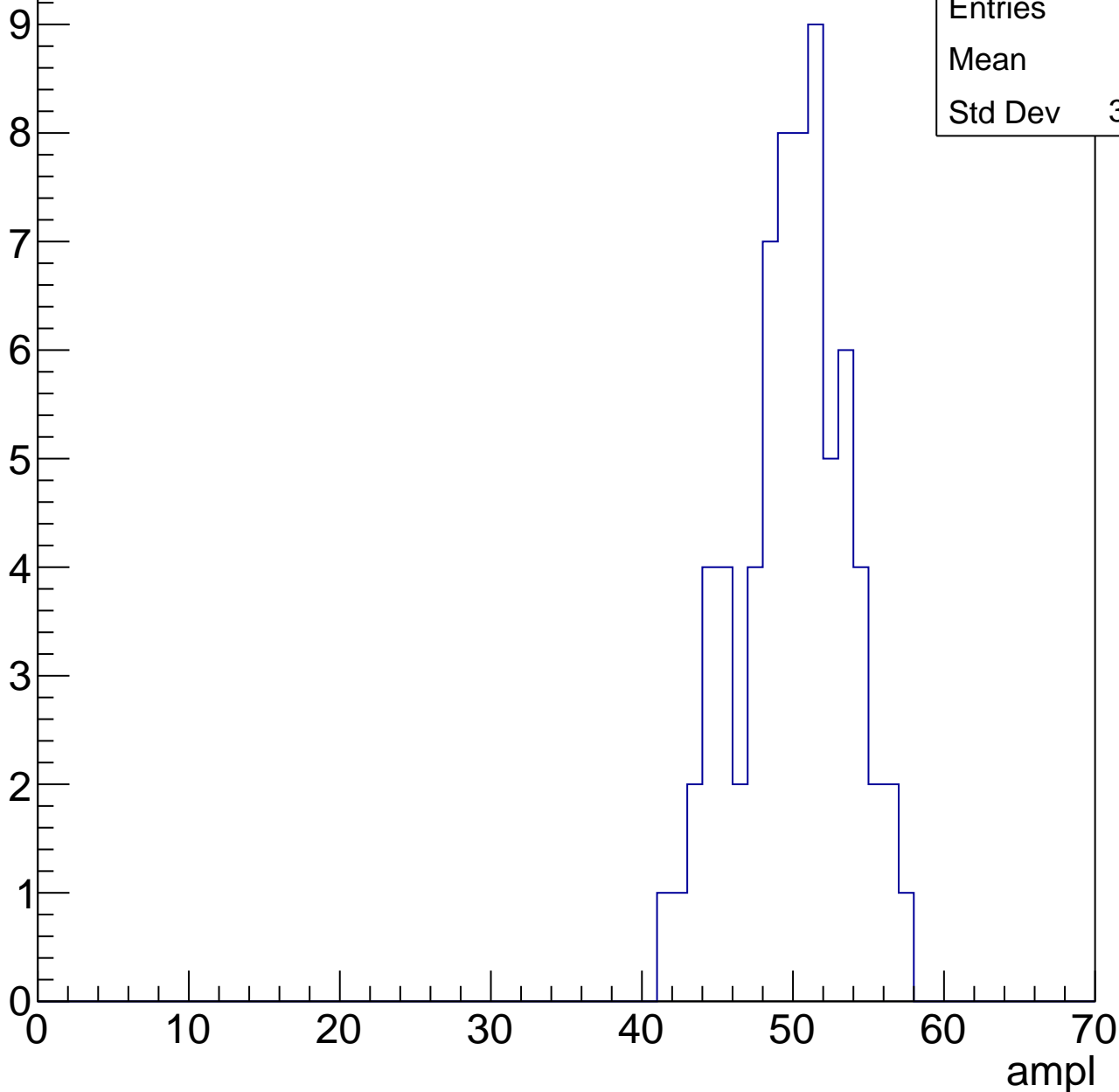


B1L101S, U4-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.5
Std Dev	3.569

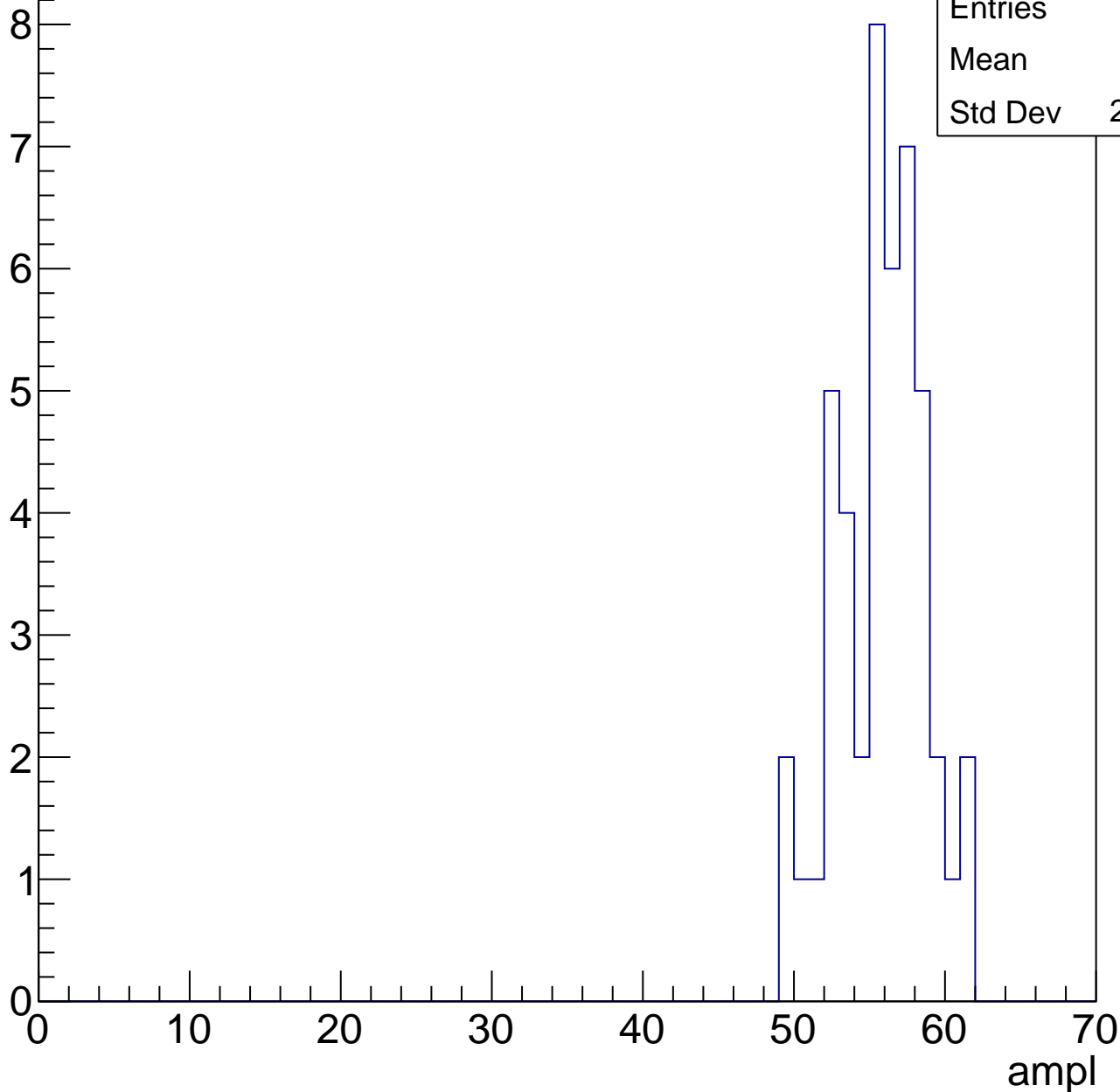


B1L101S, U4-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	55.3
Std Dev	2.873

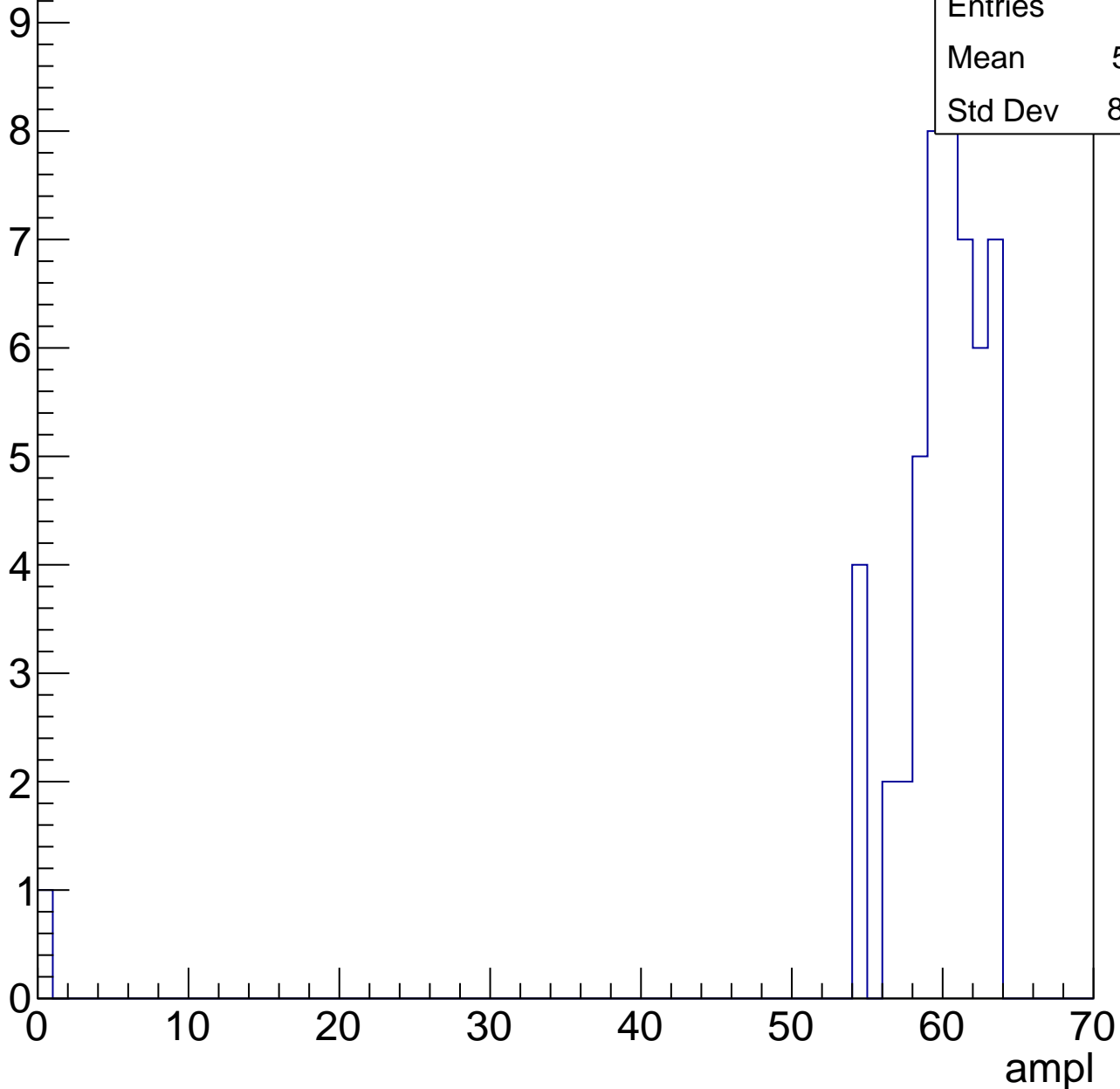


B1L101S, U4-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

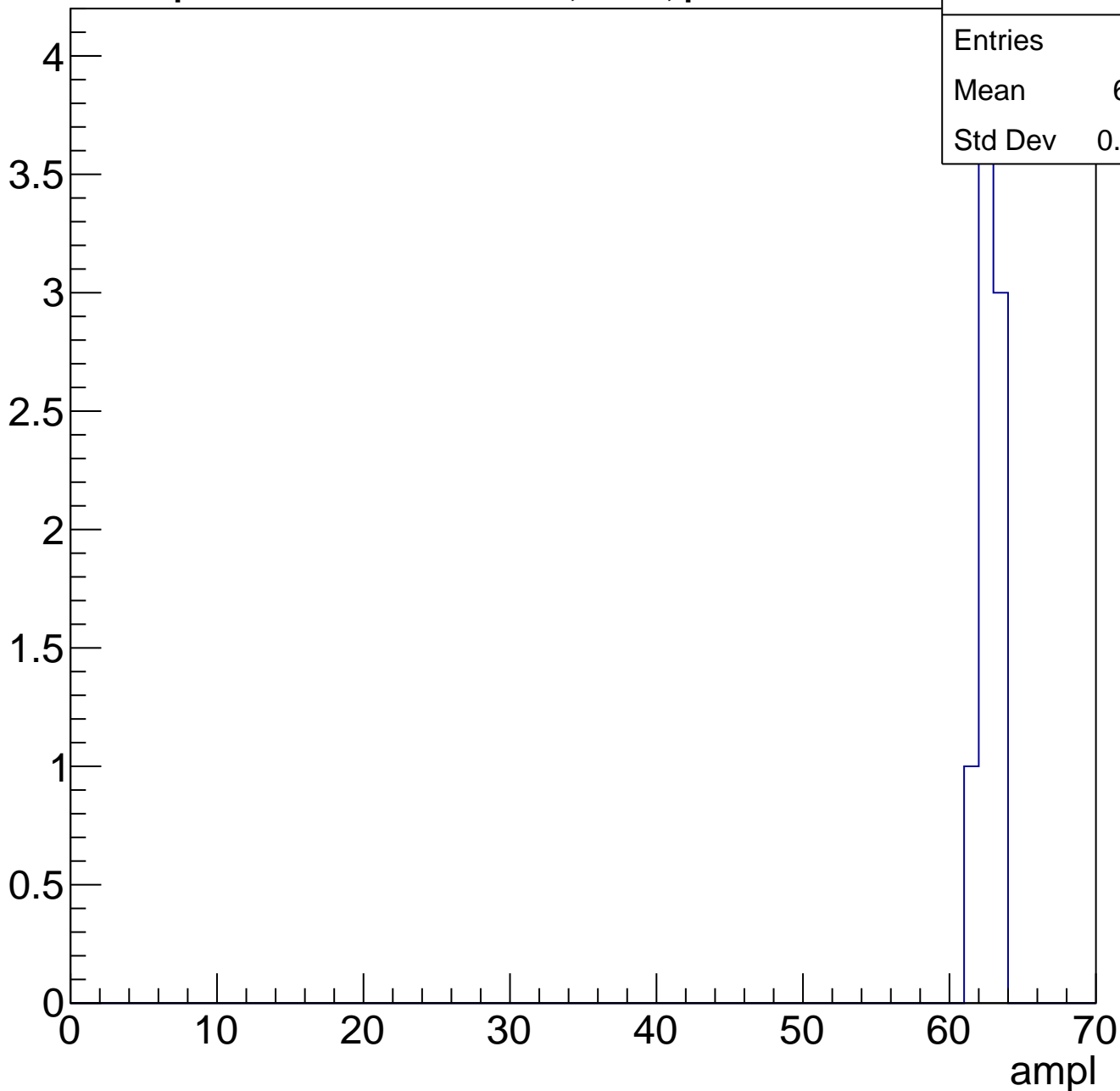
Entries	51
Mean	58.51
Std Dev	8.635



B1L101S, U4-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L101S, U4-ch96, adc0

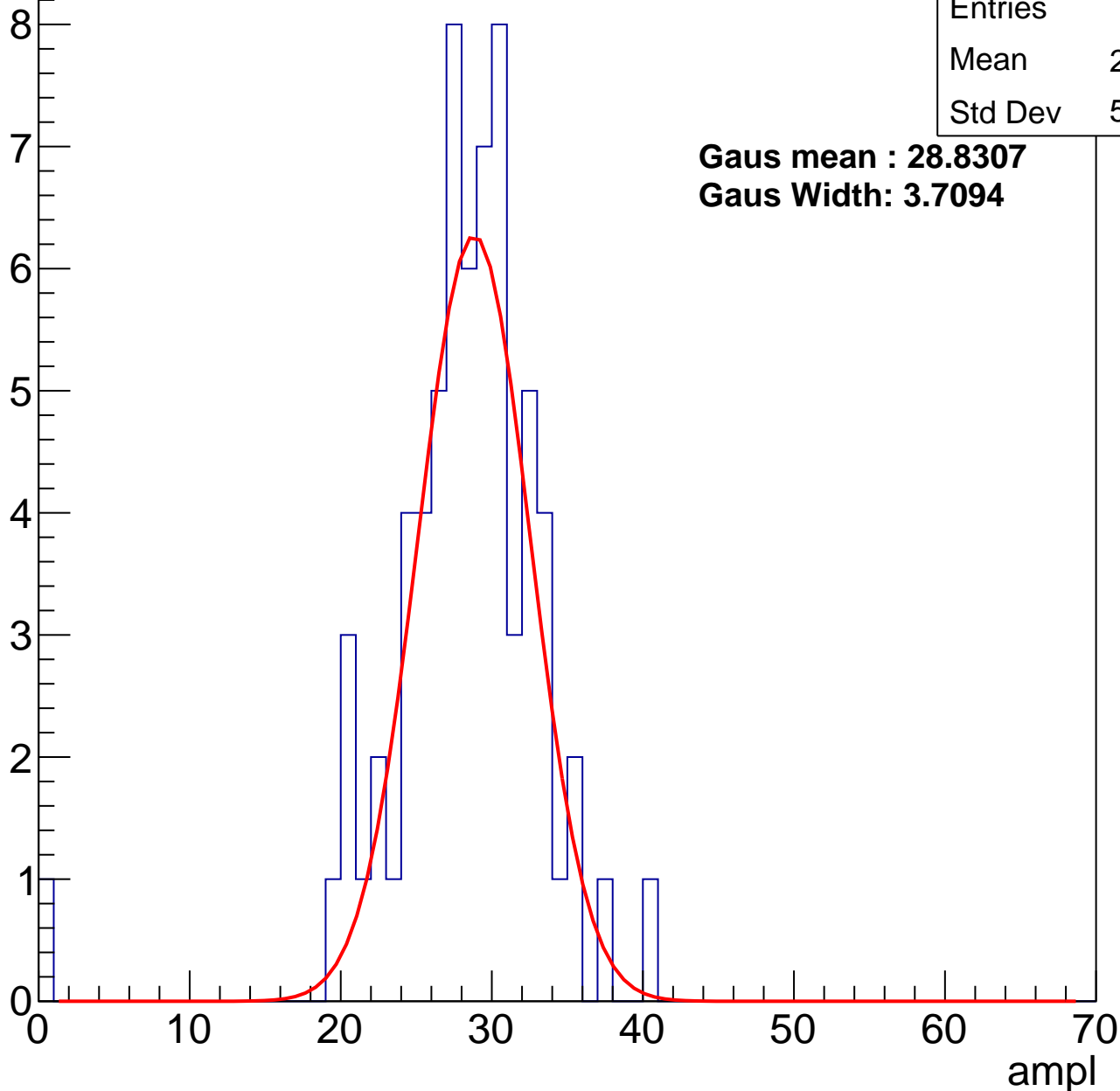
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	27.74
Std Dev	5.329

Gaus mean : 28.8307

Gaus Width: 3.7094



B1L101S, U4-ch96, adc1

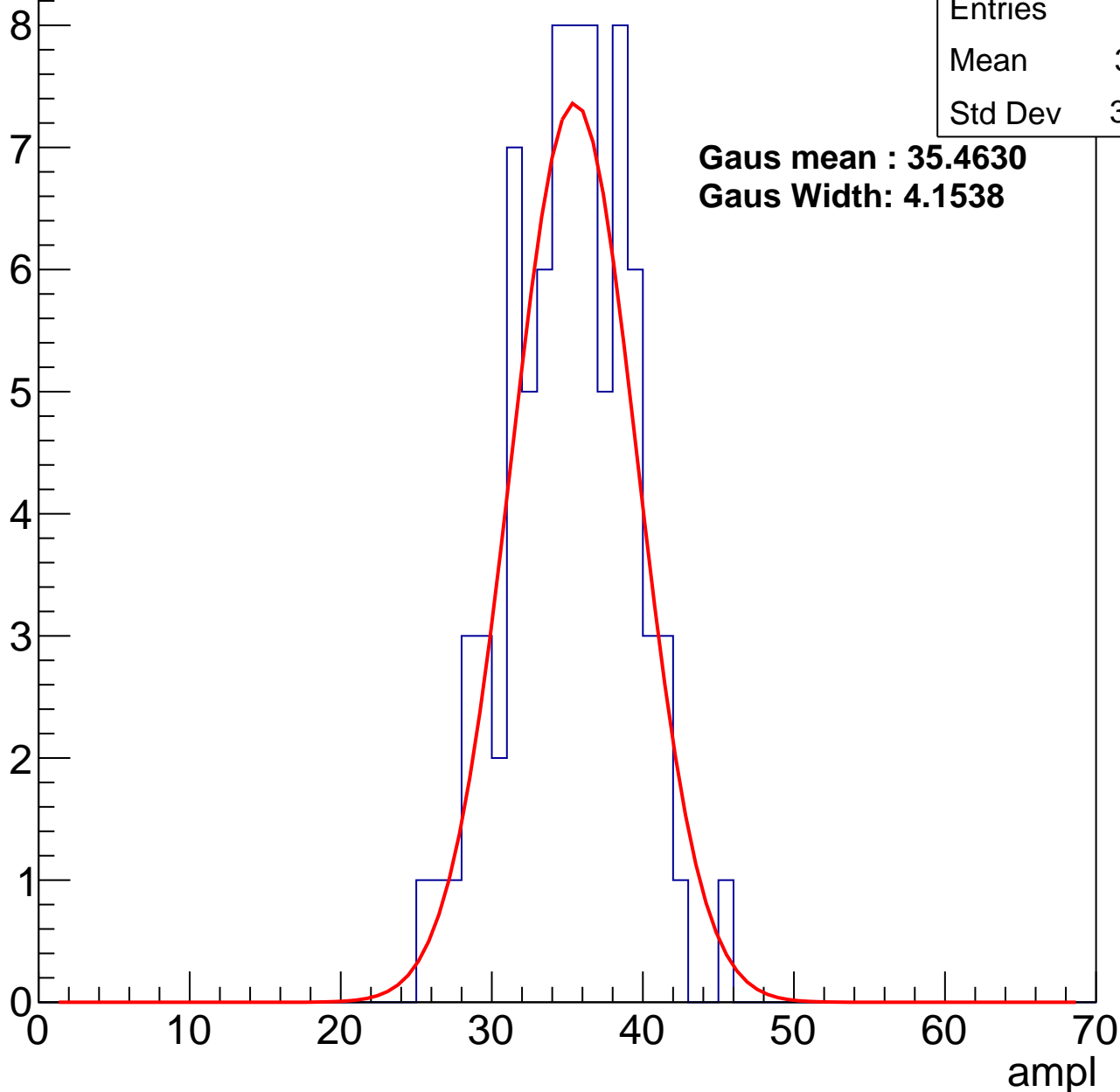
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	34.71
Std Dev	3.969

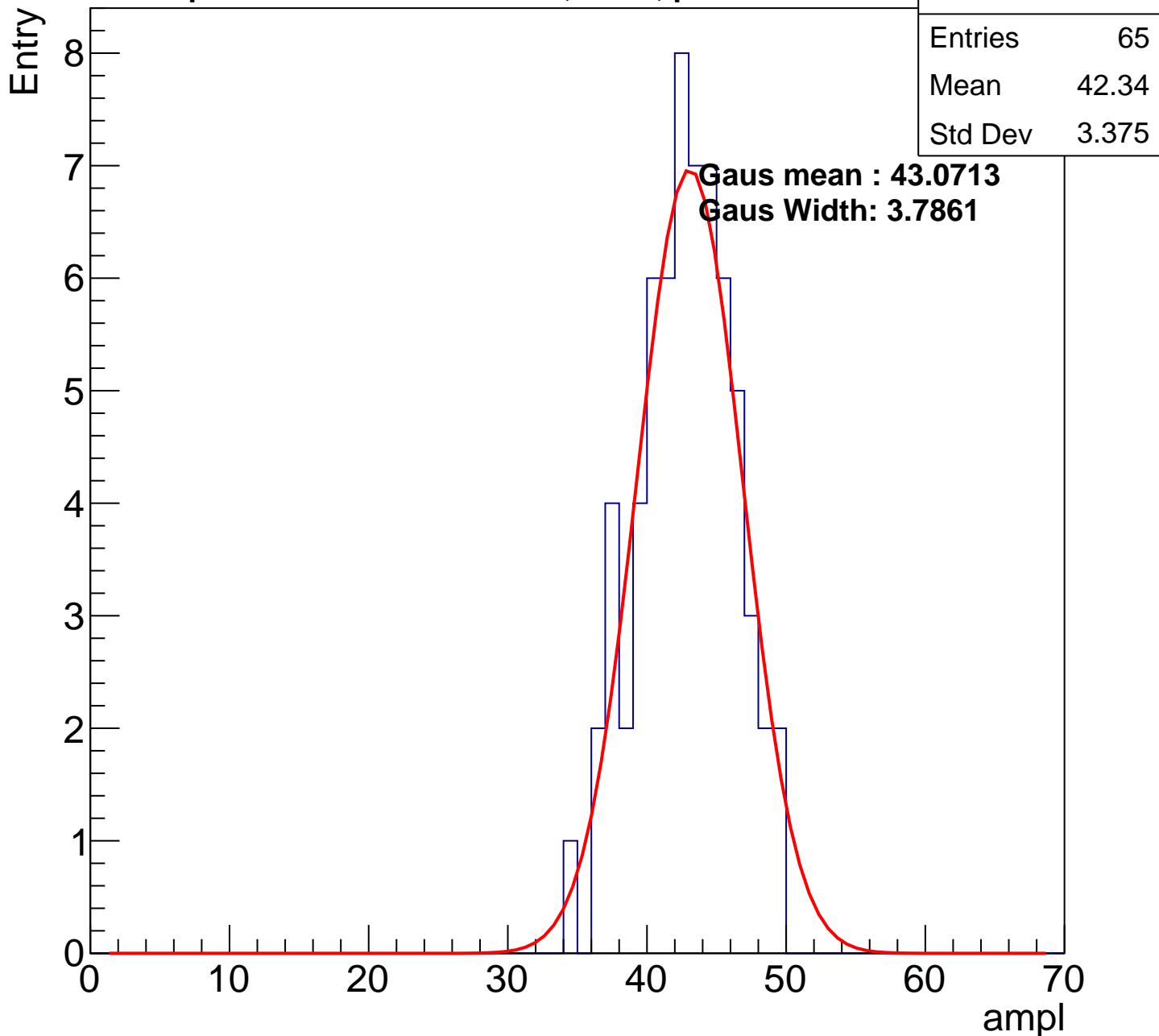
Gaus mean : 35.4630

Gaus Width: 4.1538



B1L101S, U4-ch96, adc2

calib_packv5_042523_0143.root, FC#0, port D2

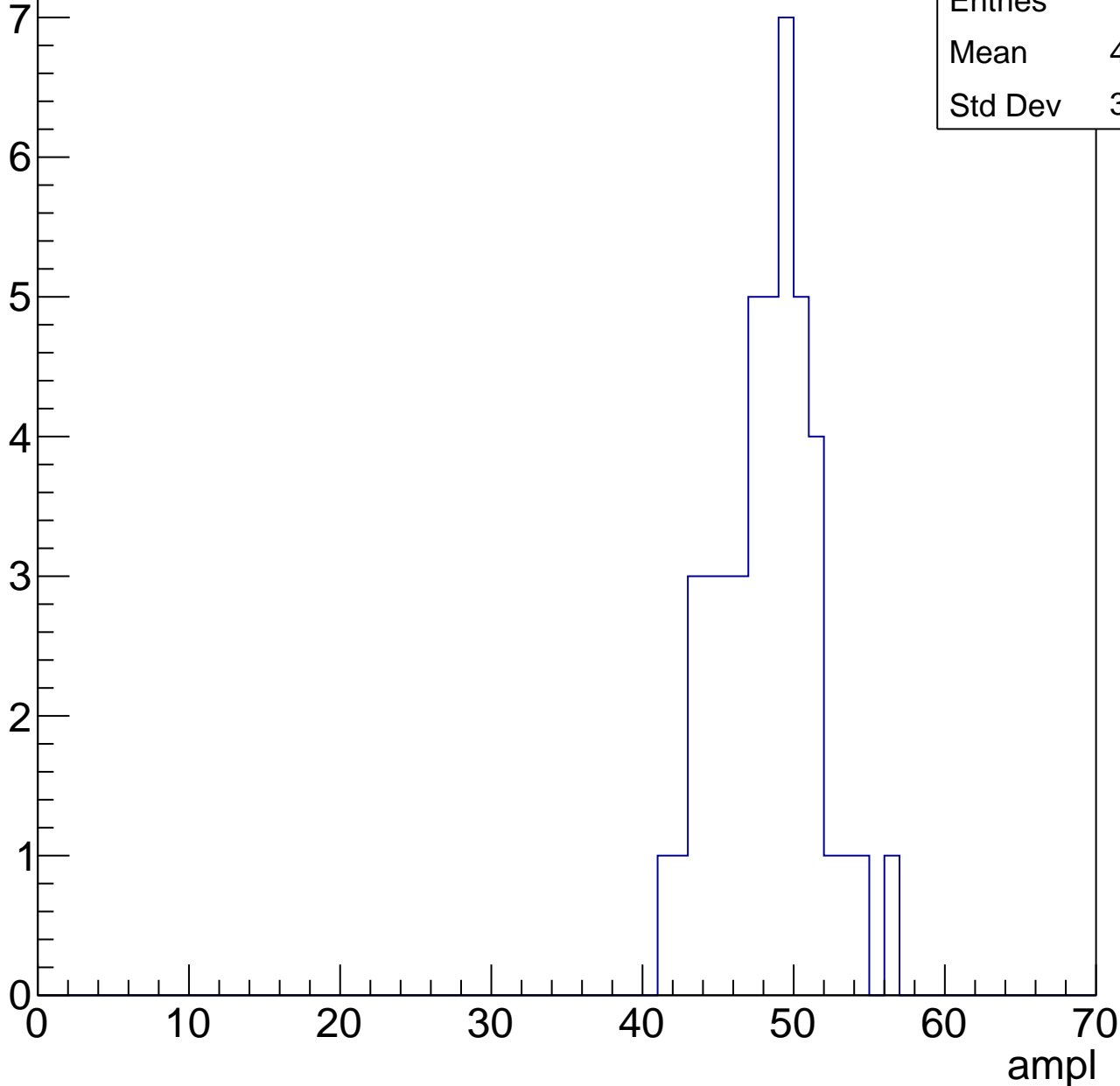


B1L101S, U4-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	47.82
Std Dev	3.214

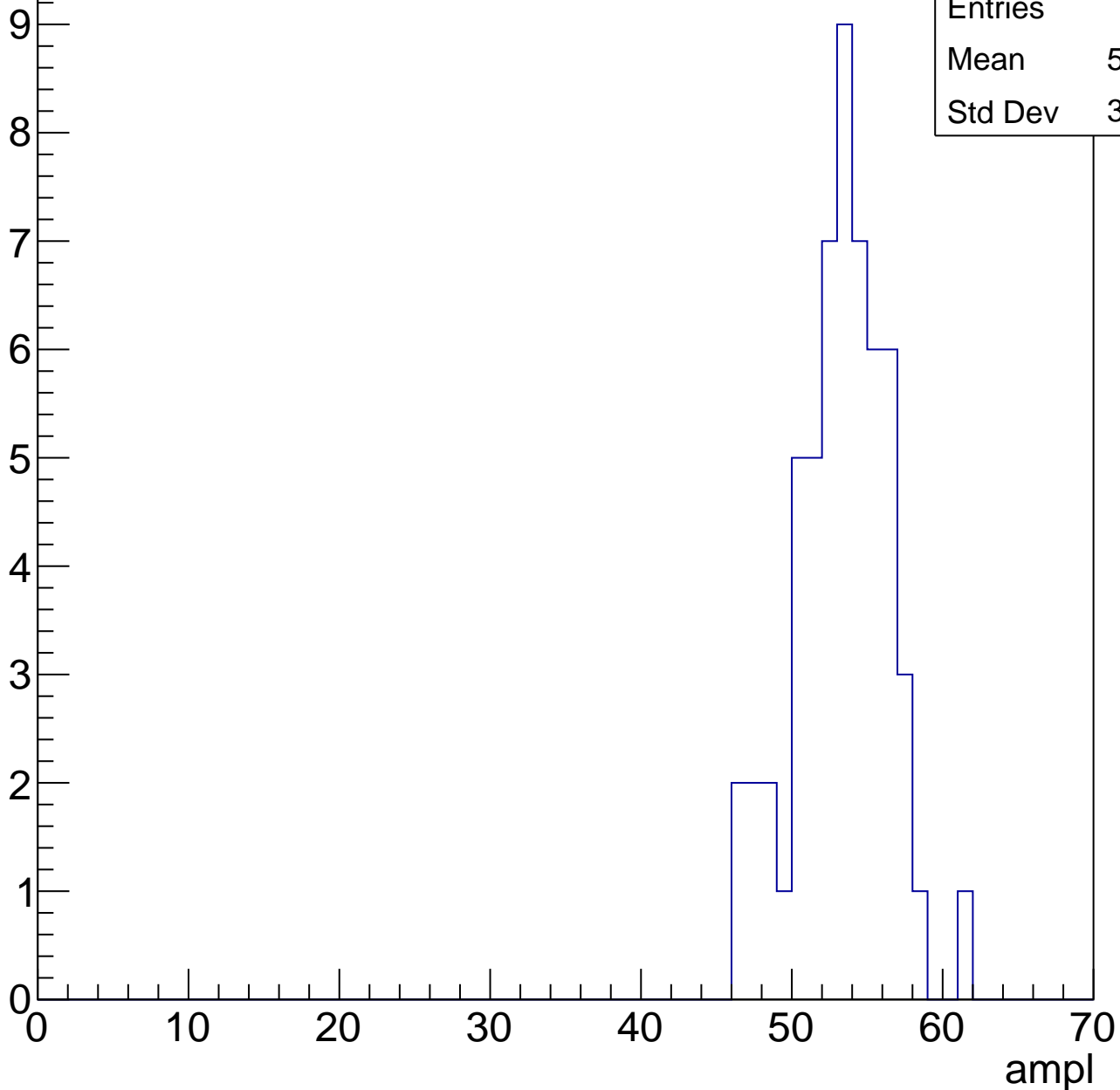


B1L101S, U4-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	52.82
Std Dev	3.044

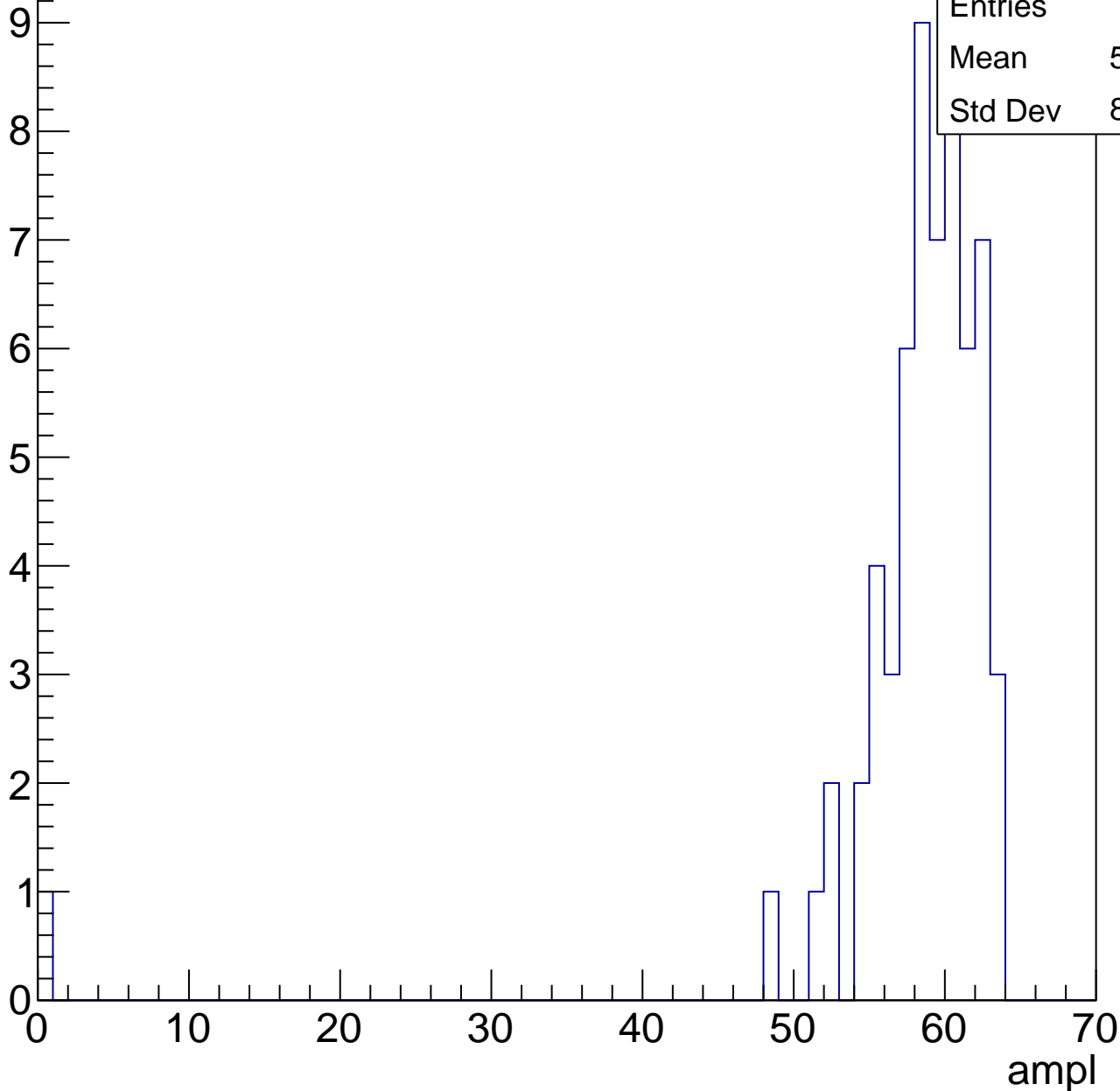


B1L101S, U4-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	57.46
Std Dev	8.034

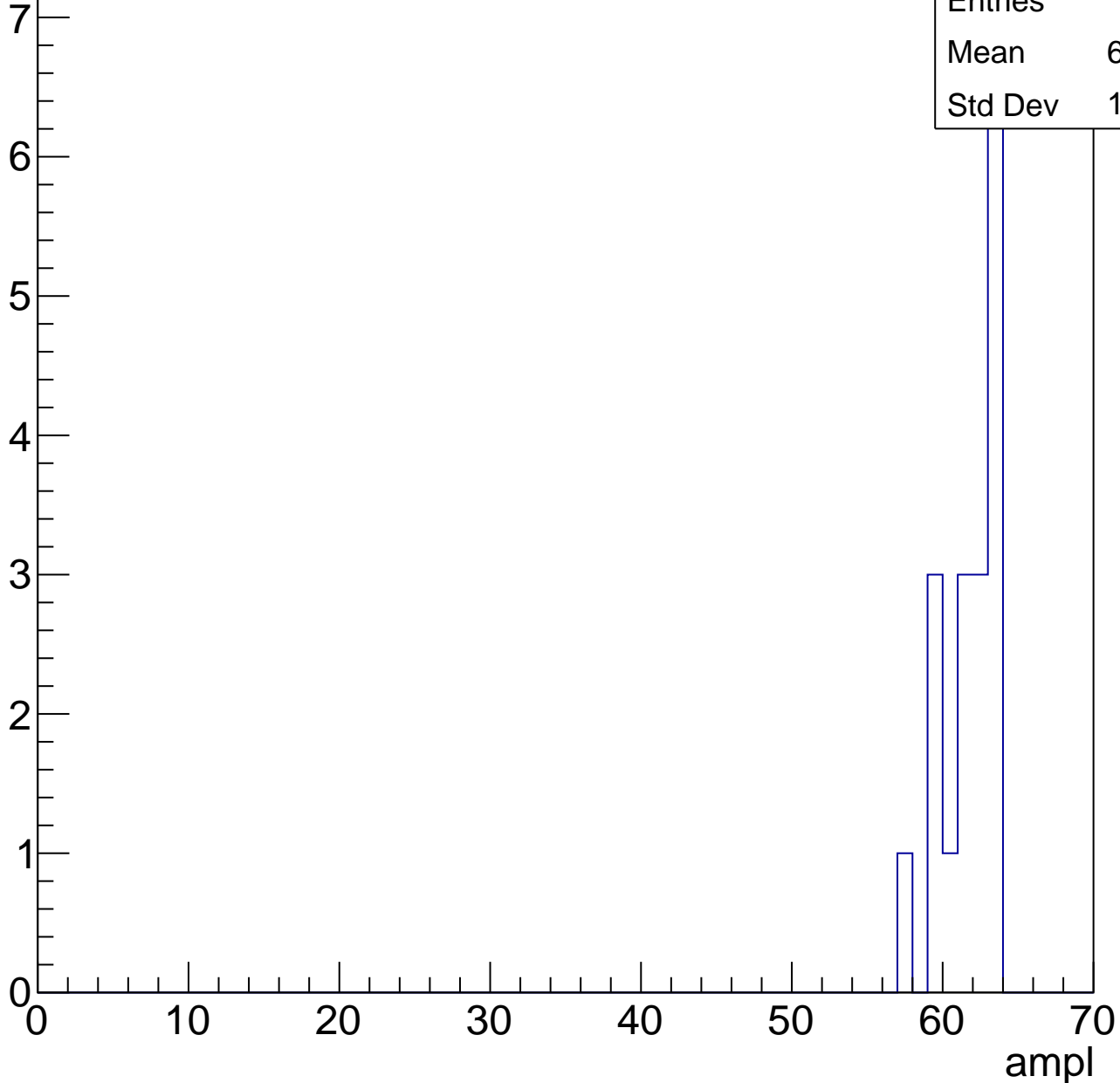


B1L101S, U4-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	61.33
Std Dev	1.795



B1L101S, U4-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U4-ch97, adc0

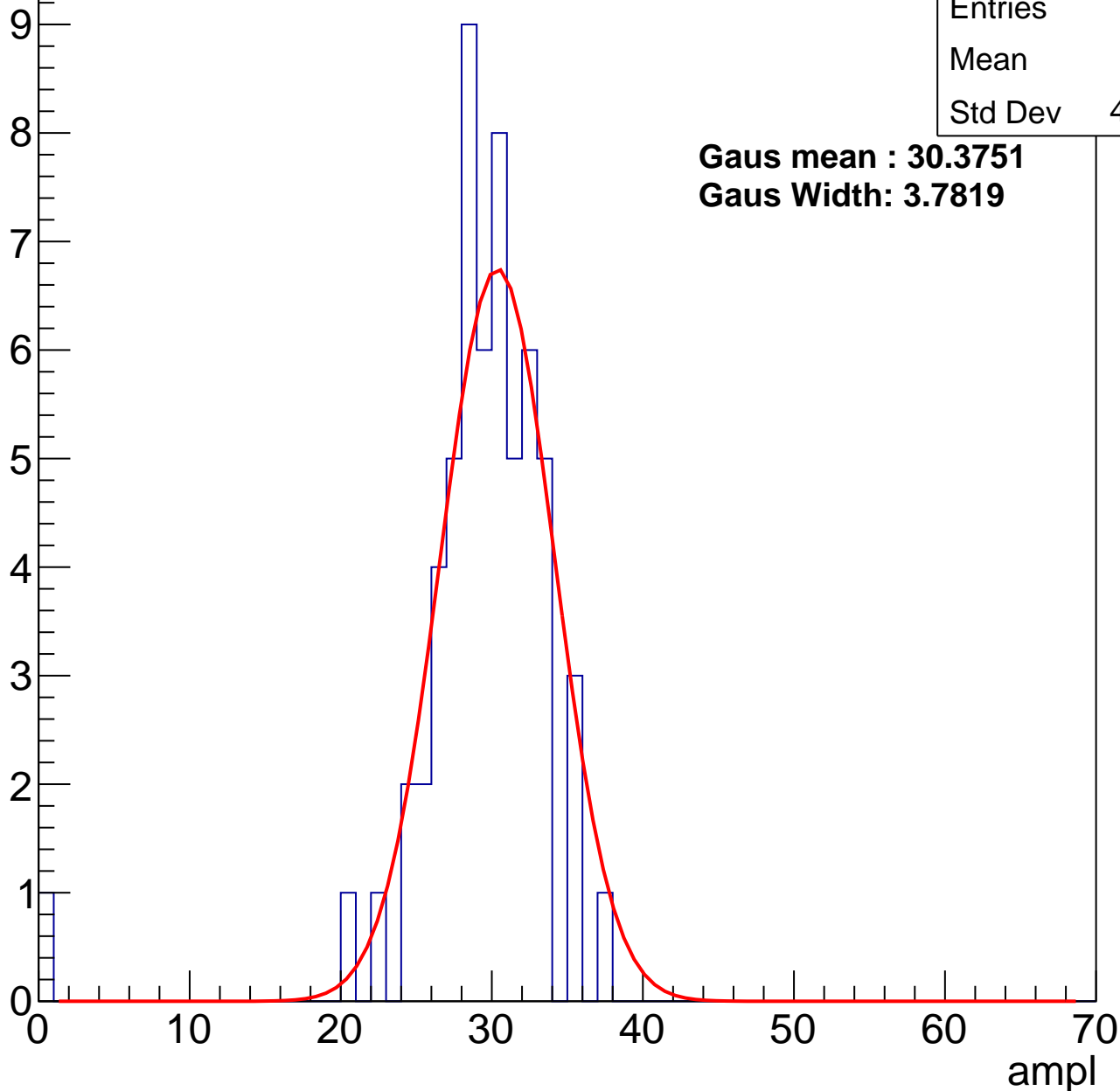
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.8
Std Dev	4.957

Gaus mean : 30.3751

Gaus Width: 3.7819



B1L101S, U4-ch97, adc1

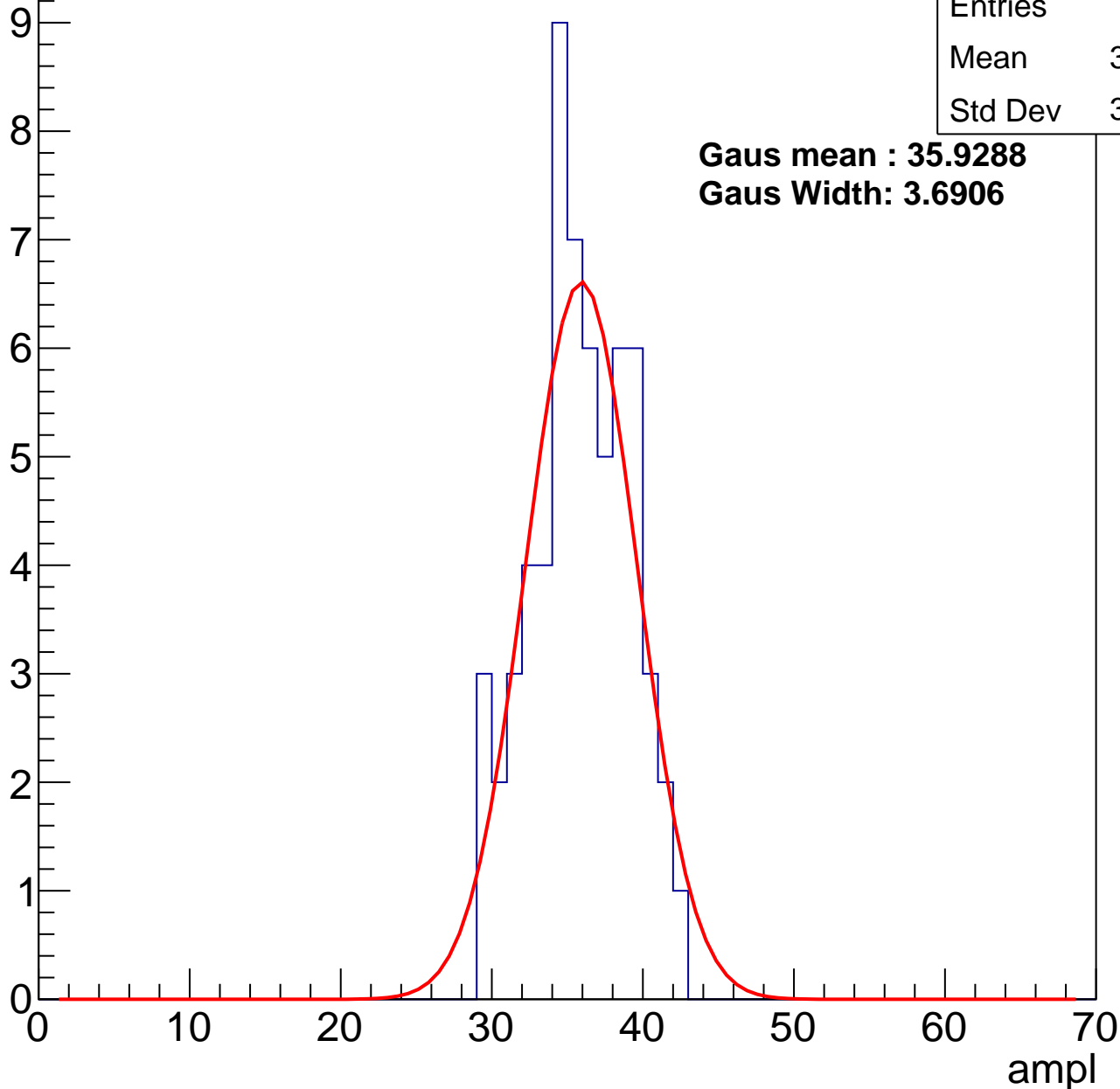
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.38
Std Dev	3.215

Gaus mean : 35.9288

Gaus Width: 3.6906



B1L101S, U4-ch97, adc2

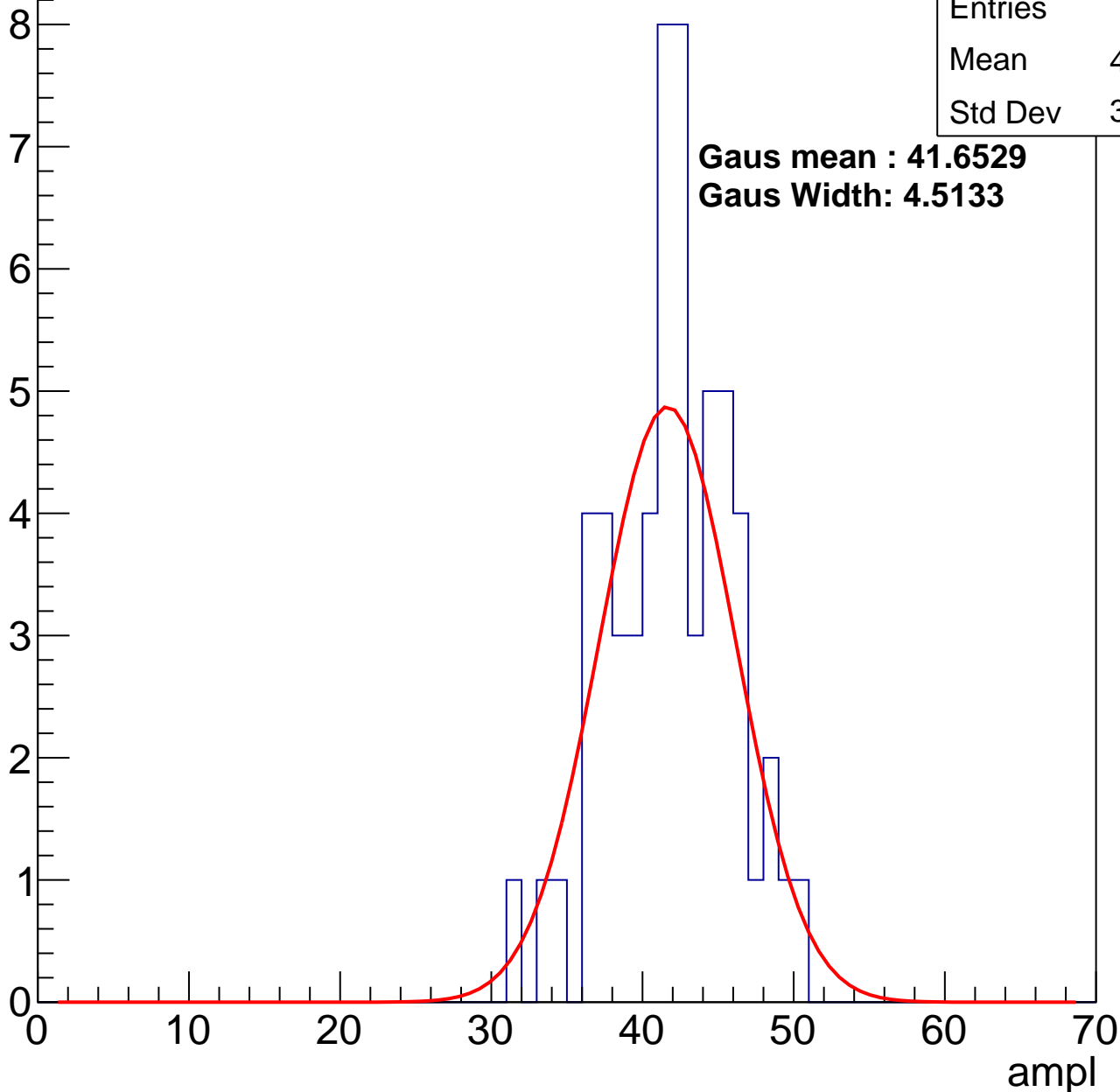
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	41.44
Std Dev	3.976

Gaus mean : 41.6529

Gaus Width: 4.5133

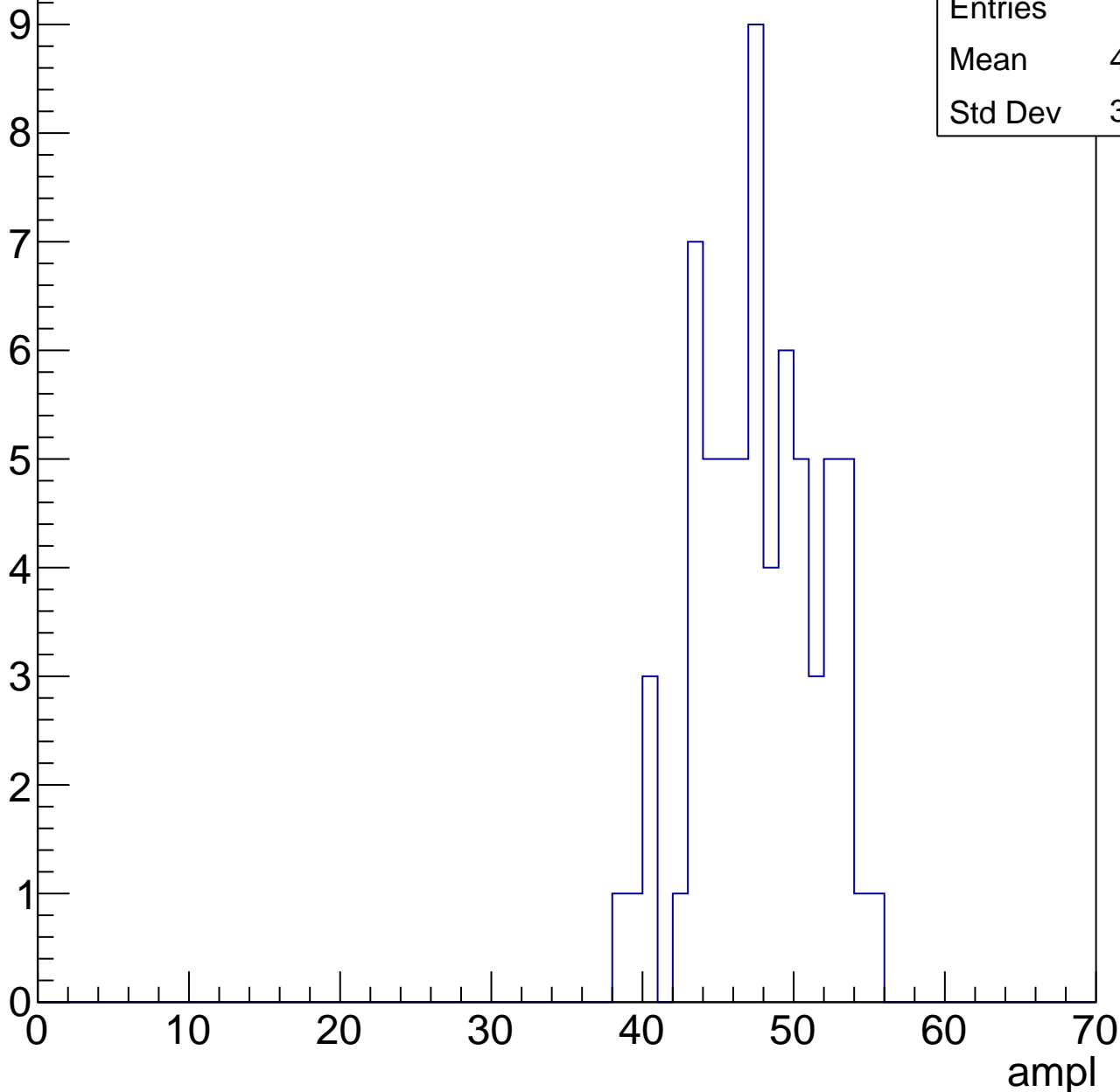


B1L101S, U4-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	47.18
Std Dev	3.932

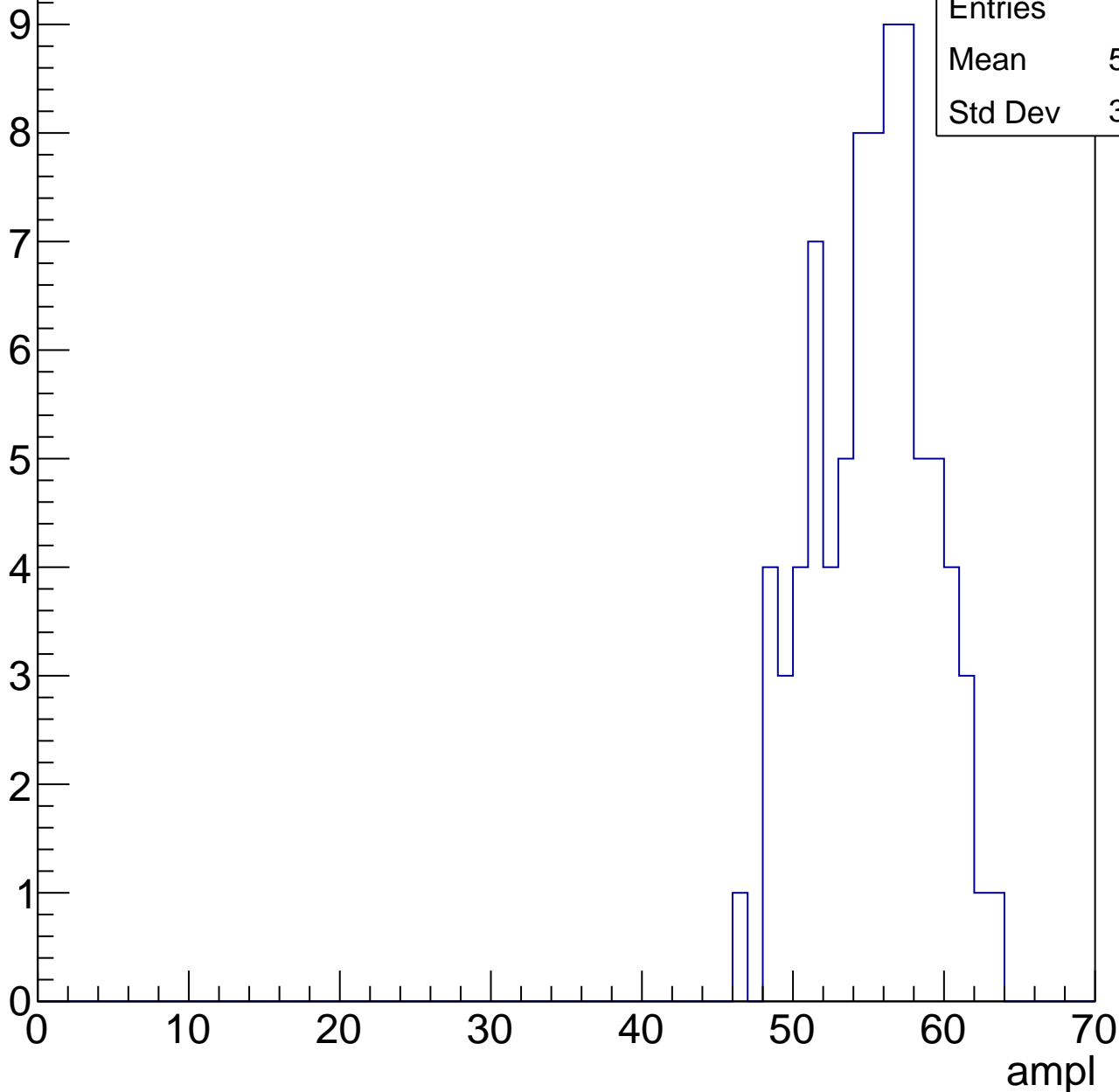


B1L101S, U4-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	54.78
Std Dev	3.765

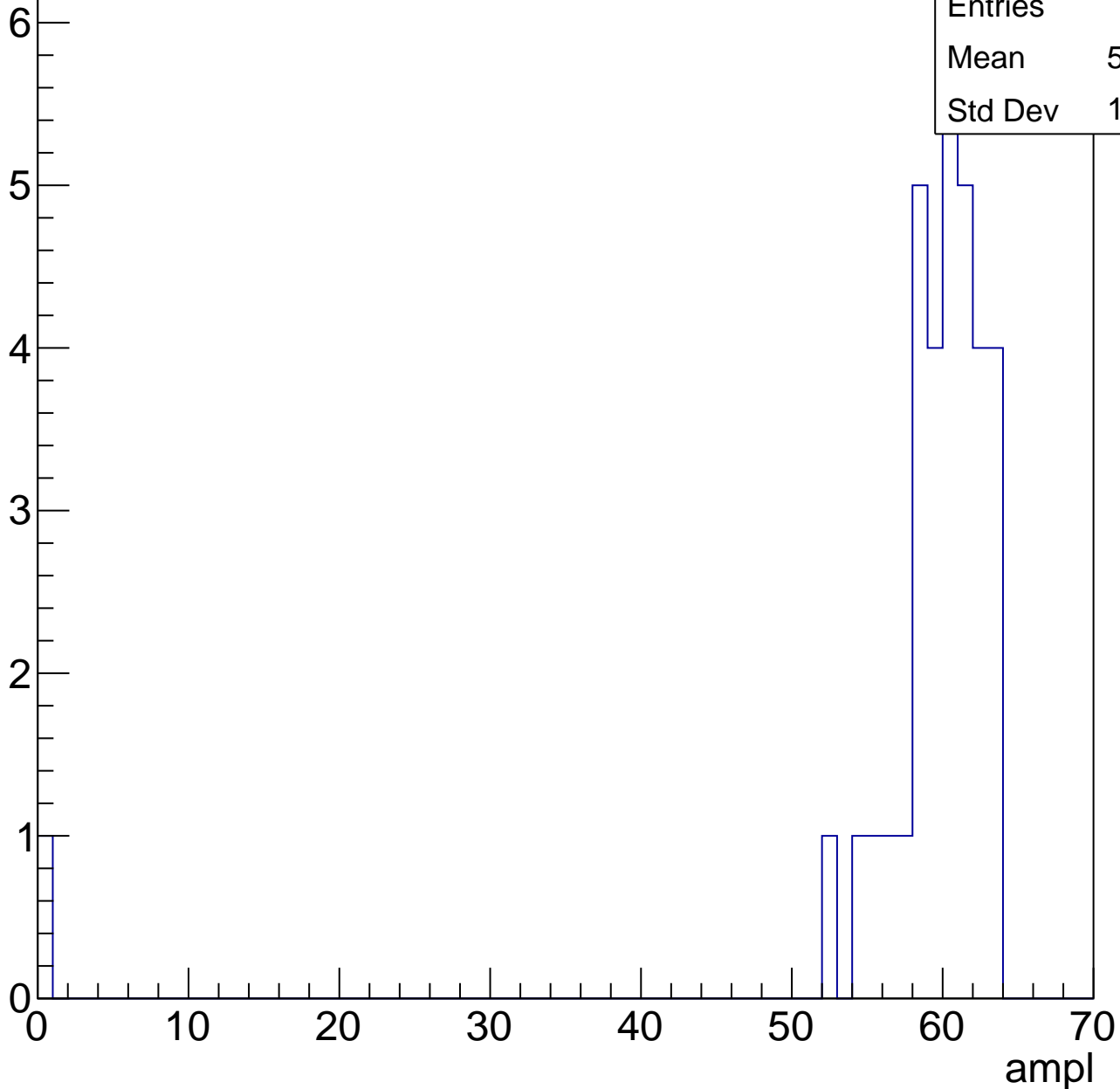


B1L101S, U4-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	57.79
Std Dev	10.38

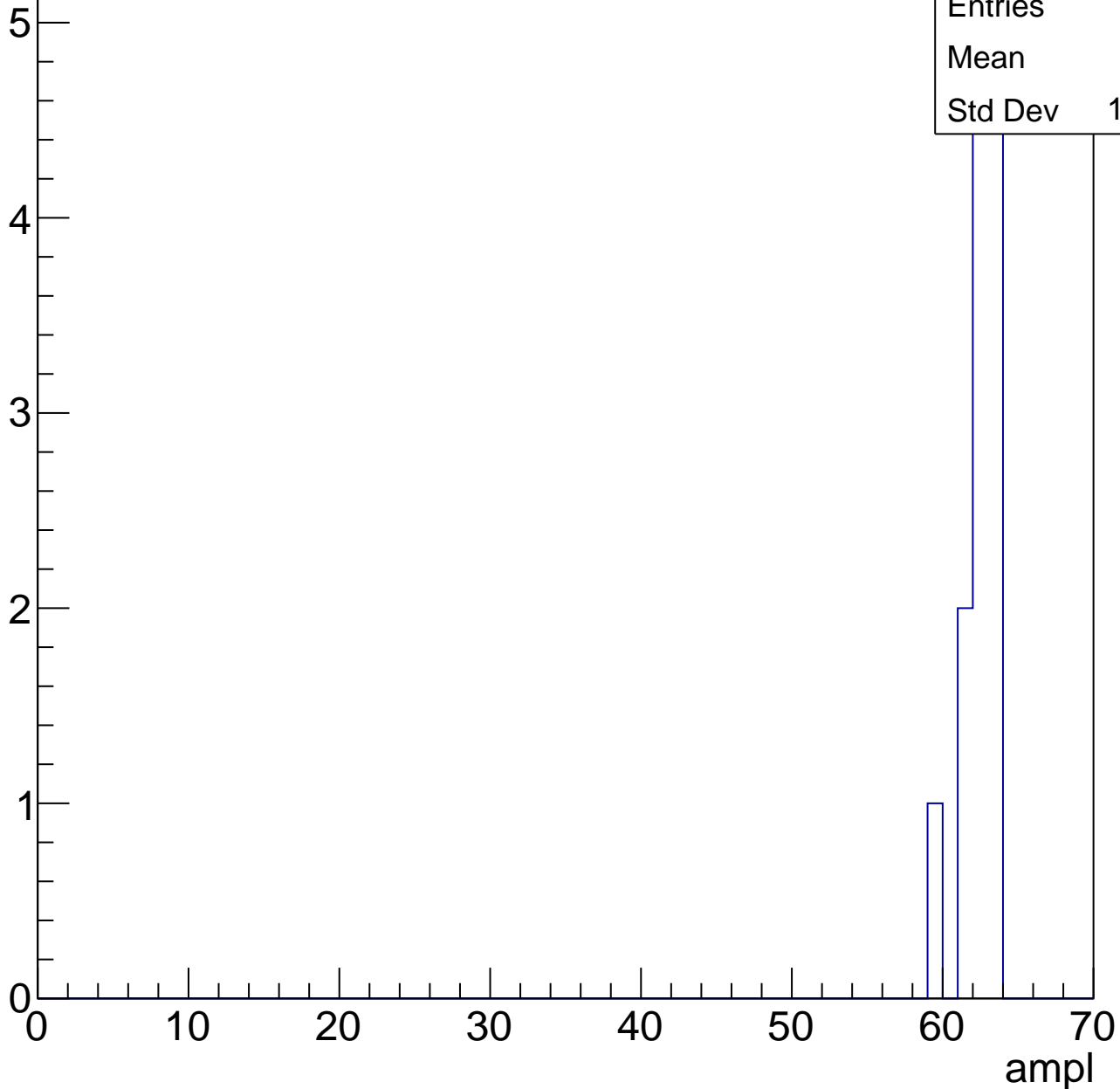


B1L101S, U4-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	62
Std Dev	1.109



B1L101S, U4-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch98, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	27.88
Std Dev	3.883

Gaus mean : 28.2893

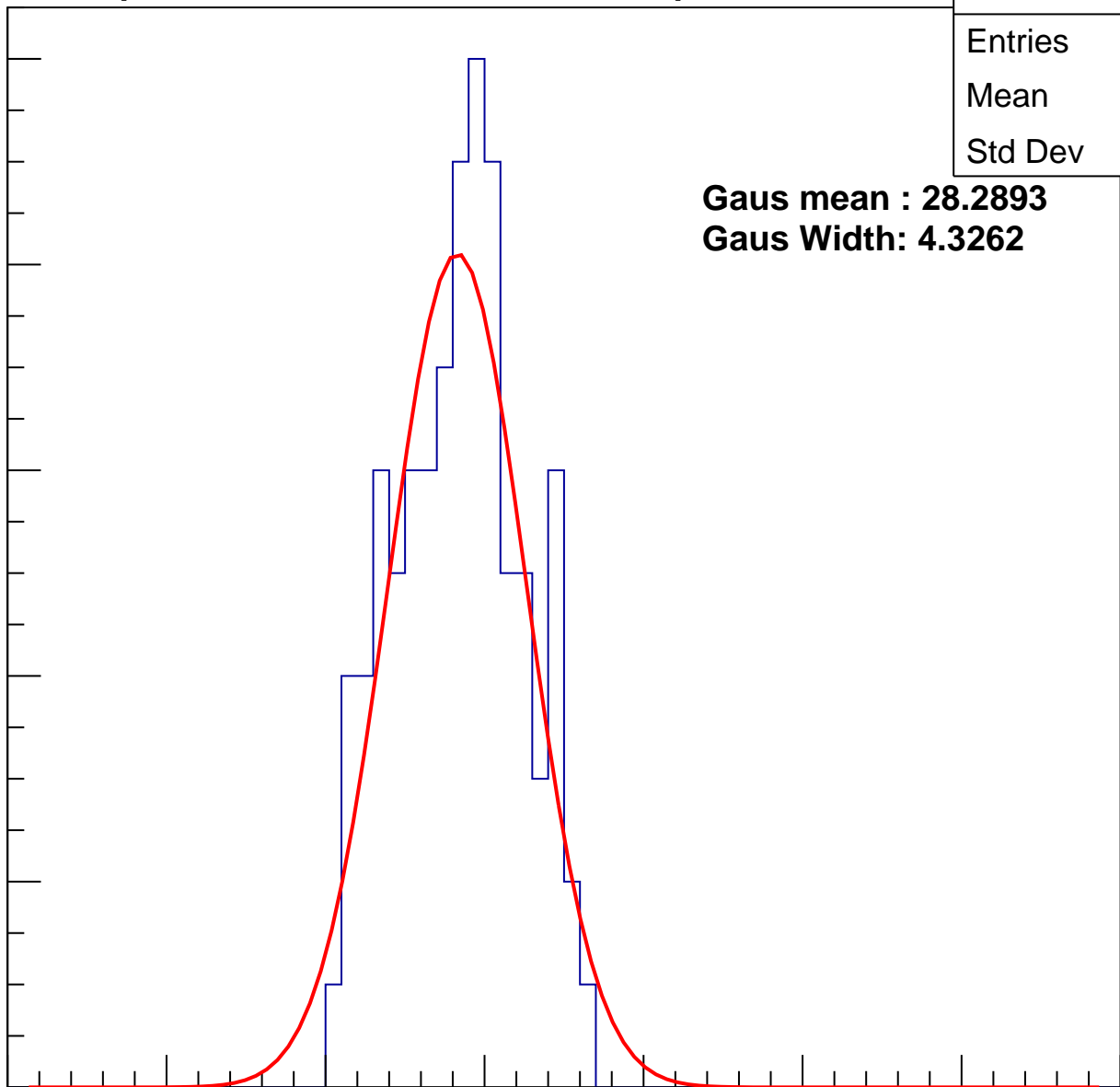
Gaus Width: 4.3262

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch98, adc1

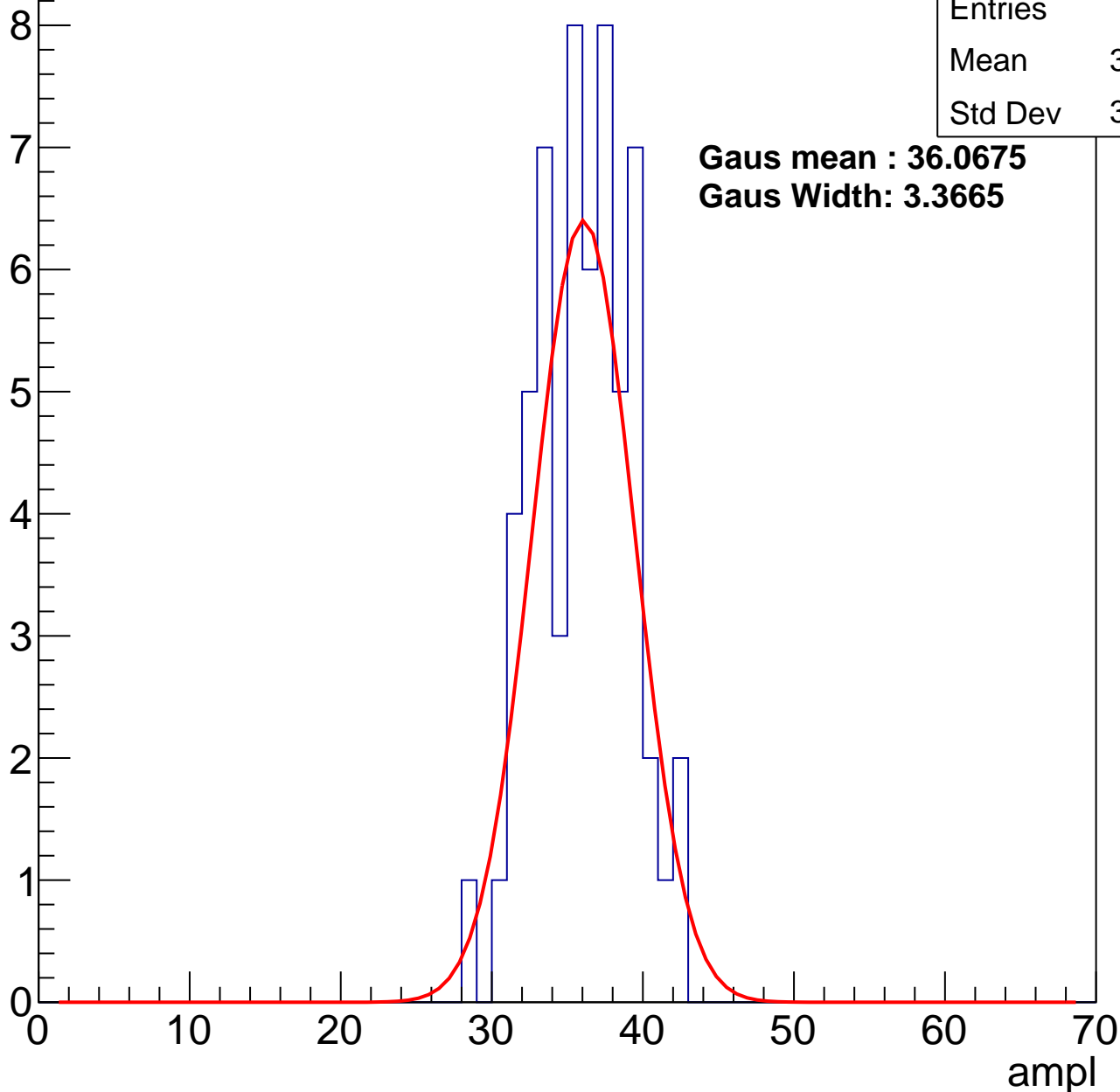
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.58
Std Dev	3.089

Gaus mean : 36.0675

Gaus Width: 3.3665



B1L101S, U4-ch98, adc2

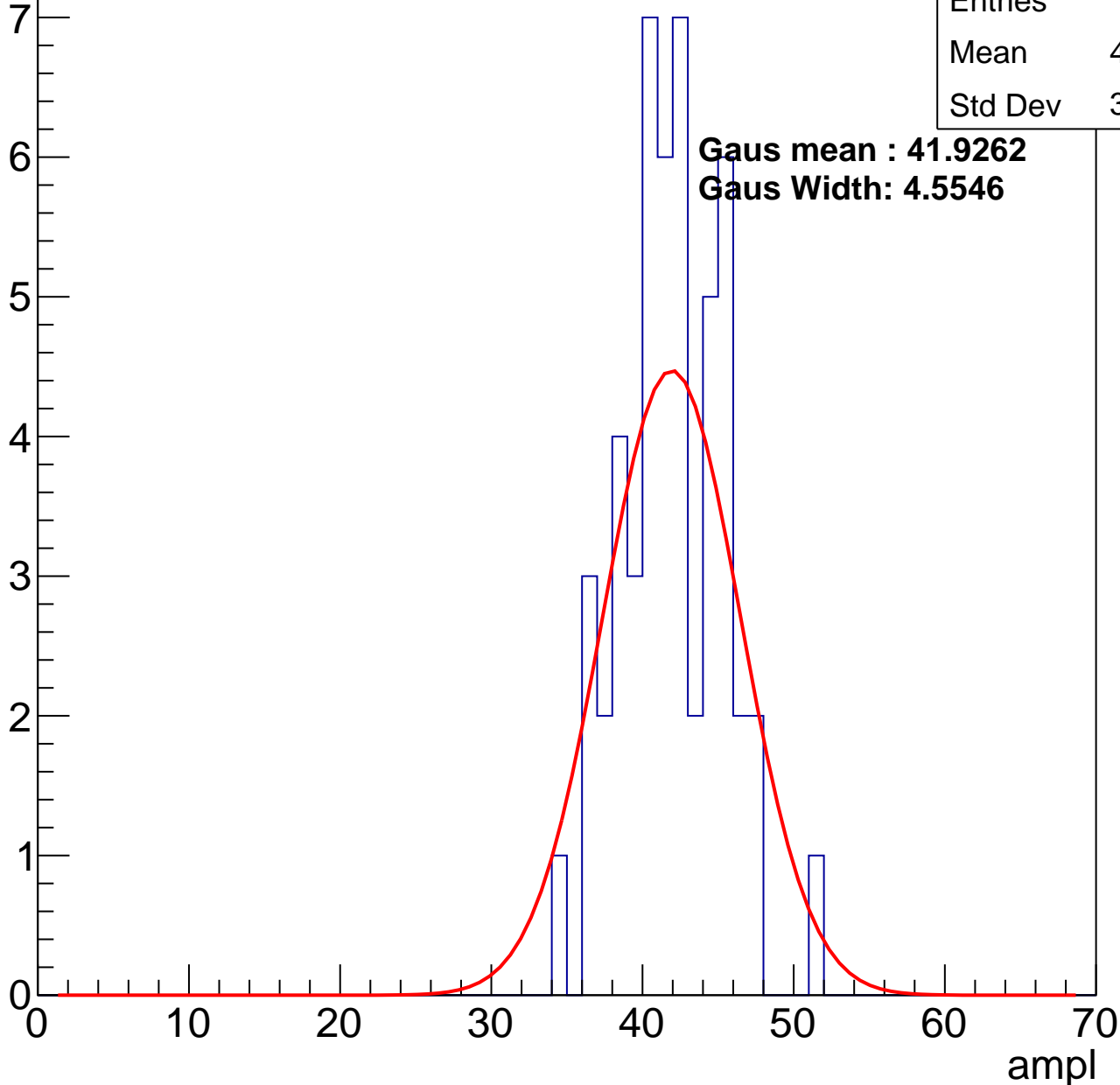
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	41.53
Std Dev	3.357

Gaus mean : 41.9262

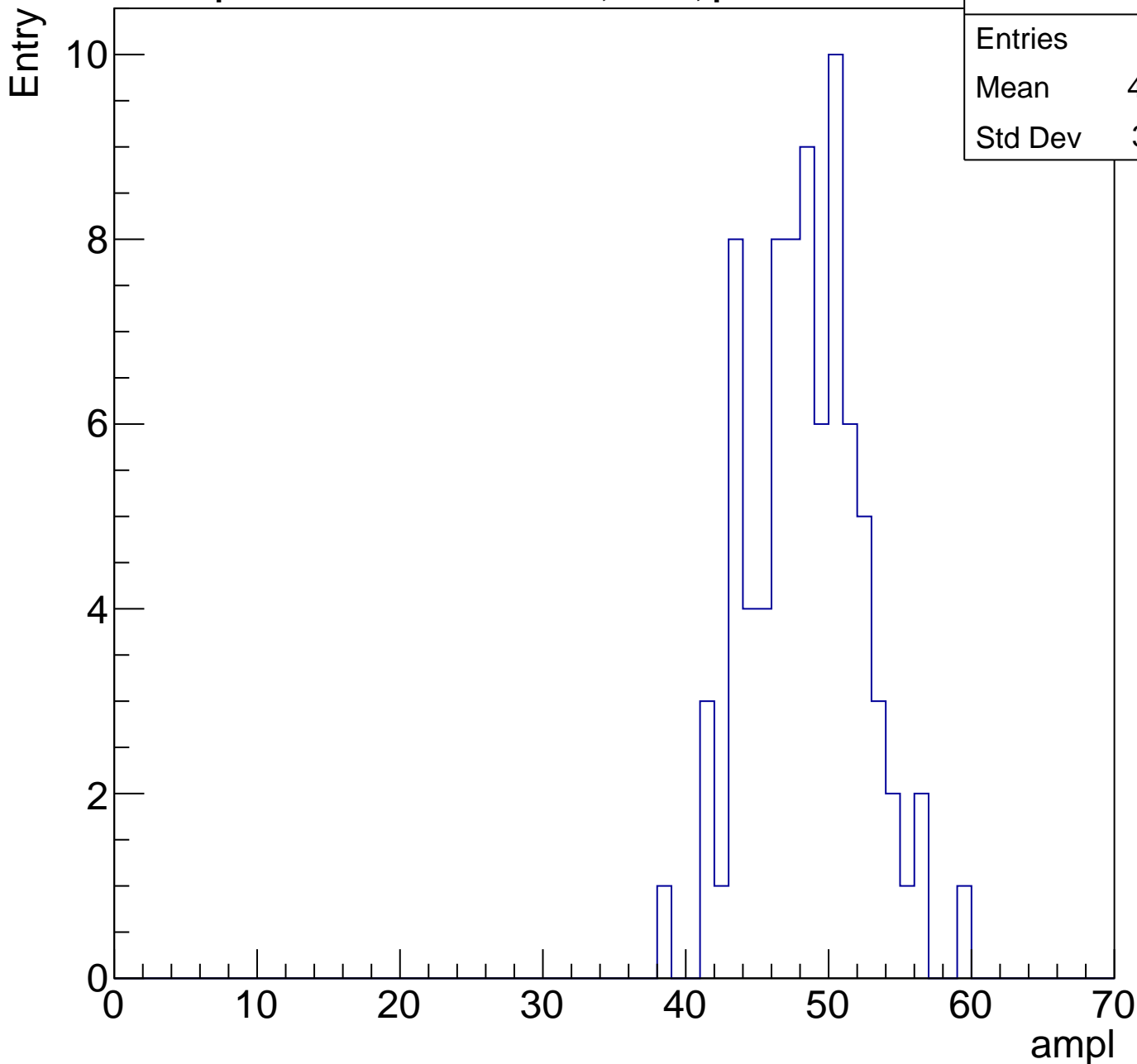
Gaus Width: 4.5546



B1L101S, U4-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	47.95
Std Dev	3.901

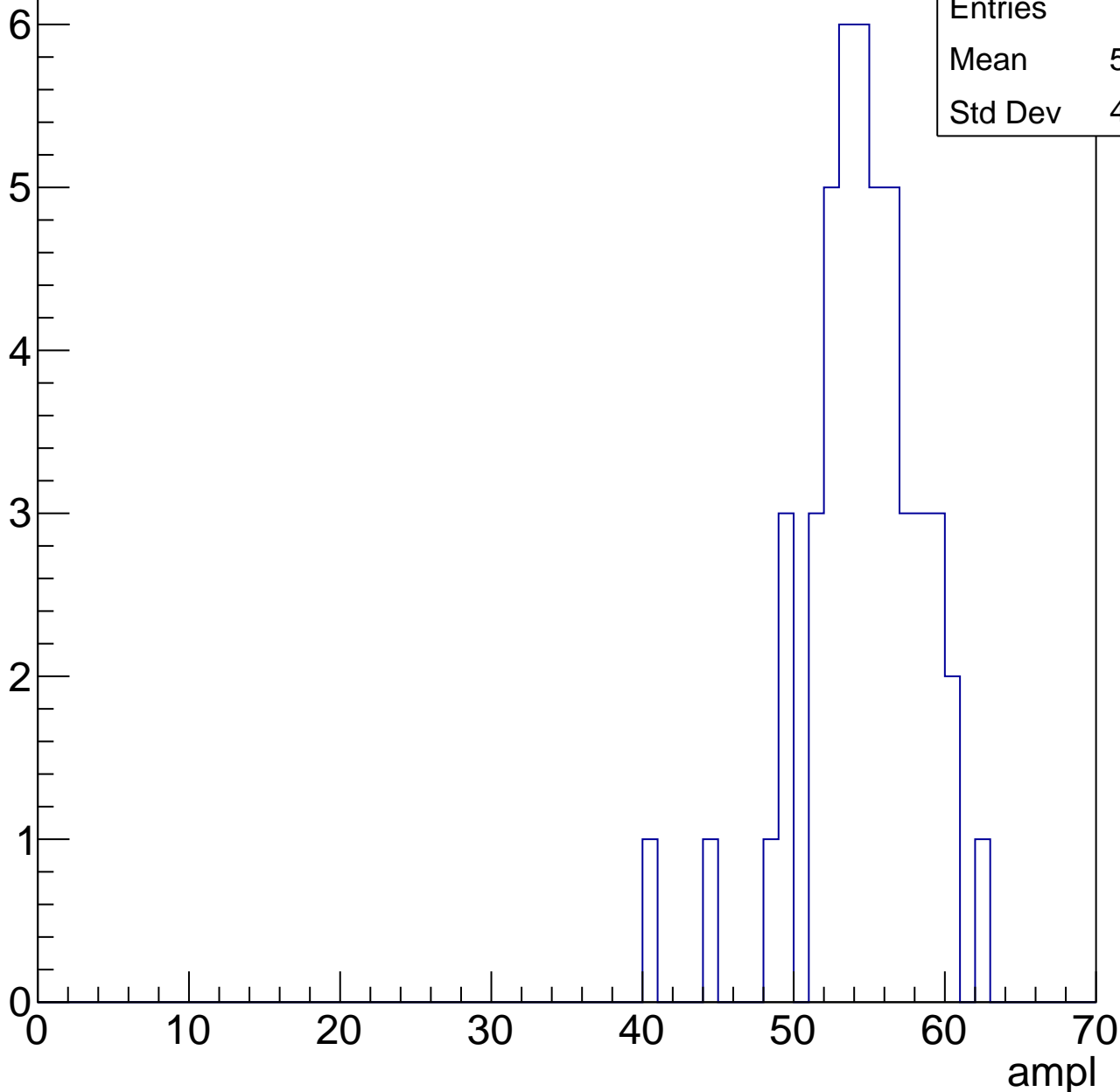


B1L101S, U4-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	54.02
Std Dev	4.008

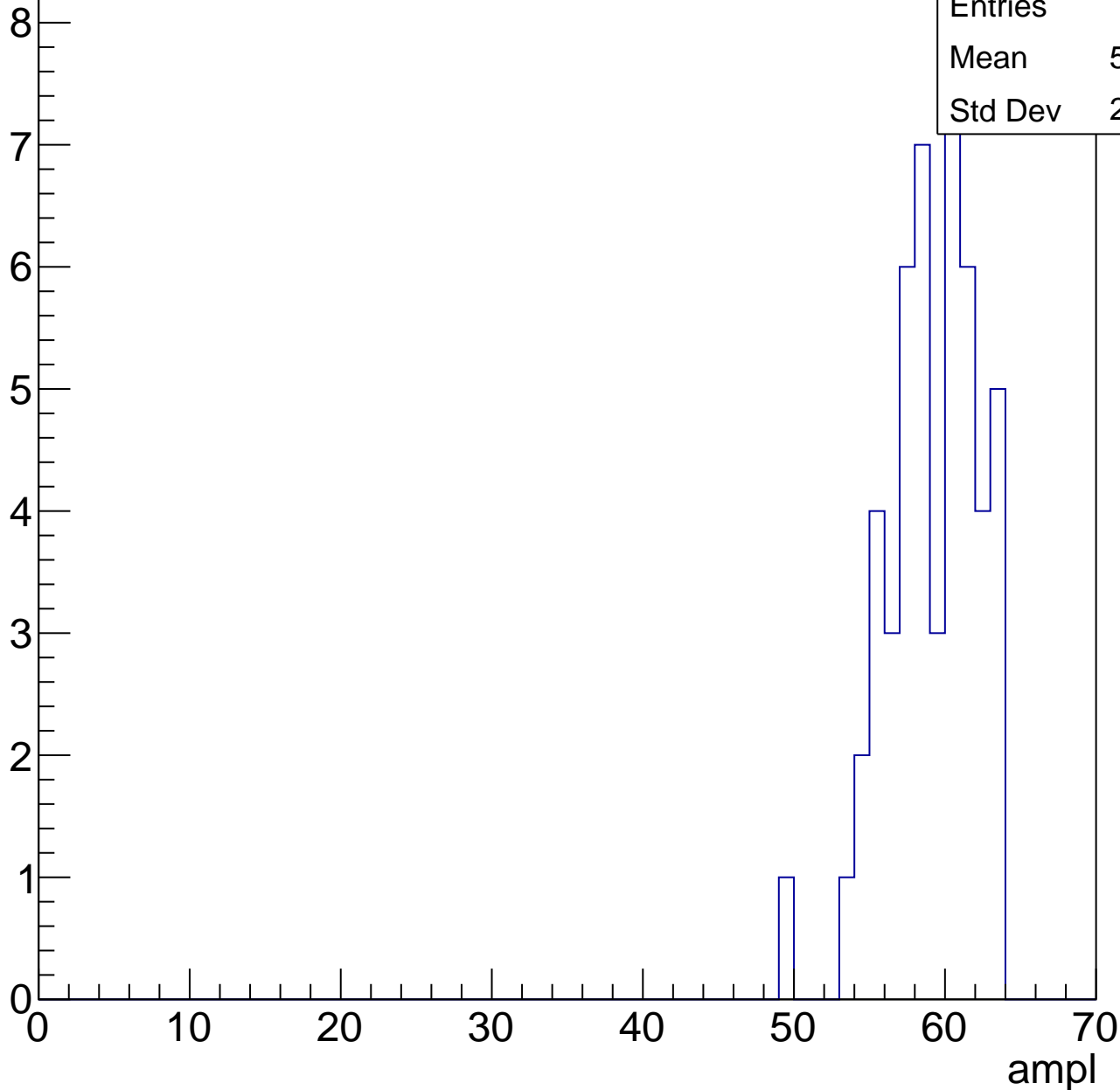


B1L101S, U4-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.64
Std Dev	2.998

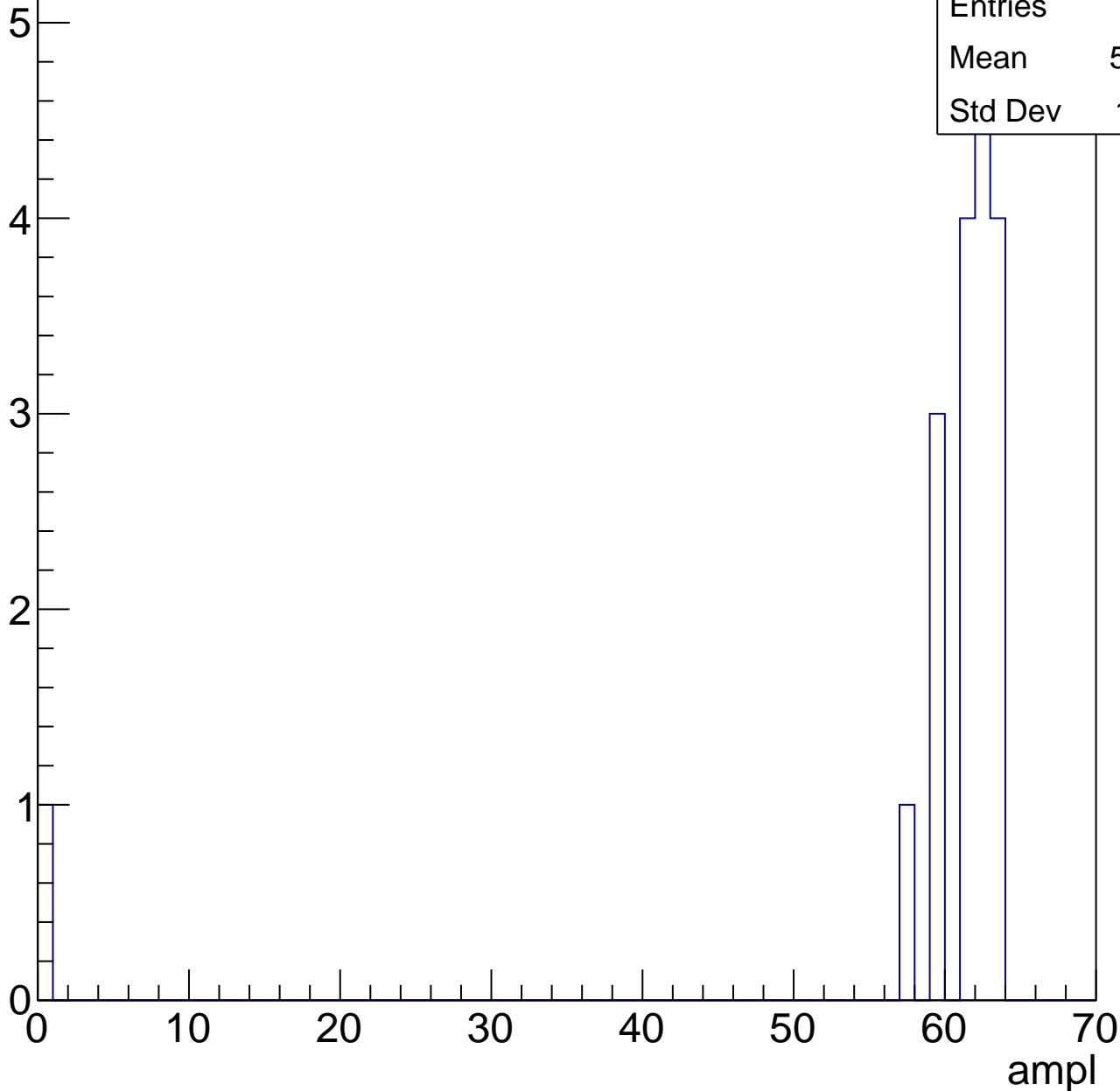


B1L101S, U4-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

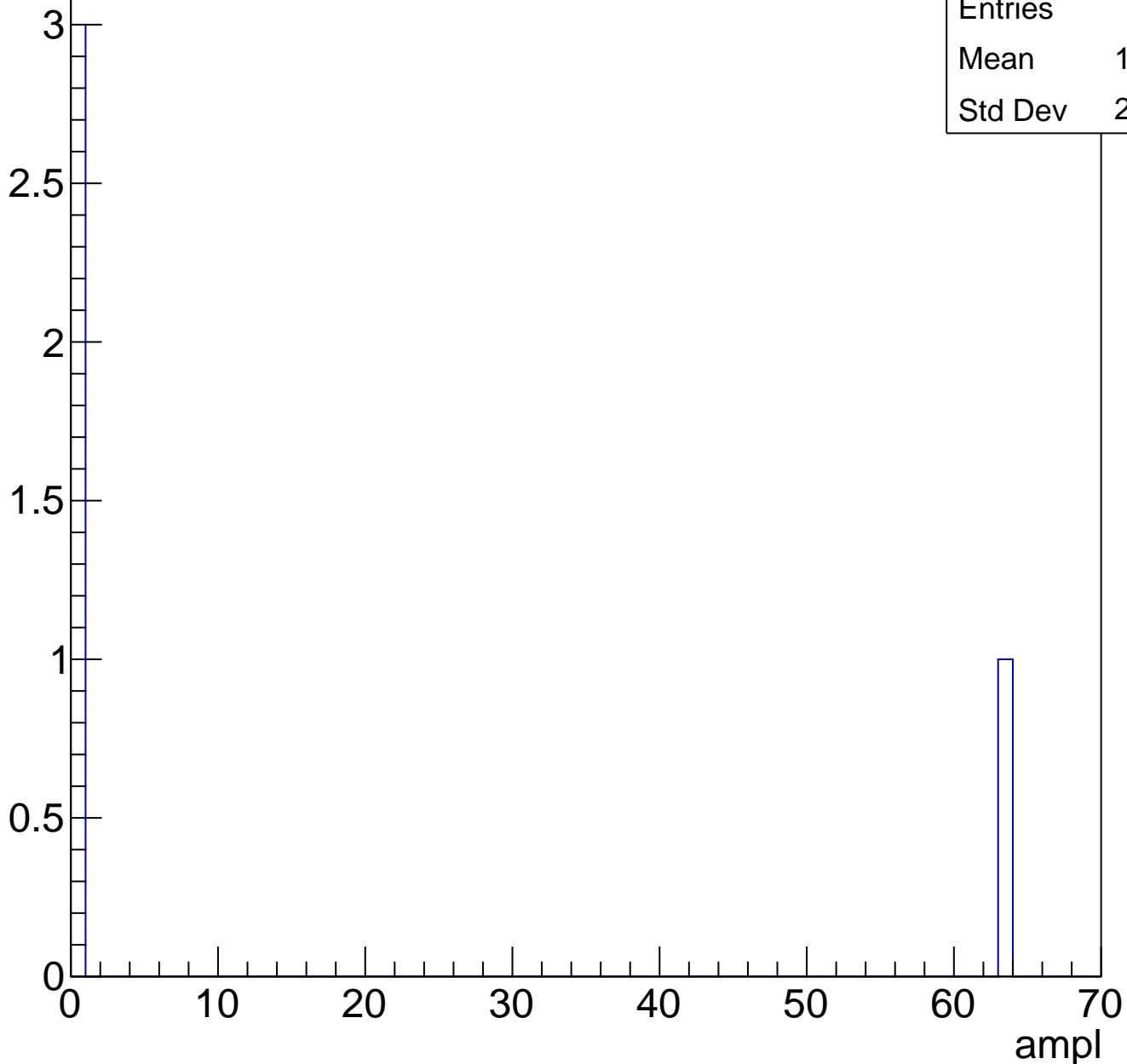
Entries	18
Mean	57.78
Std Dev	14.11



B1L101S, U4-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch99, adc0

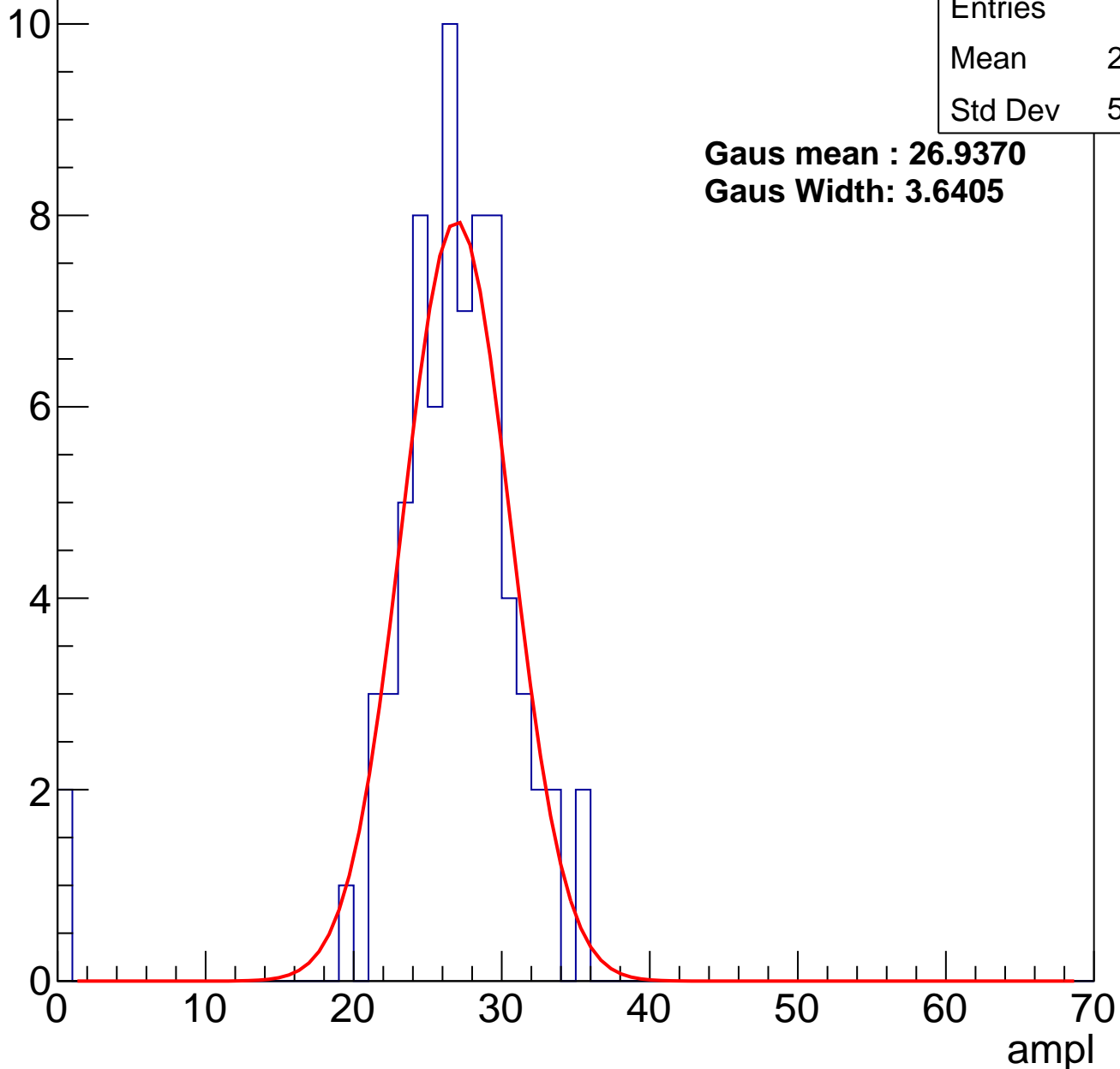
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	25.99
Std Dev	5.439

Gaus mean : 26.9370

Gaus Width: 3.6405

Entry



B1L101S, U4-ch99, adc1

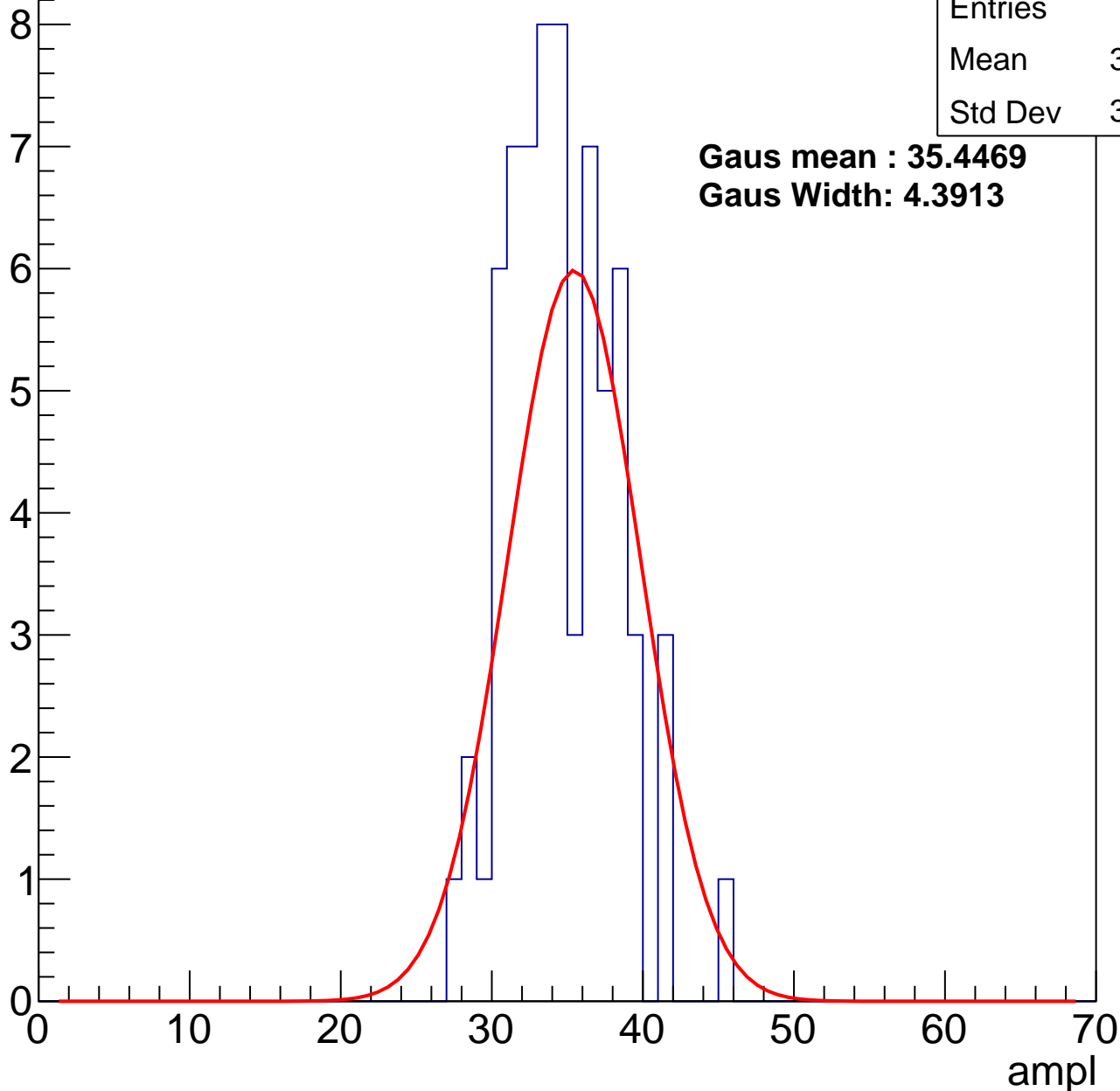
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	34.18
Std Dev	3.544

Gaus mean : 35.4469

Gaus Width: 4.3913



B1L101S, U4-ch99, adc2

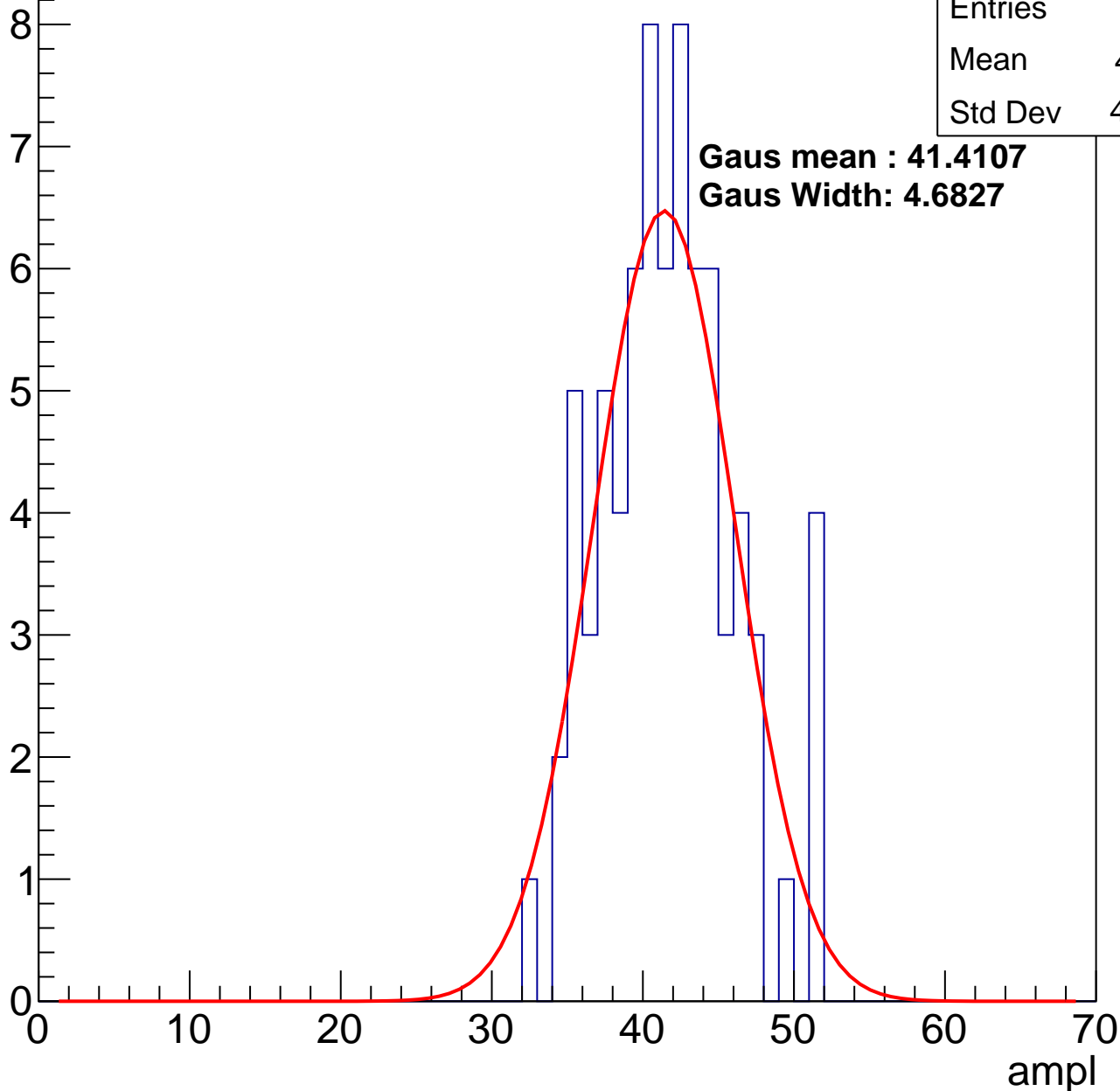
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	41.21
Std Dev	4.297

Gaus mean : 41.4107

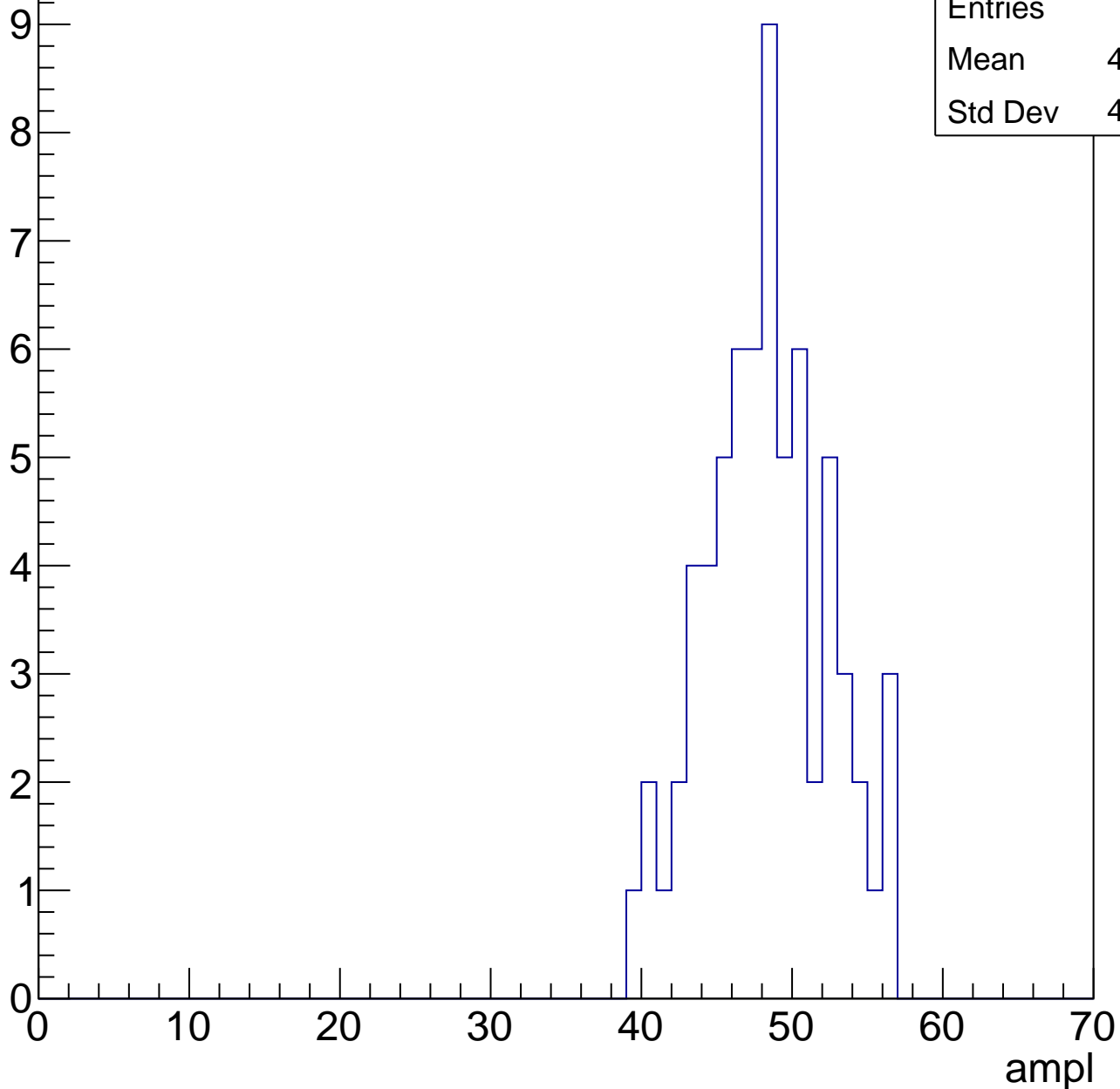
Gaus Width: 4.6827



B1L101S, U4-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

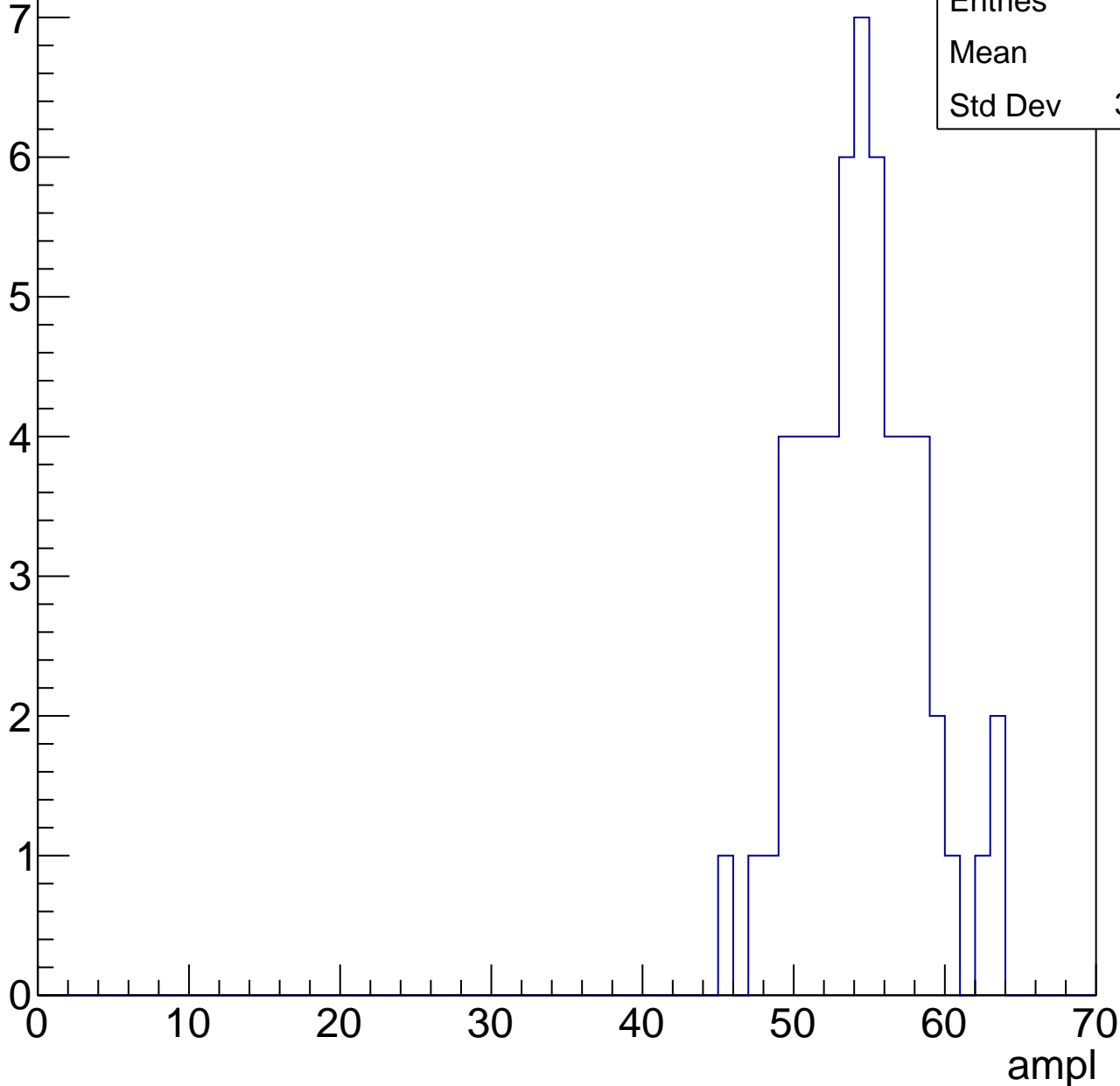


B1L101S, U4-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

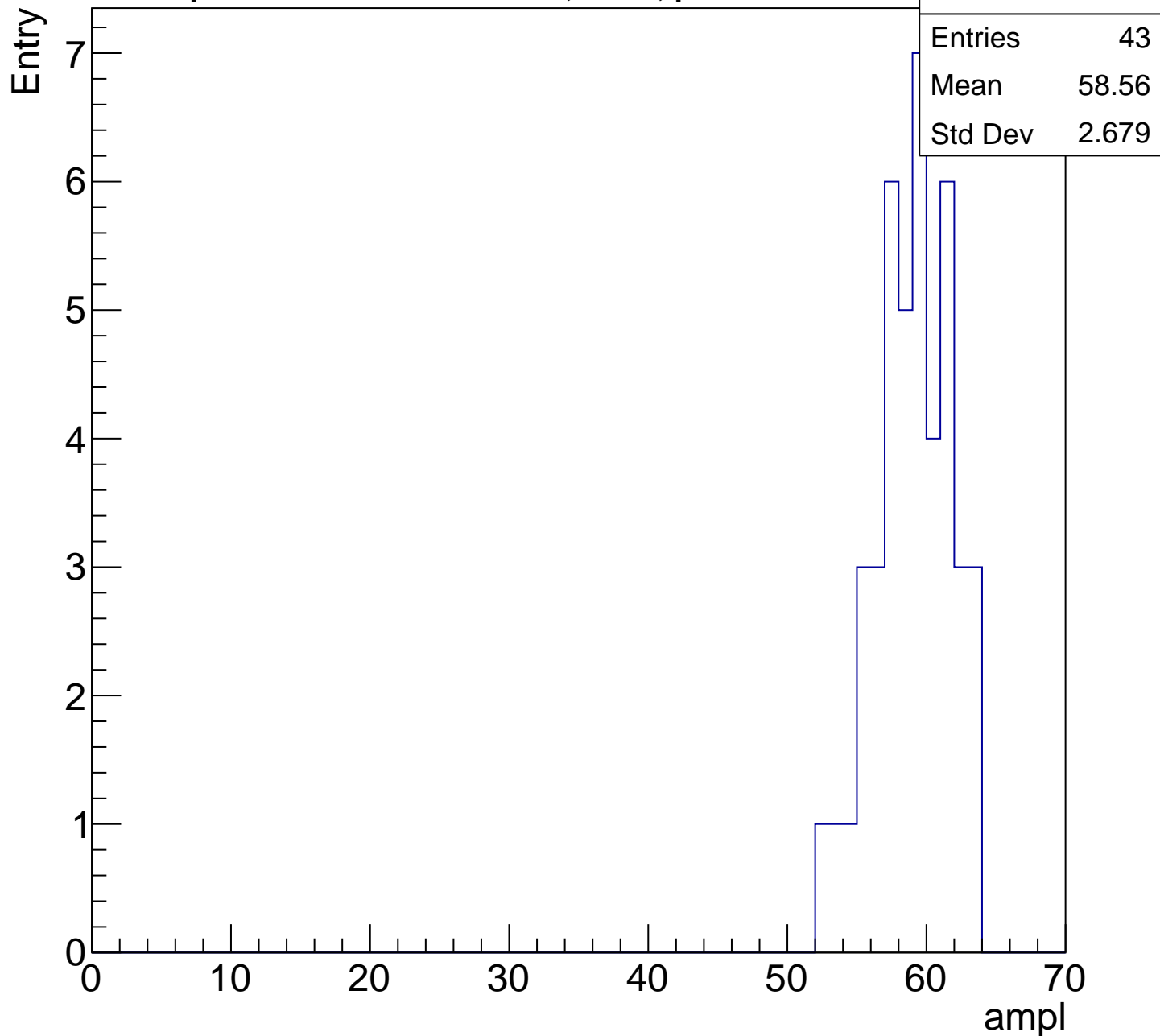
Entry

Entries	56
Mean	54
Std Dev	3.831



B1L101S, U4-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

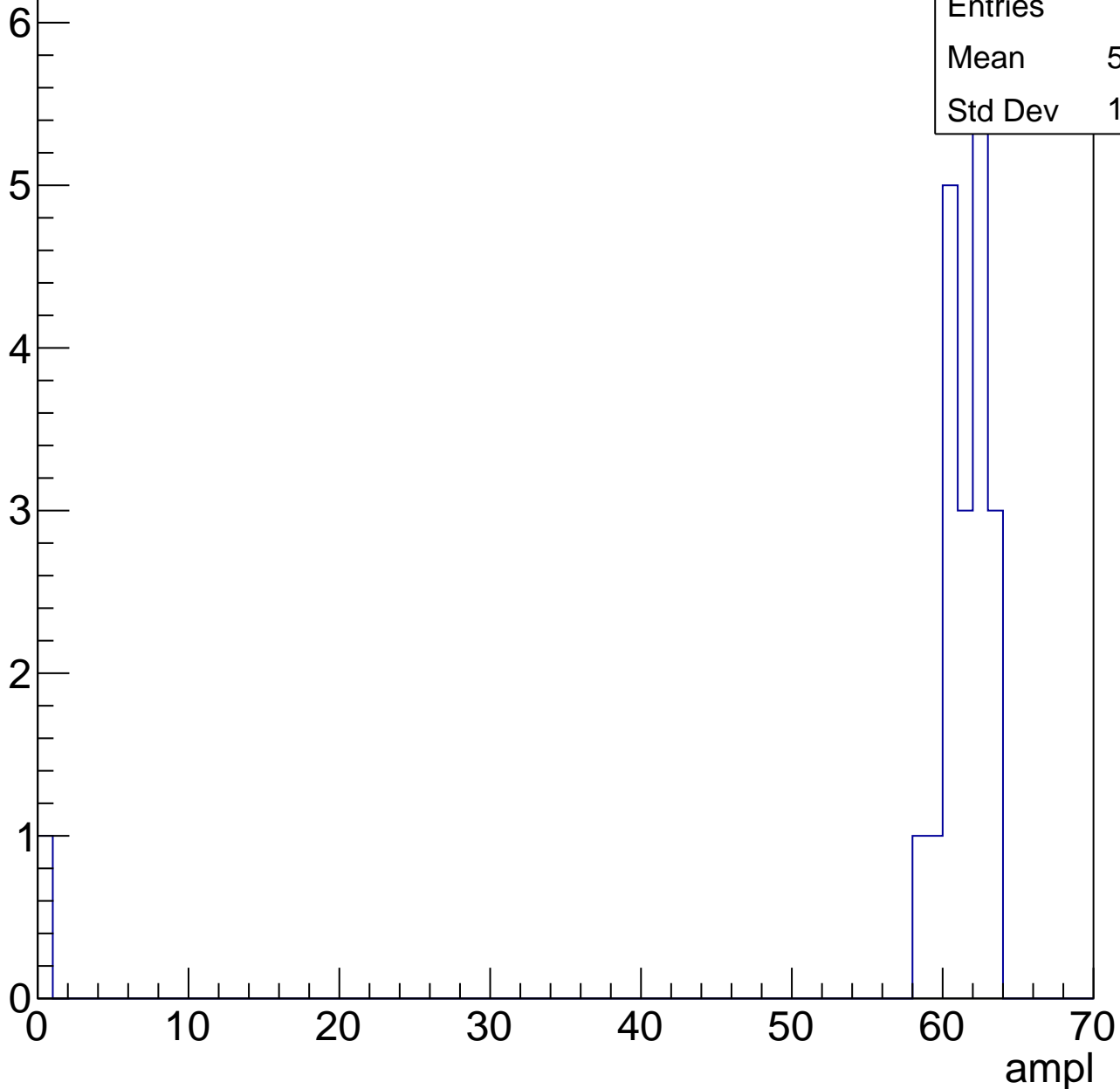


B1L101S, U4-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

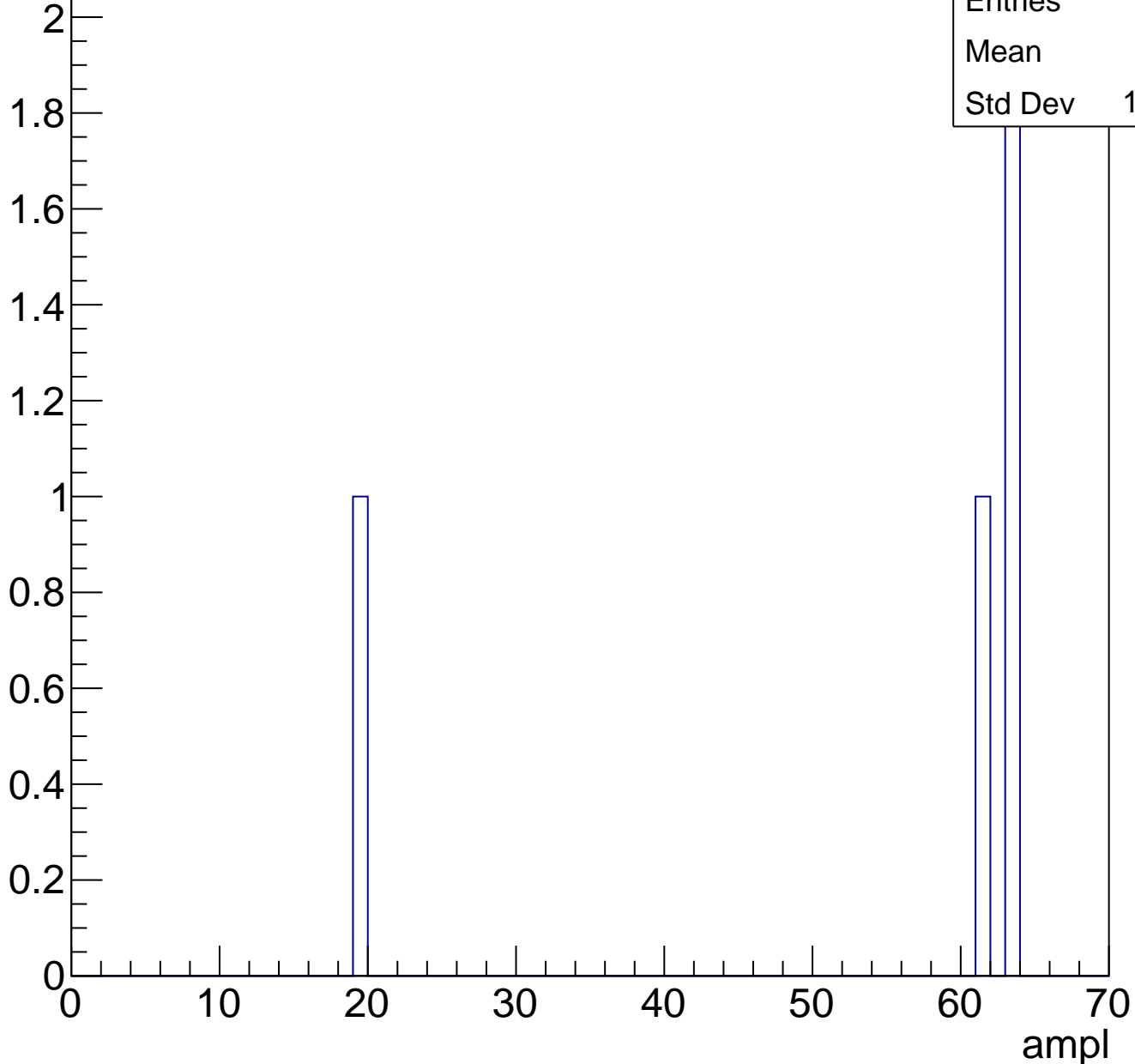
Entries	20
Mean	58.05
Std Dev	13.38



B1L101S, U4-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch100, adc0

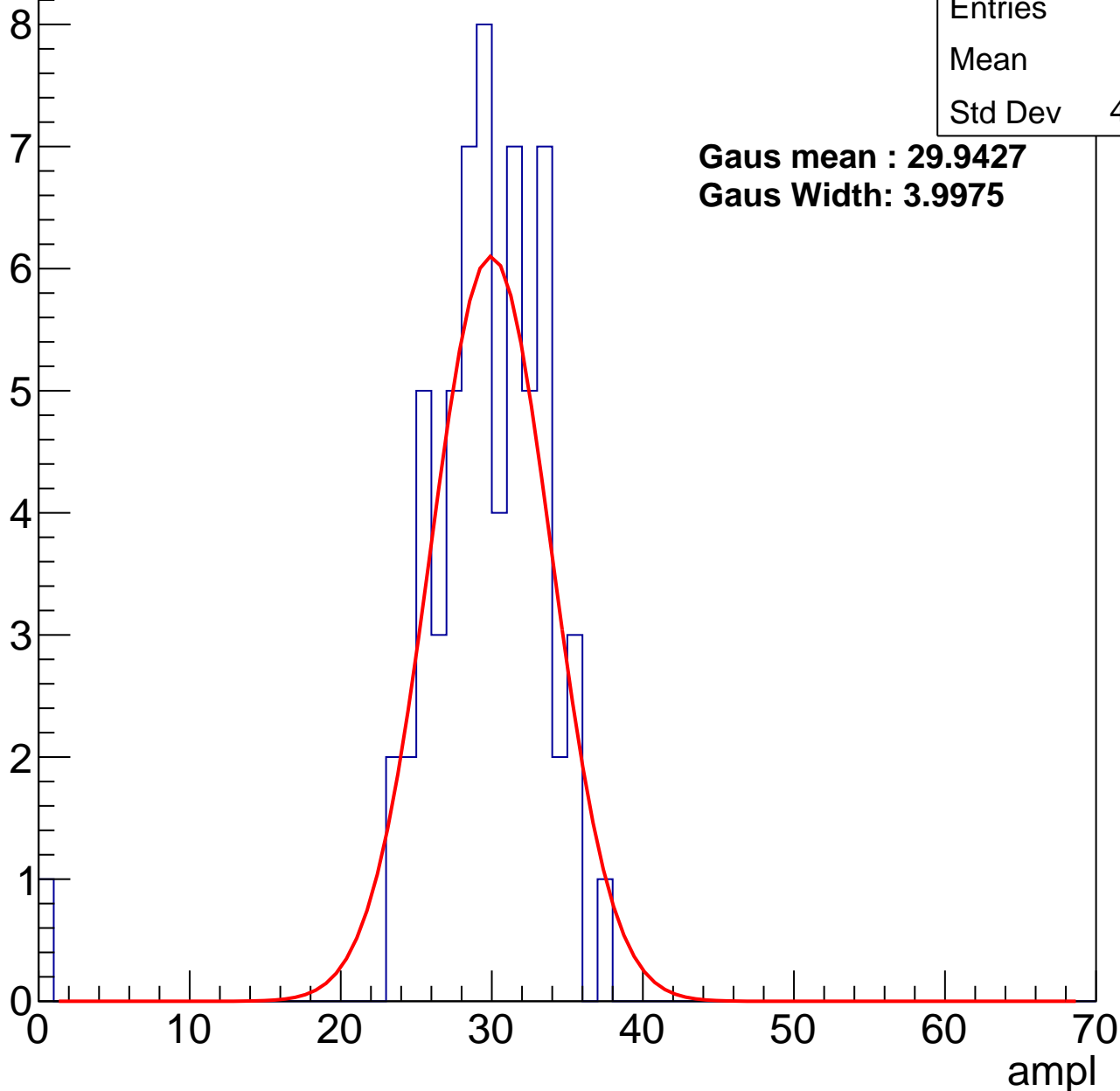
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29
Std Dev	4.932

Gaus mean : 29.9427

Gaus Width: 3.9975



B1L101S, U4-ch100, adc1

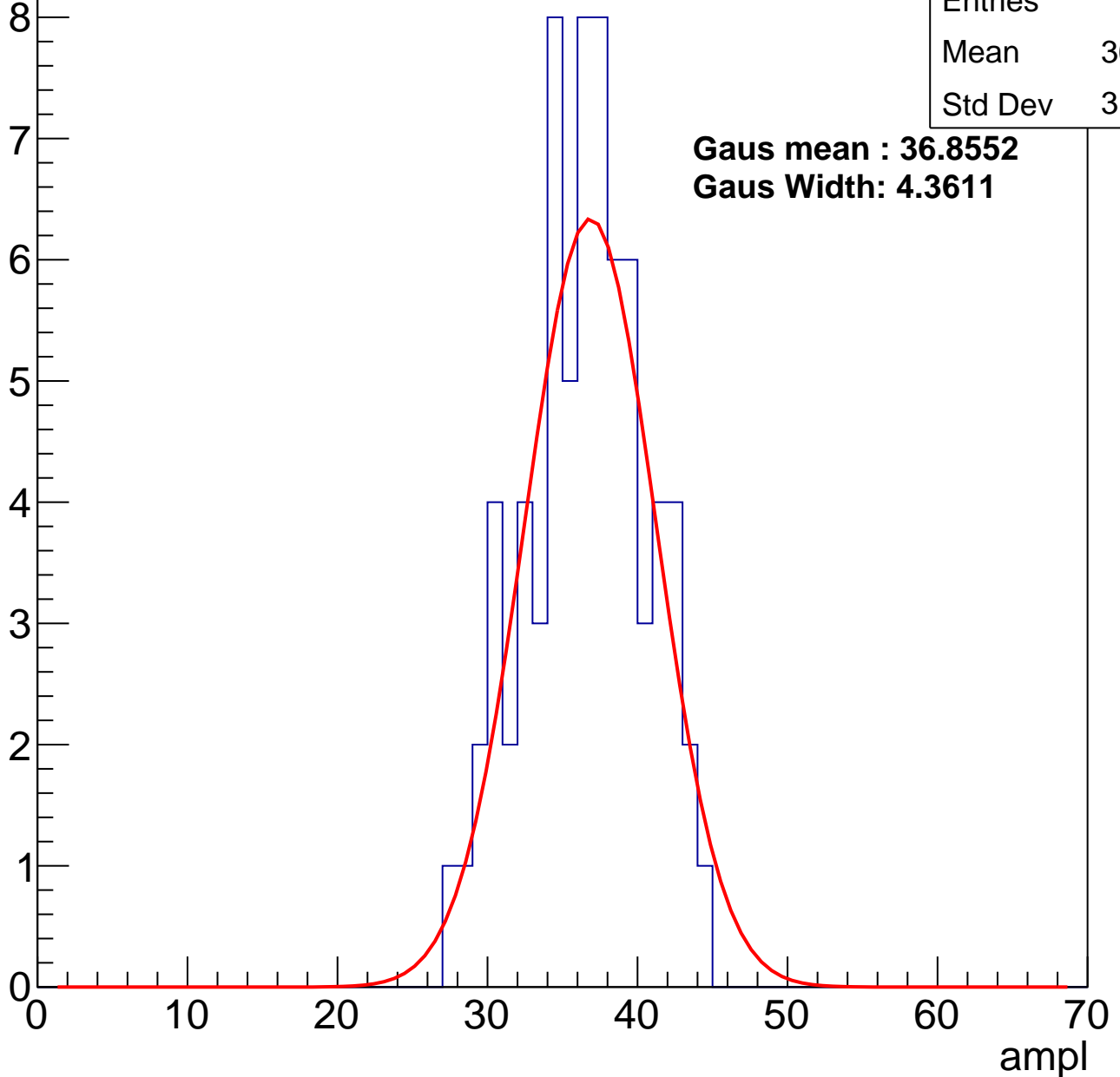
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.07
Std Dev	3.917

Gaus mean : 36.8552

Gaus Width: 4.3611



B1L101S, U4-ch100, adc2

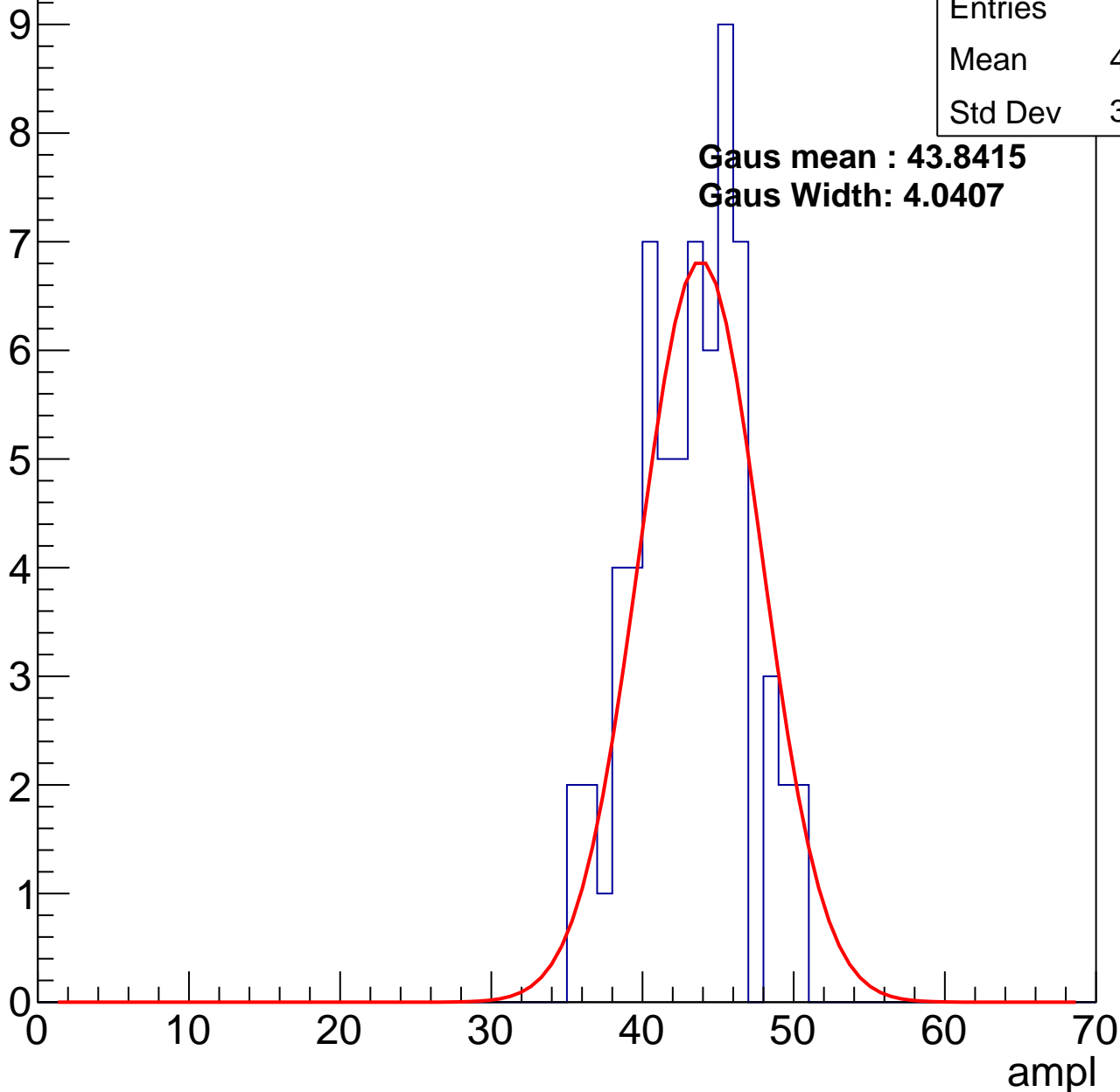
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.67
Std Dev	3.603

Gaus mean : 43.8415

Gaus Width: 4.0407

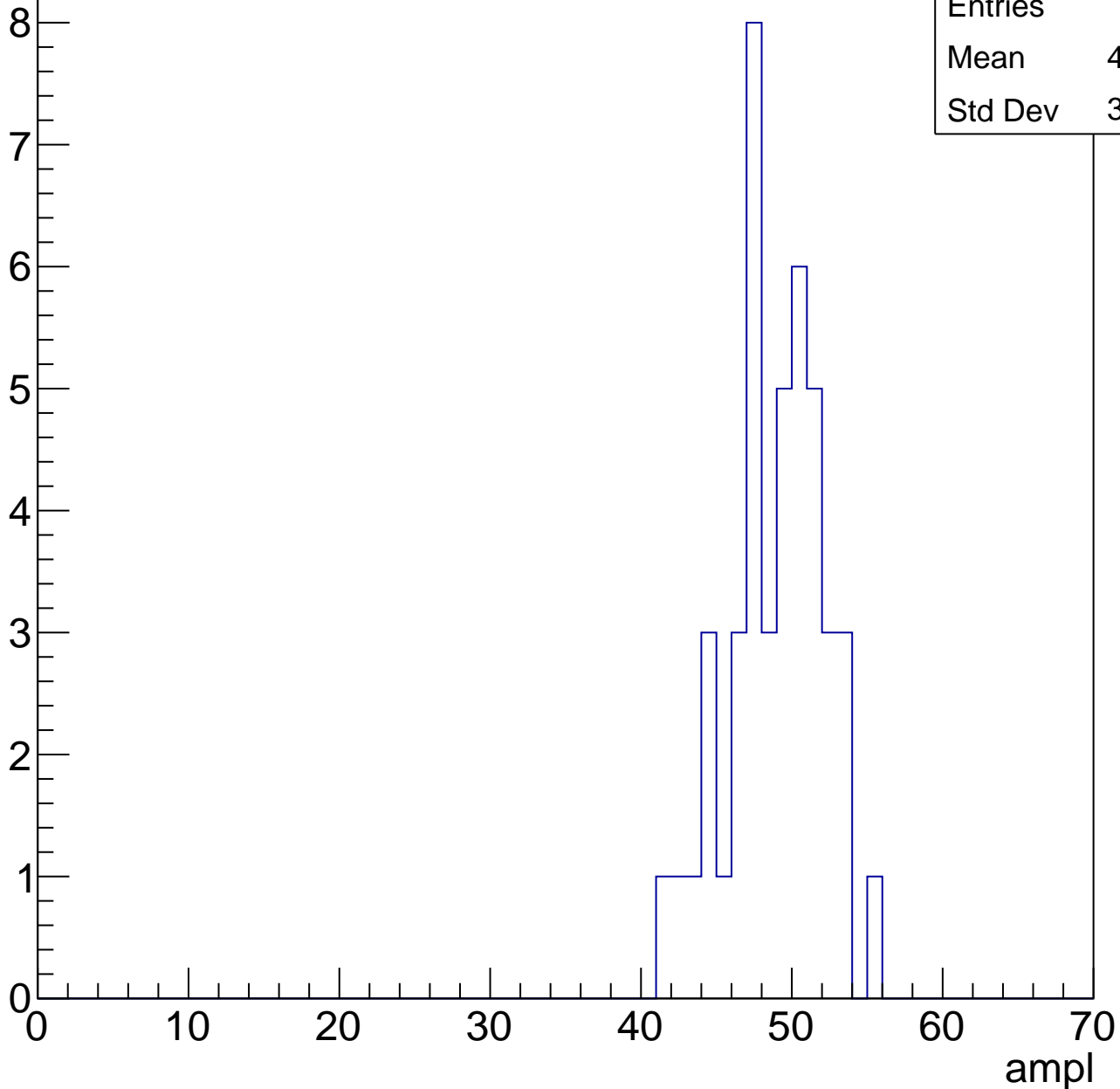


B1L101S, U4-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	48.43
Std Dev	3.107

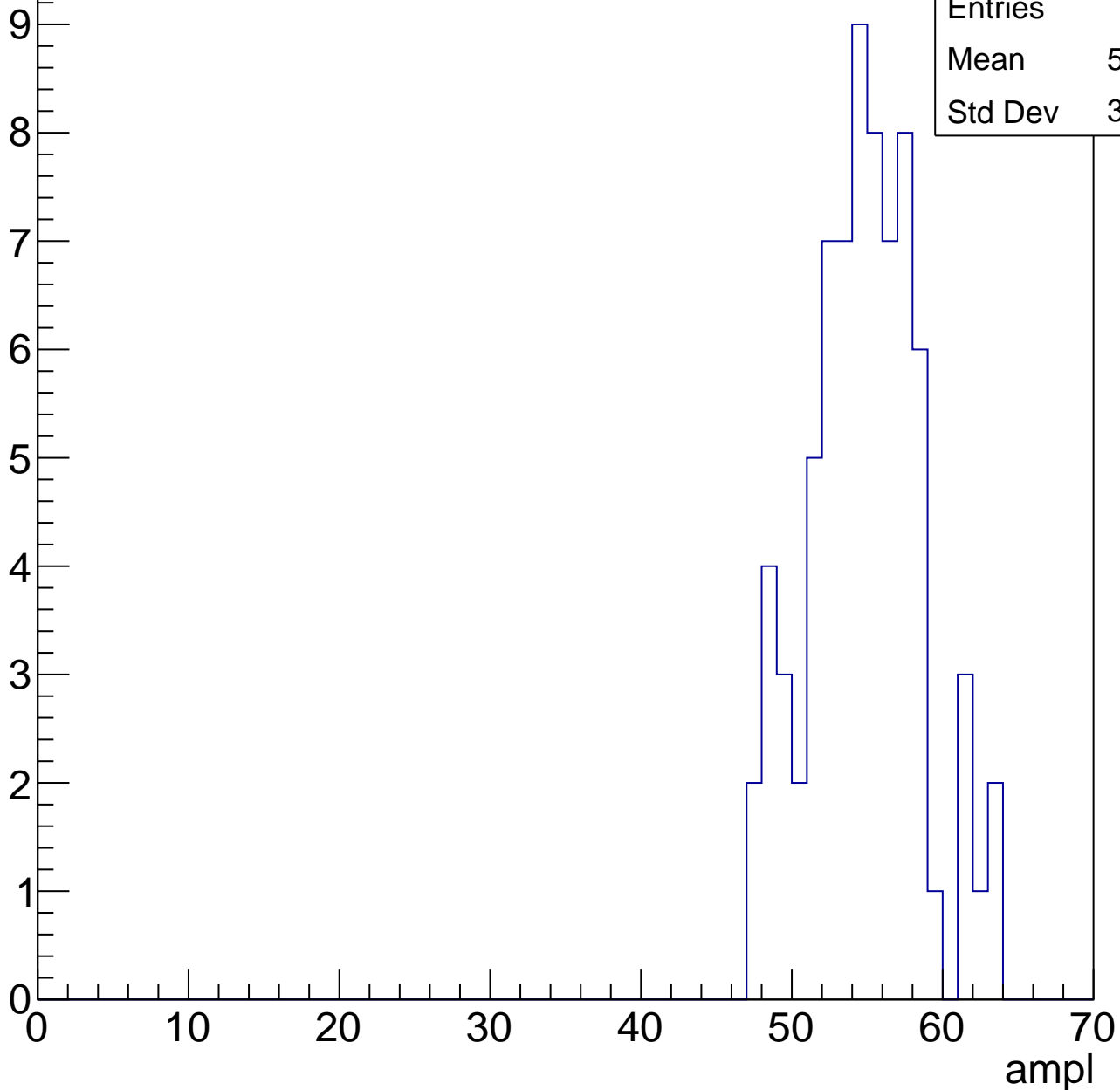


B1L101S, U4-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	54.33
Std Dev	3.693



B1L101S, U4-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

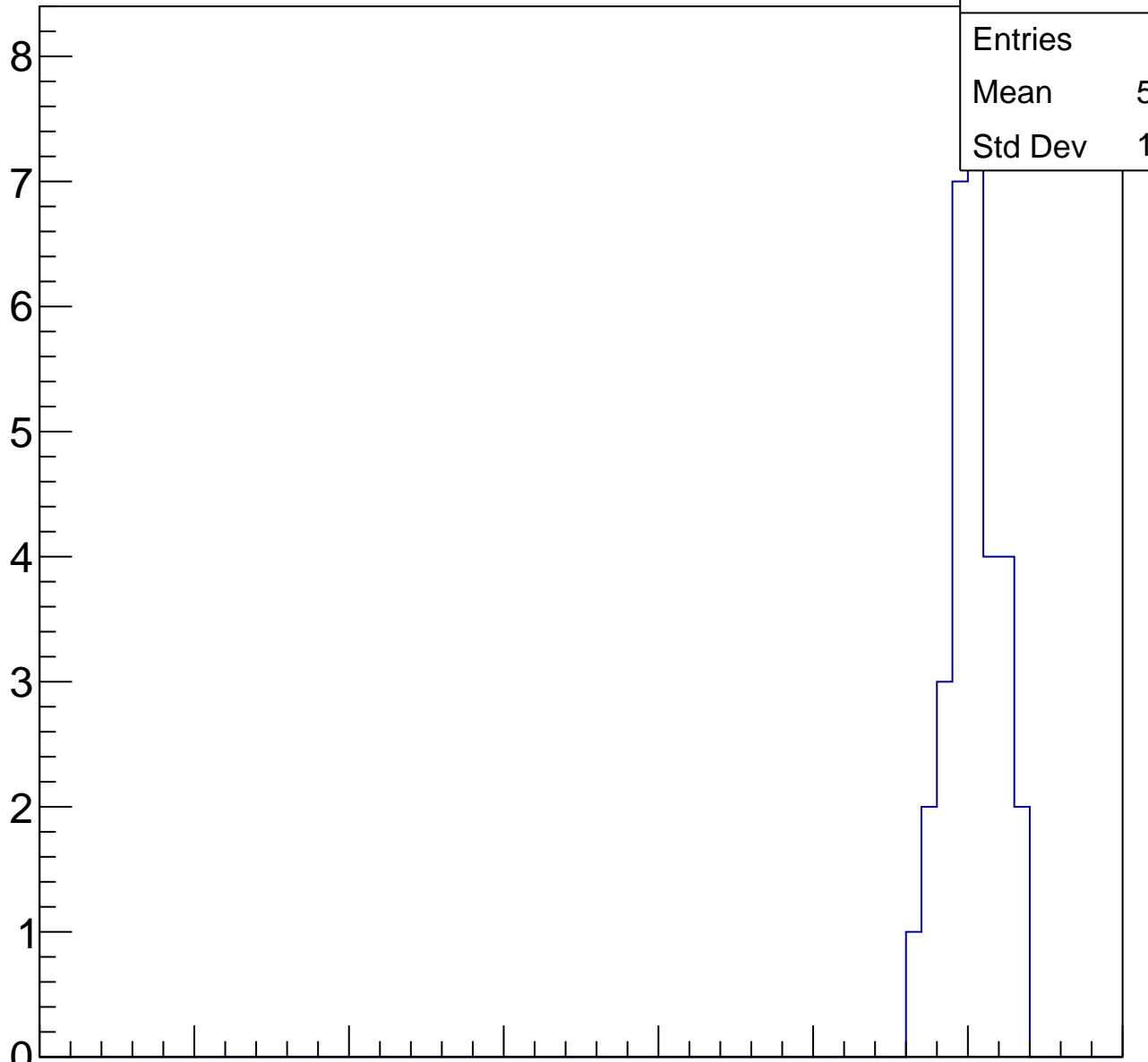
Entry

8
7
6
5
4
3
2
1
0

Entries	31
Mean	59.84
Std Dev	1.706

ampl

0 10 20 30 40 50 60 70

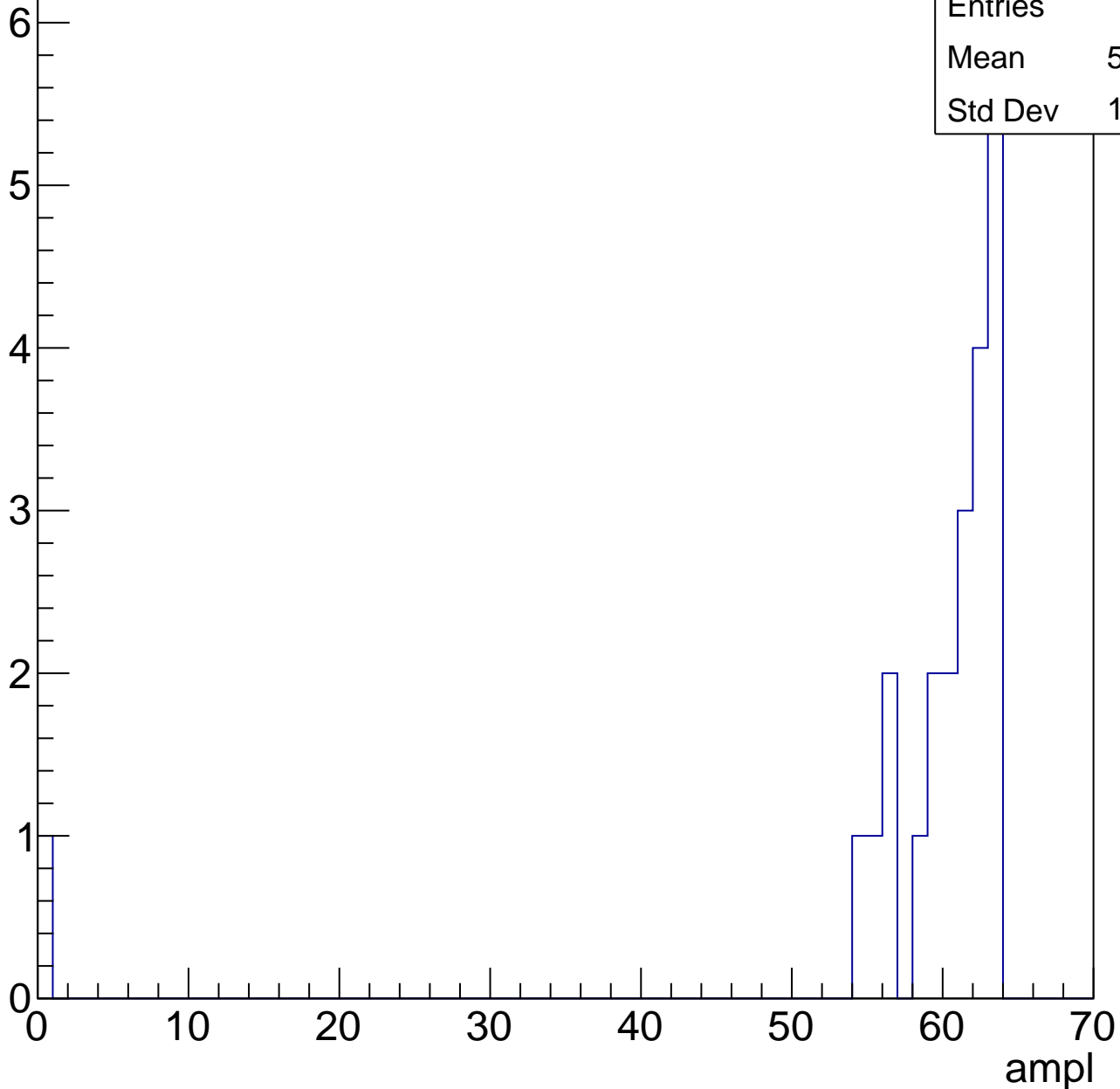


B1L101S, U4-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

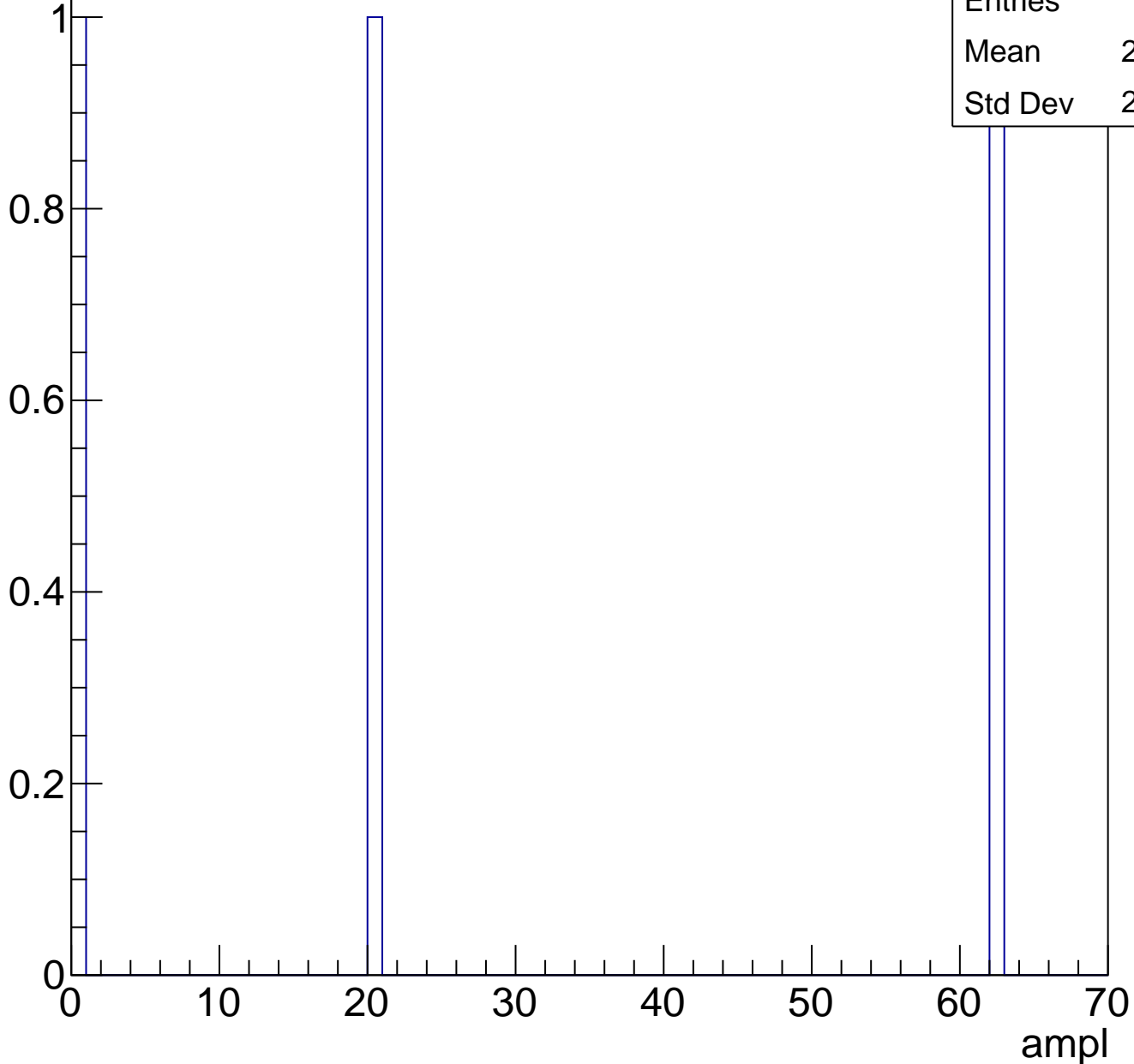
Entries	23
Mean	57.65
Std Dev	12.59



B1L101S, U4-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.33
Std Dev	25.84

B1L101S, U4-ch101, adc0

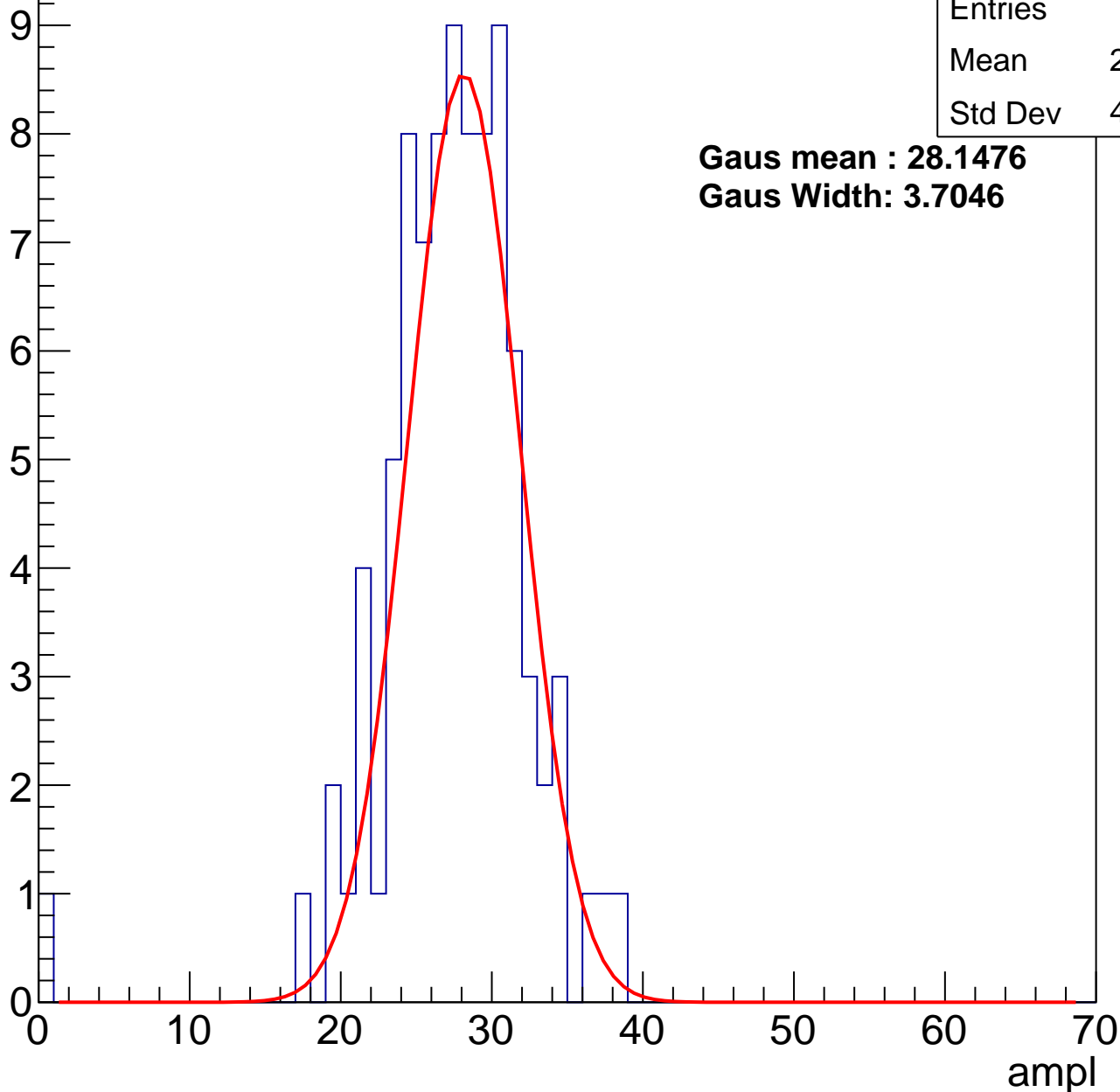
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	26.98
Std Dev	4.945

Gaus mean : 28.1476

Gaus Width: 3.7046



B1L101S, U4-ch101, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	35.23
Std Dev	3.356

Gaus mean : 35.6844

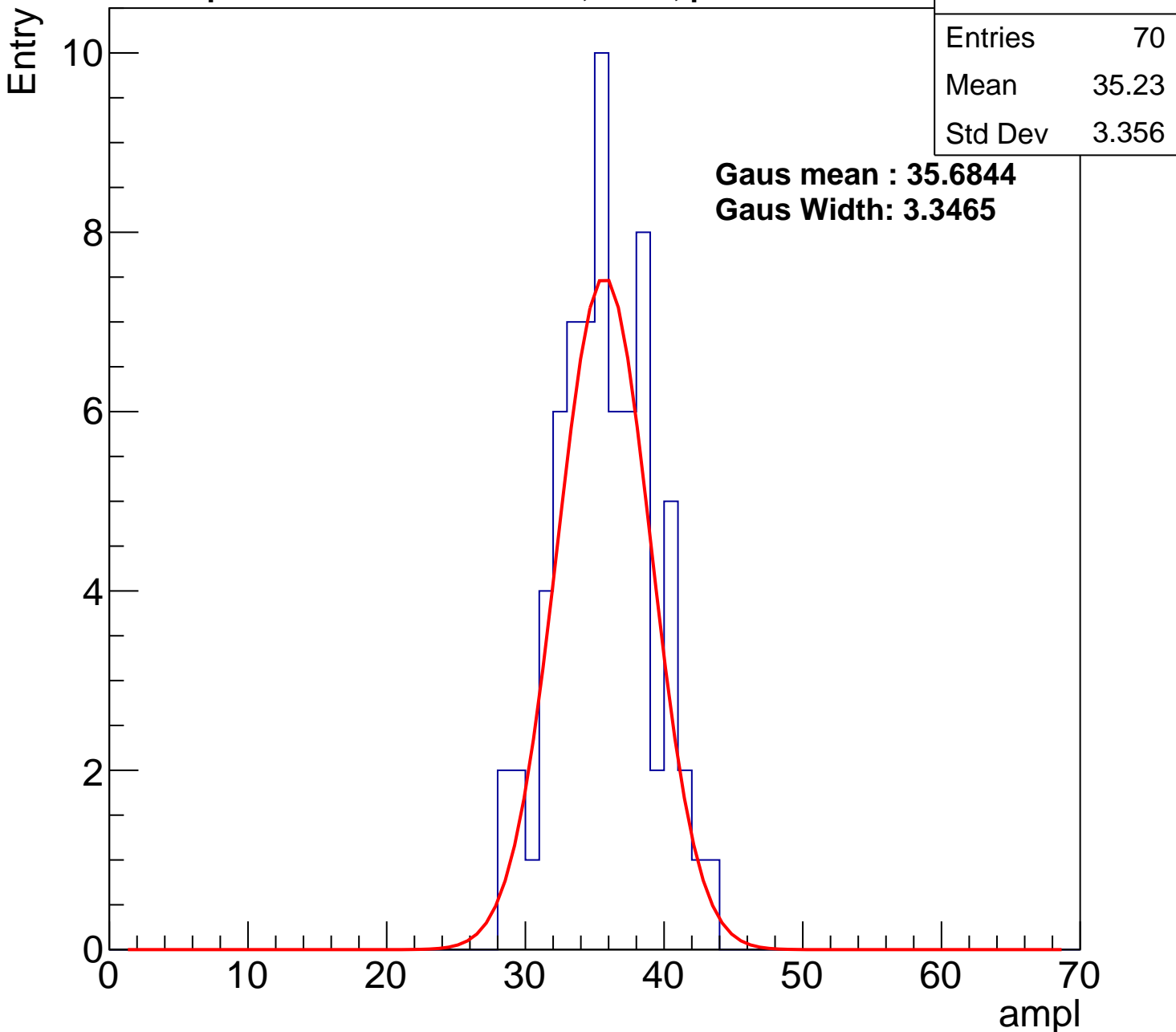
Gaus Width: 3.3465

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch101, adc2

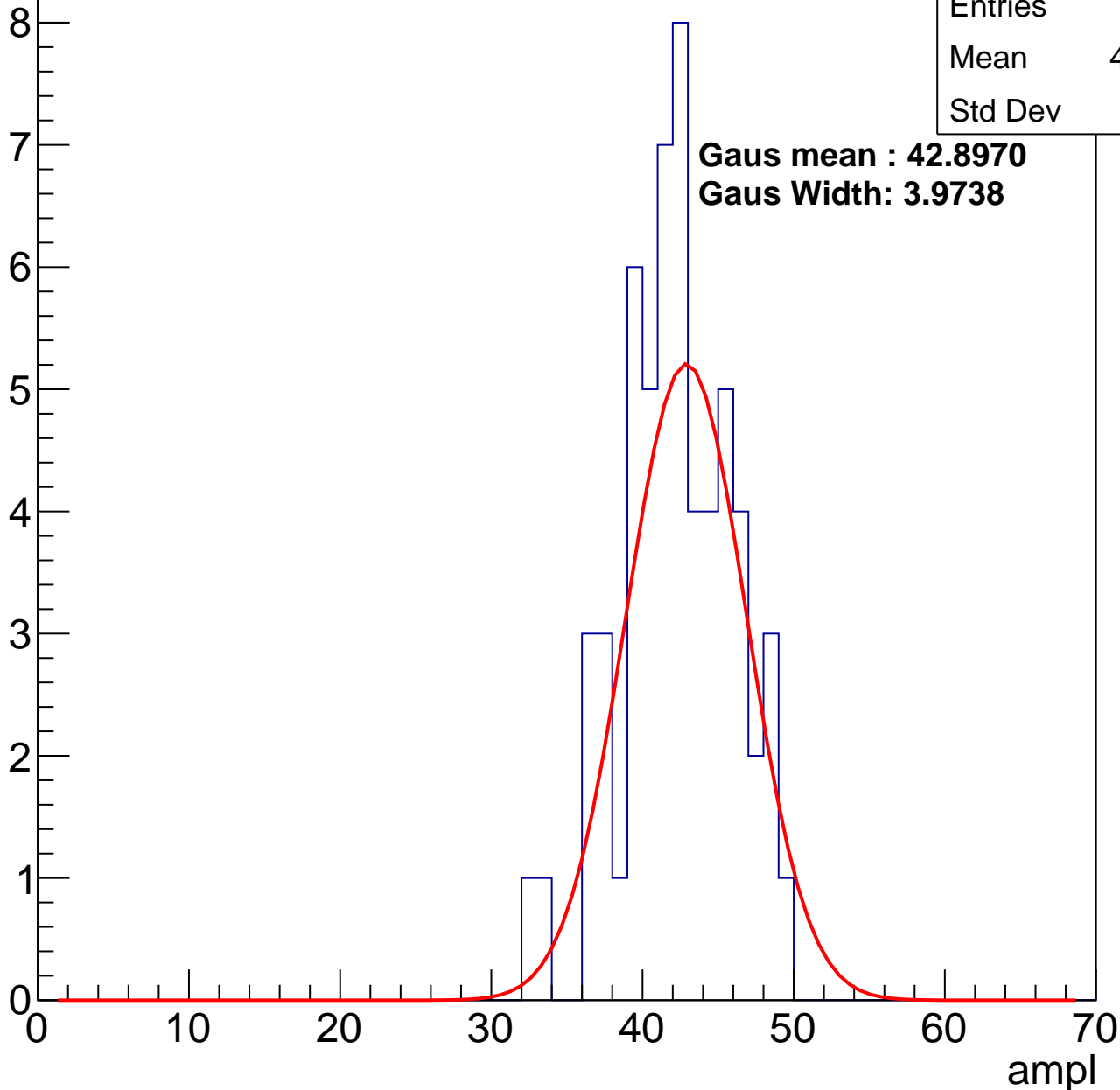
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.78
Std Dev	3.7

Gaus mean : 42.8970

Gaus Width: 3.9738

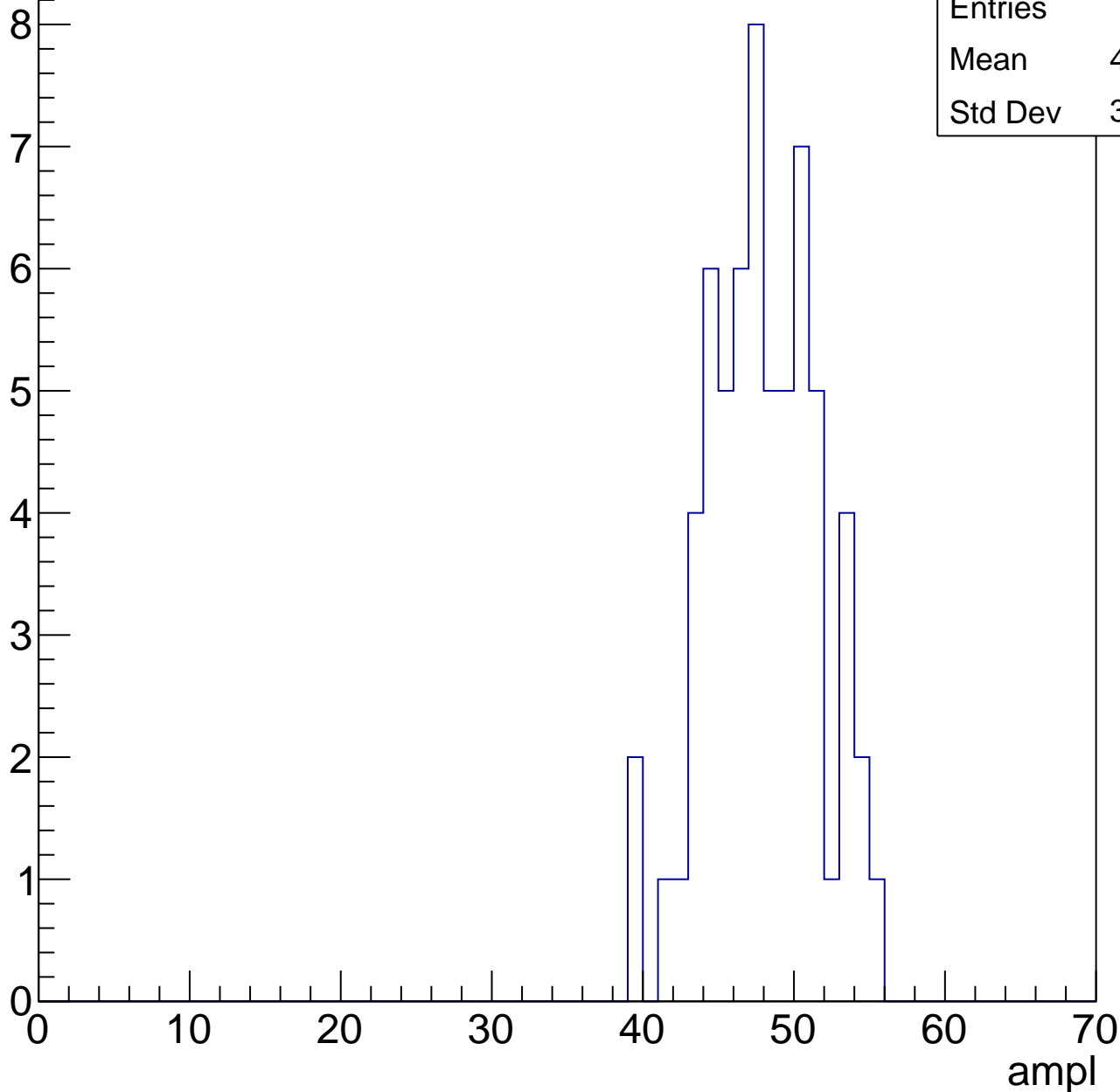


B1L101S, U4-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	47.48
Std Dev	3.603

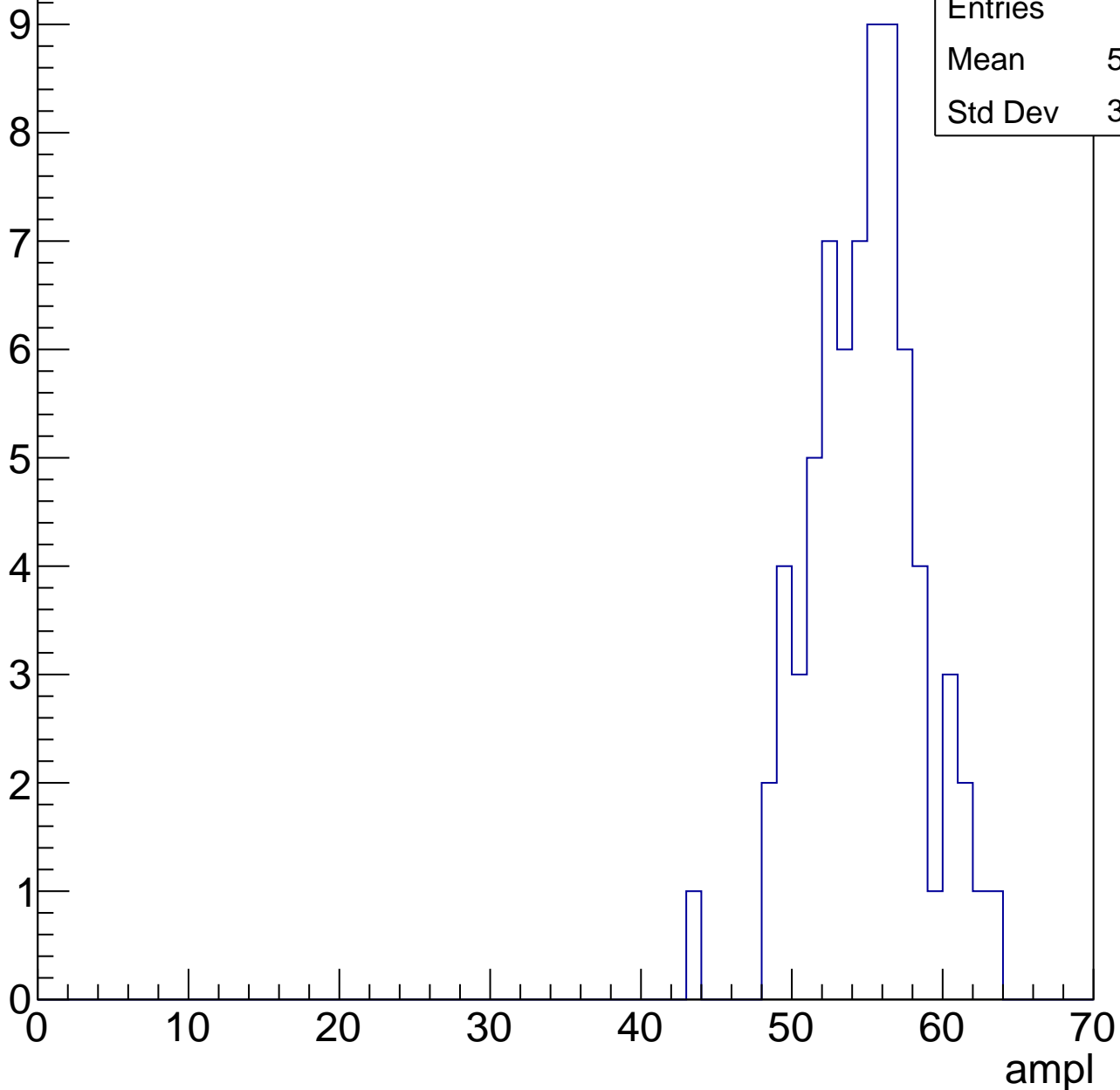


B1L101S, U4-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

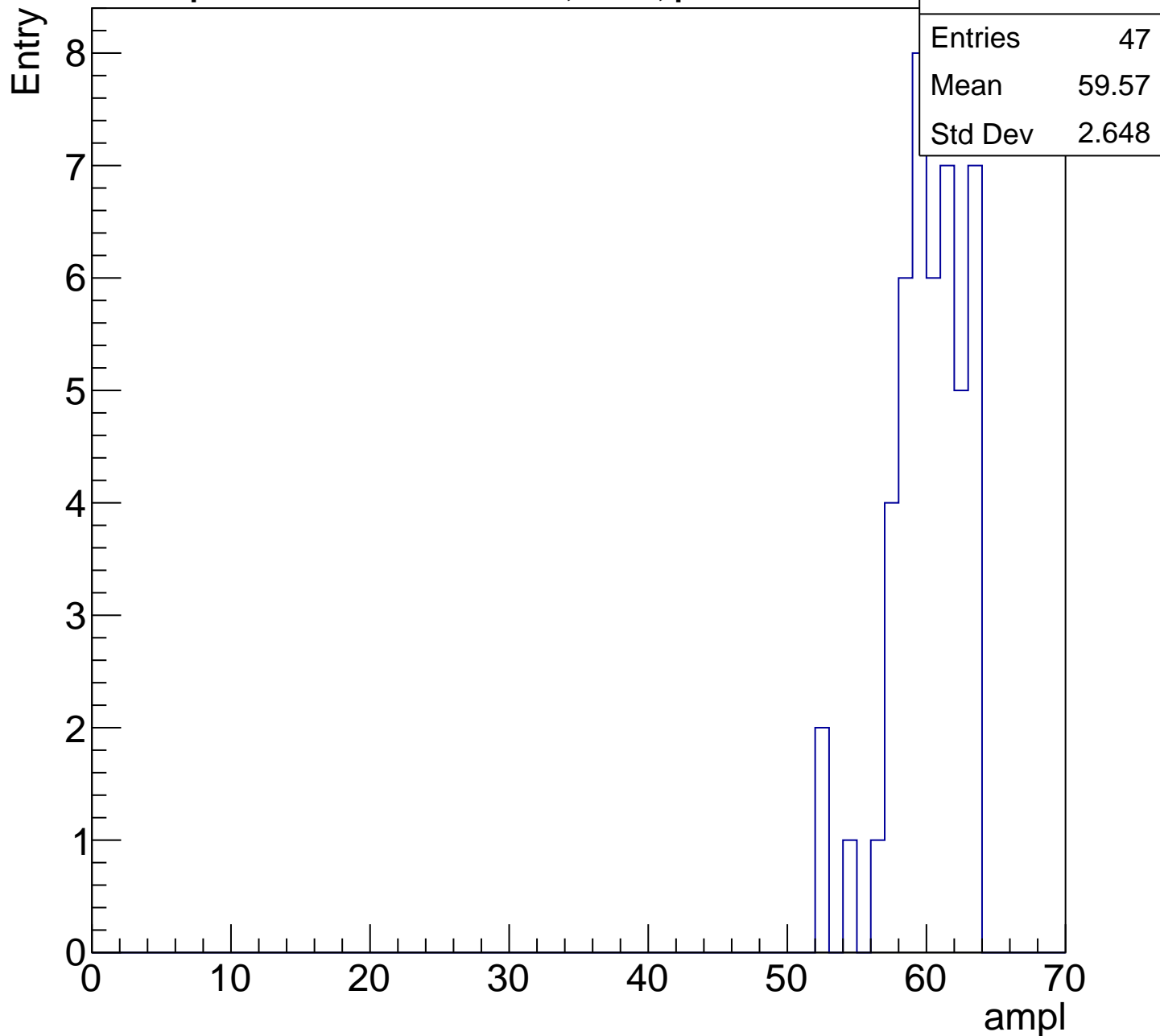
Entry

Entries	71
Mean	54.35
Std Dev	3.658



B1L101S, U4-ch101, adc5

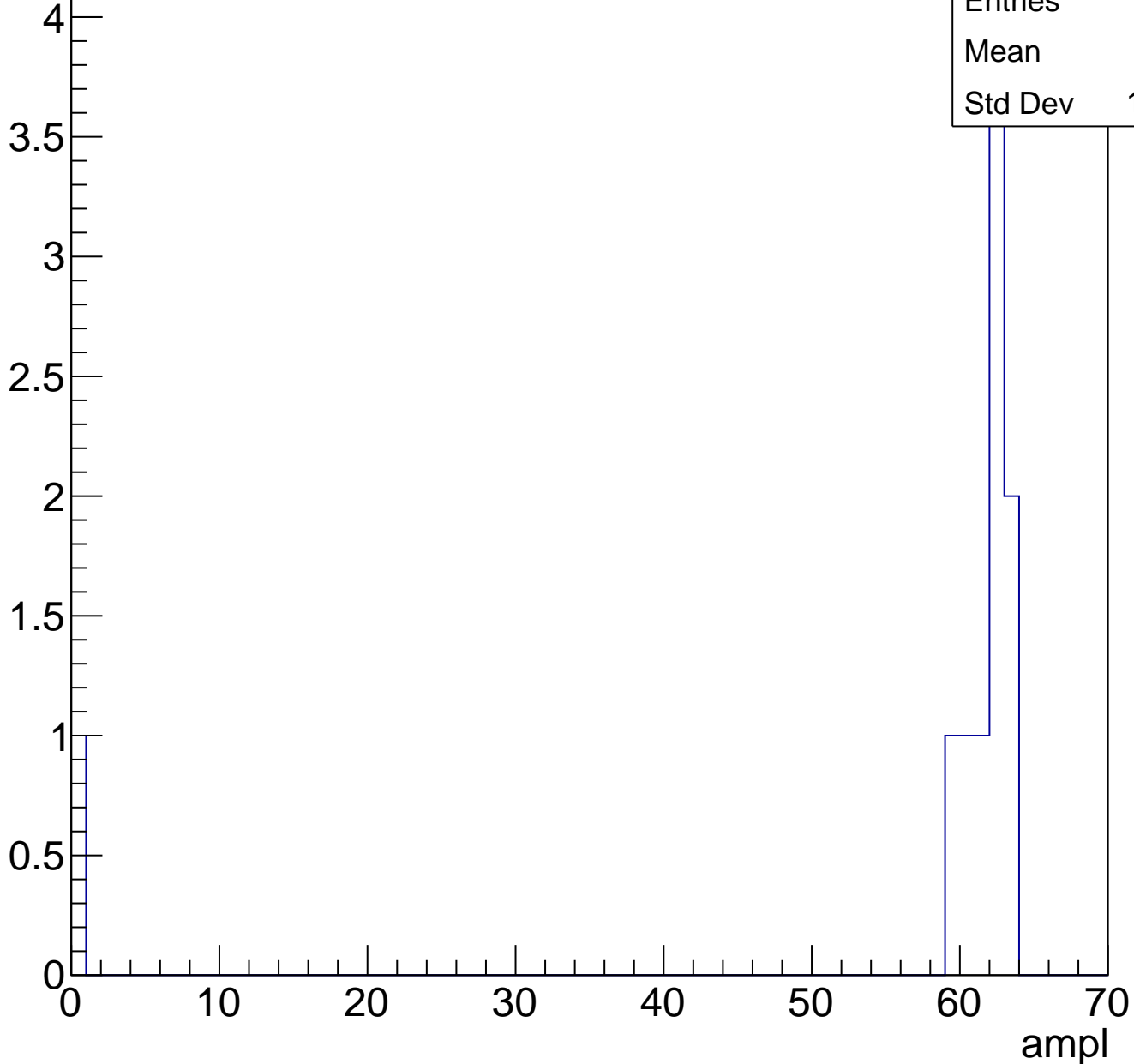
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch102, adc0

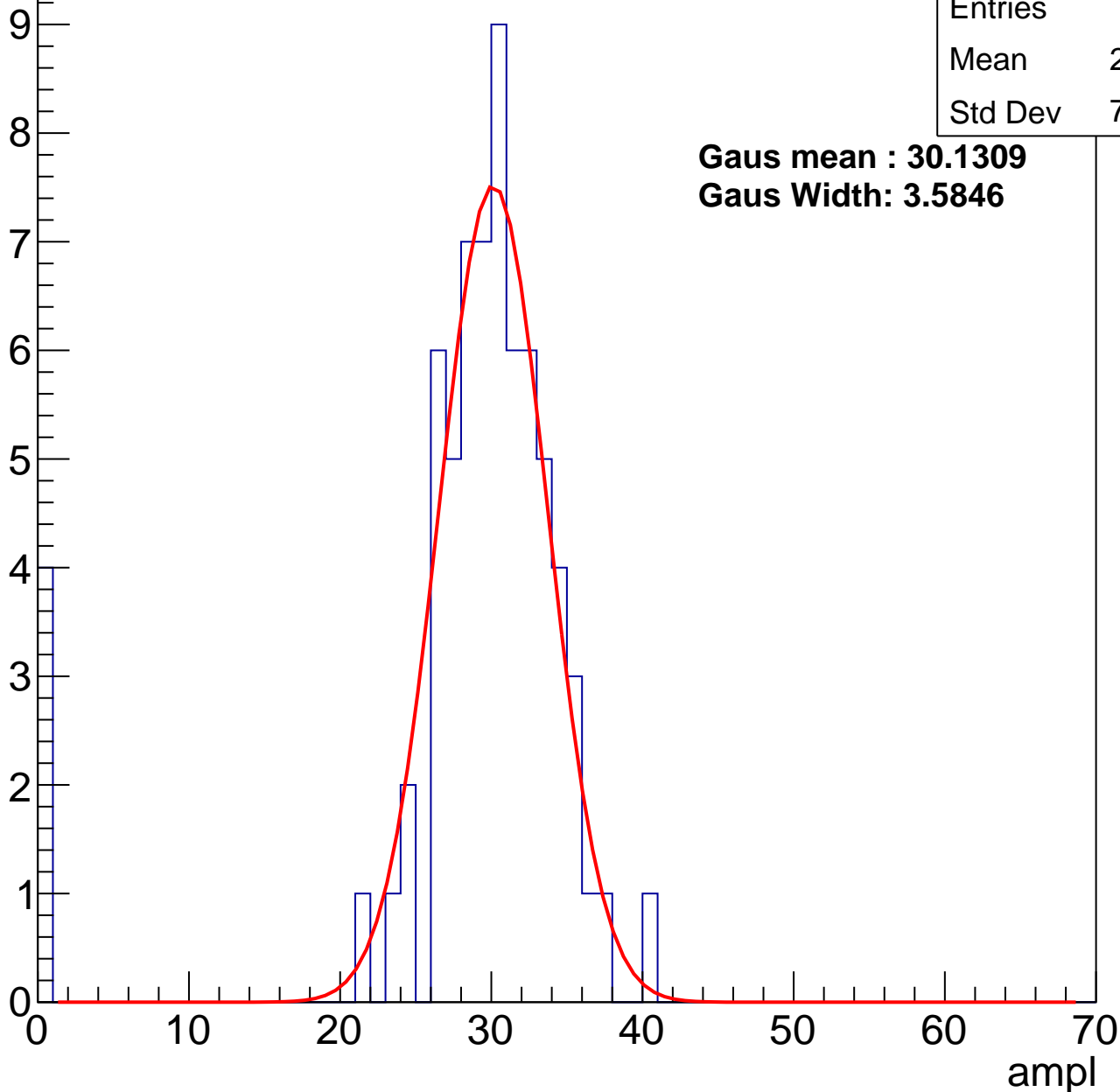
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.25
Std Dev	7.769

Gaus mean : 30.1309

Gaus Width: 3.5846



B1L101S, U4-ch102, adc1

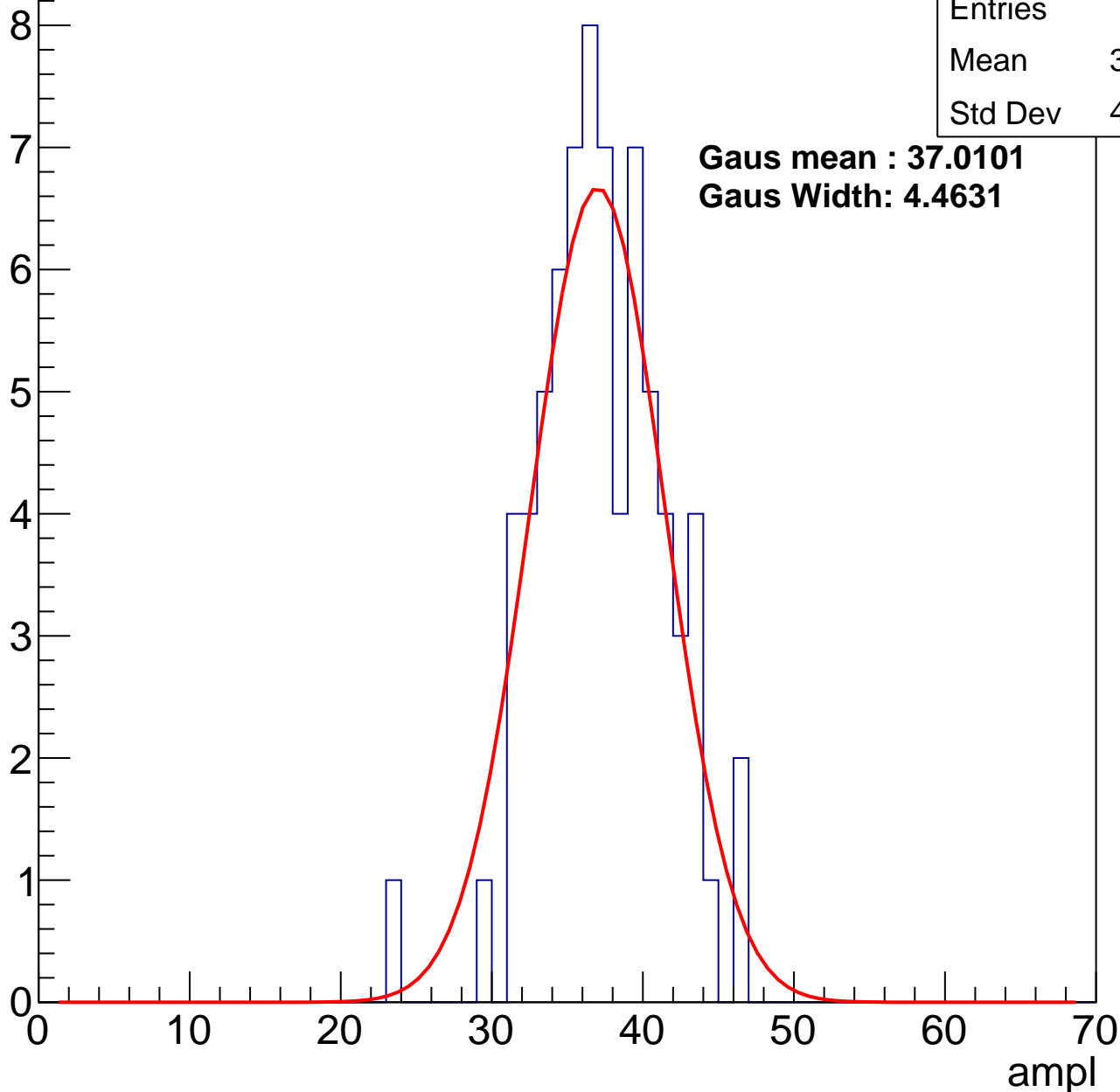
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.82
Std Dev	4.126

Gaus mean : 37.0101

Gaus Width: 4.4631

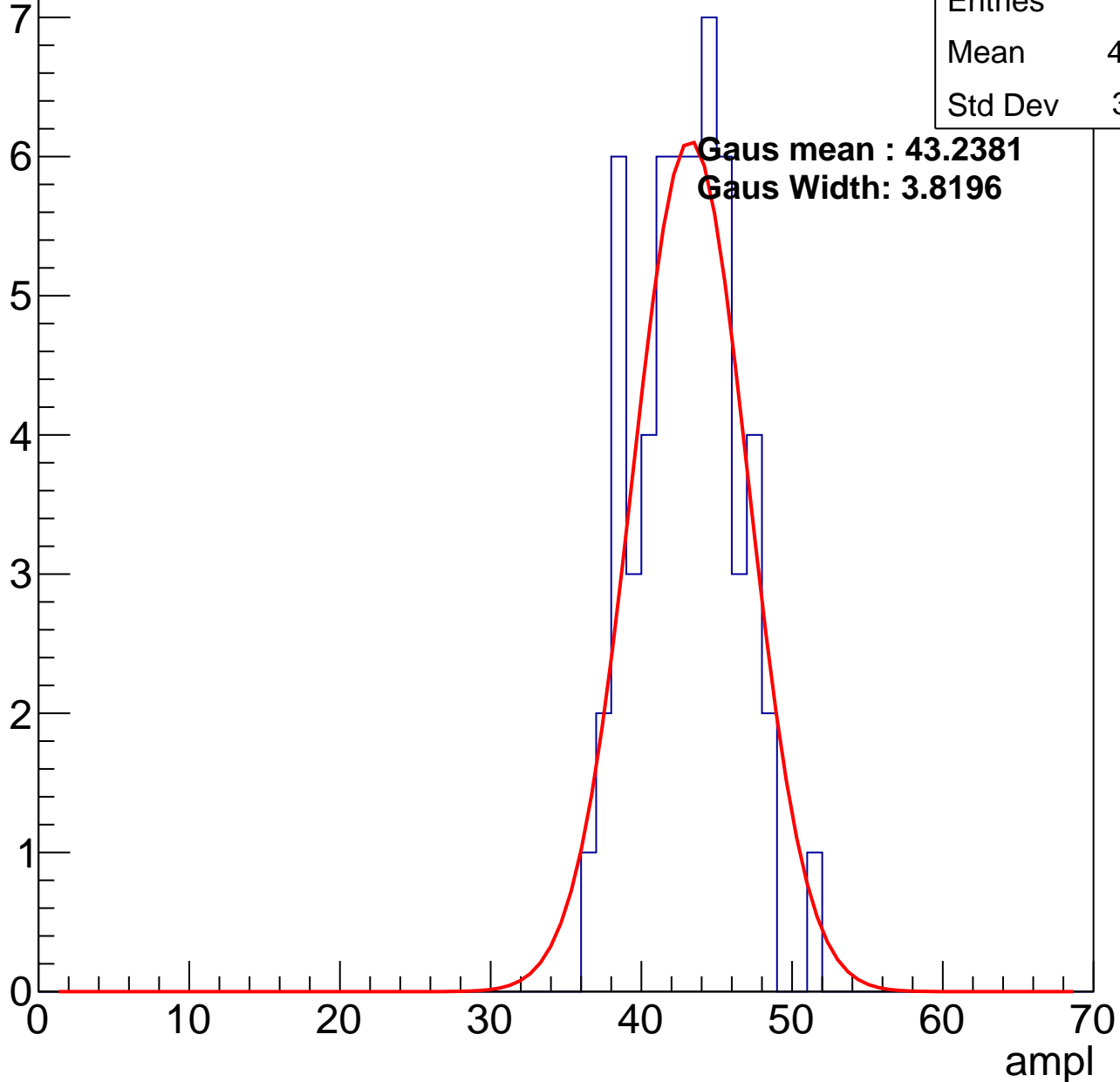


B1L101S, U4-ch102, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.49
Std Dev	3.261

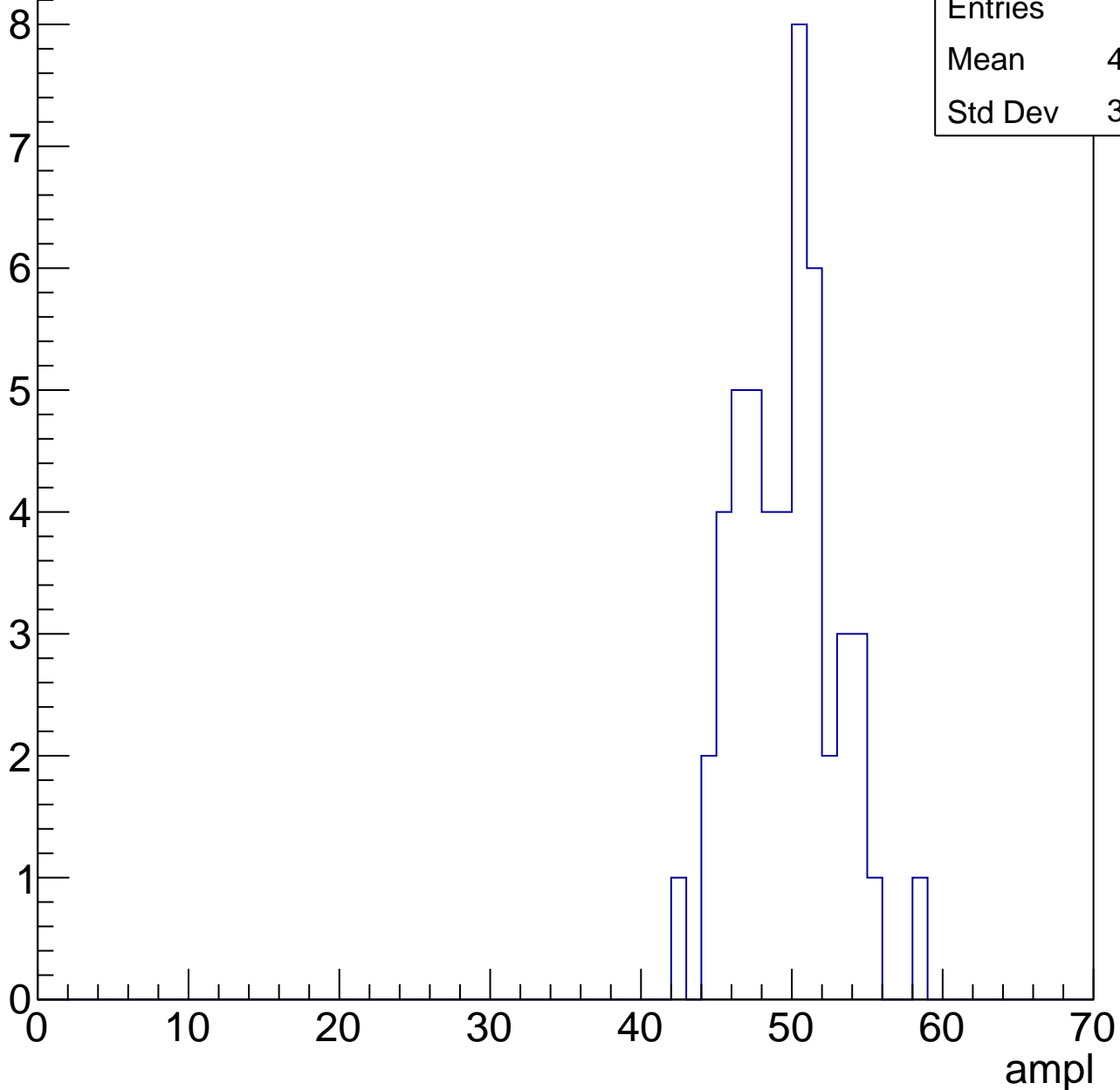


B1L101S, U4-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

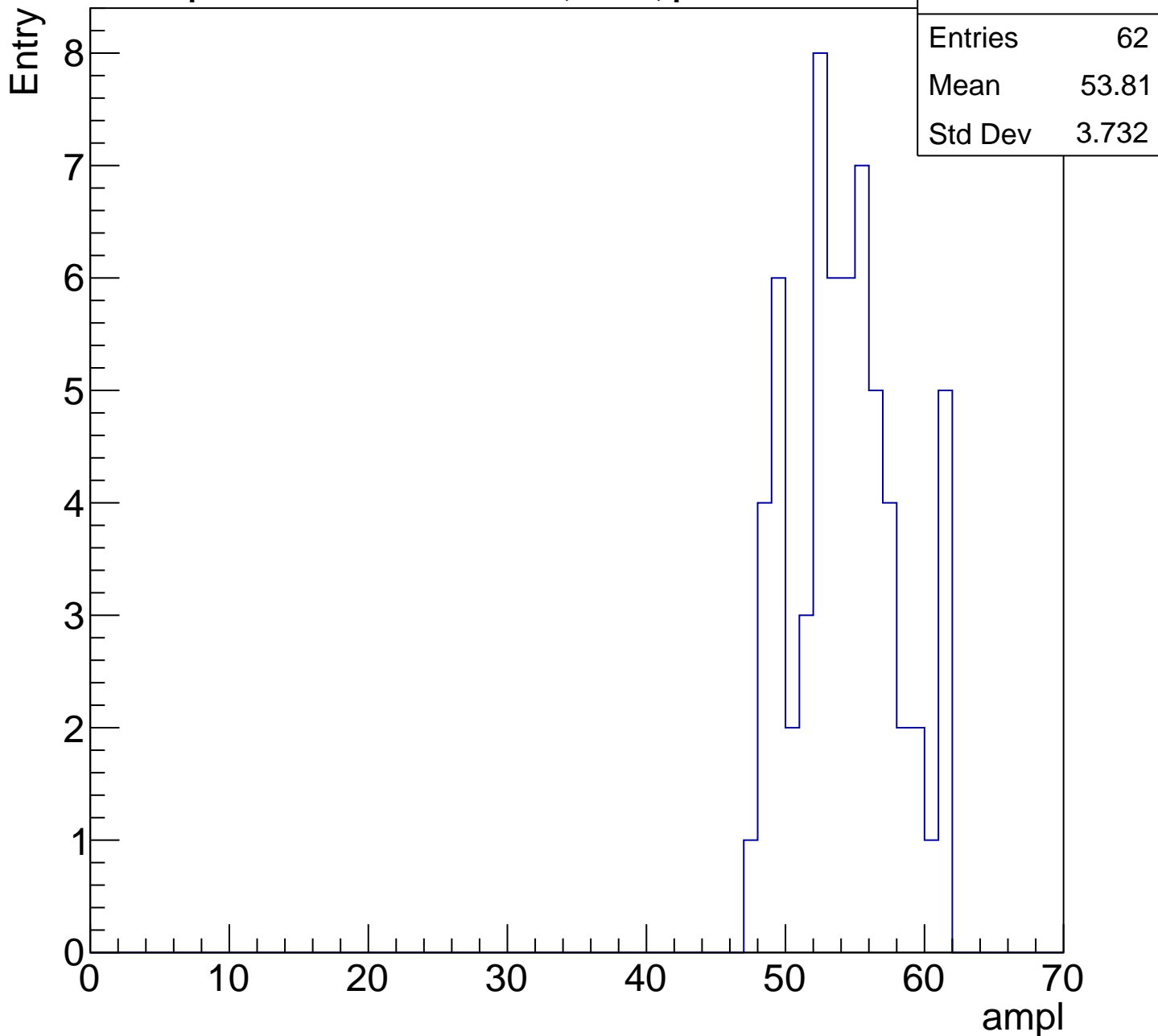
Entry

Entries	49
Mean	49.12
Std Dev	3.262



B1L101S, U4-ch102, adc4

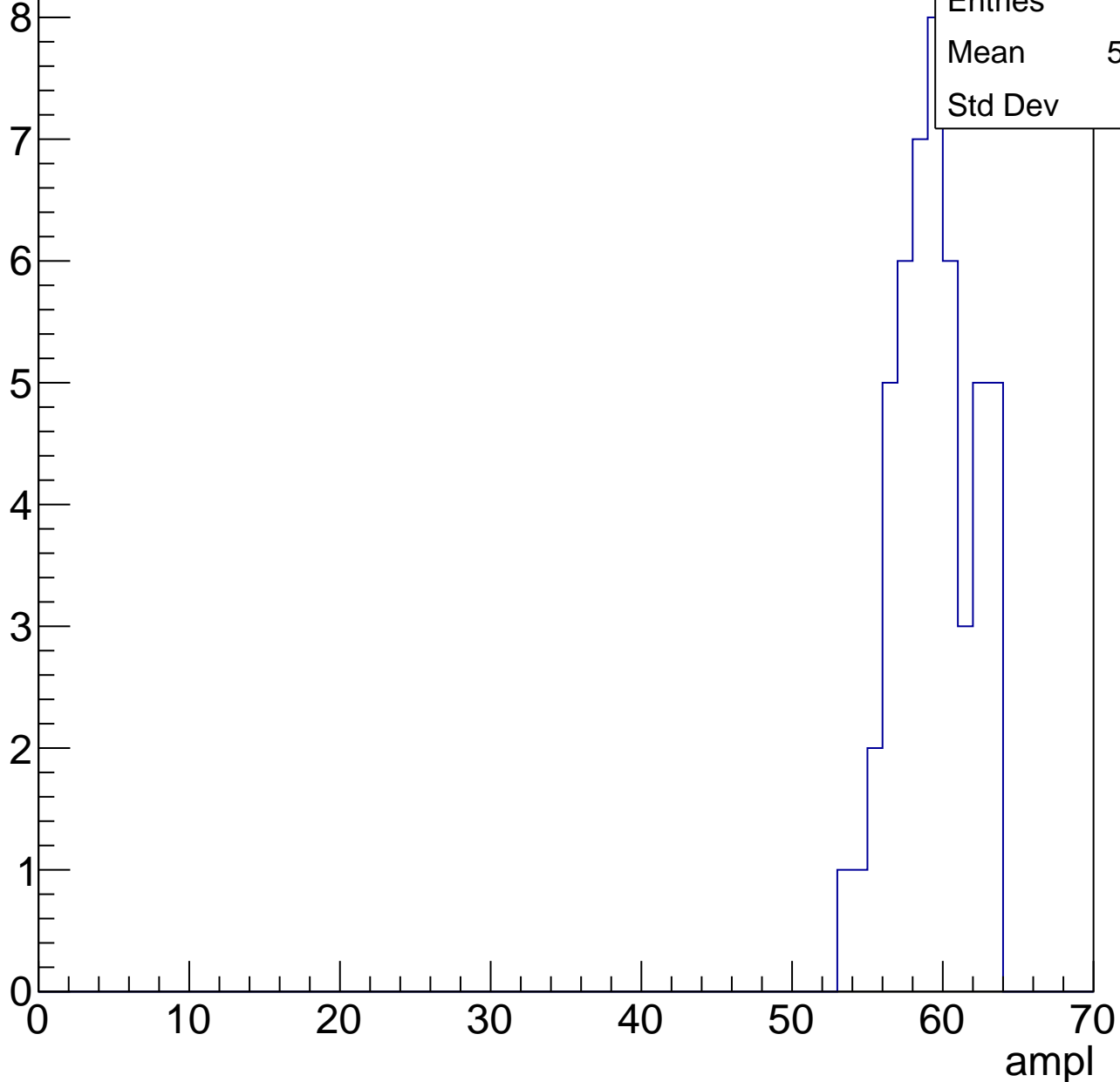
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

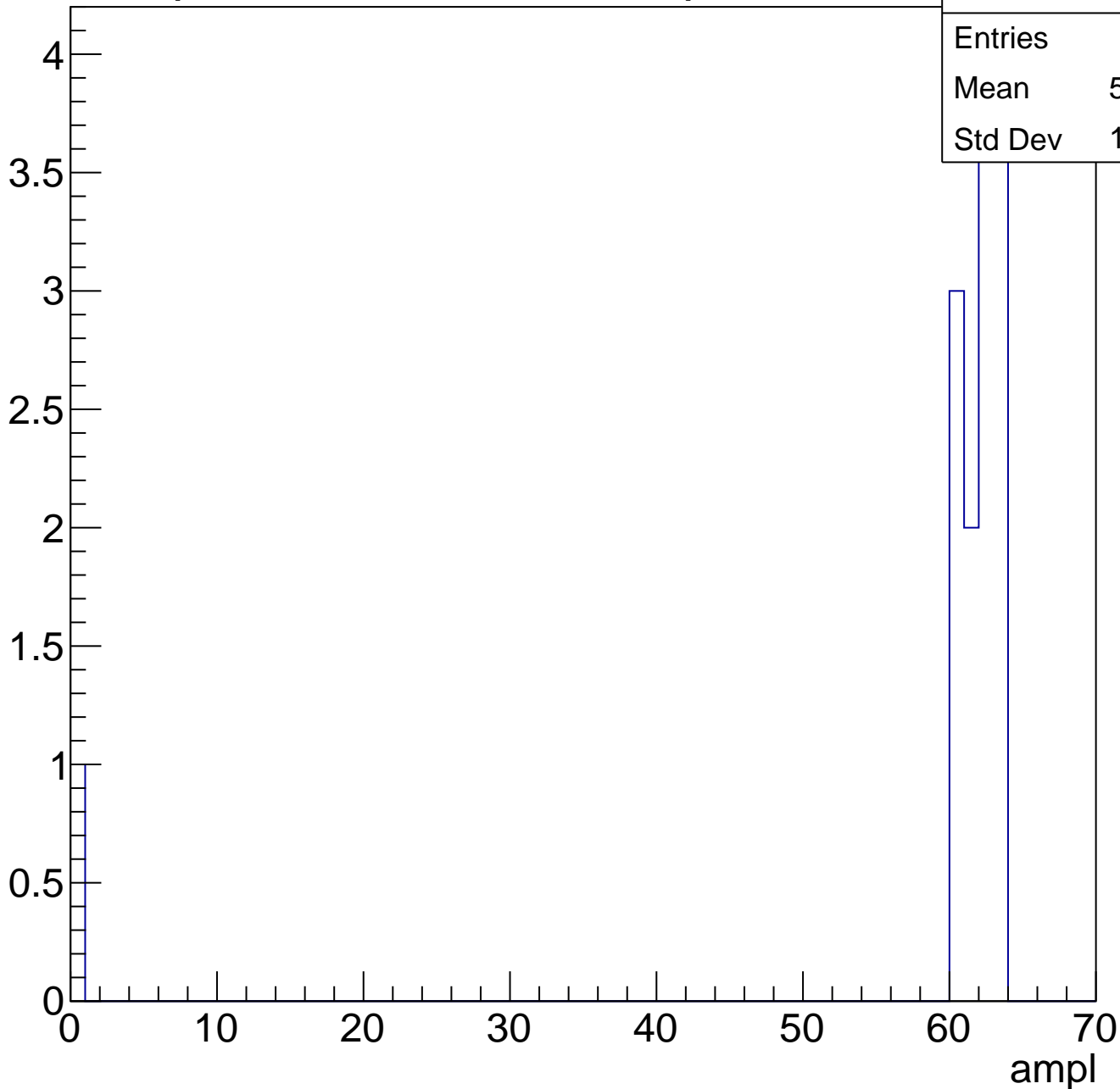


Entries	49
Mean	58.88
Std Dev	2.52

B1L101S, U4-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L101S, U4-ch103, adc0

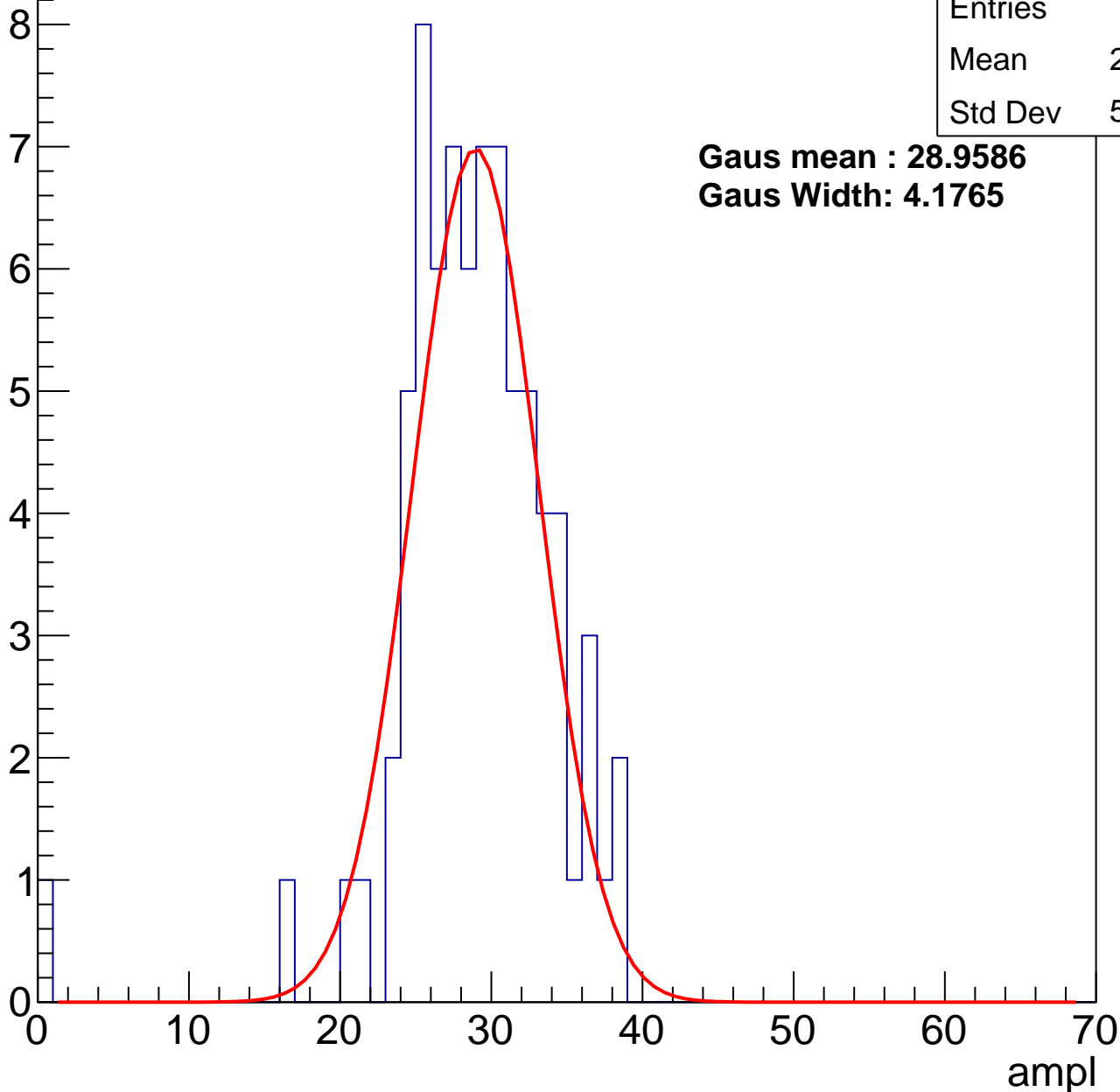
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.42
Std Dev	5.329

Gaus mean : 28.9586

Gaus Width: 4.1765



B1L101S, U4-ch103, adc1

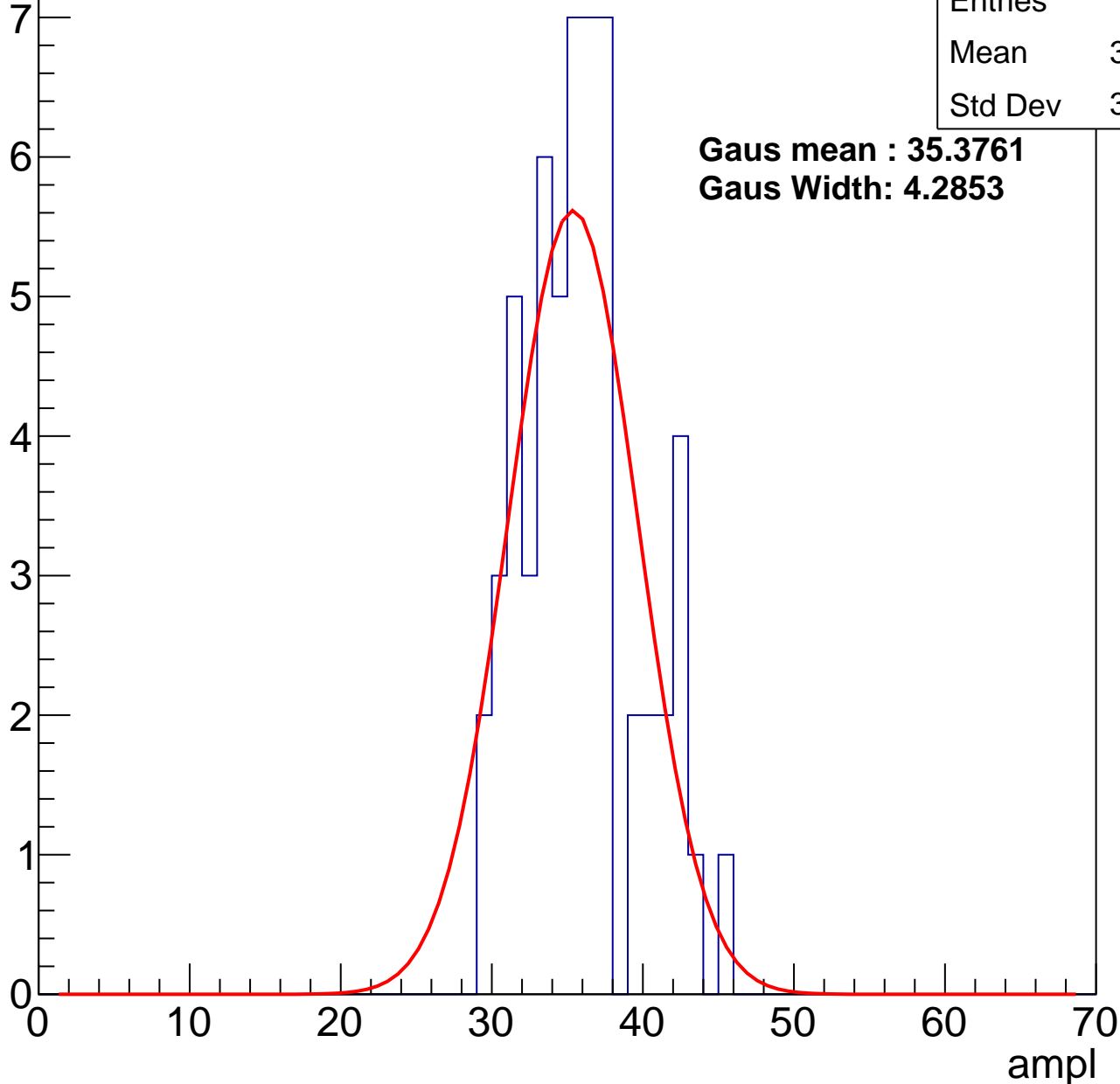
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	35.42
Std Dev	3.797

Gaus mean : 35.3761

Gaus Width: 4.2853



B1L101S, U4-ch103, adc2

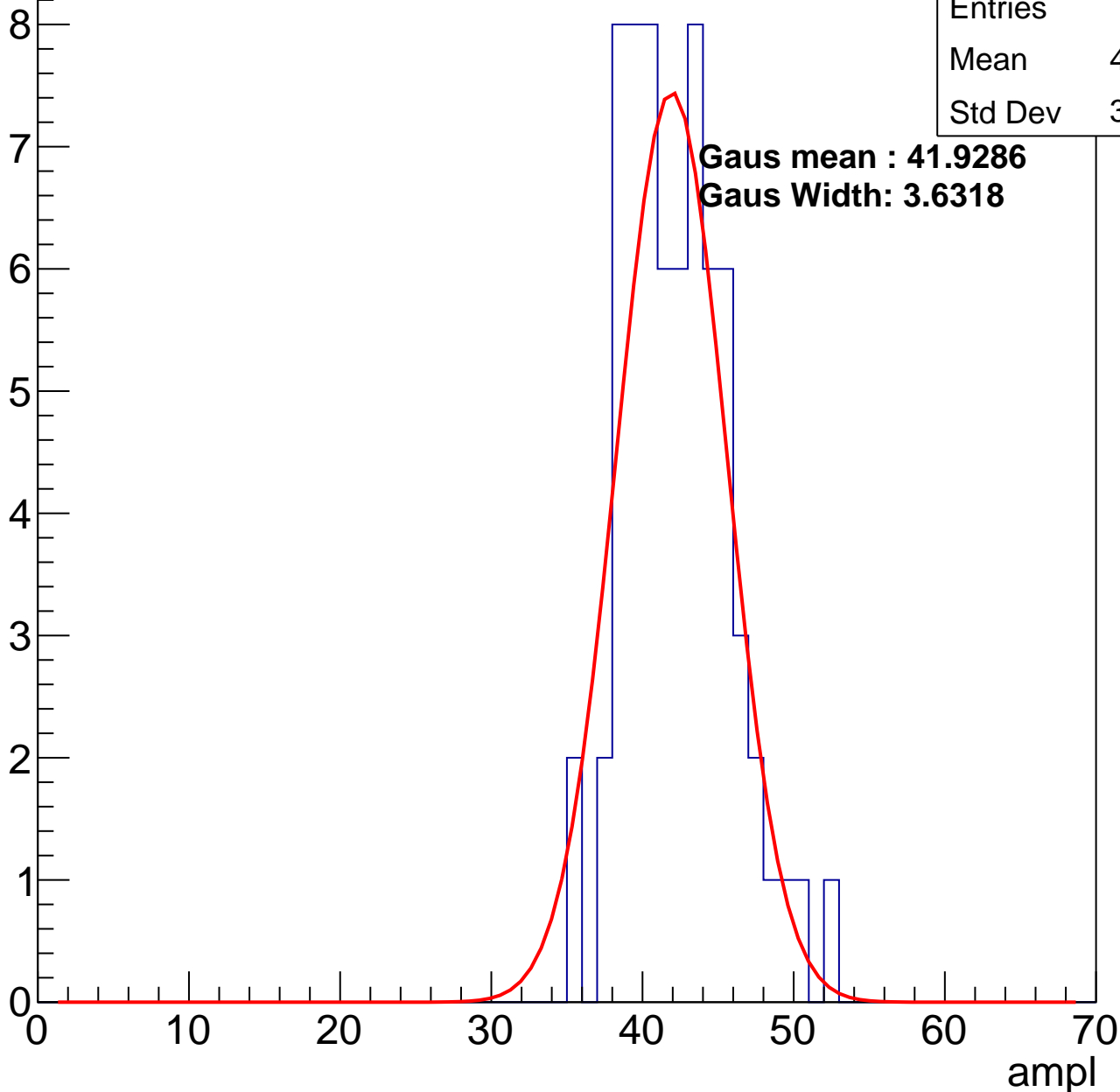
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	41.84
Std Dev	3.442

Gaus mean : 41.9286

Gaus Width: 3.6318

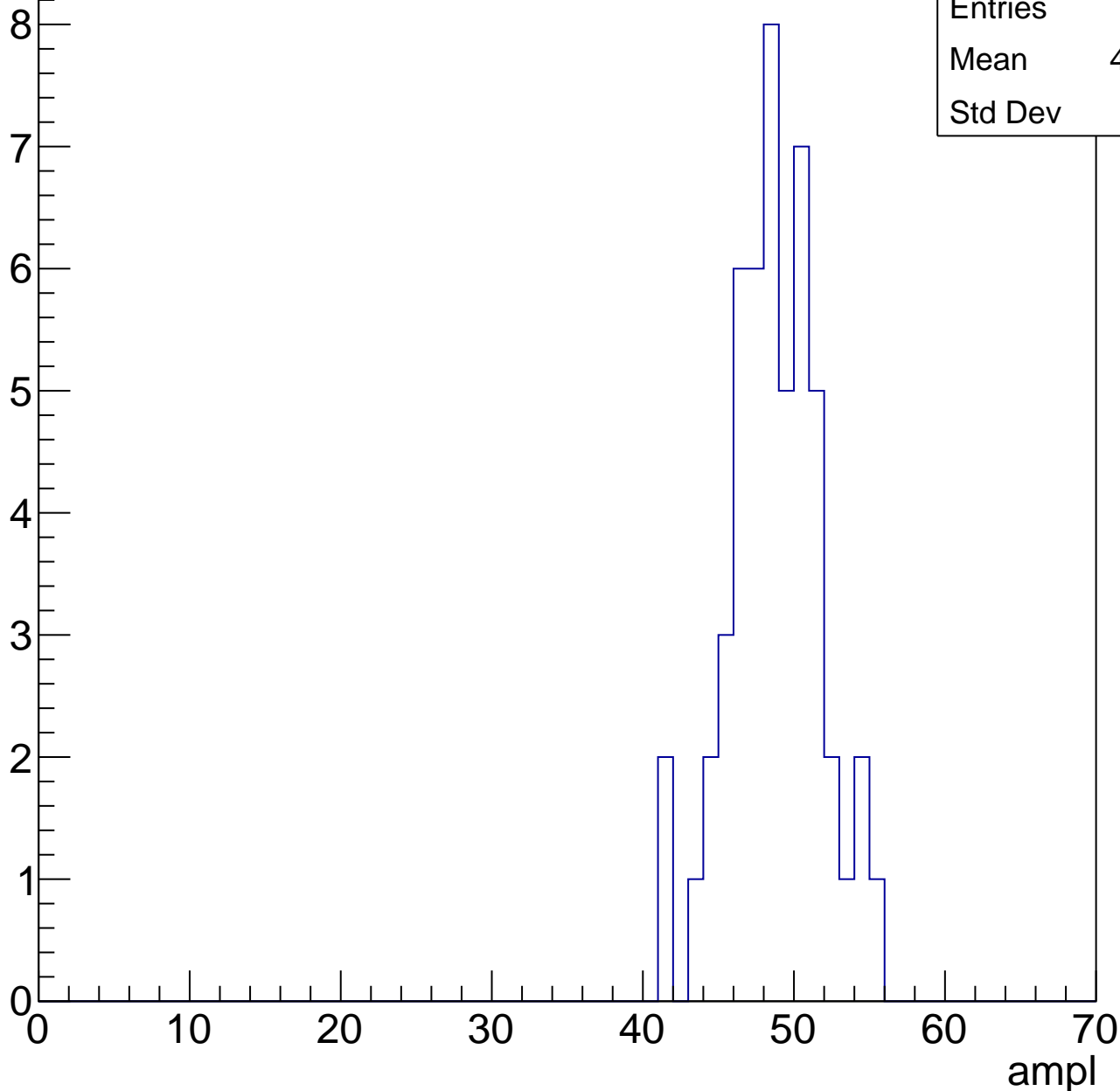


B1L101S, U4-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	48.24
Std Dev	3.02

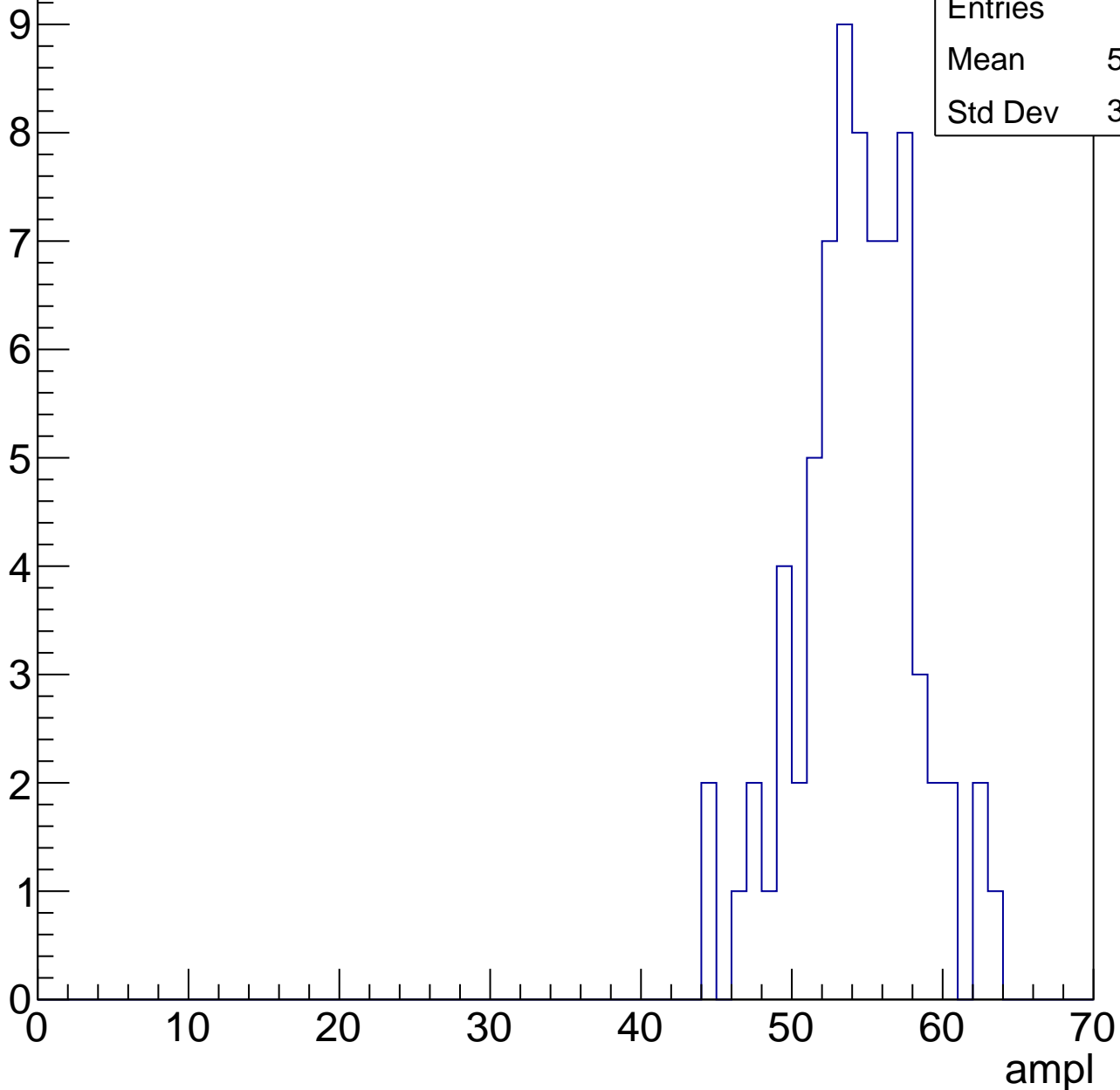


B1L101S, U4-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

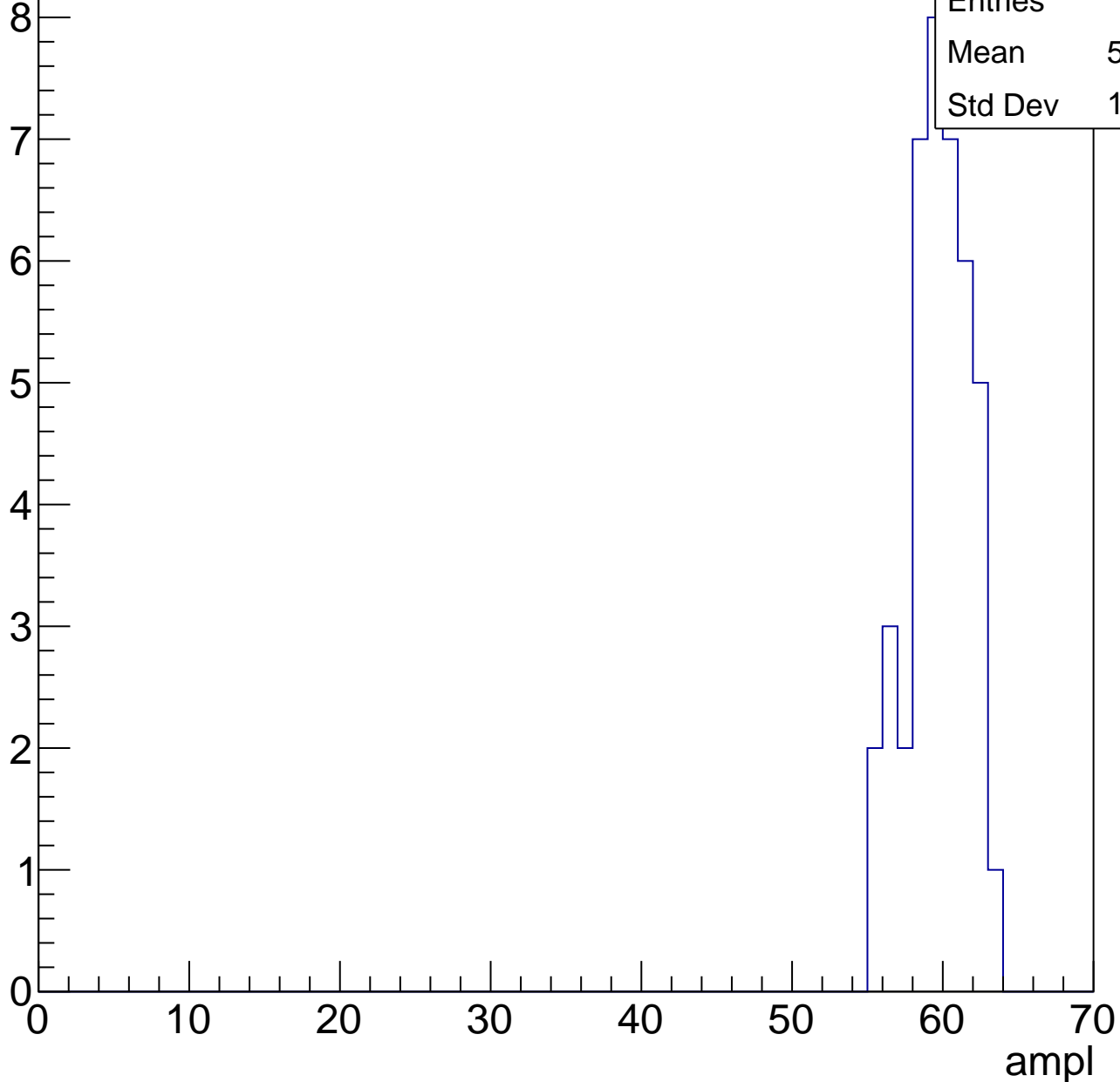
Entries	73
Mean	53.86
Std Dev	3.876



B1L101S, U4-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



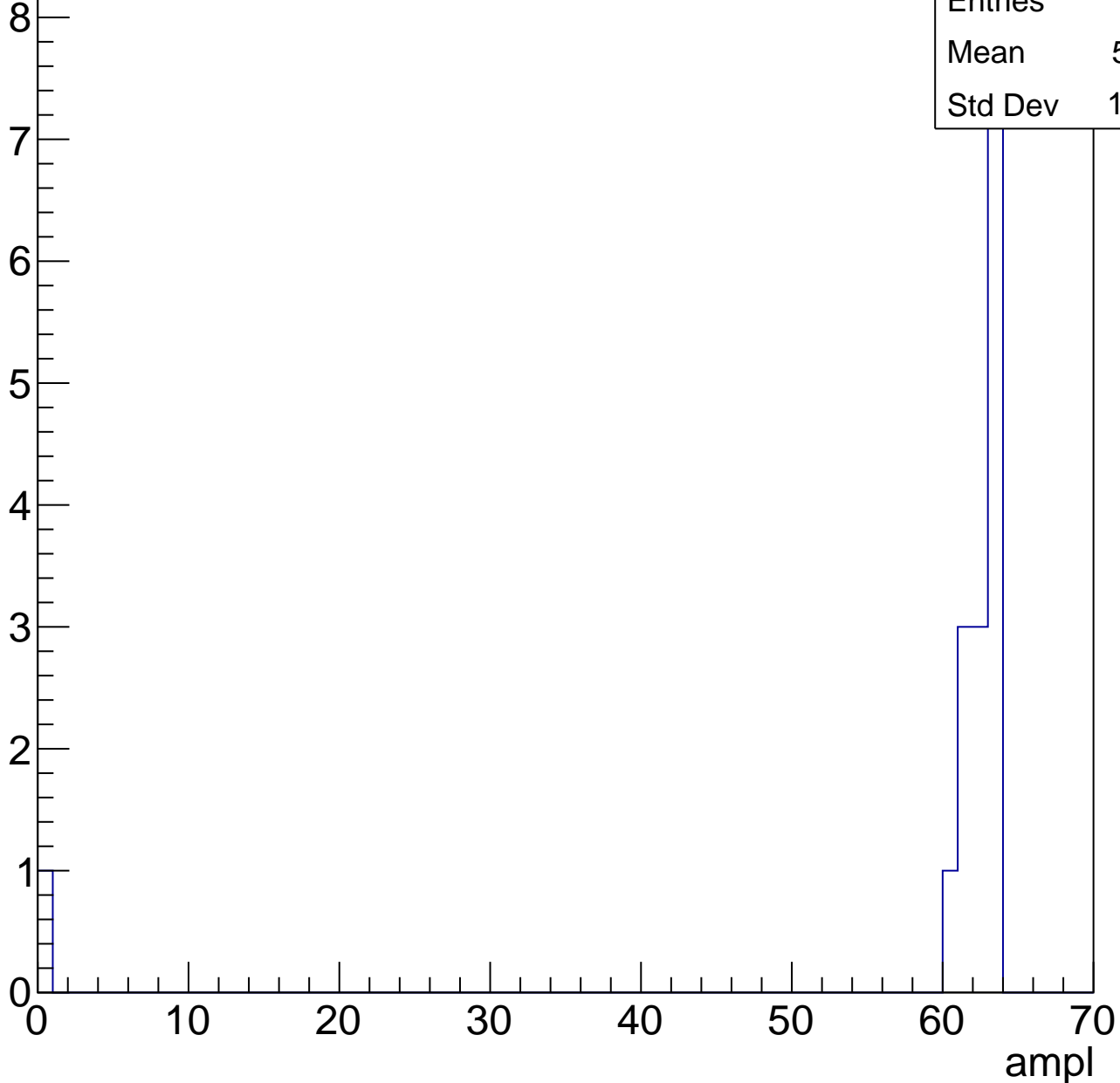
Entries	41
Mean	59.24
Std Dev	1.997

B1L101S, U4-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	58.31
Std Dev	15.09



B1L101S, U4-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch104, adc0

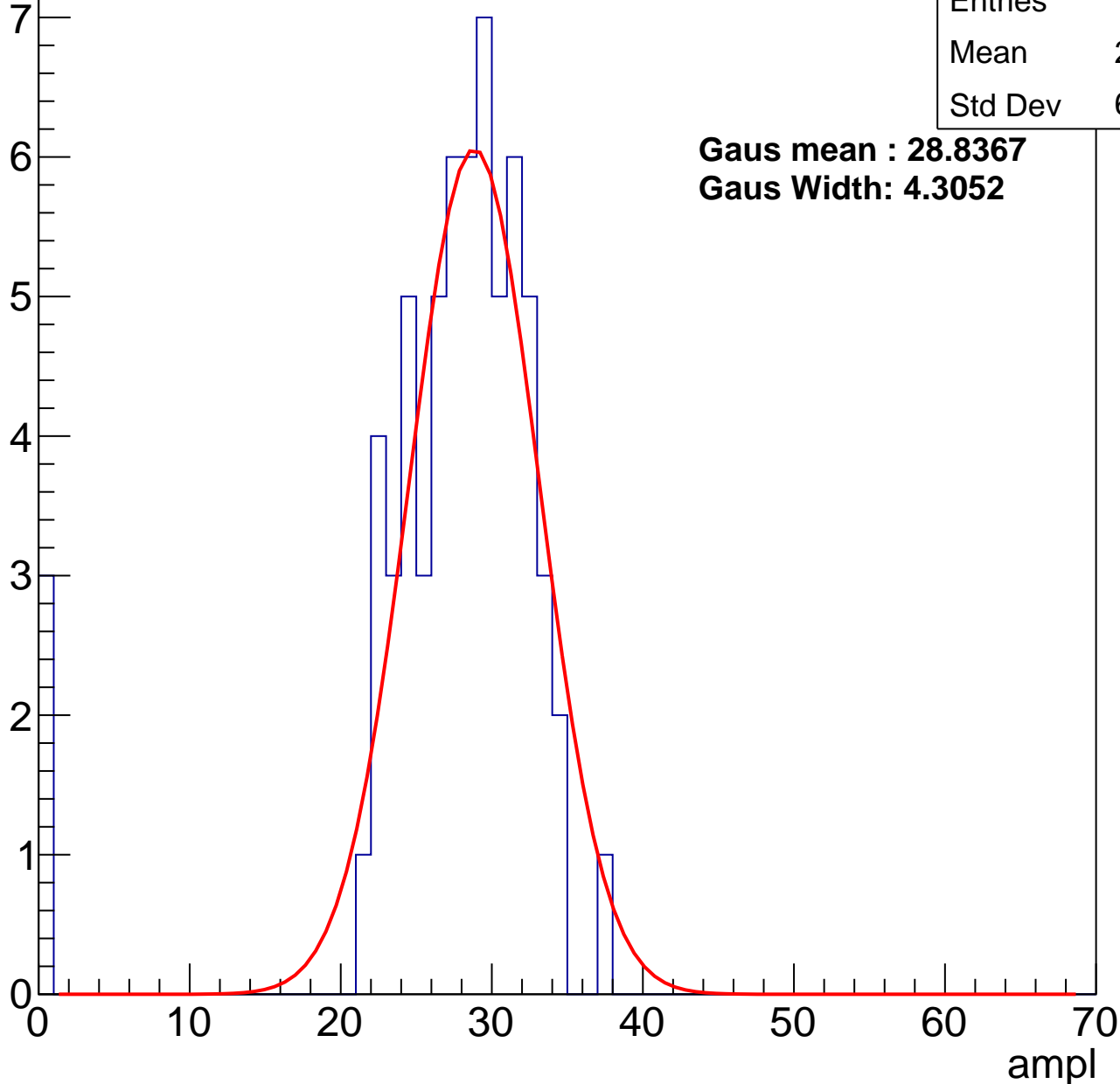
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	26.71
Std Dev	6.831

Gaus mean : 28.8367

Gaus Width: 4.3052



B1L101S, U4-ch104, adc1

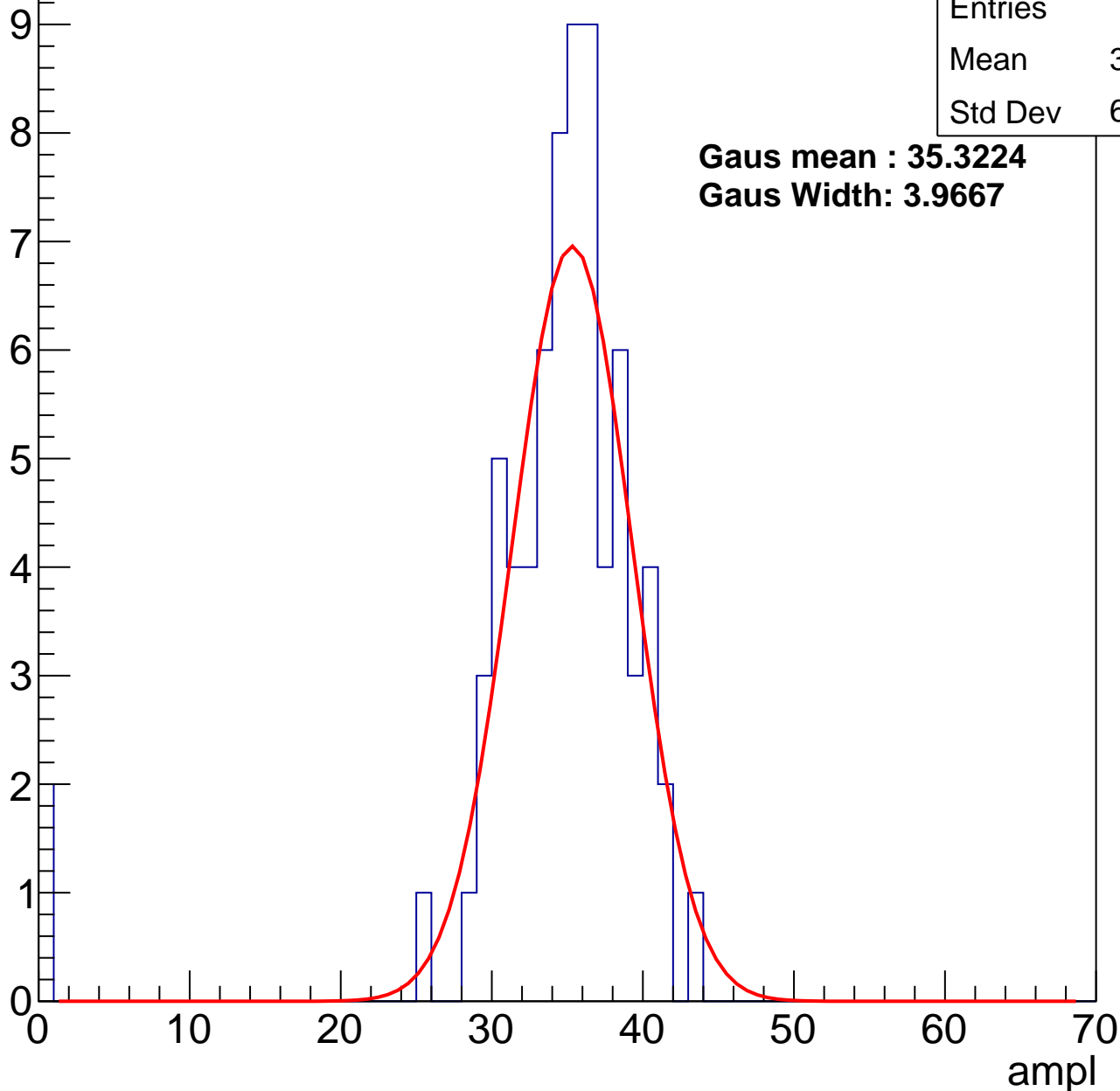
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	33.74
Std Dev	6.679

Gaus mean : 35.3224

Gaus Width: 3.9667



B1L101S, U4-ch104, adc2

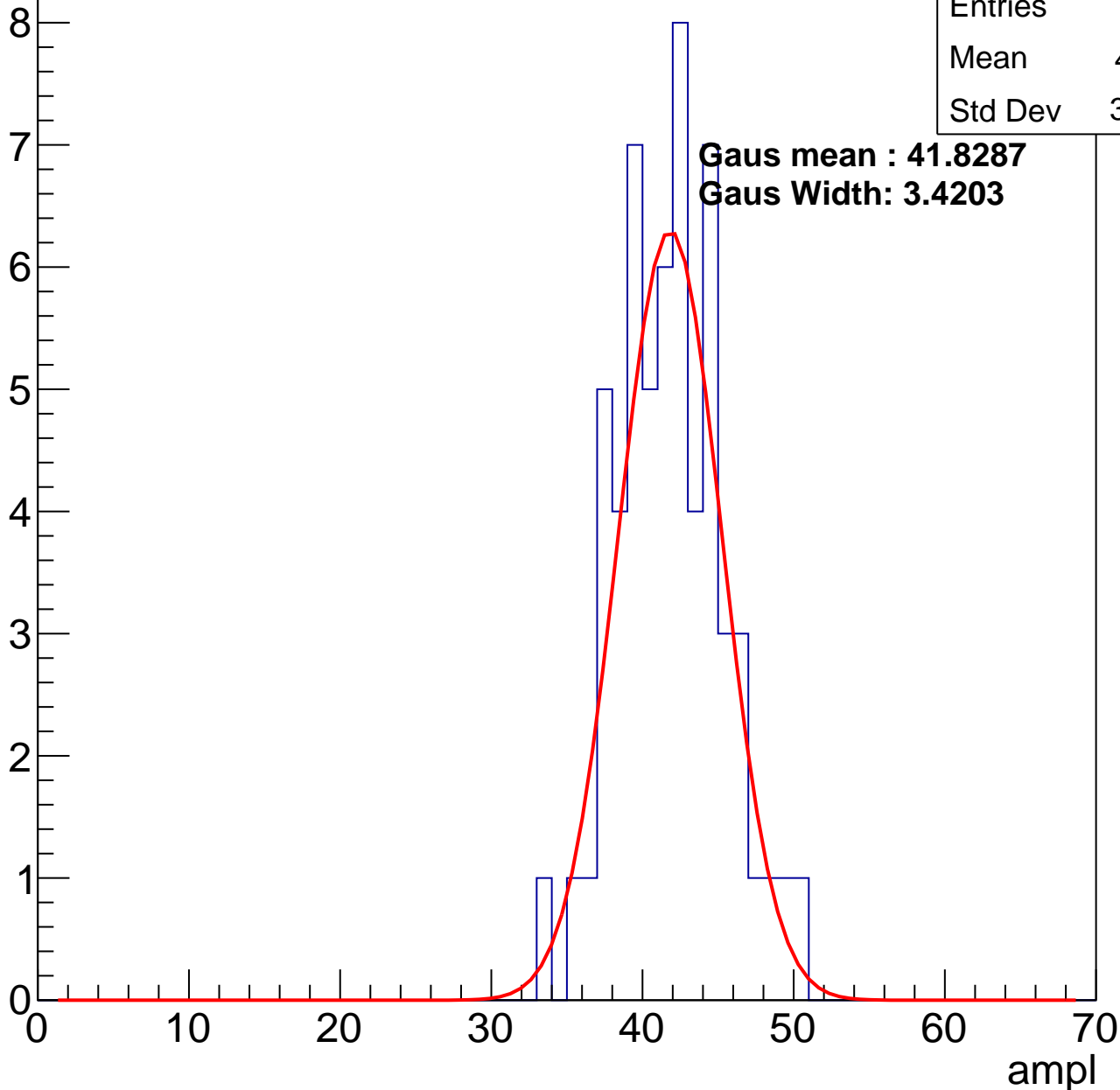
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	41.41
Std Dev	3.455

Gaus mean : 41.8287

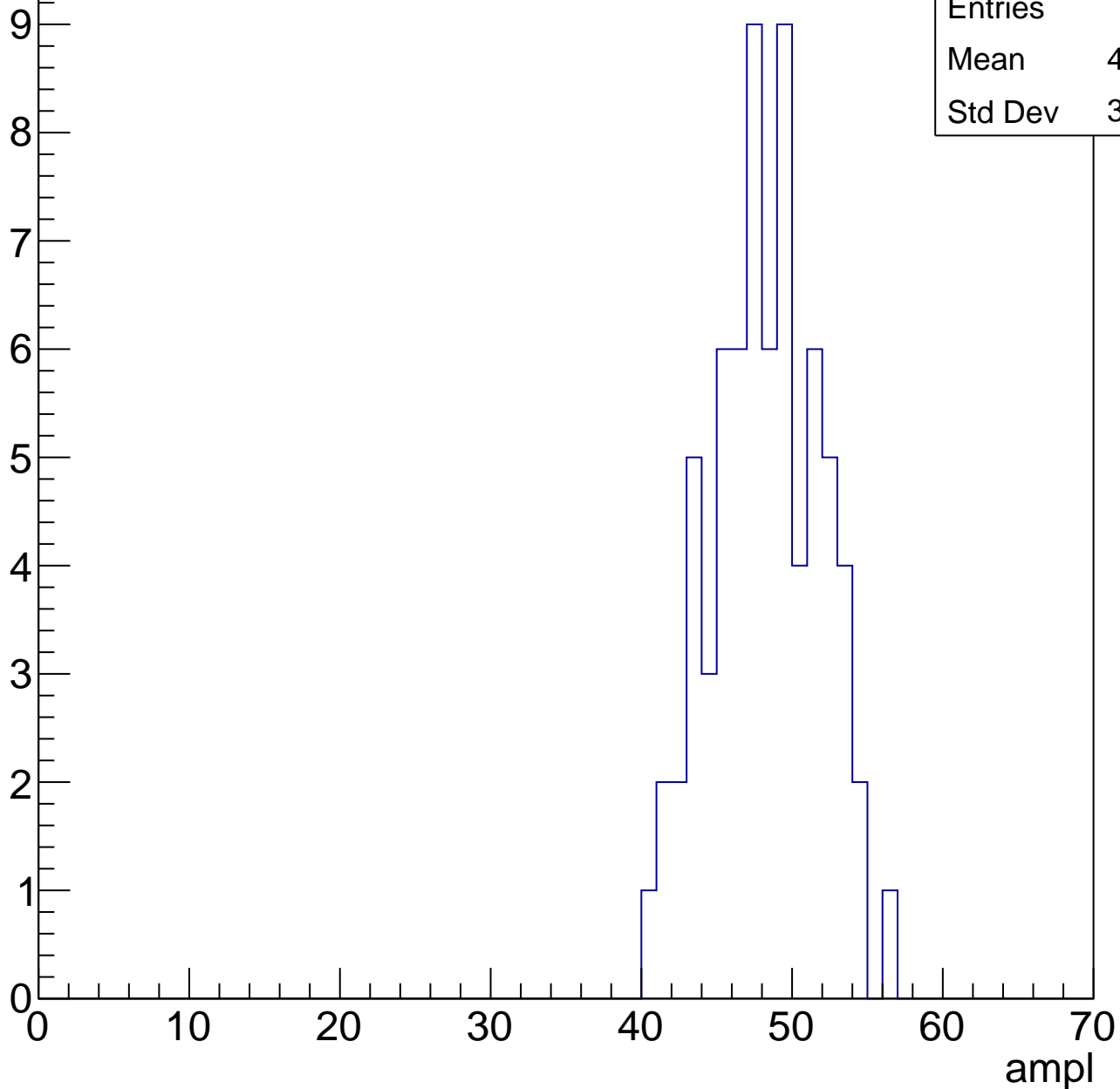
Gaus Width: 3.4203



B1L101S, U4-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

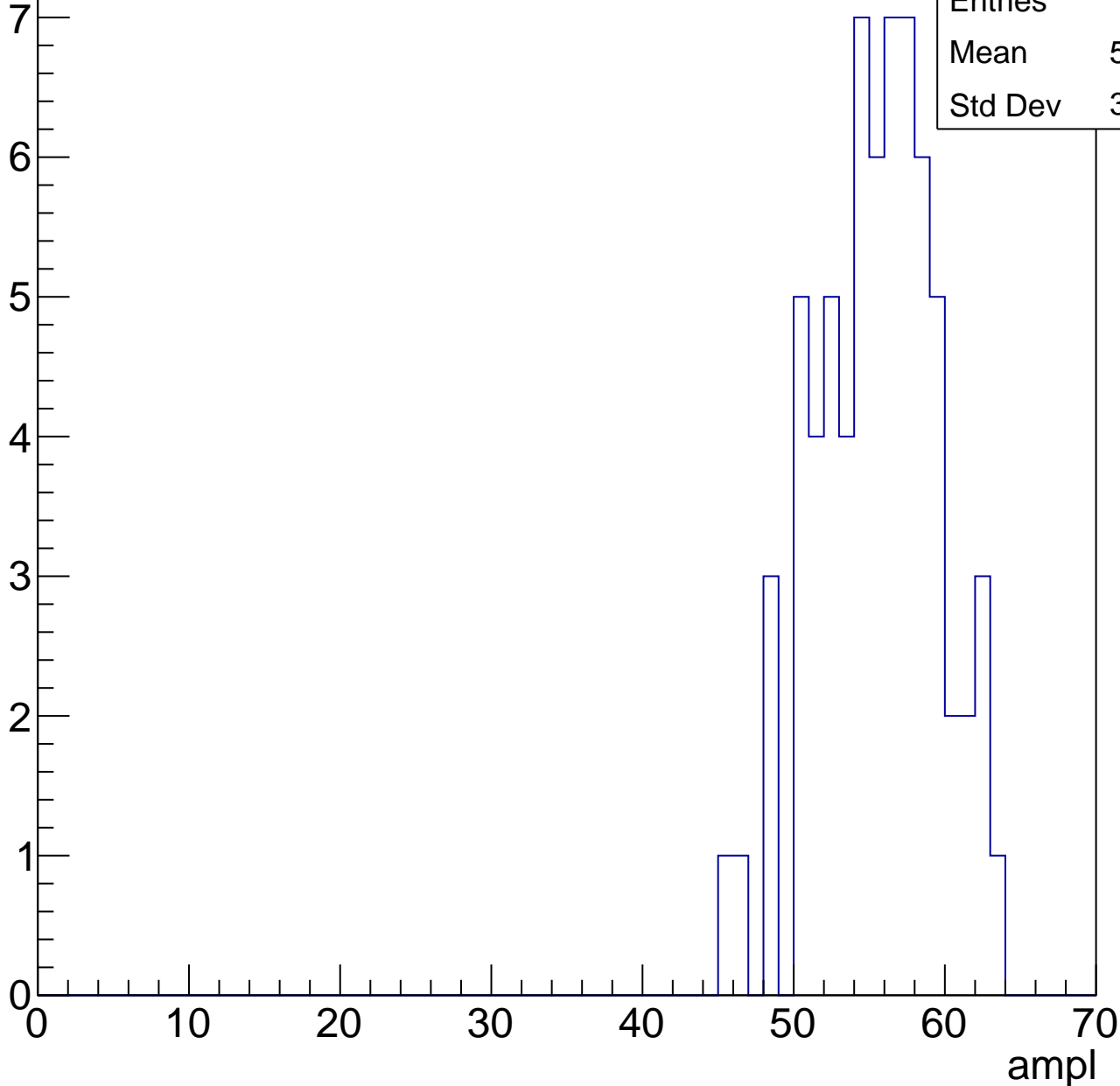


B1L101S, U4-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	54.99
Std Dev	3.969



B1L101S, U4-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	59.19
Std Dev	2.847

ampl

0

10

20

30

40

50

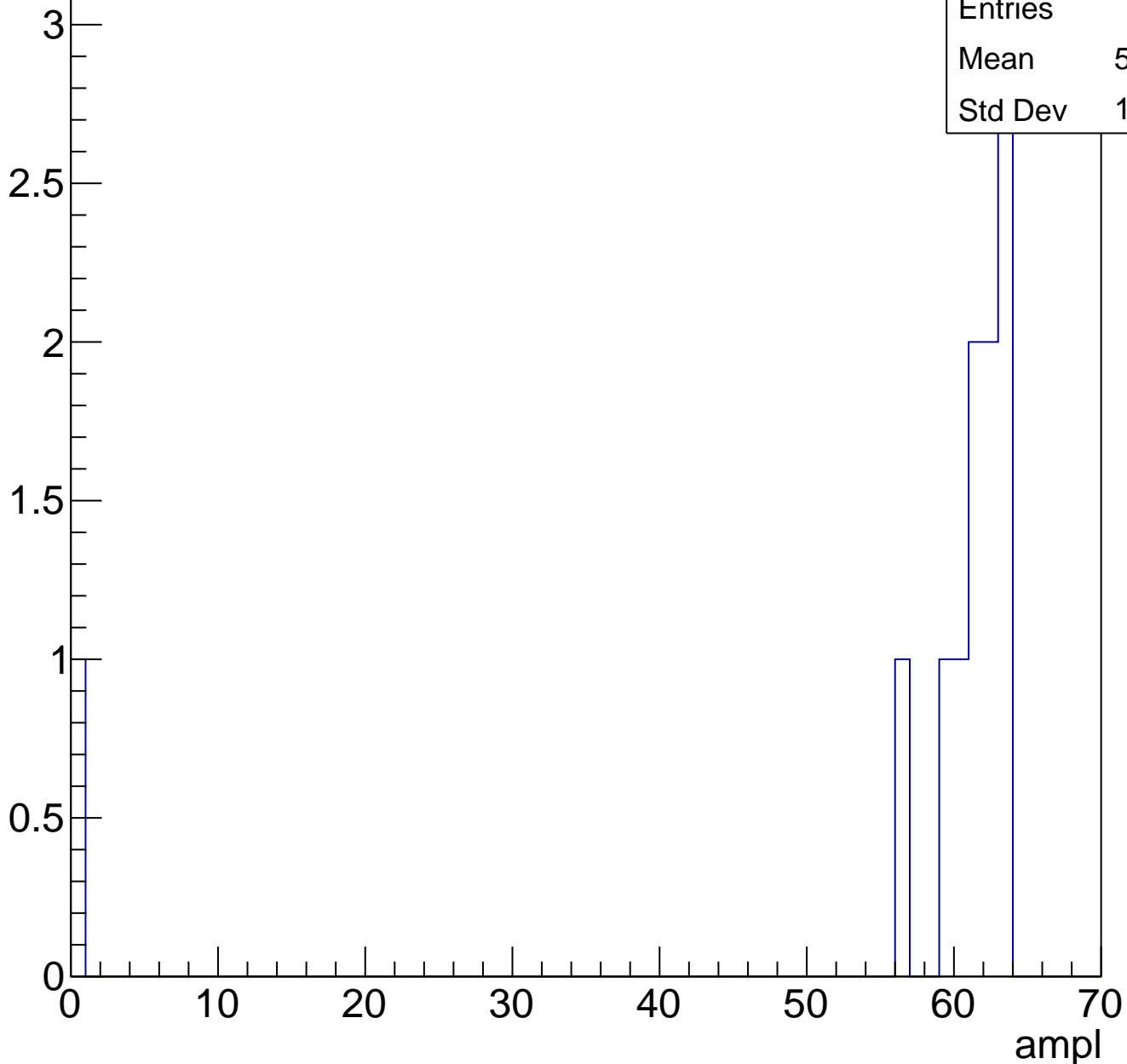
60

70

B1L101S, U4-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	11
Mean	55.45
Std Dev	17.65

B1L101S, U4-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch105, adc0

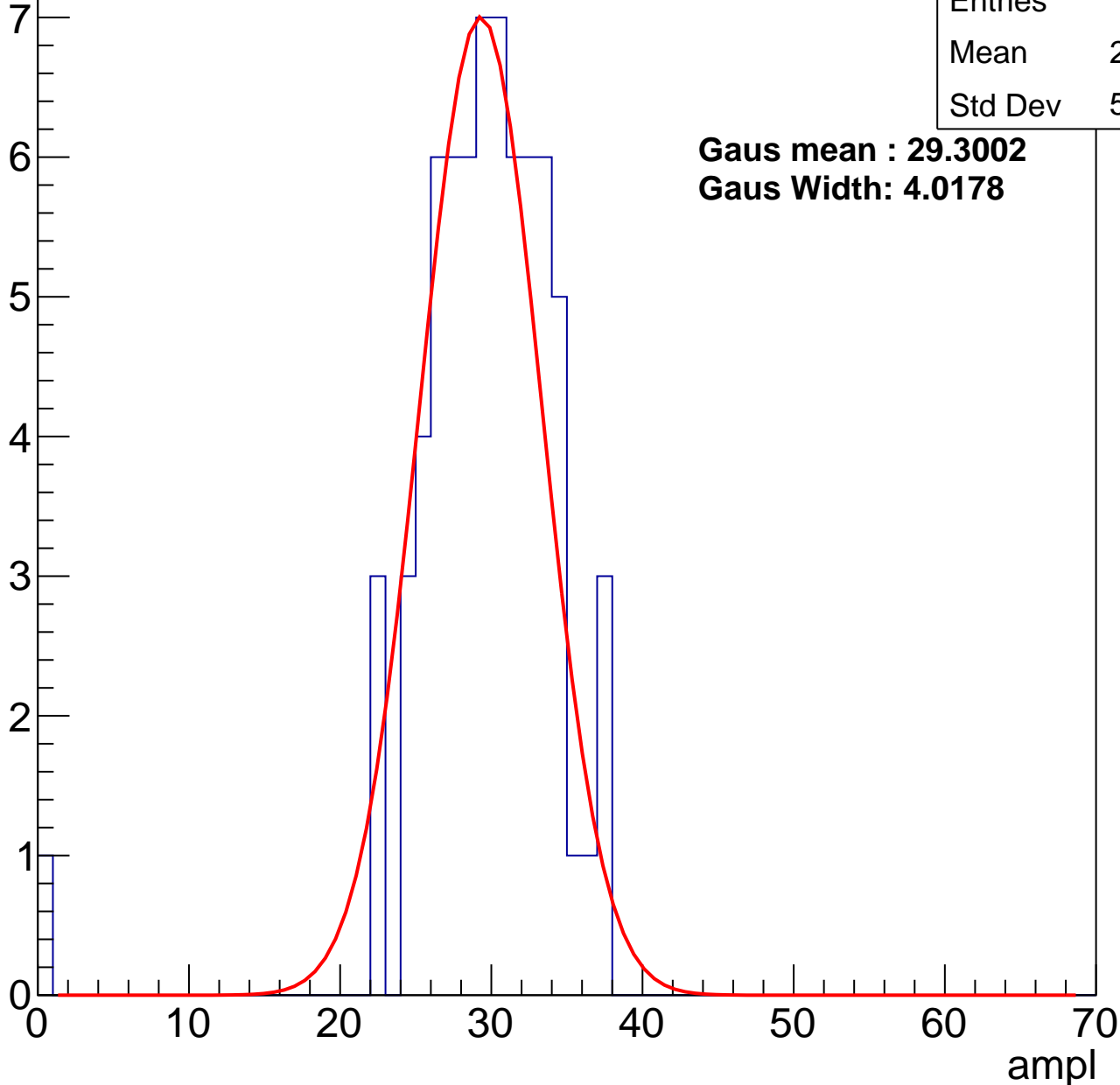
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.08
Std Dev	5.026

Gaus mean : 29.3002

Gaus Width: 4.0178



B1L101S, U4-ch105, adc1

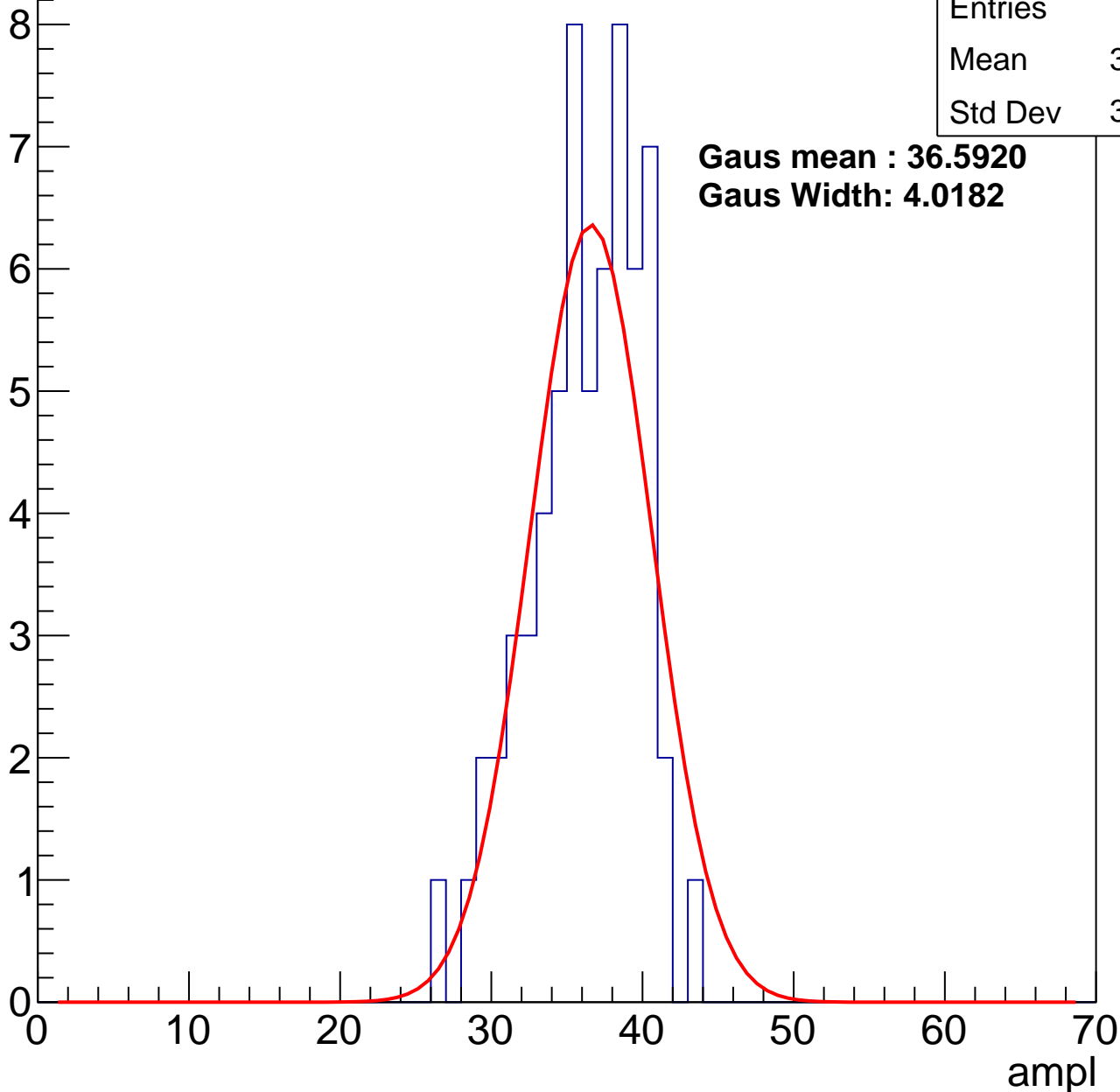
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.75
Std Dev	3.579

Gaus mean : 36.5920

Gaus Width: 4.0182



B1L101S, U4-ch105, adc2

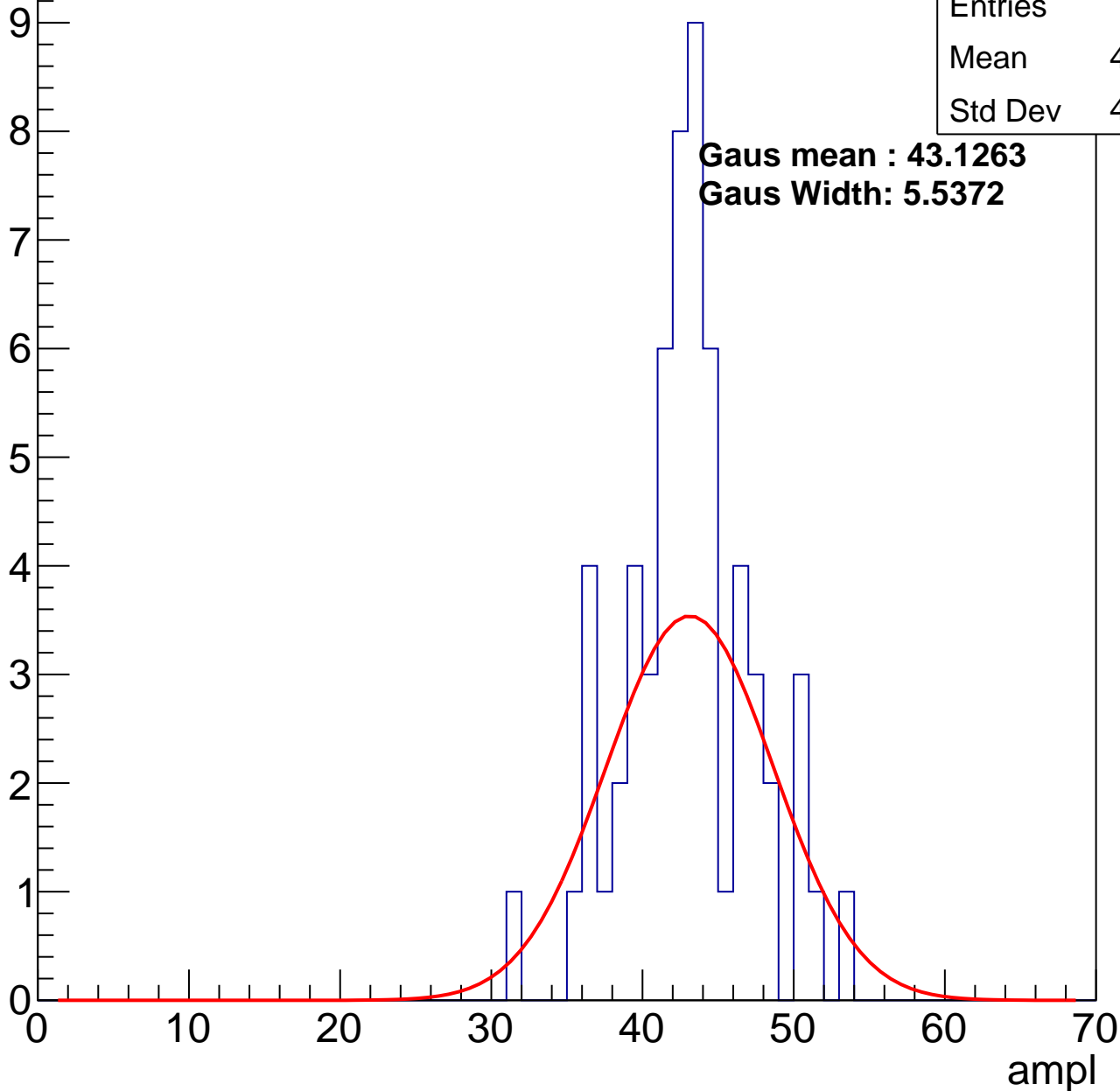
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.53
Std Dev	4.193

Gaus mean : 43.1263

Gaus Width: 5.5372



B1L101S, U4-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

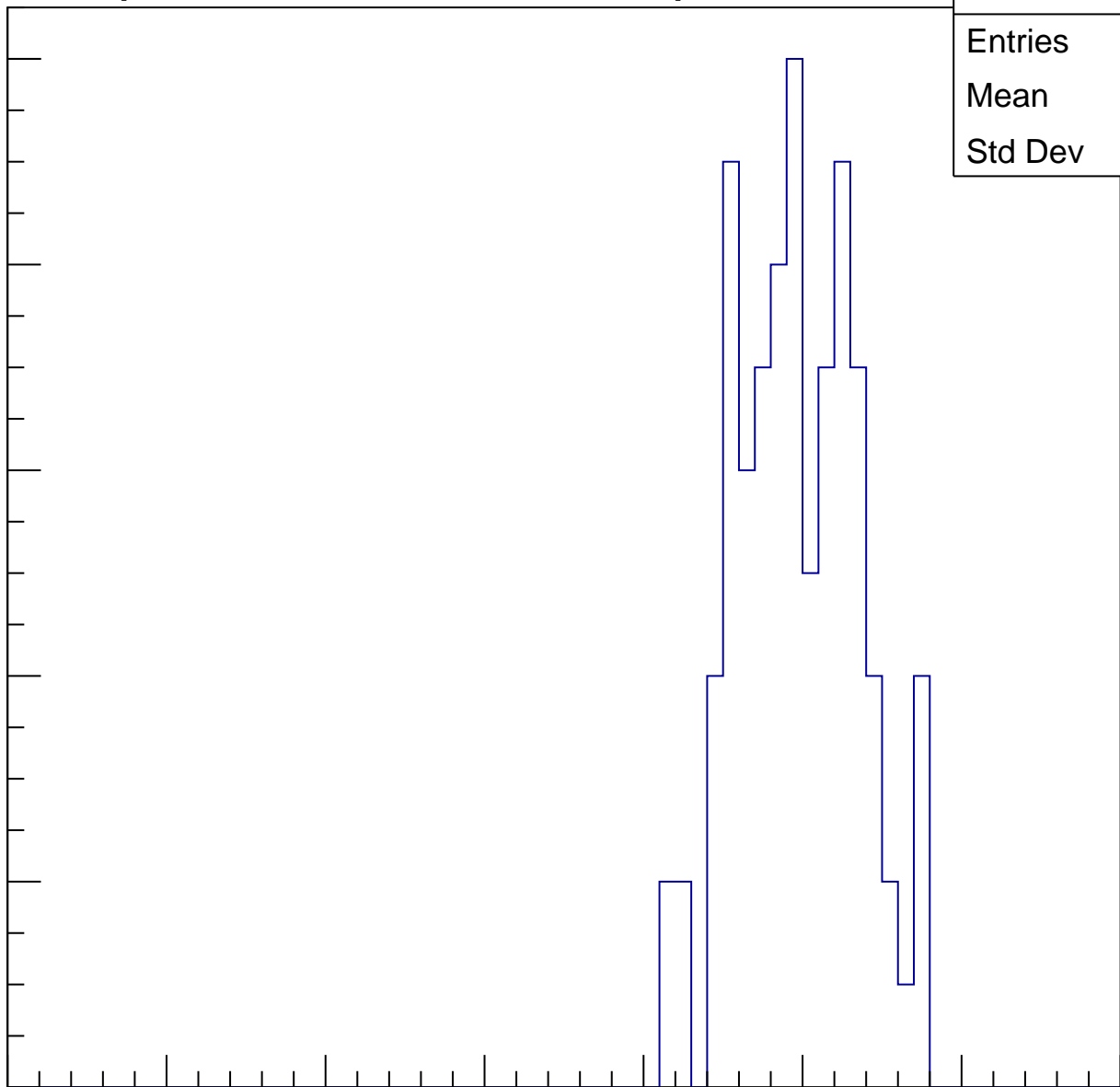
Entries	87
Mean	49.22
Std Dev	3.804

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

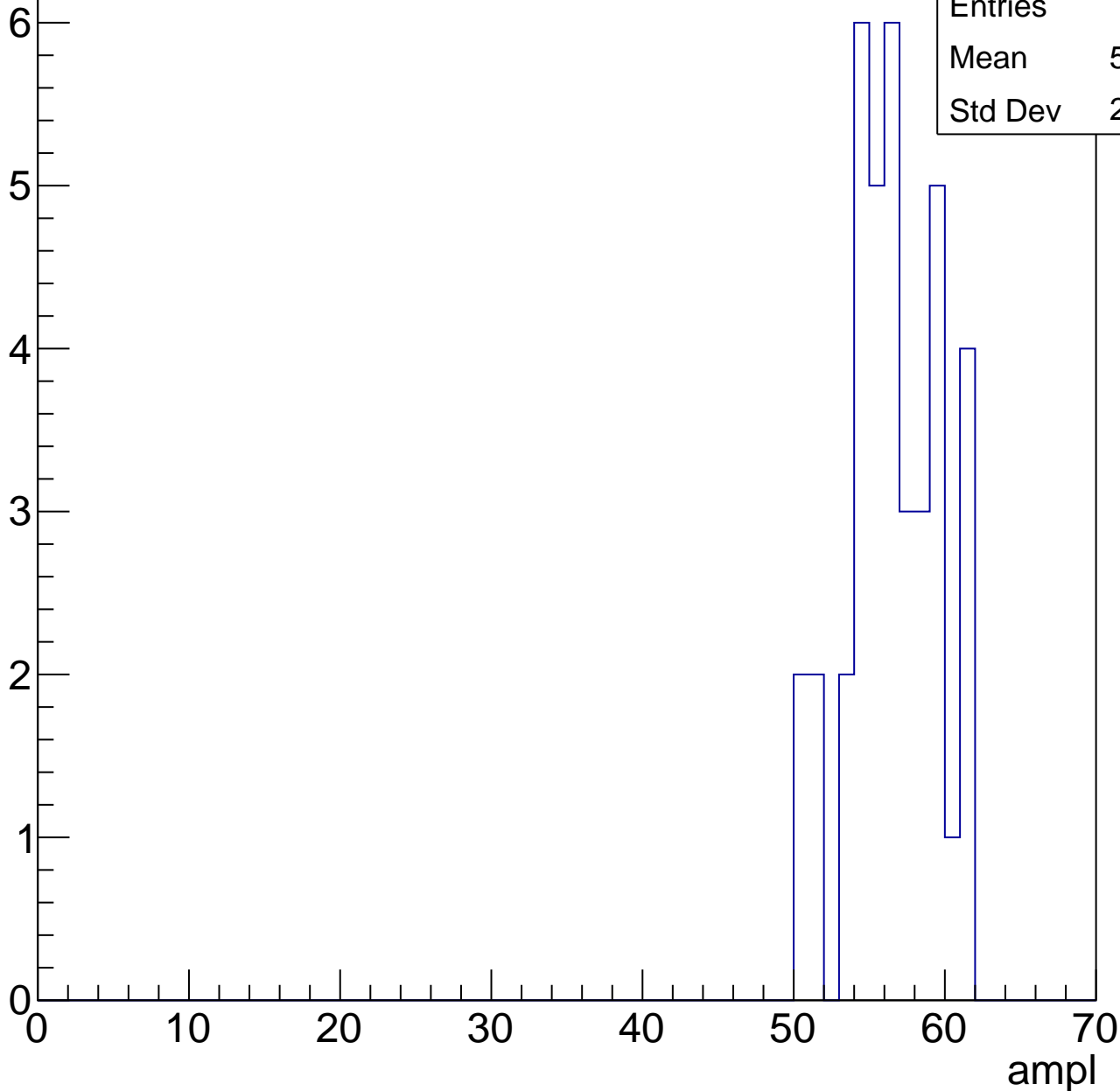


B1L101S, U4-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	56.08
Std Dev	2.973



B1L101S, U4-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	52
Mean	59.77
Std Dev	2.672

Entry

10

8

6

4

2

0

0

10

20

30

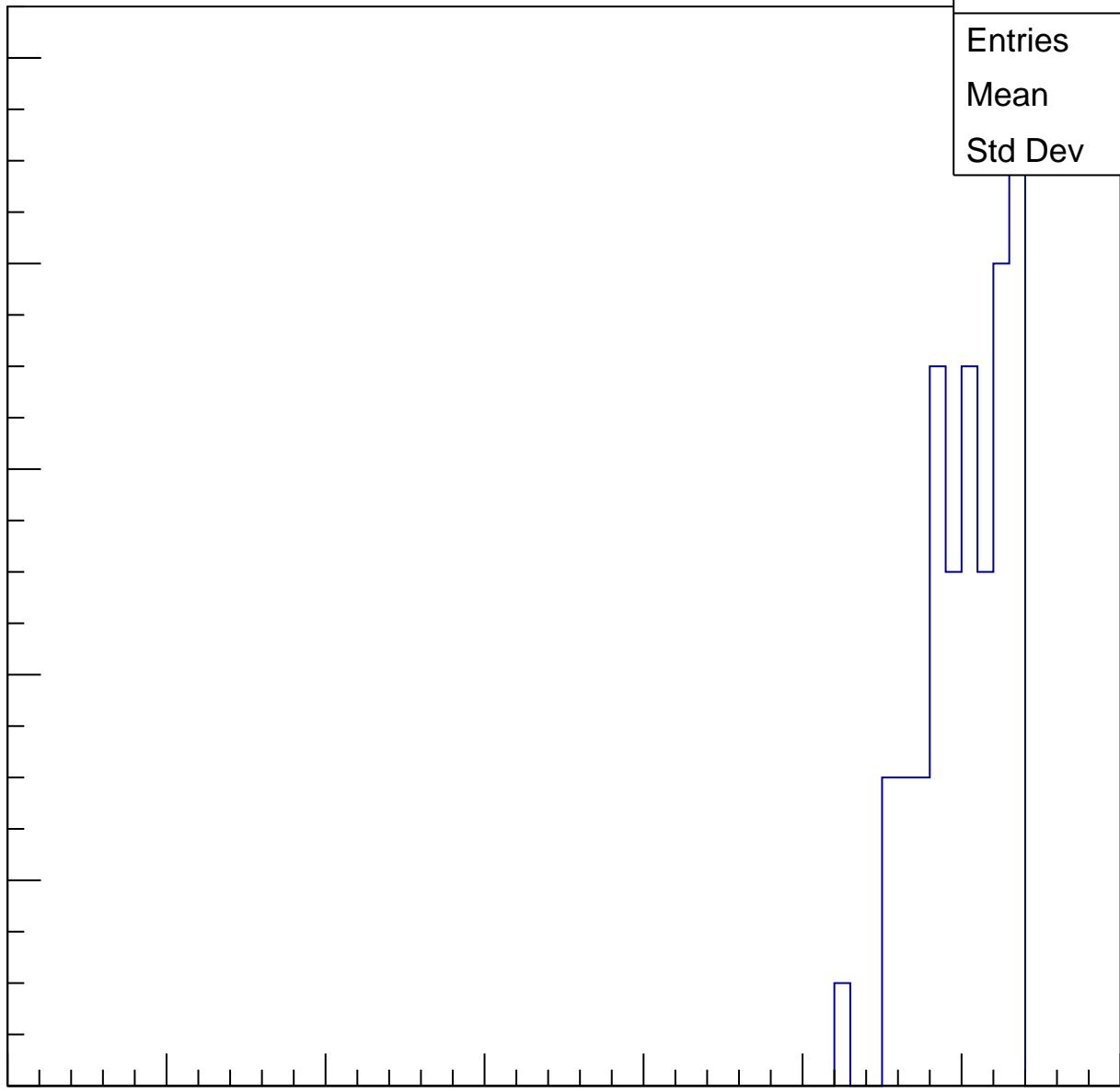
40

50

60

70

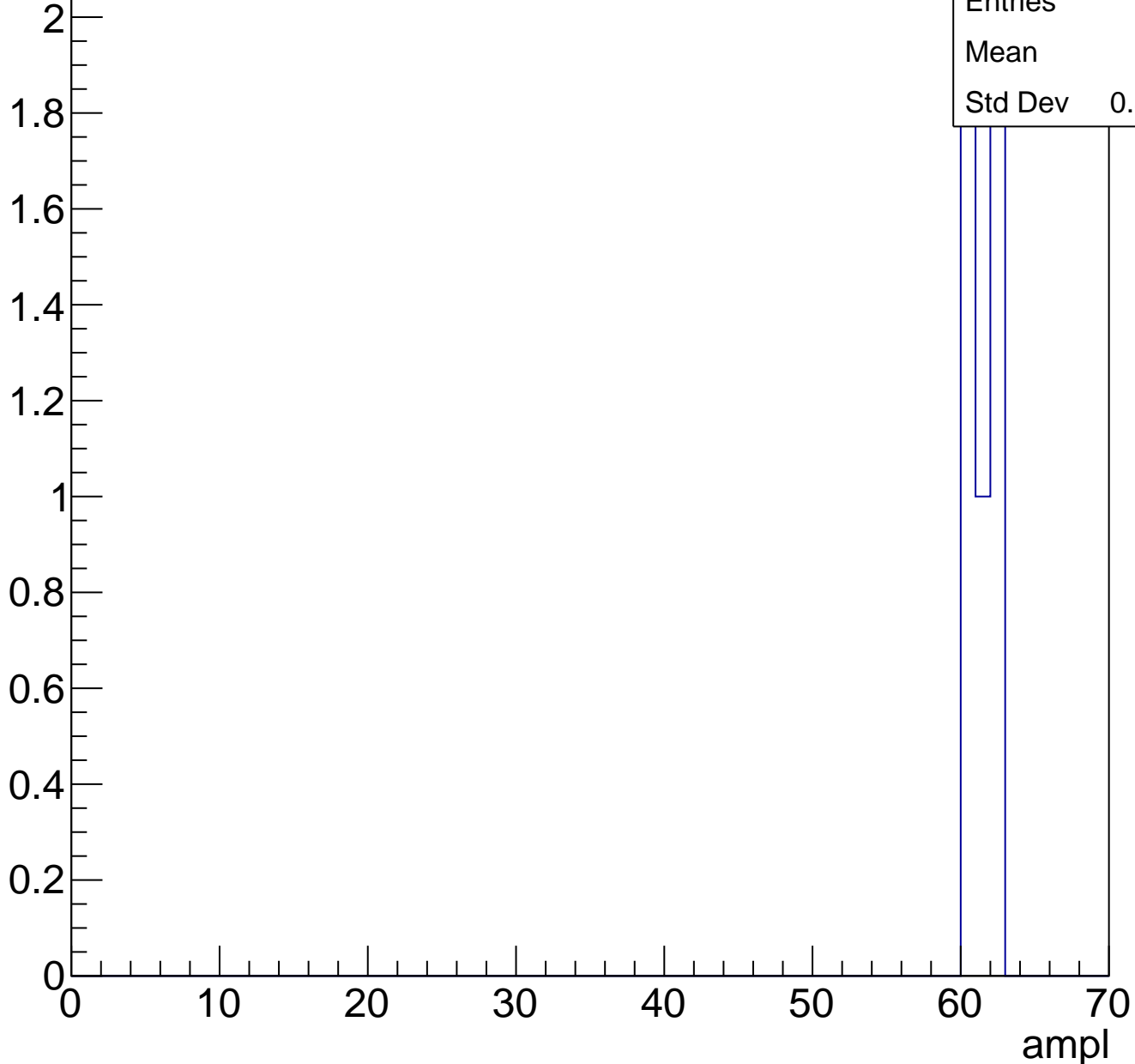
ampl



B1L101S, U4-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch106, adc0

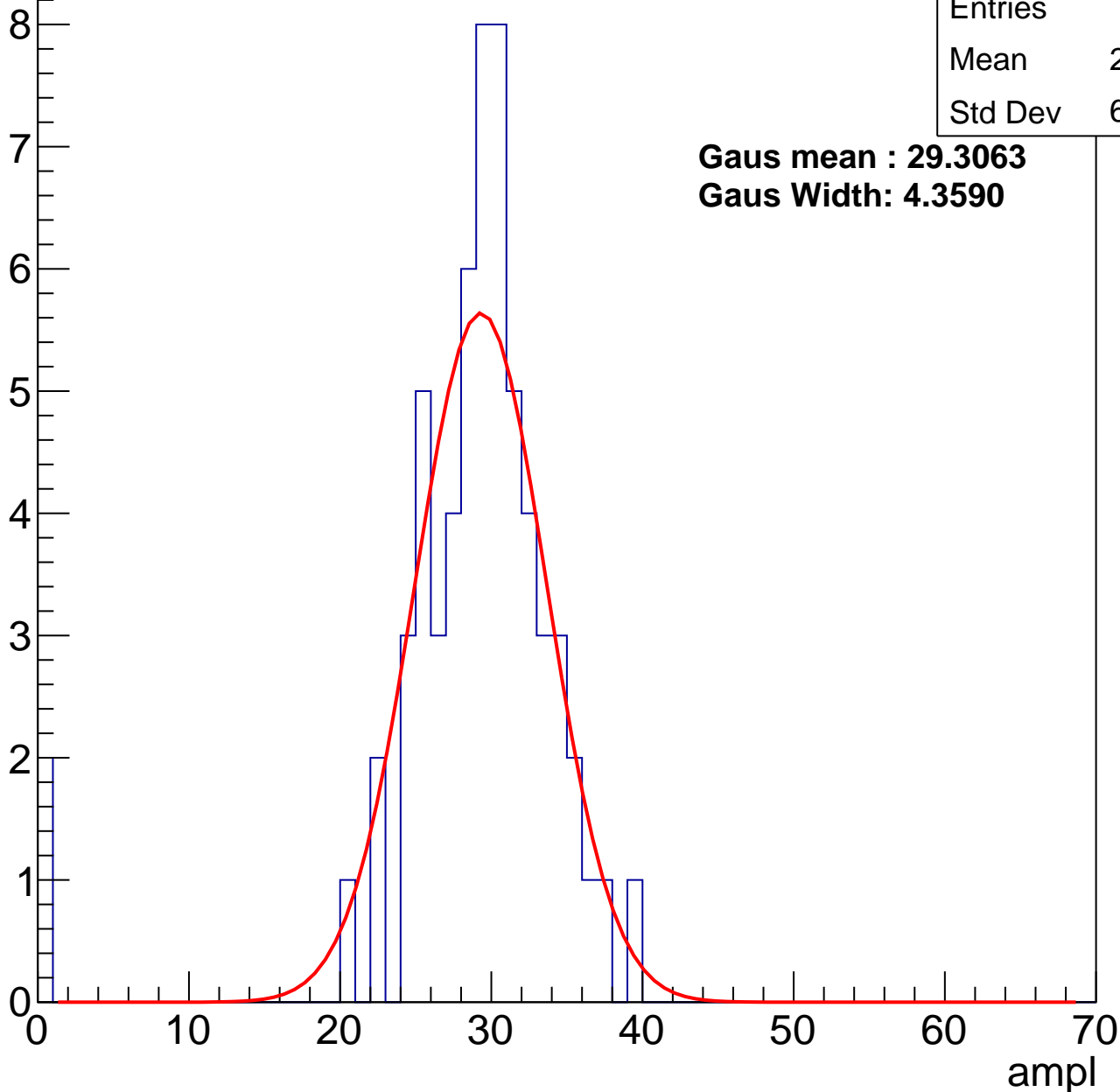
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.27
Std Dev	6.356

Gaus mean : 29.3063

Gaus Width: 4.3590



B1L101S, U4-ch106, adc1

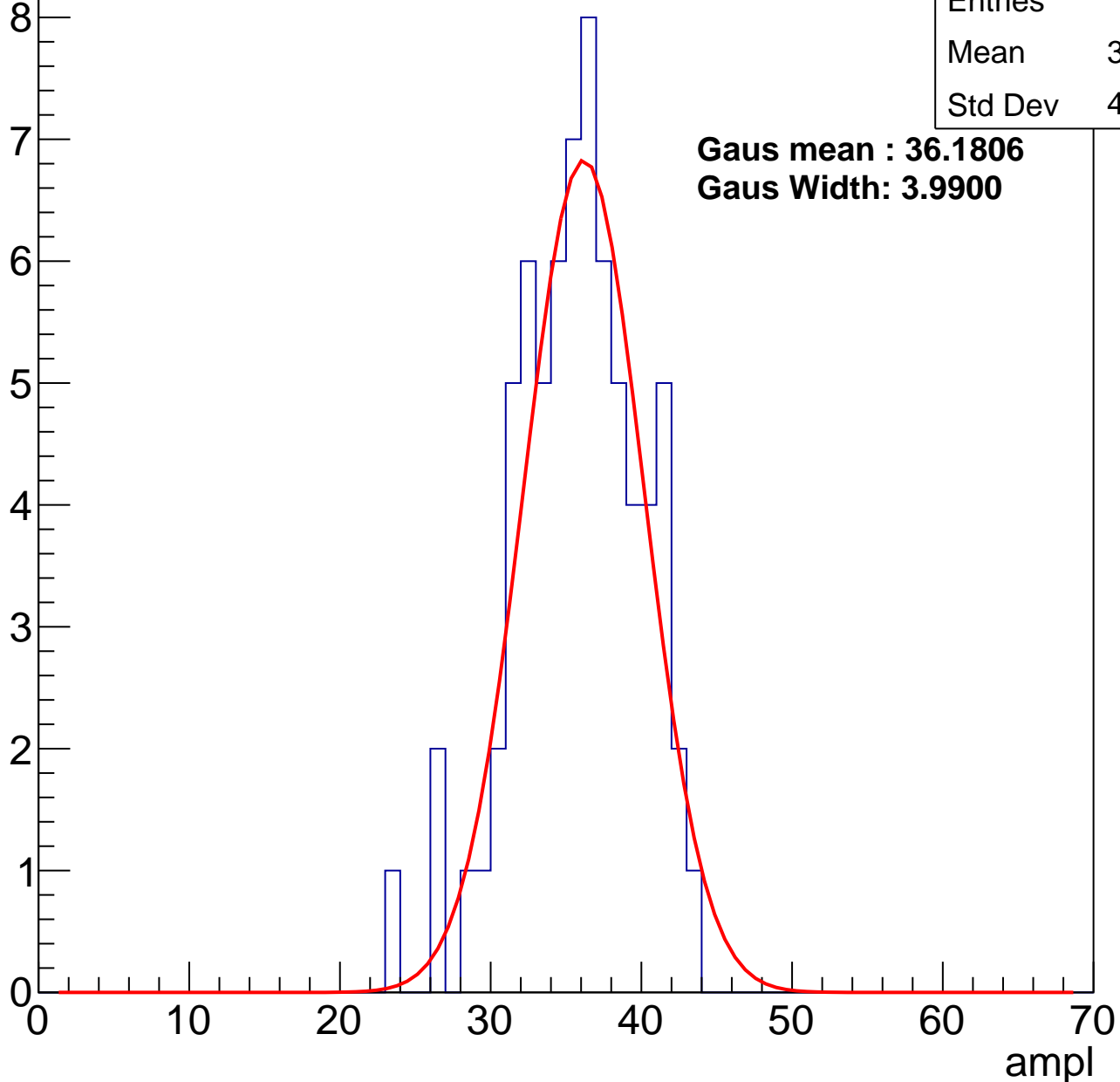
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.23
Std Dev	4.077

Gaus mean : 36.1806

Gaus Width: 3.9900



B1L101S, U4-ch106, adc2

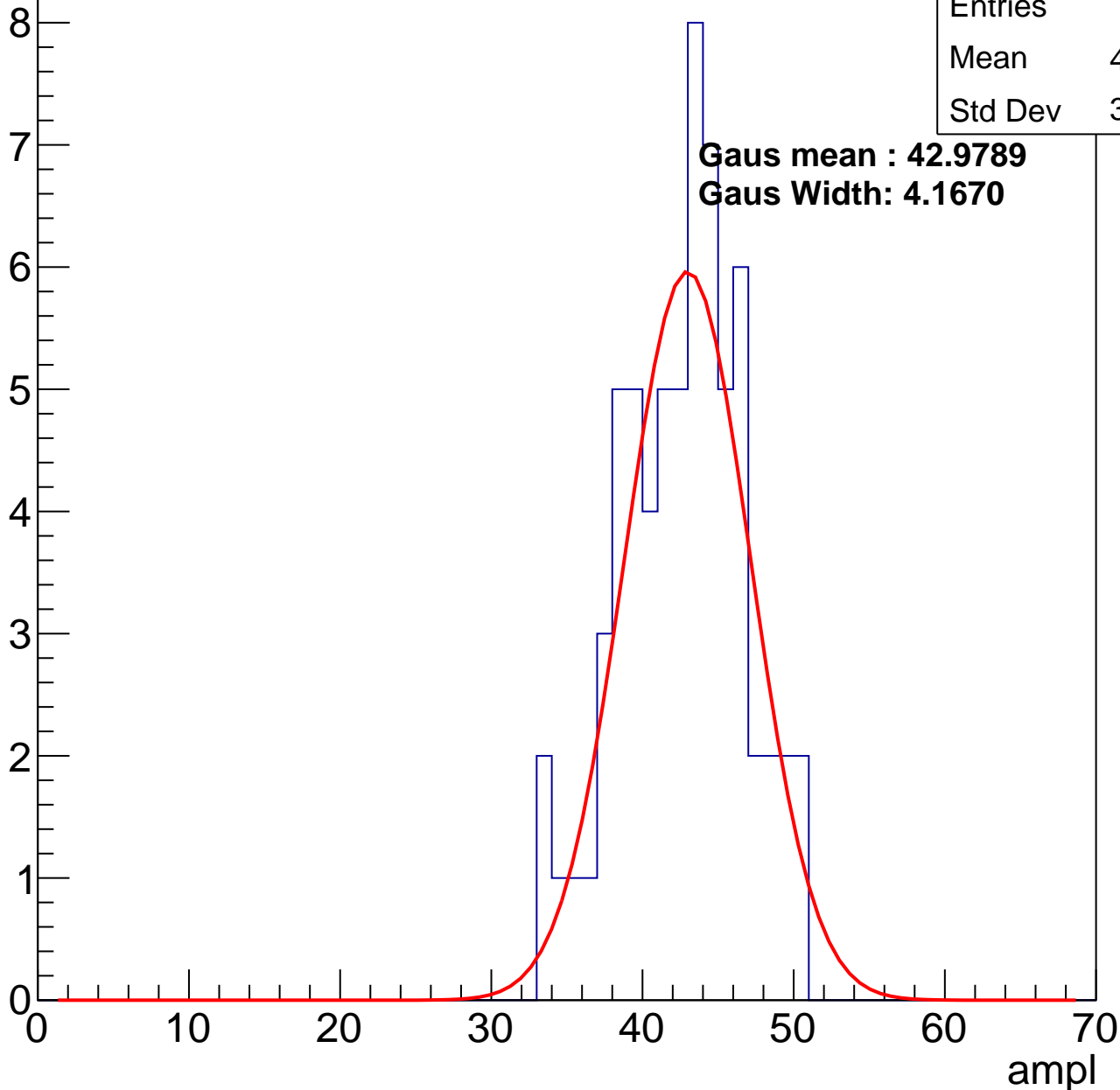
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.17
Std Dev	3.998

Gaus mean : 42.9789

Gaus Width: 4.1670

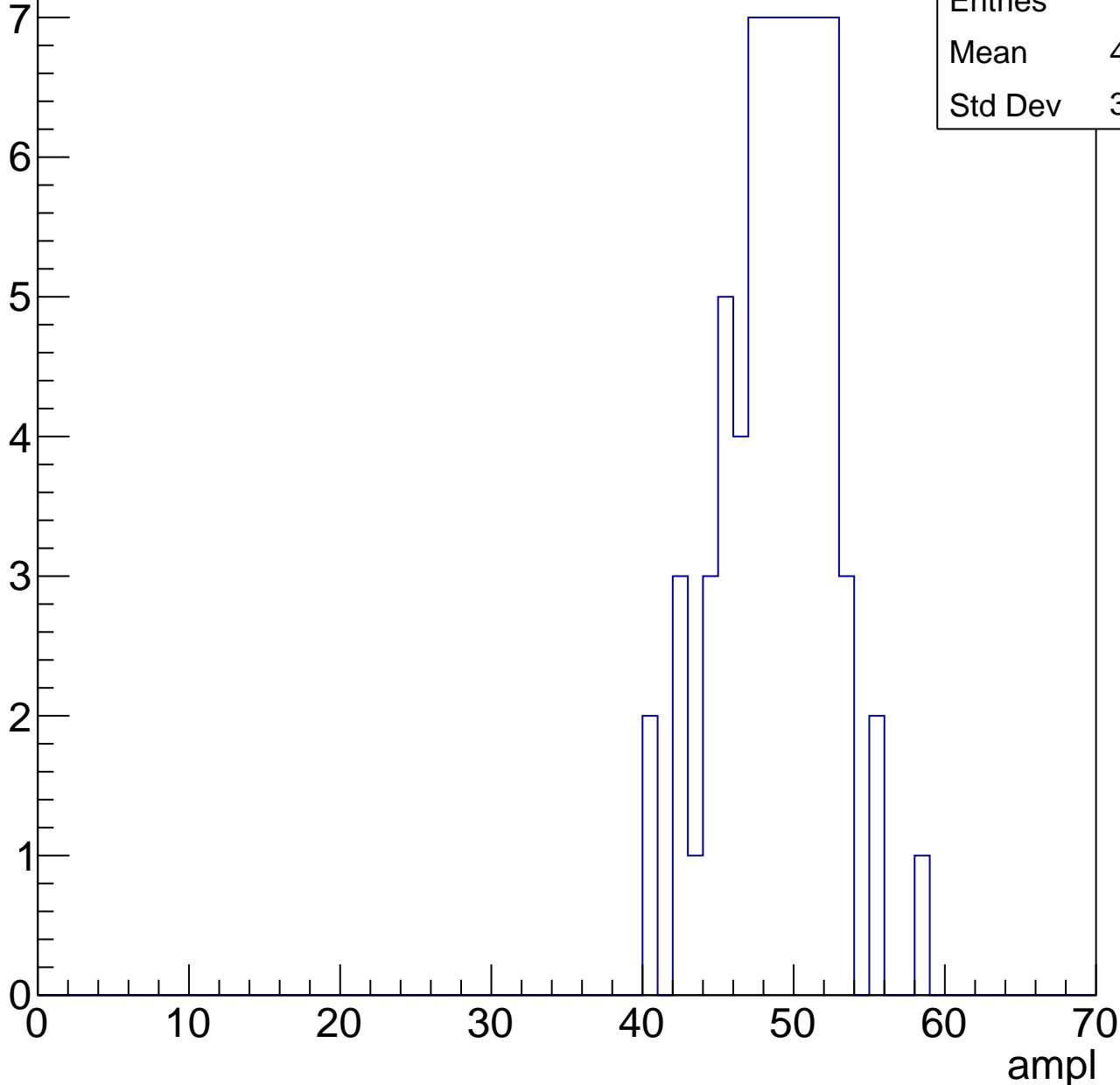


B1L101S, U4-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	48.42
Std Dev	3.585

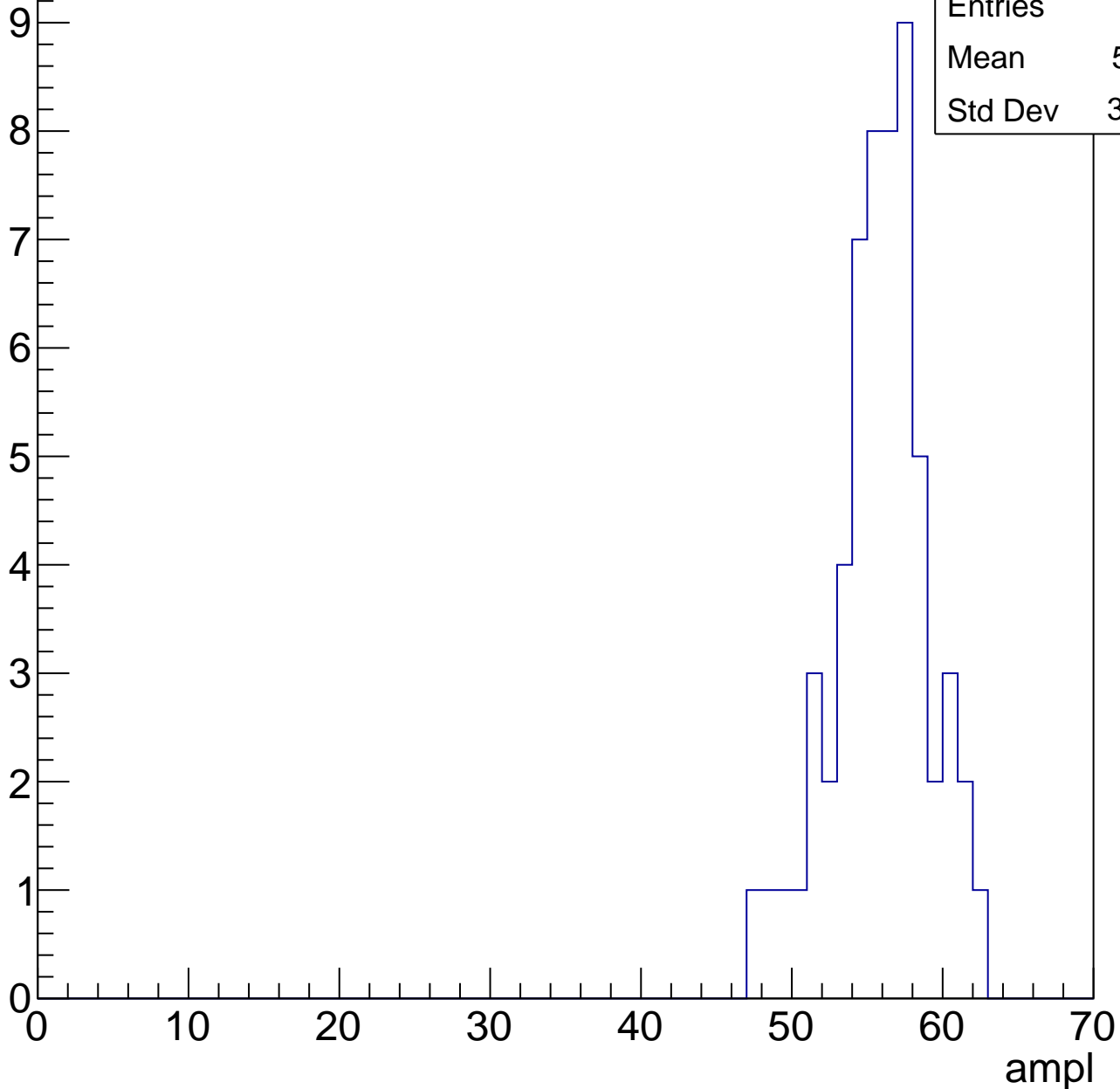


B1L101S, U4-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

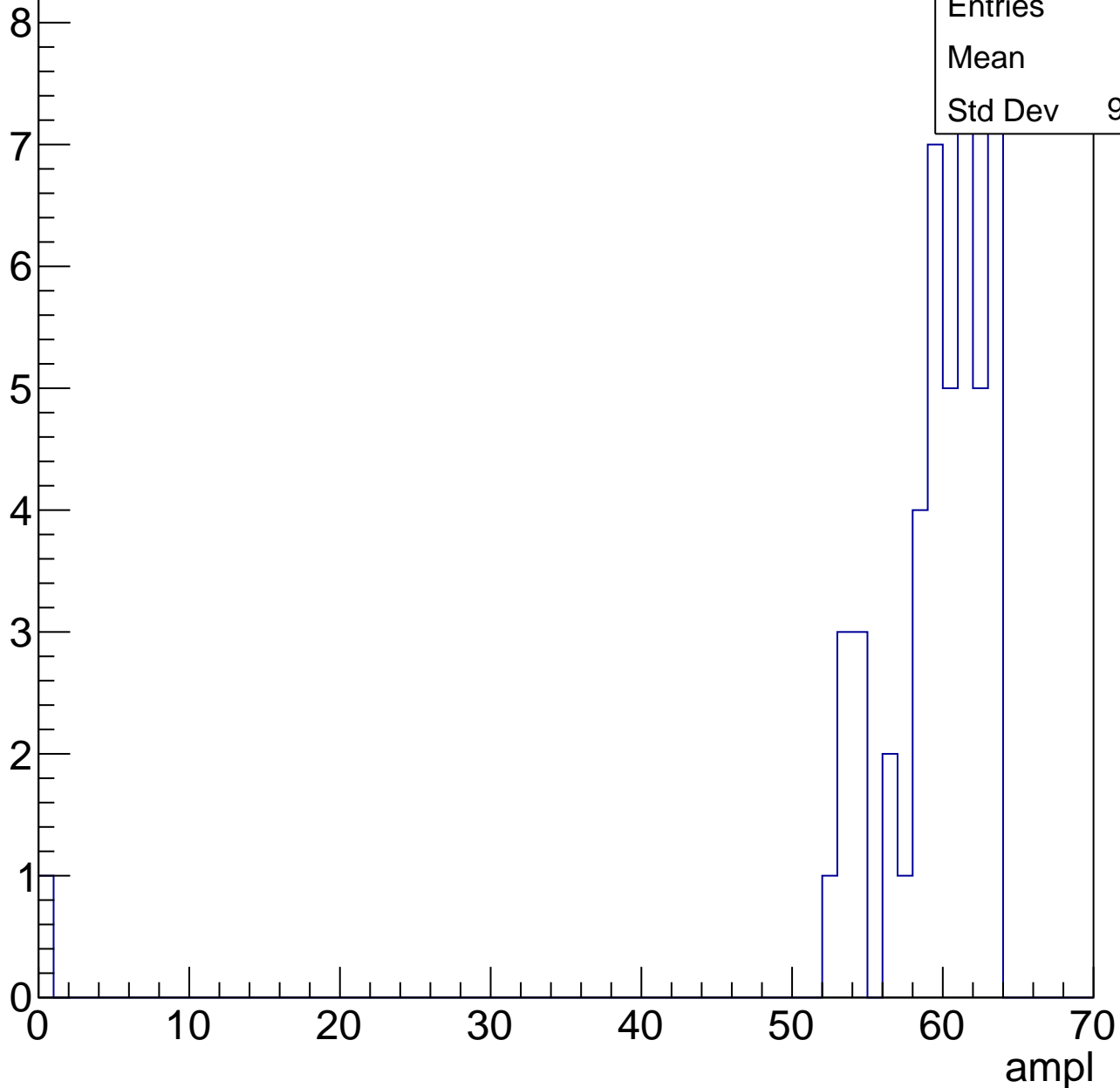
Entries	58
Mean	55.41
Std Dev	3.135



B1L101S, U4-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

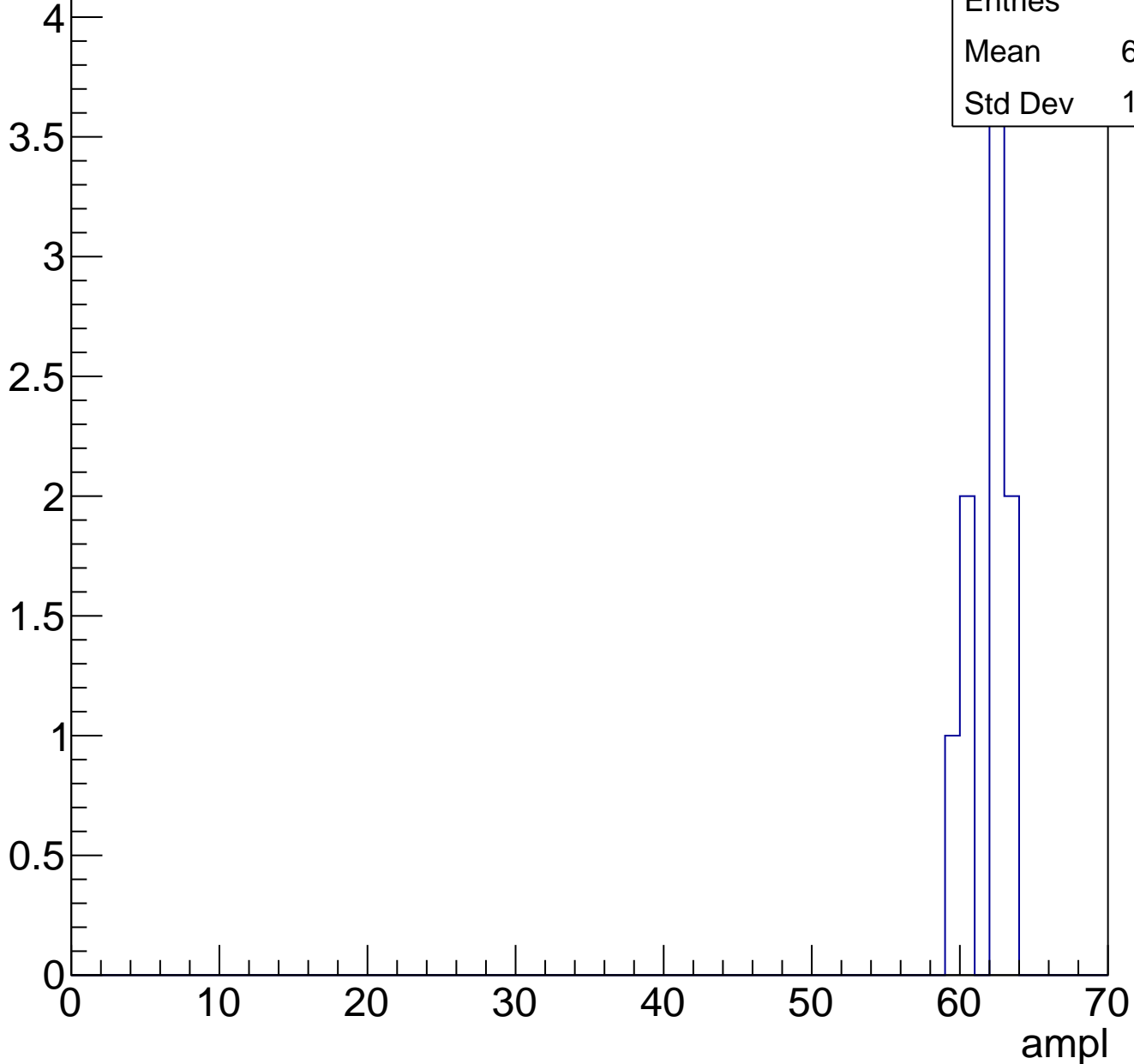


Entries	48
Mean	58.1
Std Dev	9.026

B1L101S, U4-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch107, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	28.38
Std Dev	2.918

Gaus mean : 29.0743

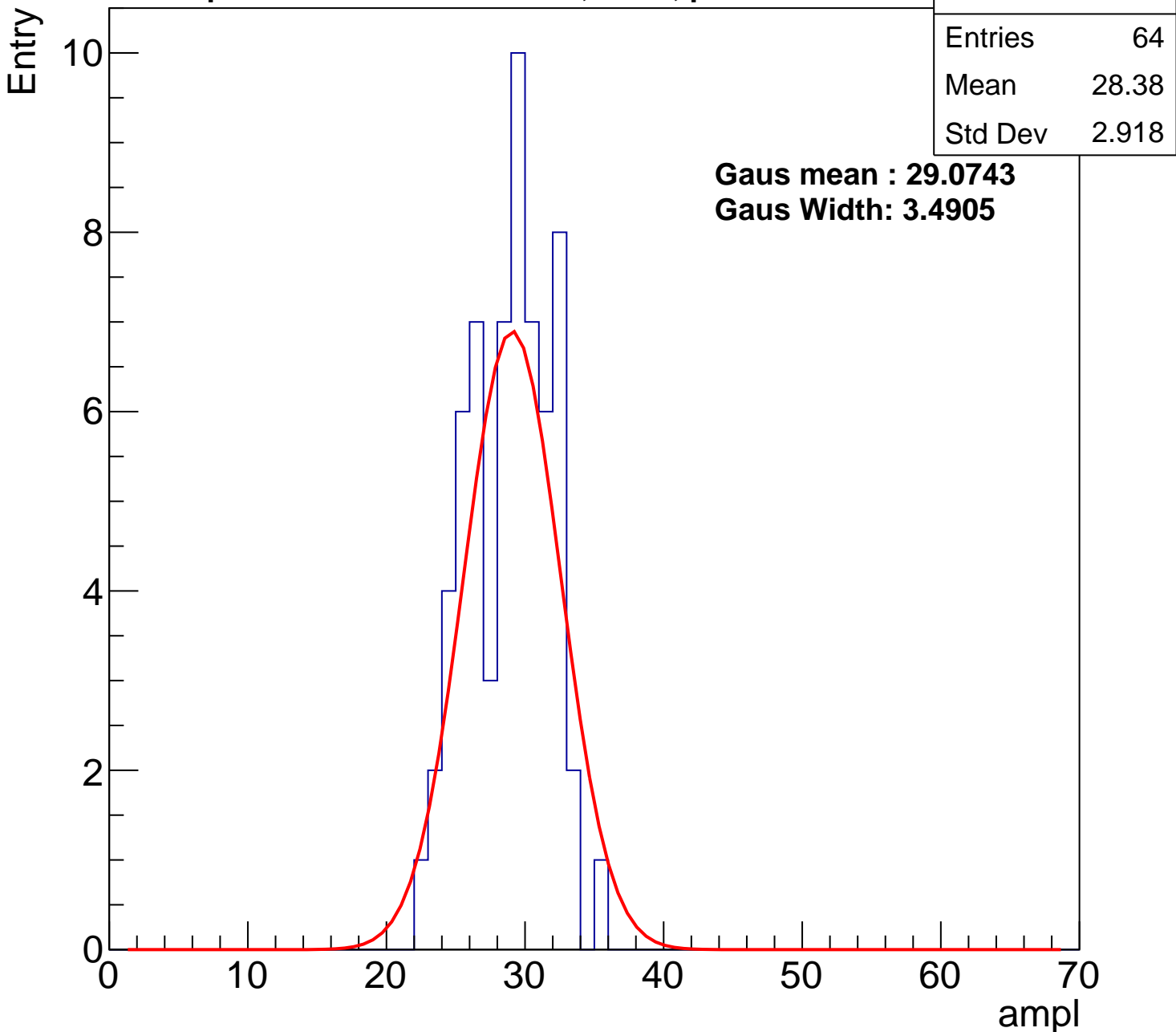
Gaus Width: 3.4905

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



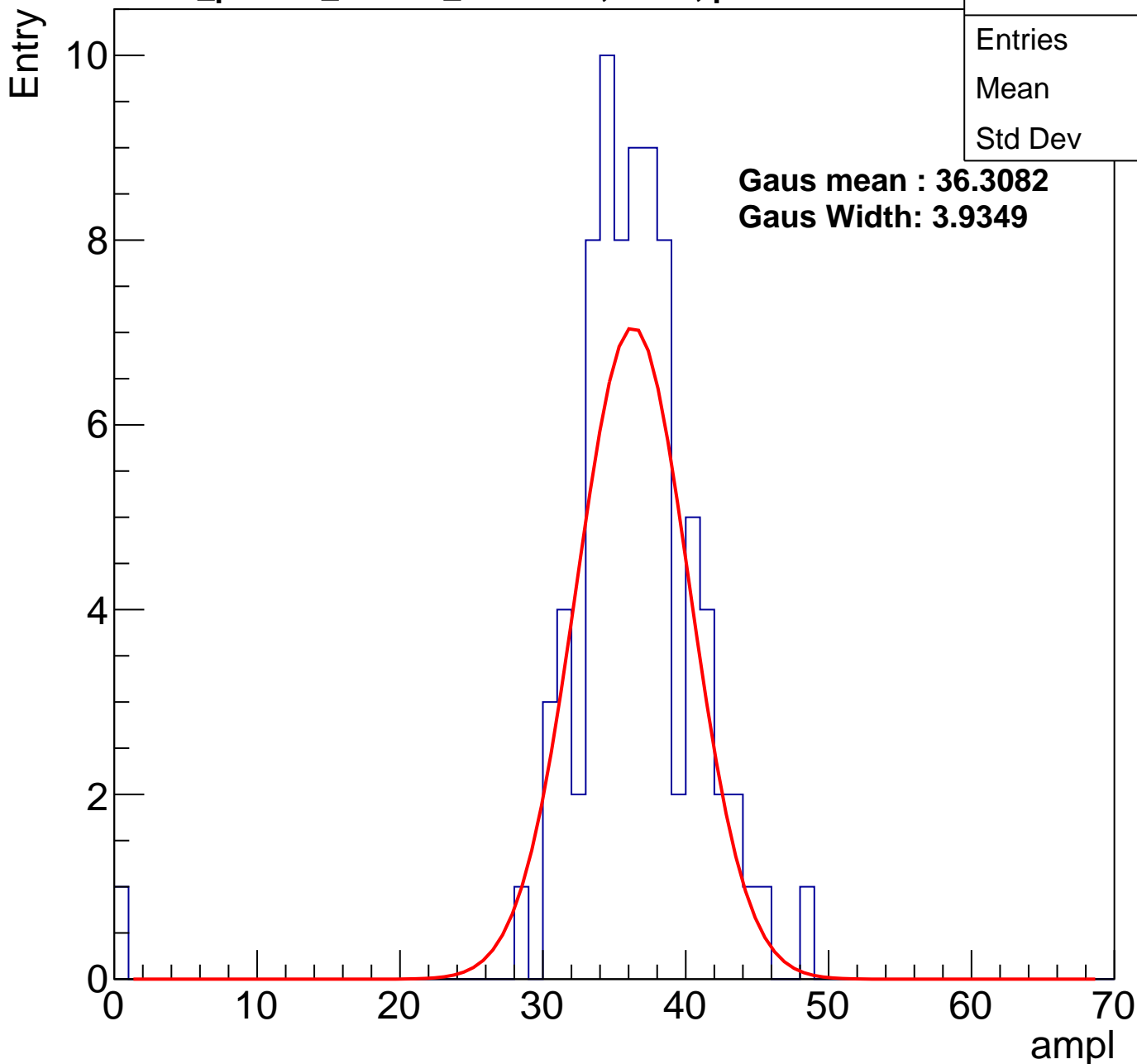
B1L101S, U4-ch107, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	35.8
Std Dev	5.46

Gaus mean : 36.3082

Gaus Width: 3.9349



B1L101S, U4-ch107, adc2

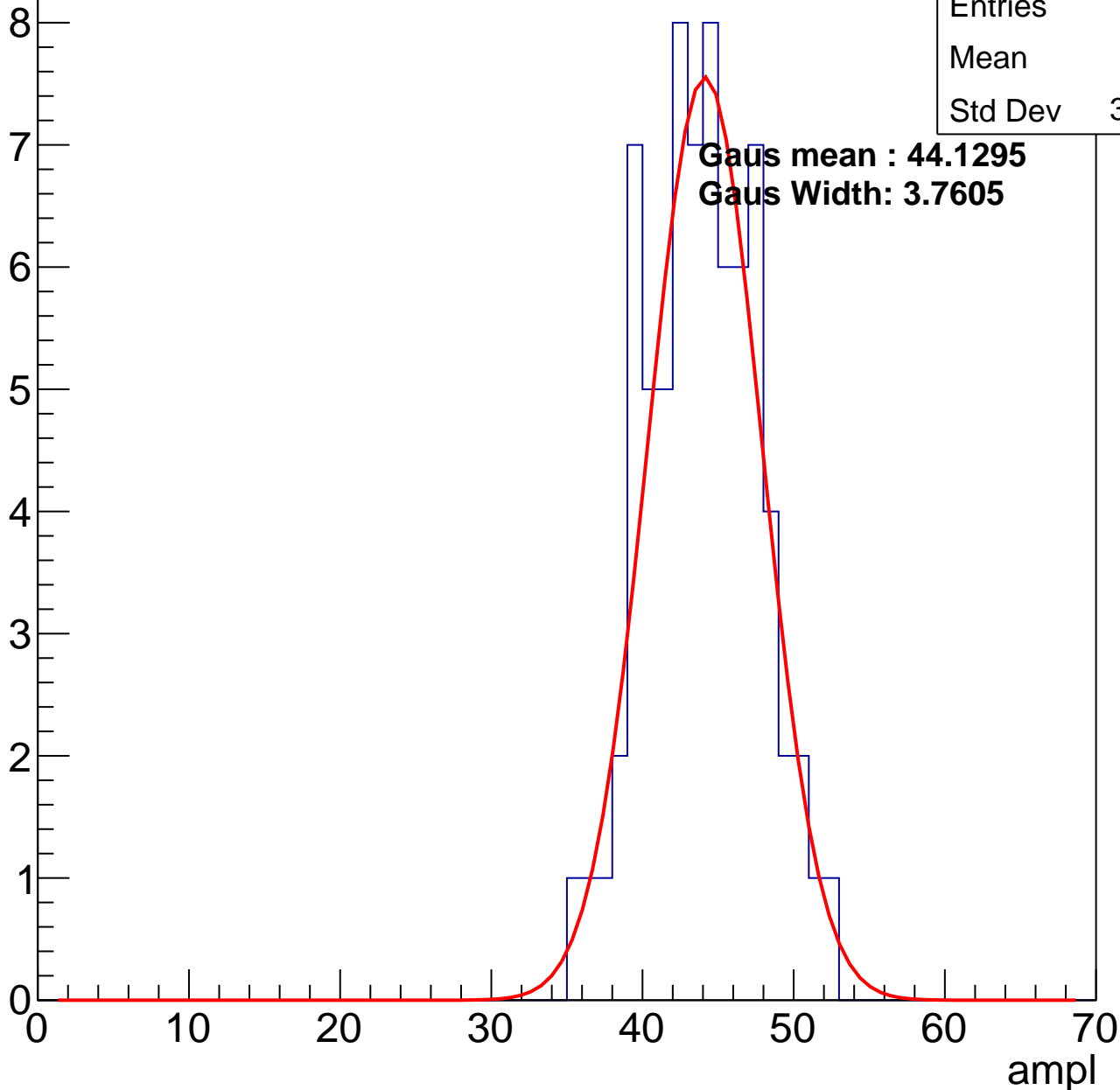
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.5
Std Dev	3.636

Gaus mean : 44.1295

Gaus Width: 3.7605

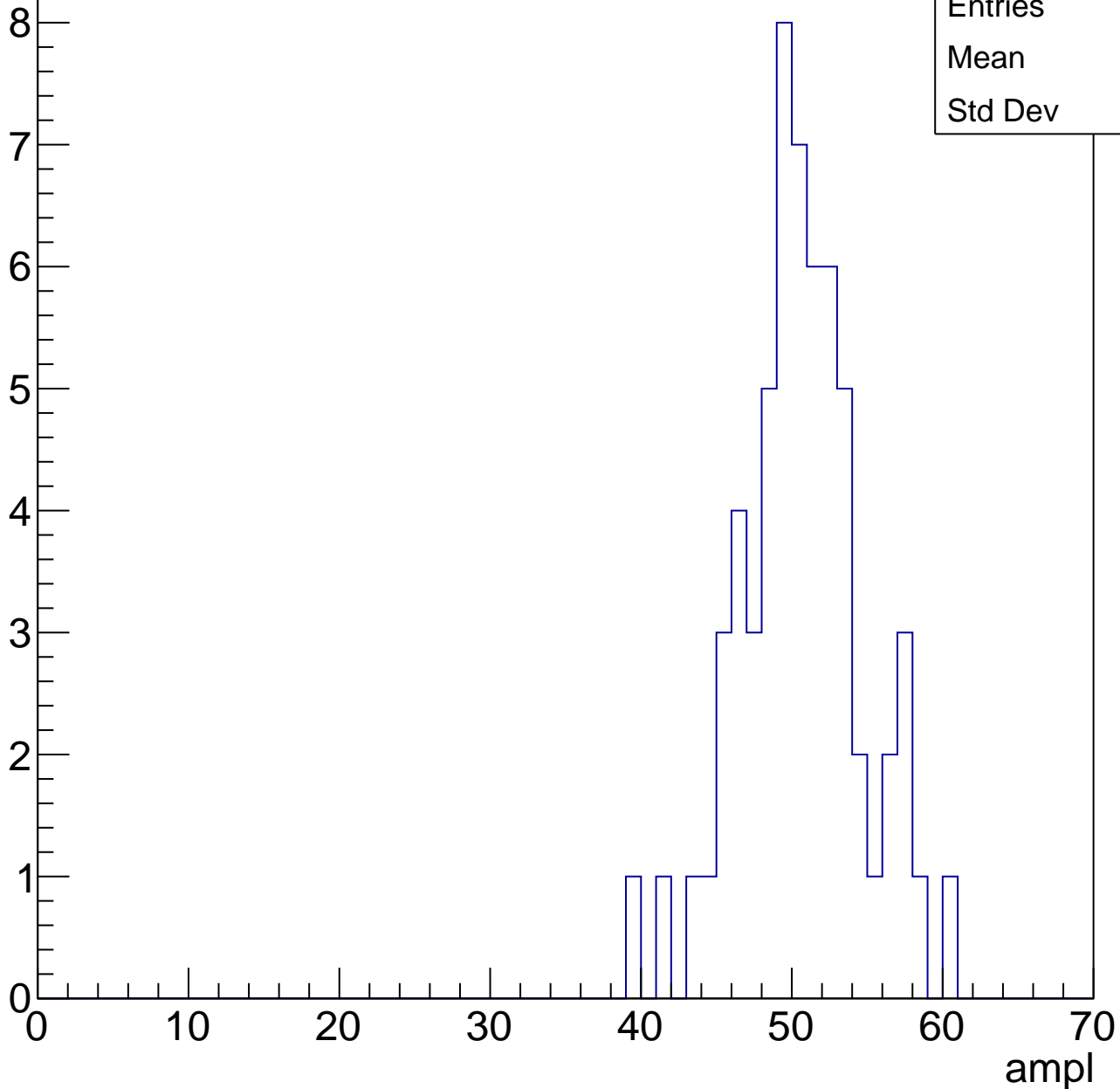


B1L101S, U4-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	50.1
Std Dev	4.04

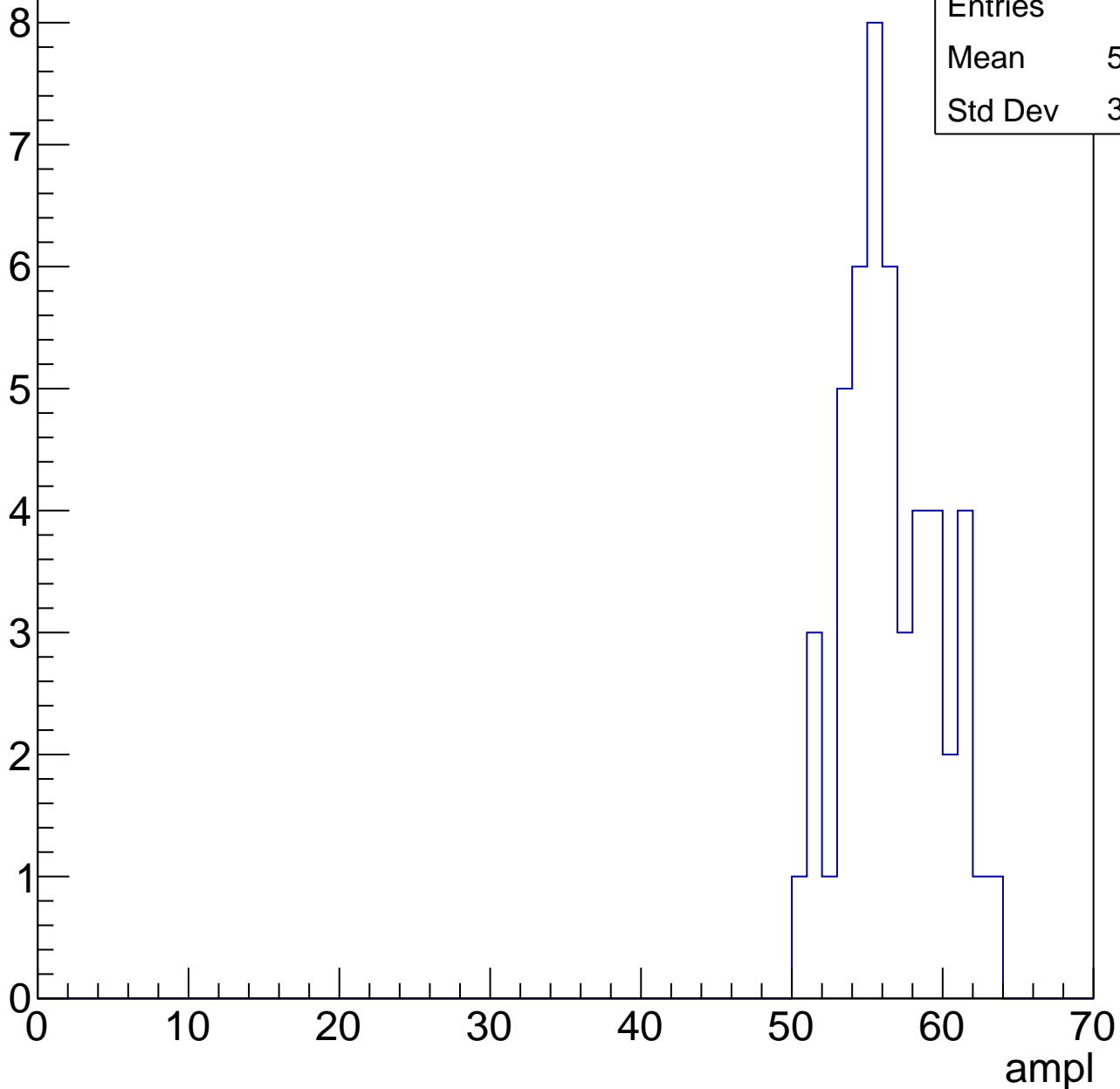


B1L101S, U4-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.08
Std Dev	3.116

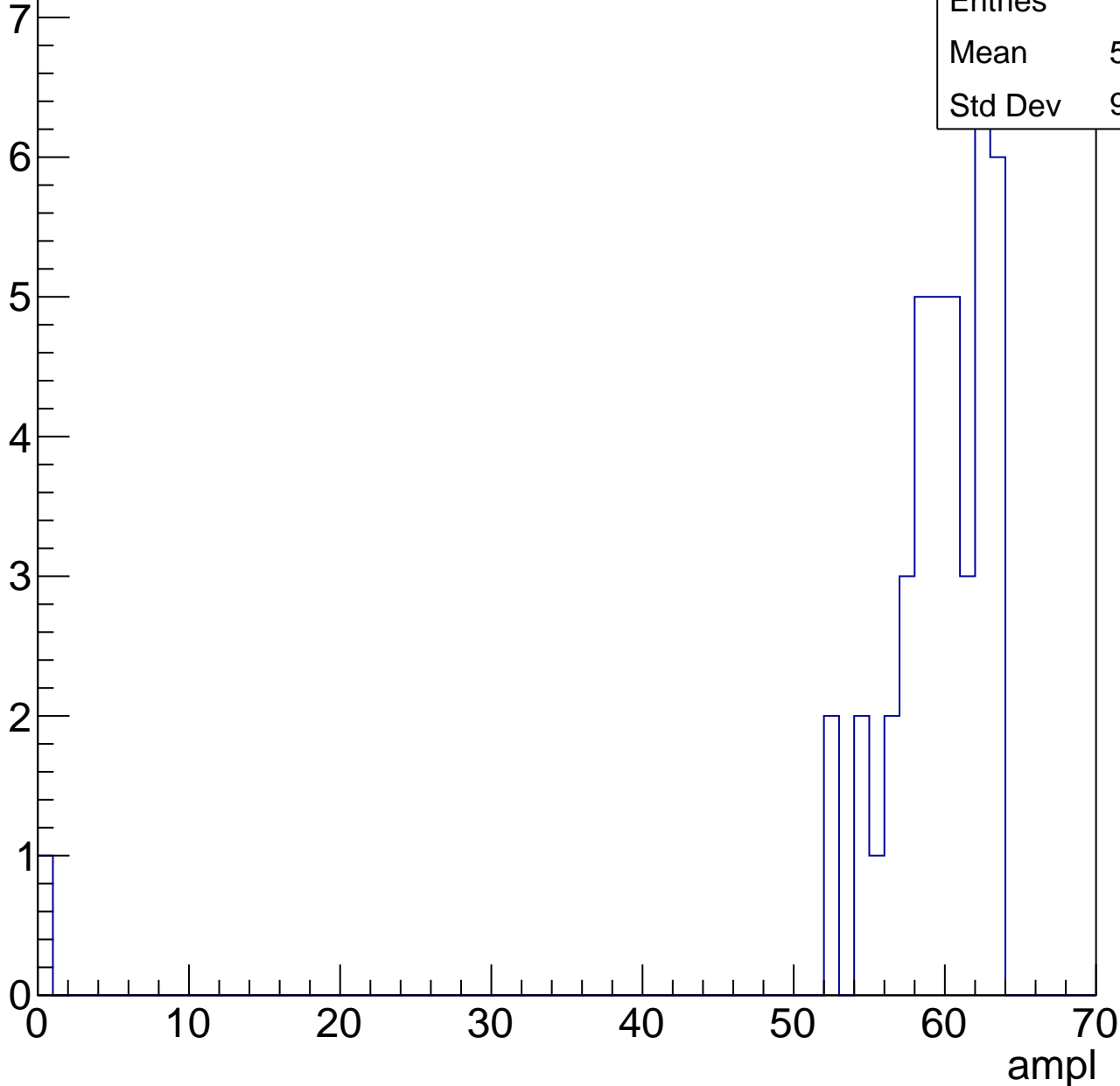


B1L101S, U4-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

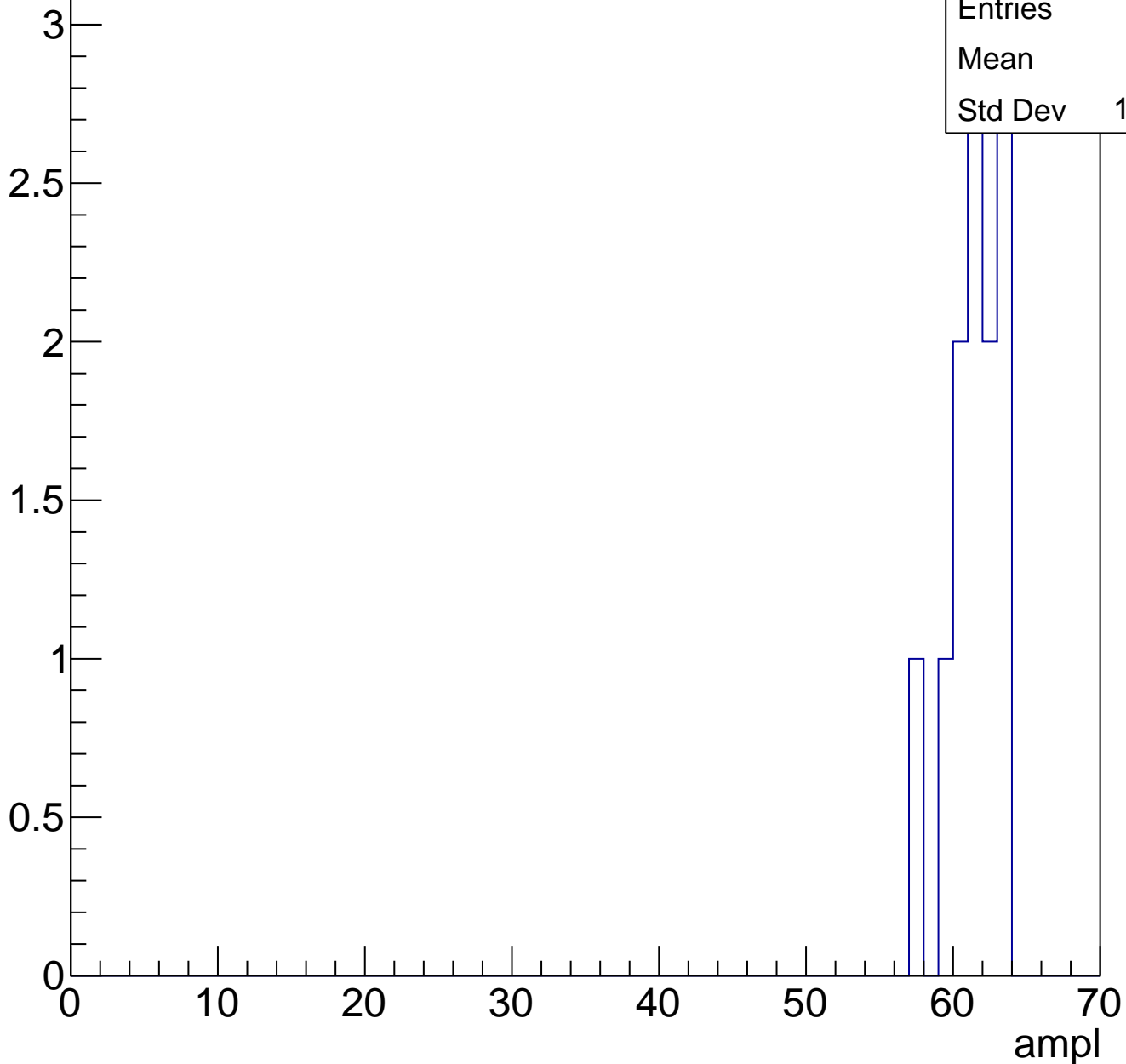
Entries	42
Mean	57.86
Std Dev	9.513



B1L101S, U4-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch108, adc0

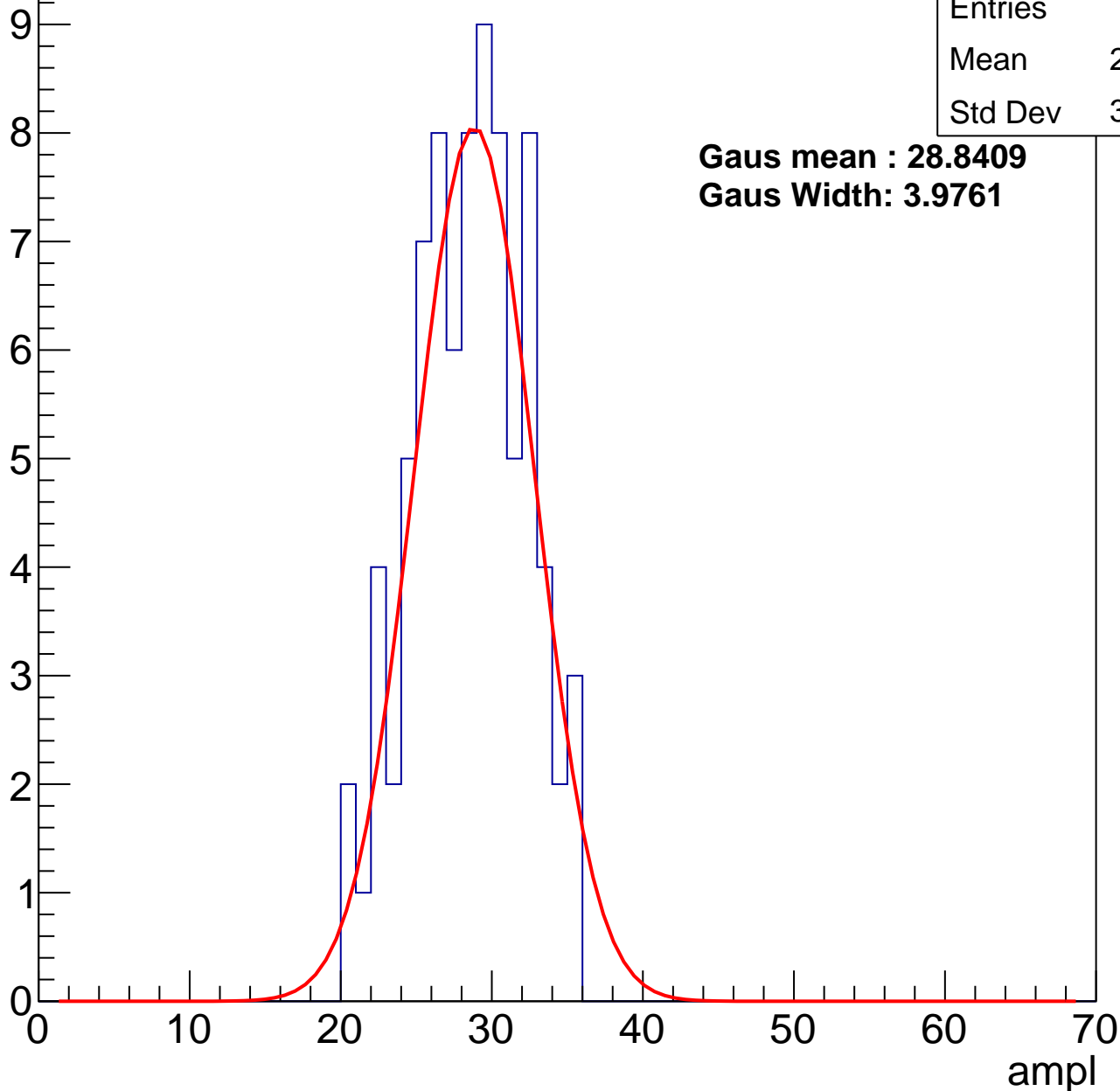
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	28.06
Std Dev	3.637

Gaus mean : 28.8409

Gaus Width: 3.9761



B1L101S, U4-ch108, adc1

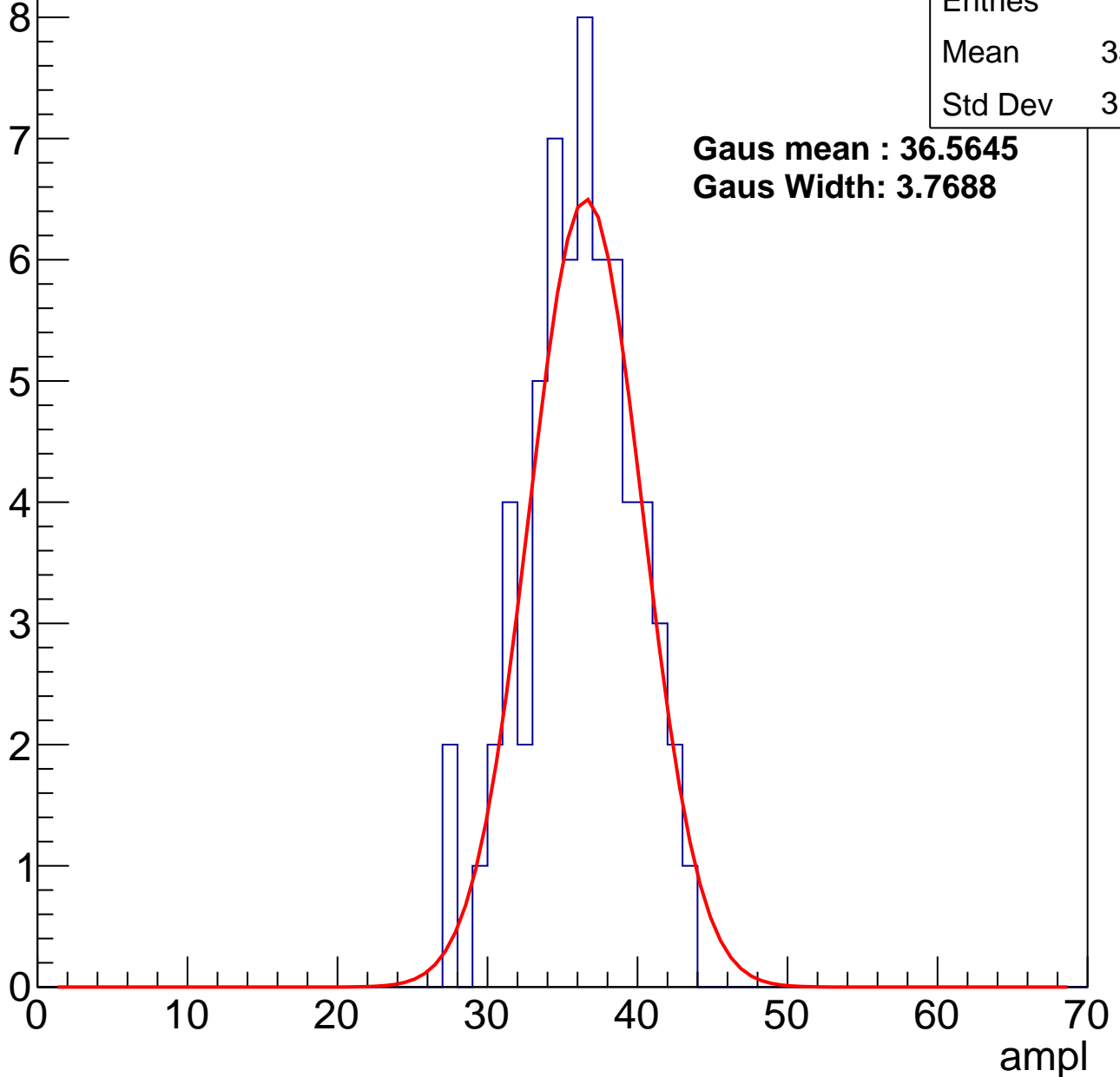
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	35.68
Std Dev	3.594

Gaus mean : 36.5645

Gaus Width: 3.7688



B1L101S, U4-ch108, adc2

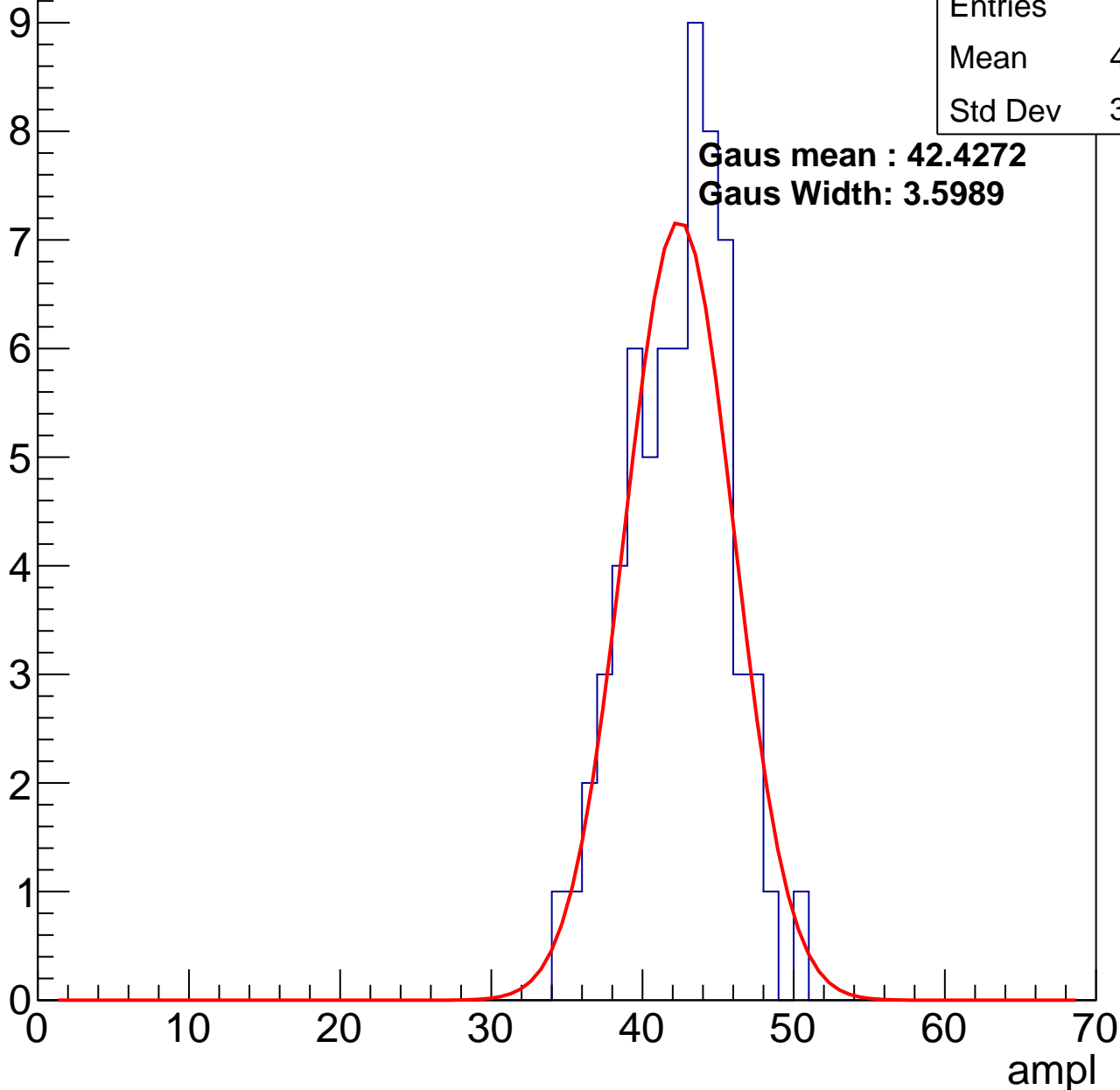
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.92
Std Dev	3.327

Gaus mean : 42.4272

Gaus Width: 3.5989

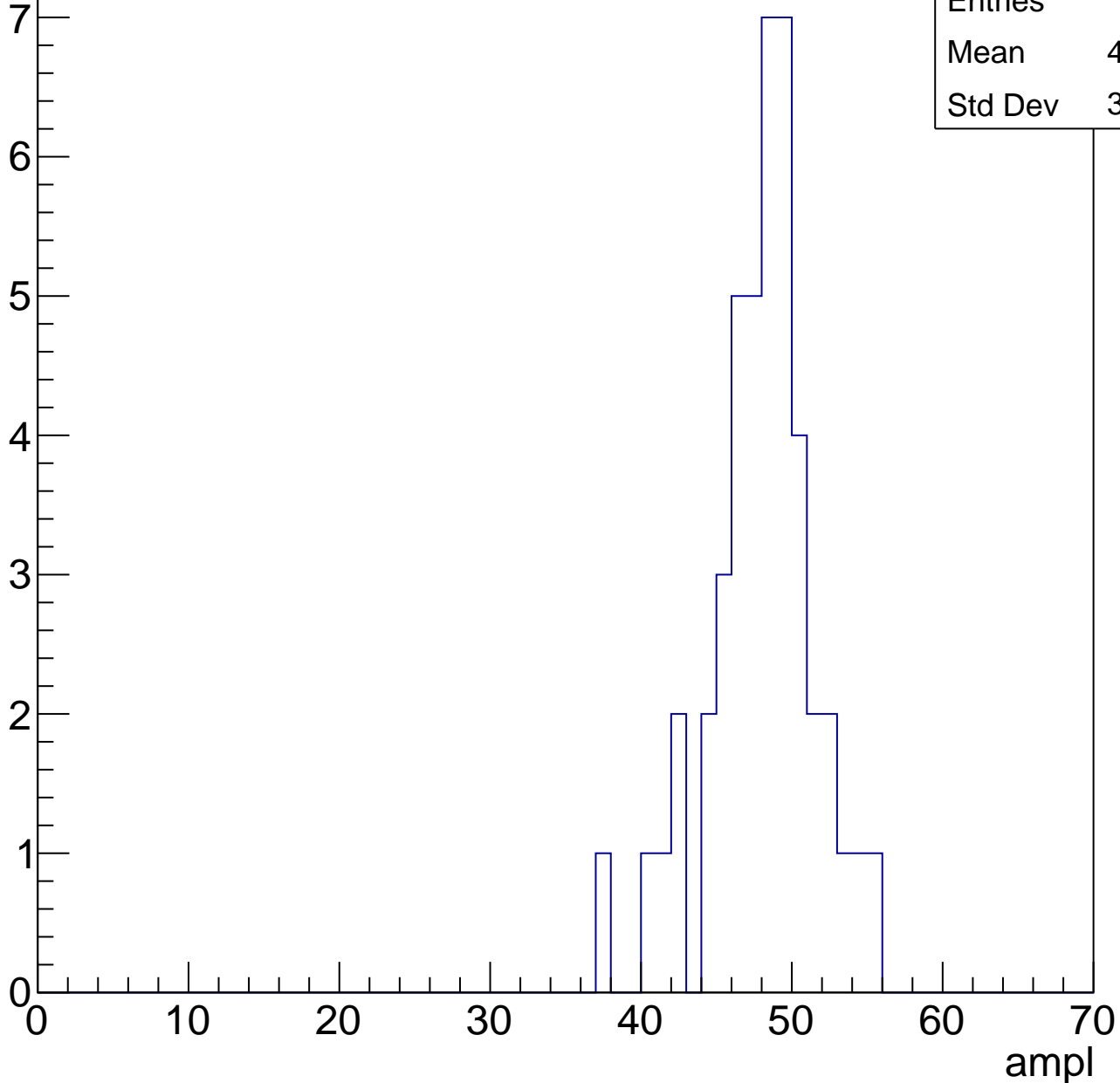


B1L101S, U4-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	47.49
Std Dev	3.532

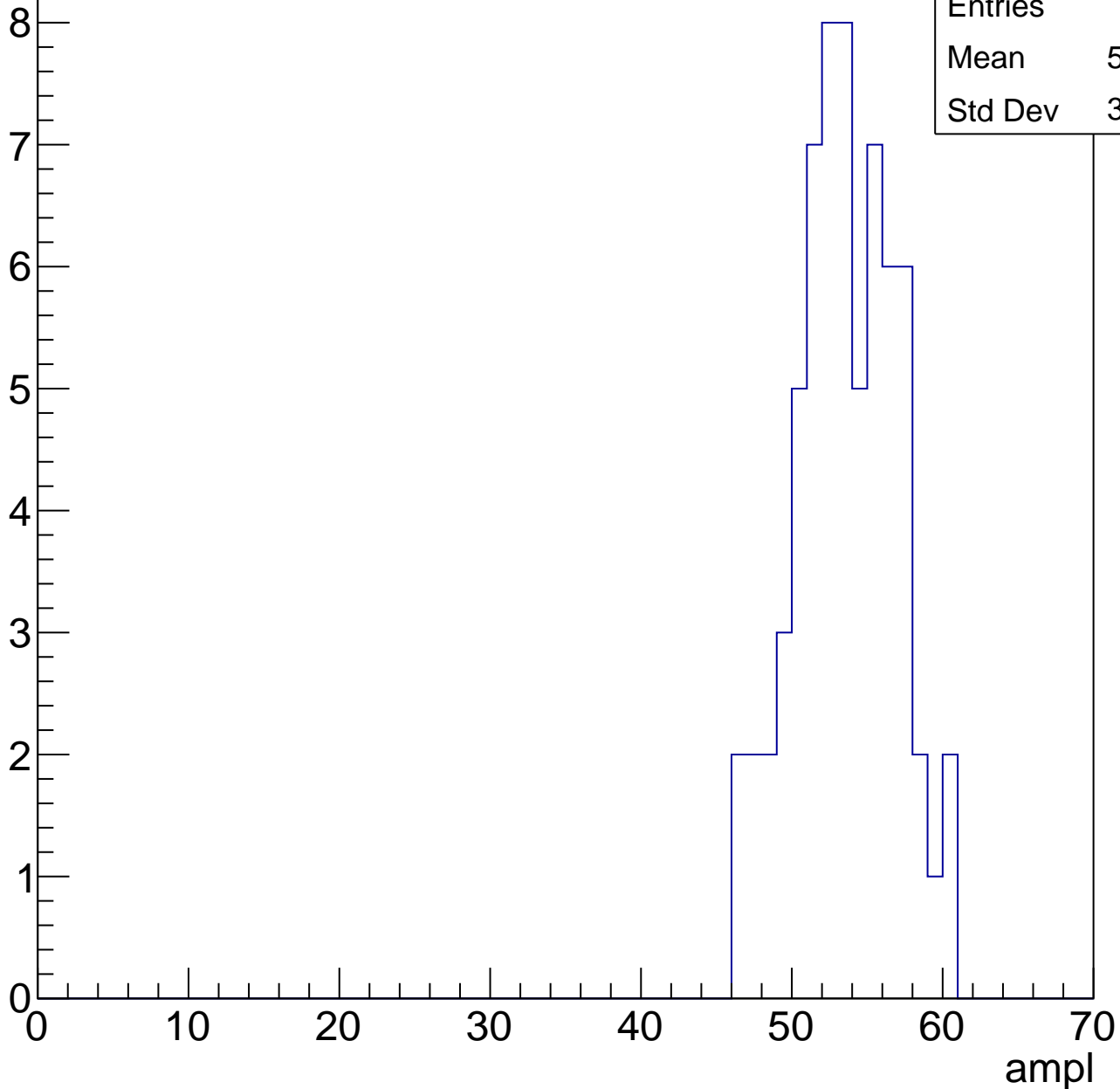


B1L101S, U4-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

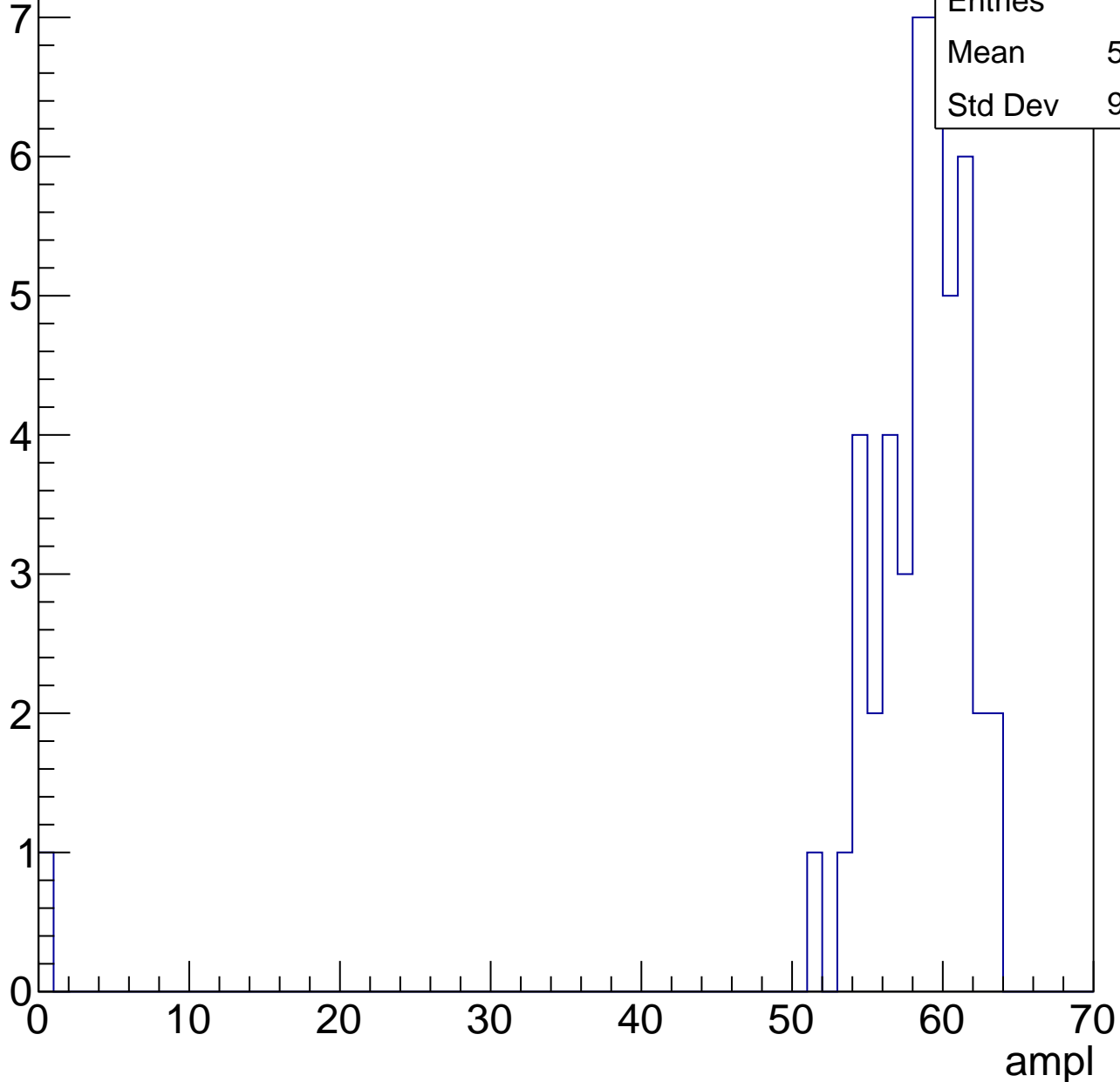
Entries	66
Mean	53.09
Std Dev	3.292



B1L101S, U4-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

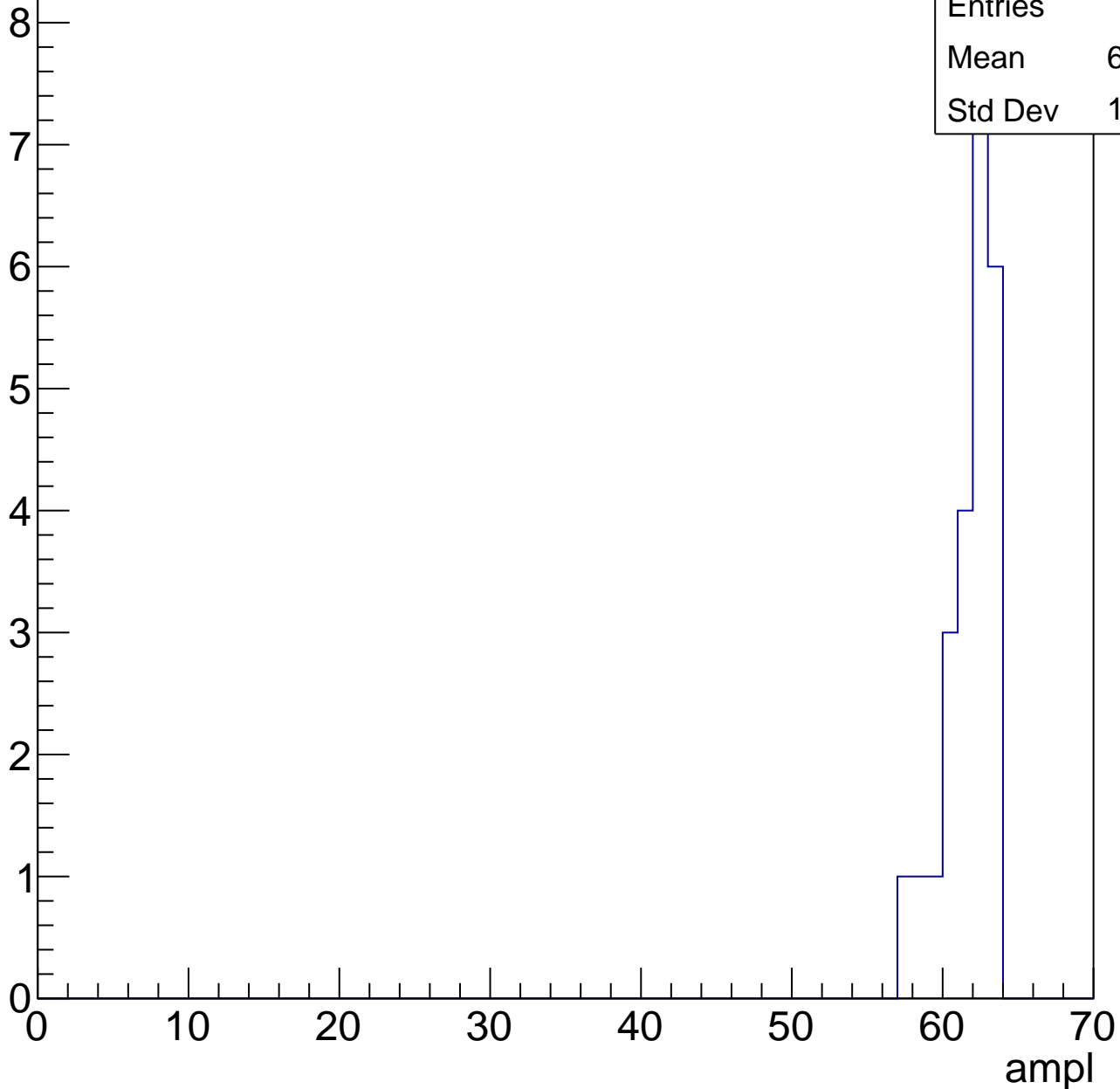


B1L101S, U4-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

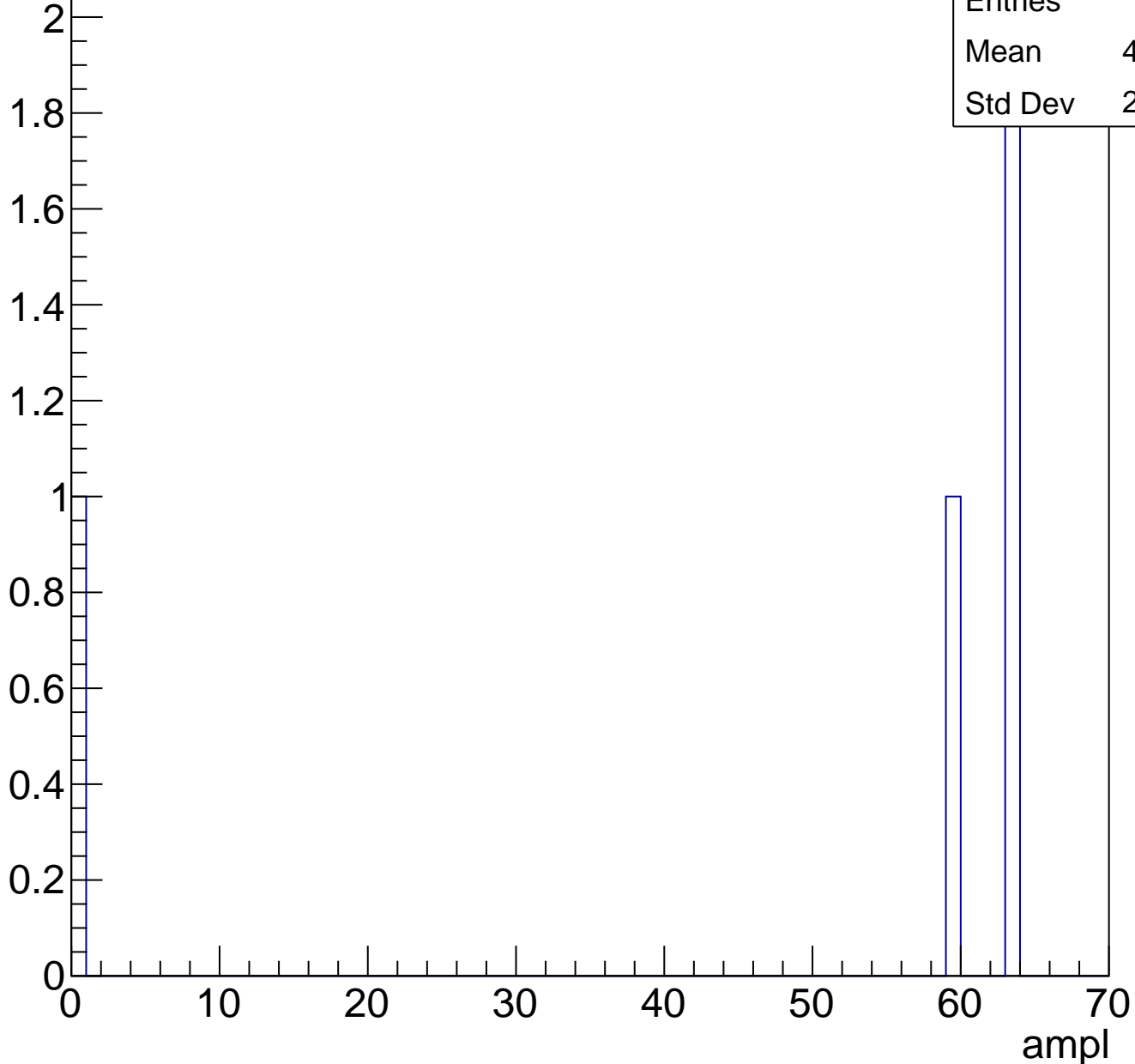
Entries	24
Mean	61.33
Std Dev	1.599



B1L101S, U4-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch109, adc0

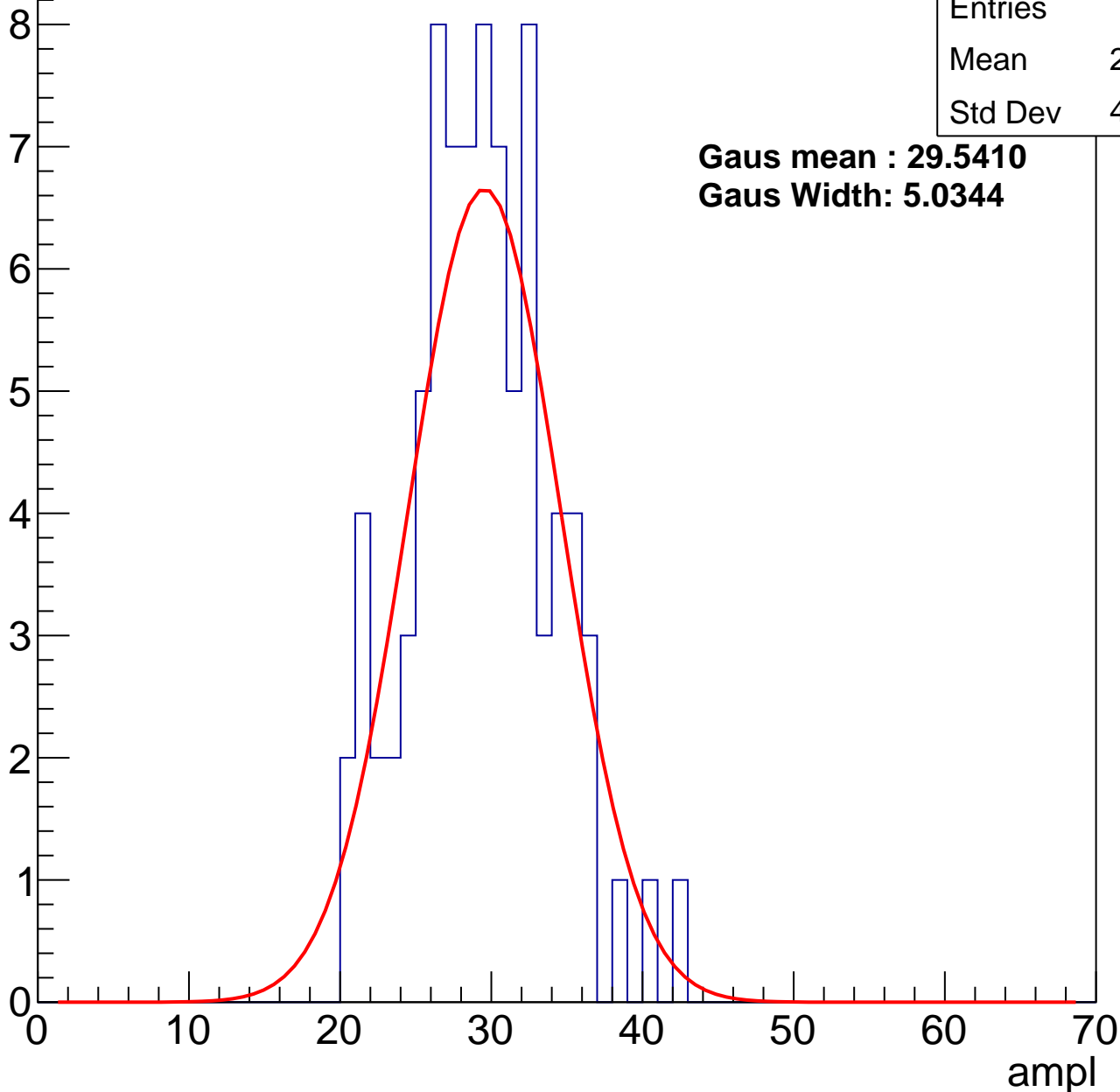
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	28.94
Std Dev	4.549

Gaus mean : 29.5410

Gaus Width: 5.0344



B1L101S, U4-ch109, adc1

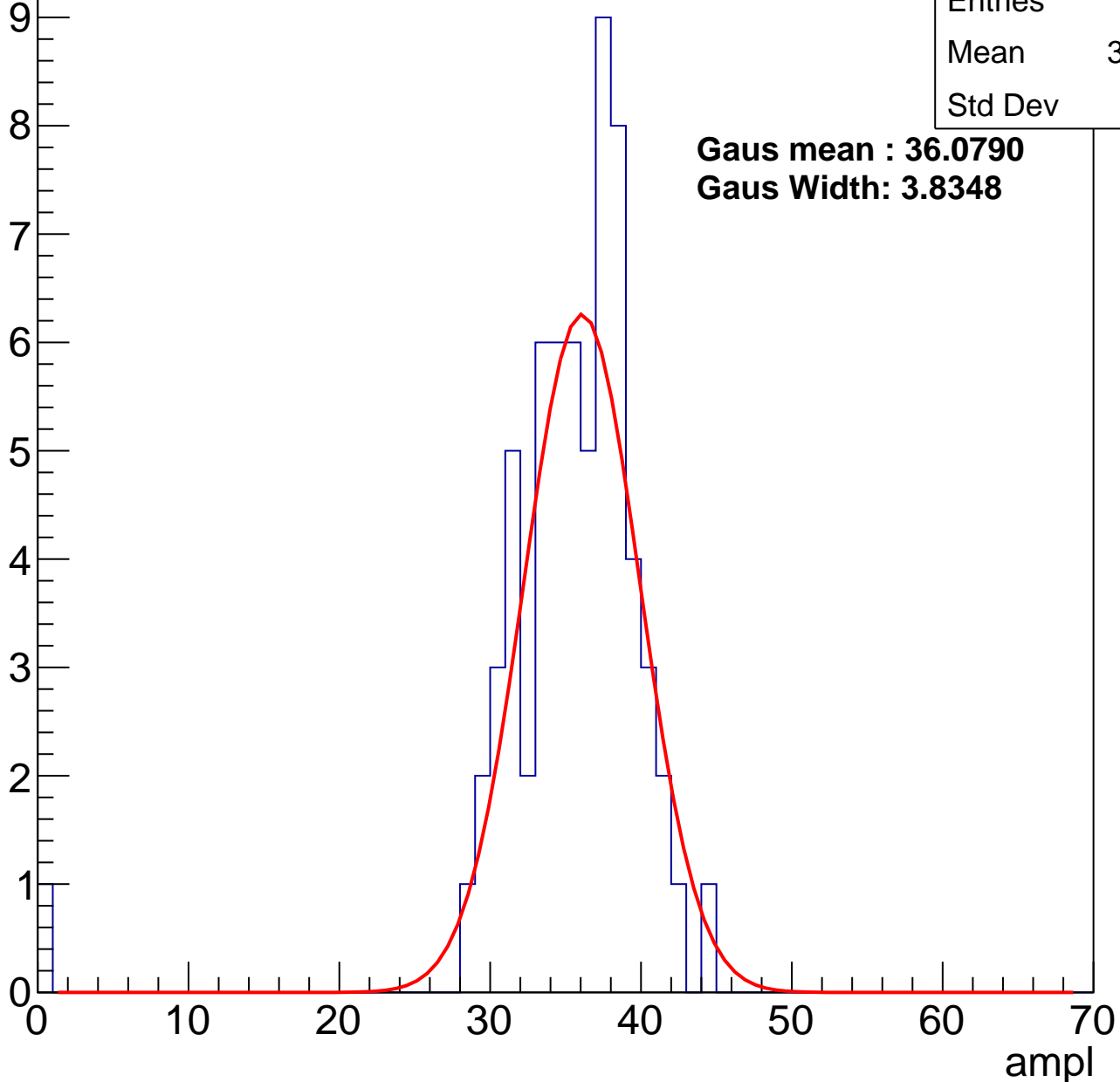
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	34.89
Std Dev	5.55

Gaus mean : 36.0790

Gaus Width: 3.8348



B1L101S, U4-ch109, adc2

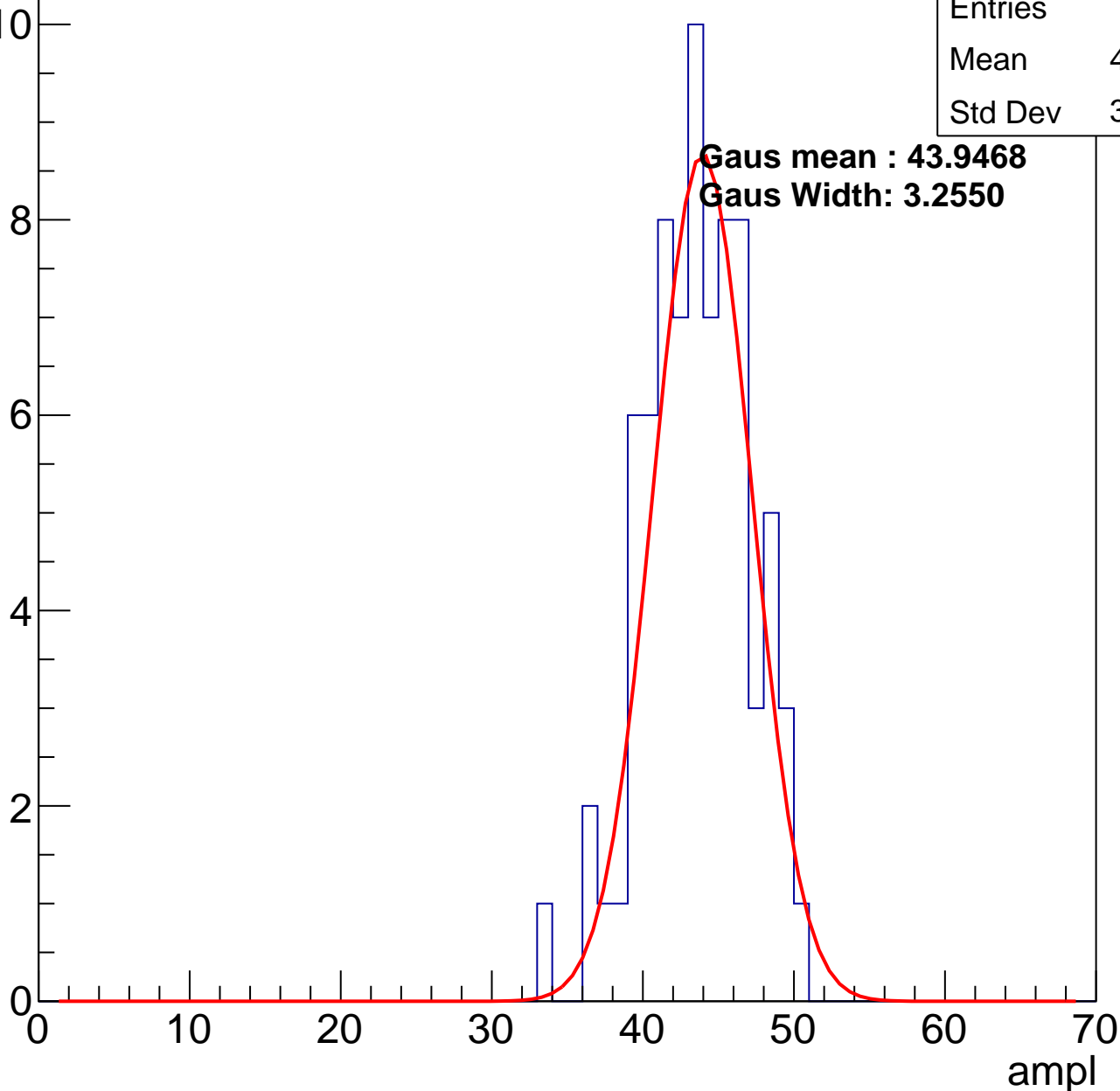
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.12
Std Dev	3.396

Gaus mean : 43.9468

Gaus Width: 3.2550

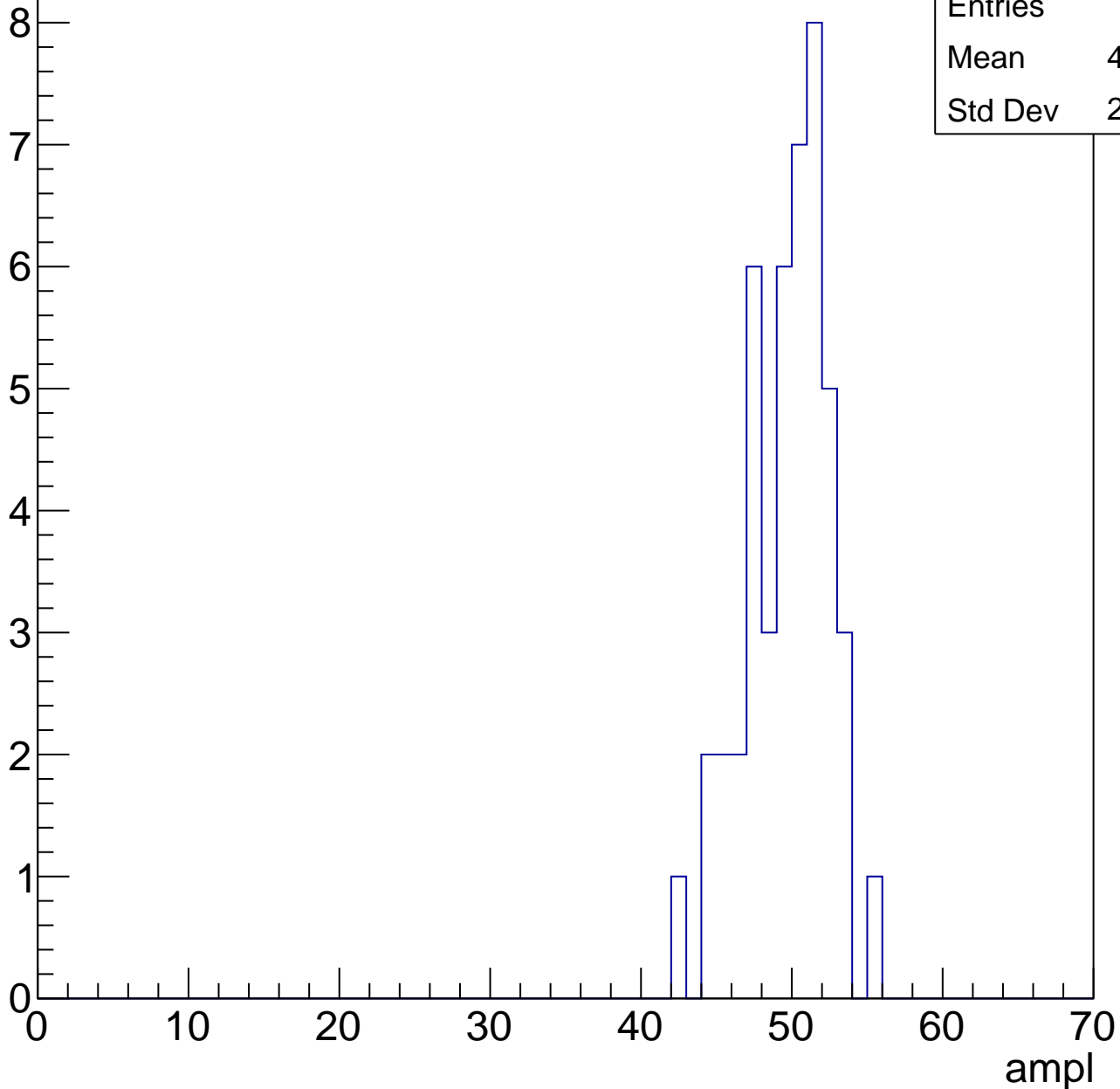


B1L101S, U4-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	49.22
Std Dev	2.726

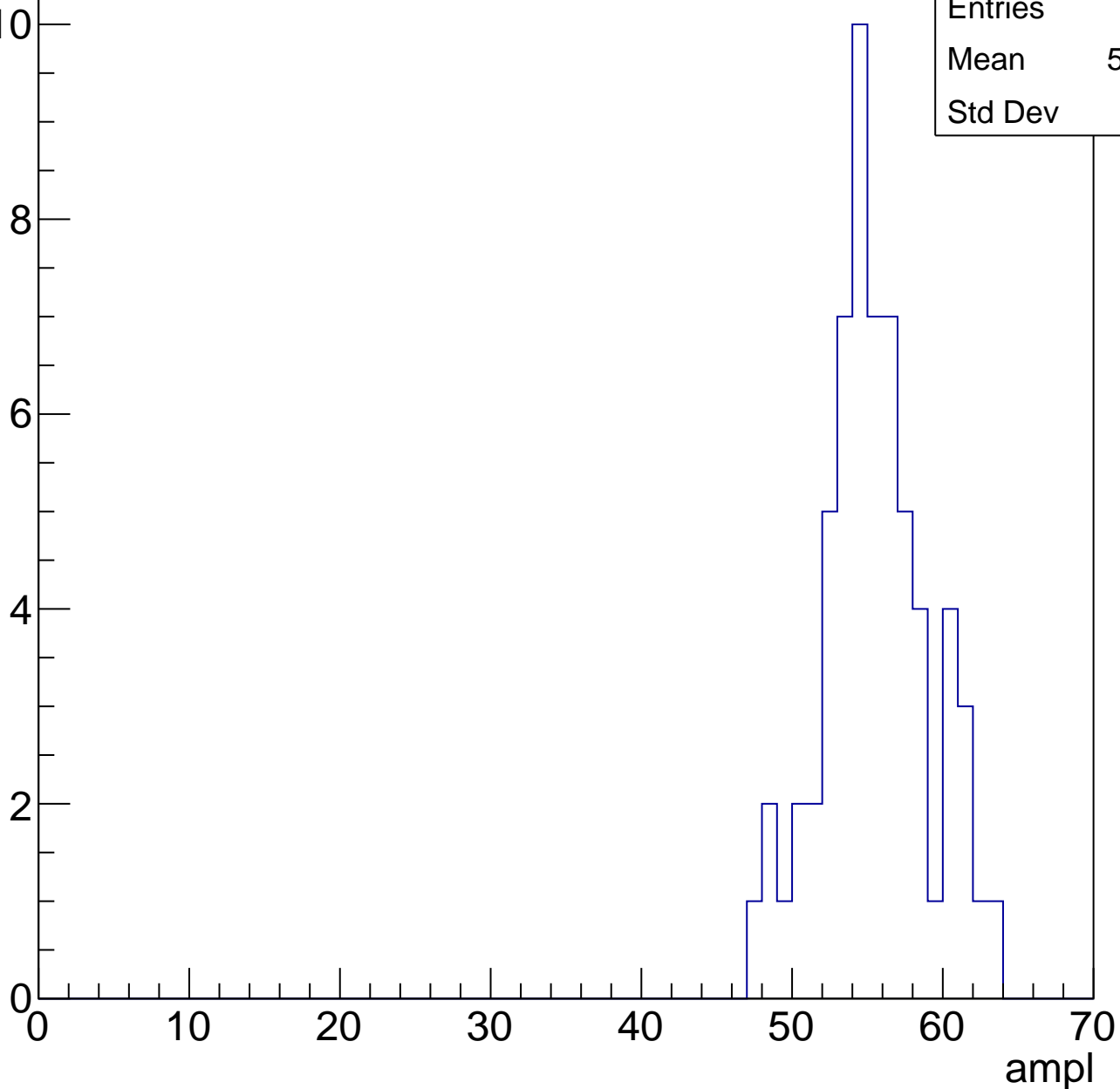


B1L101S, U4-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

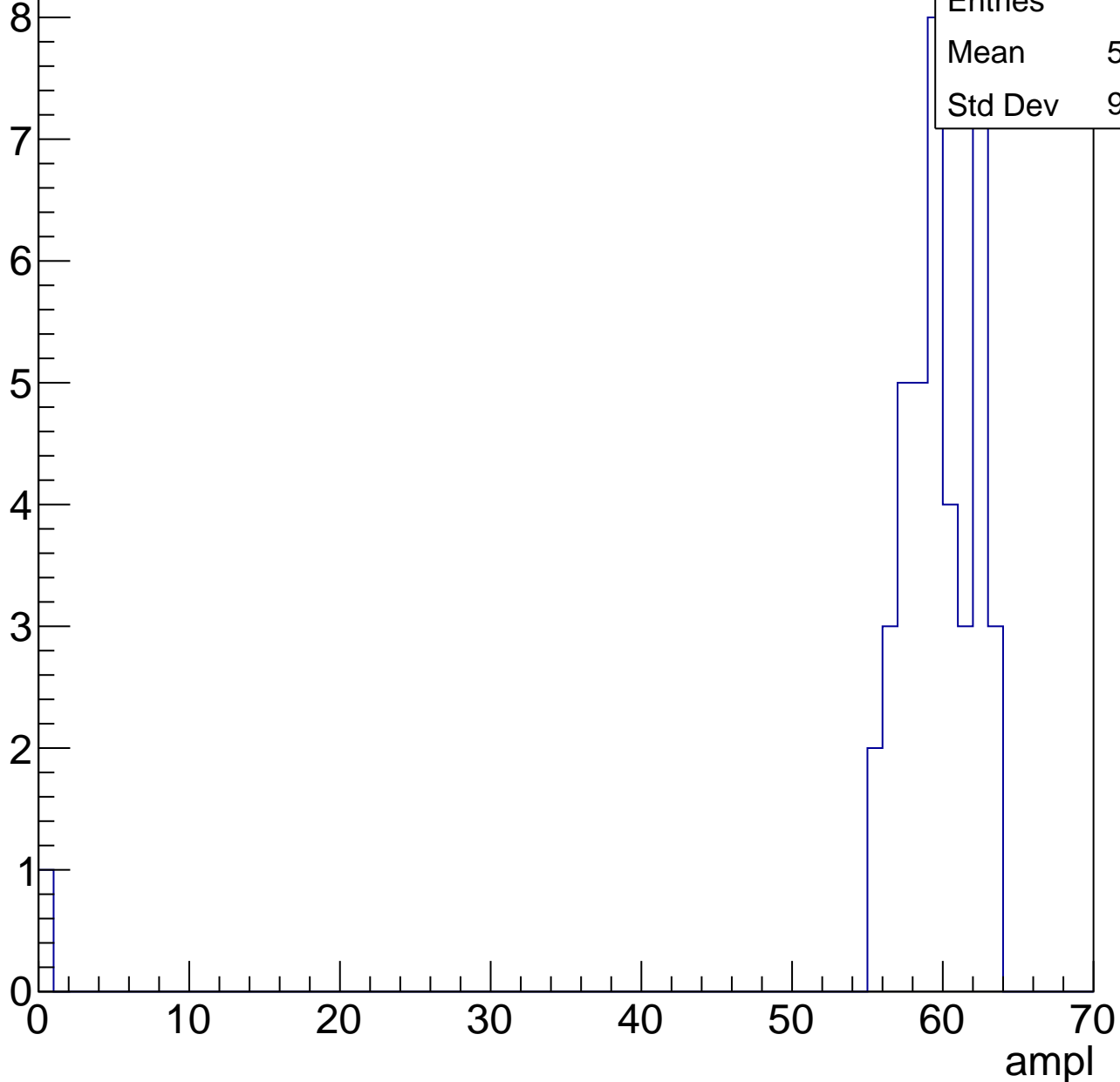
Entries	63
Mean	55.02
Std Dev	3.48



B1L101S, U4-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

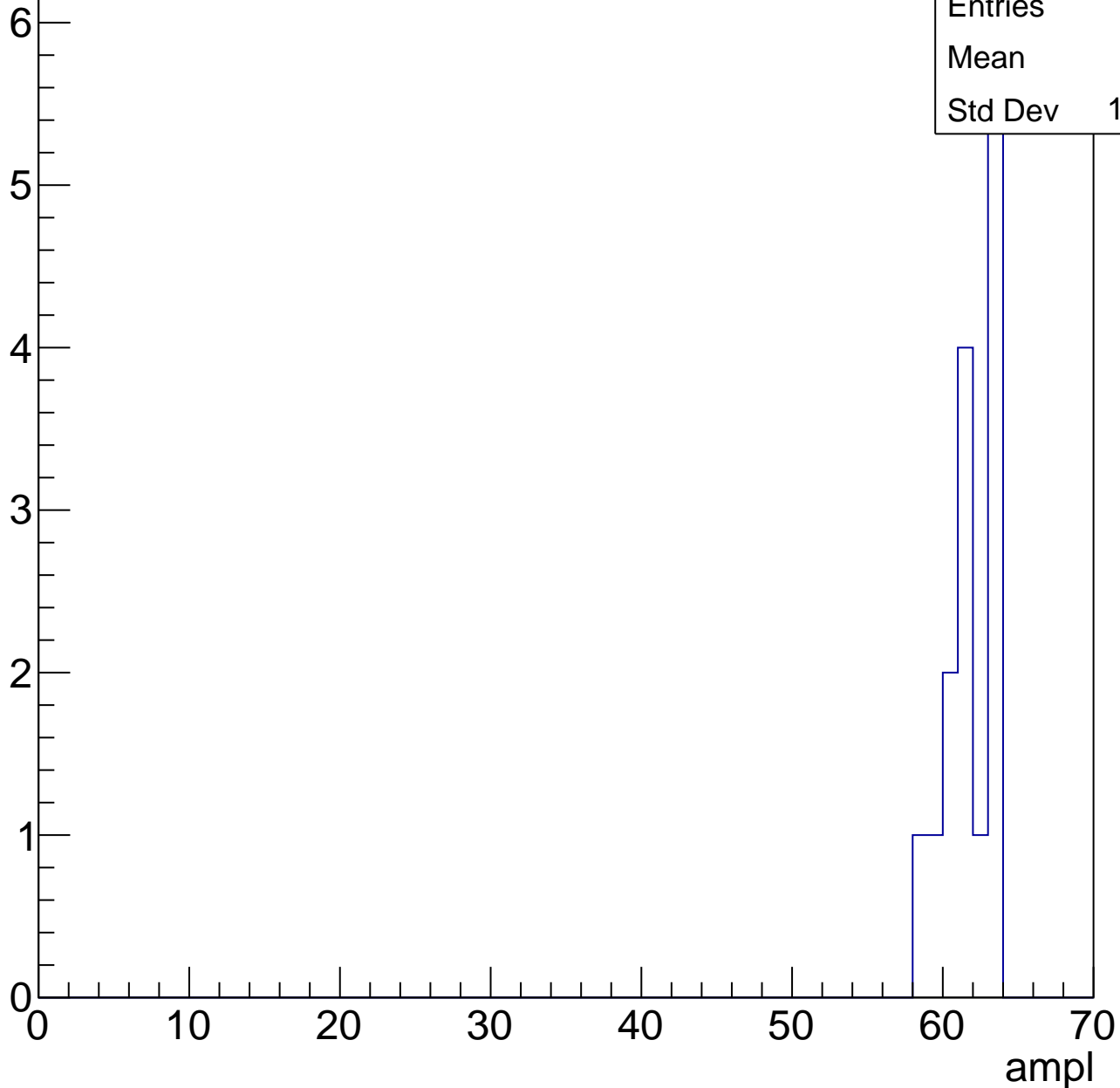
Entry



B1L101S, U4-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch110, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	27.97
Std Dev	6.003

Gaus mean : 28.7243

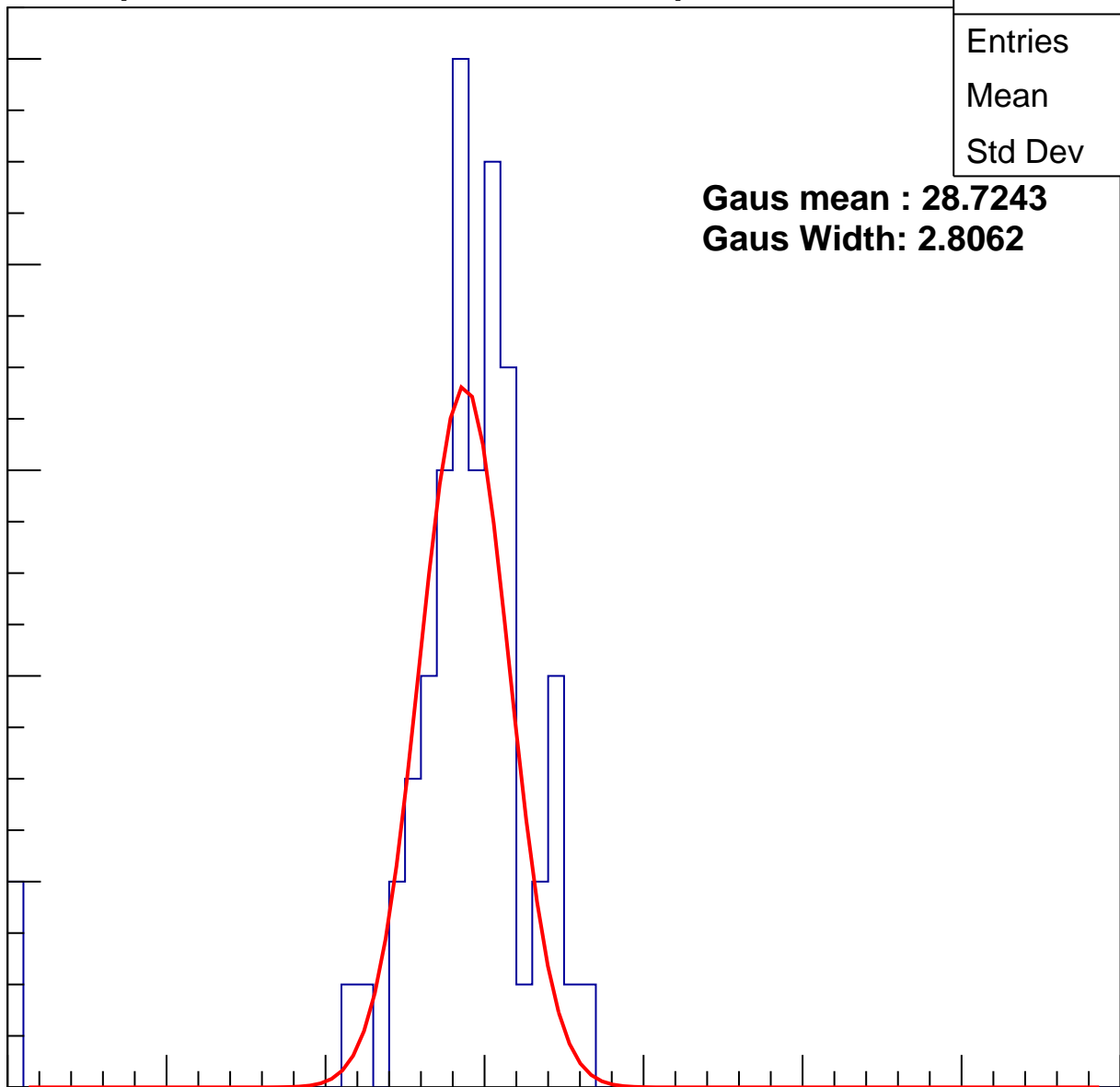
Gaus Width: 2.8062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch110, adc1

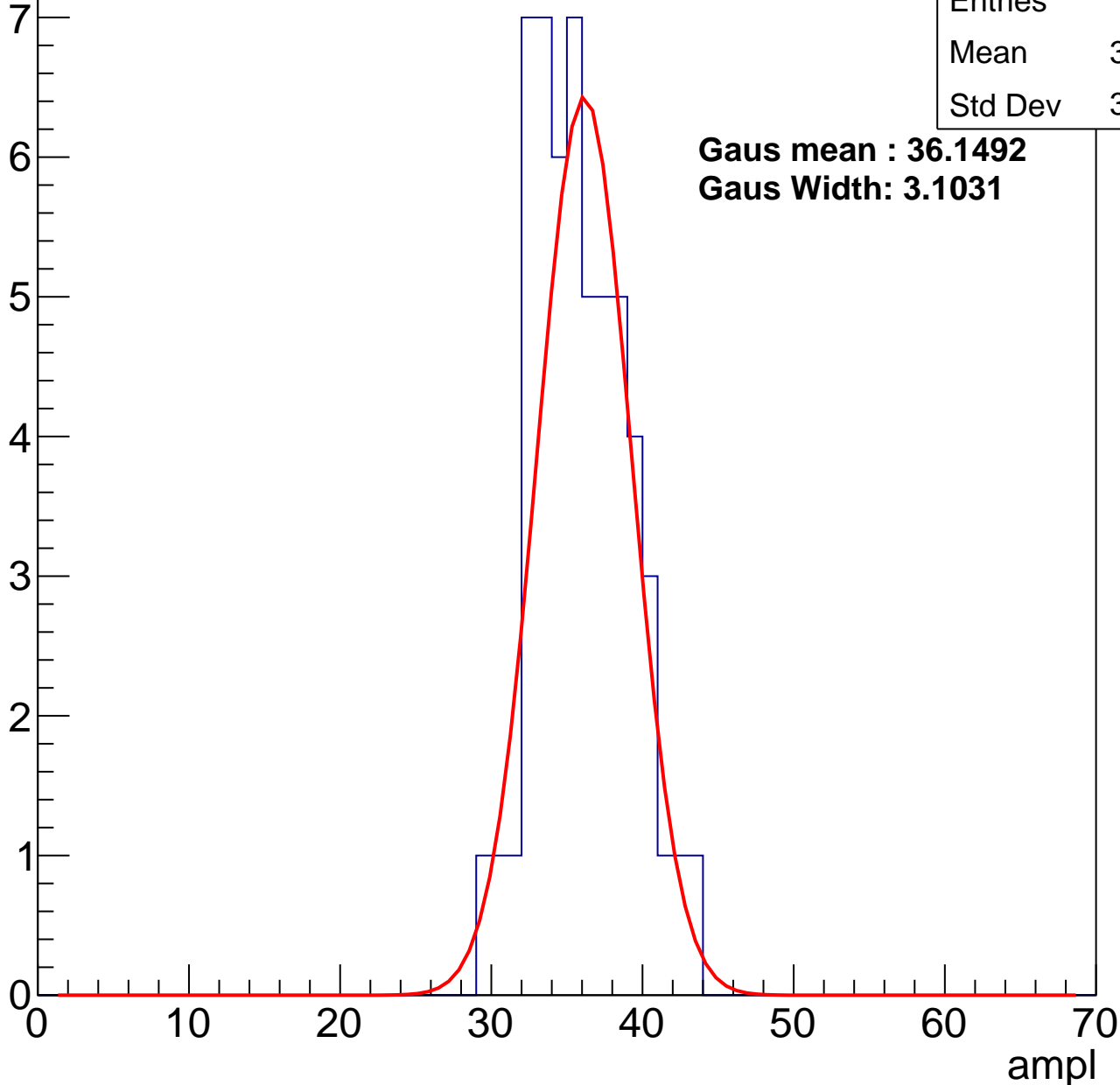
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	35.47
Std Dev	3.074

Gaus mean : 36.1492

Gaus Width: 3.1031



B1L101S, U4-ch110, adc2

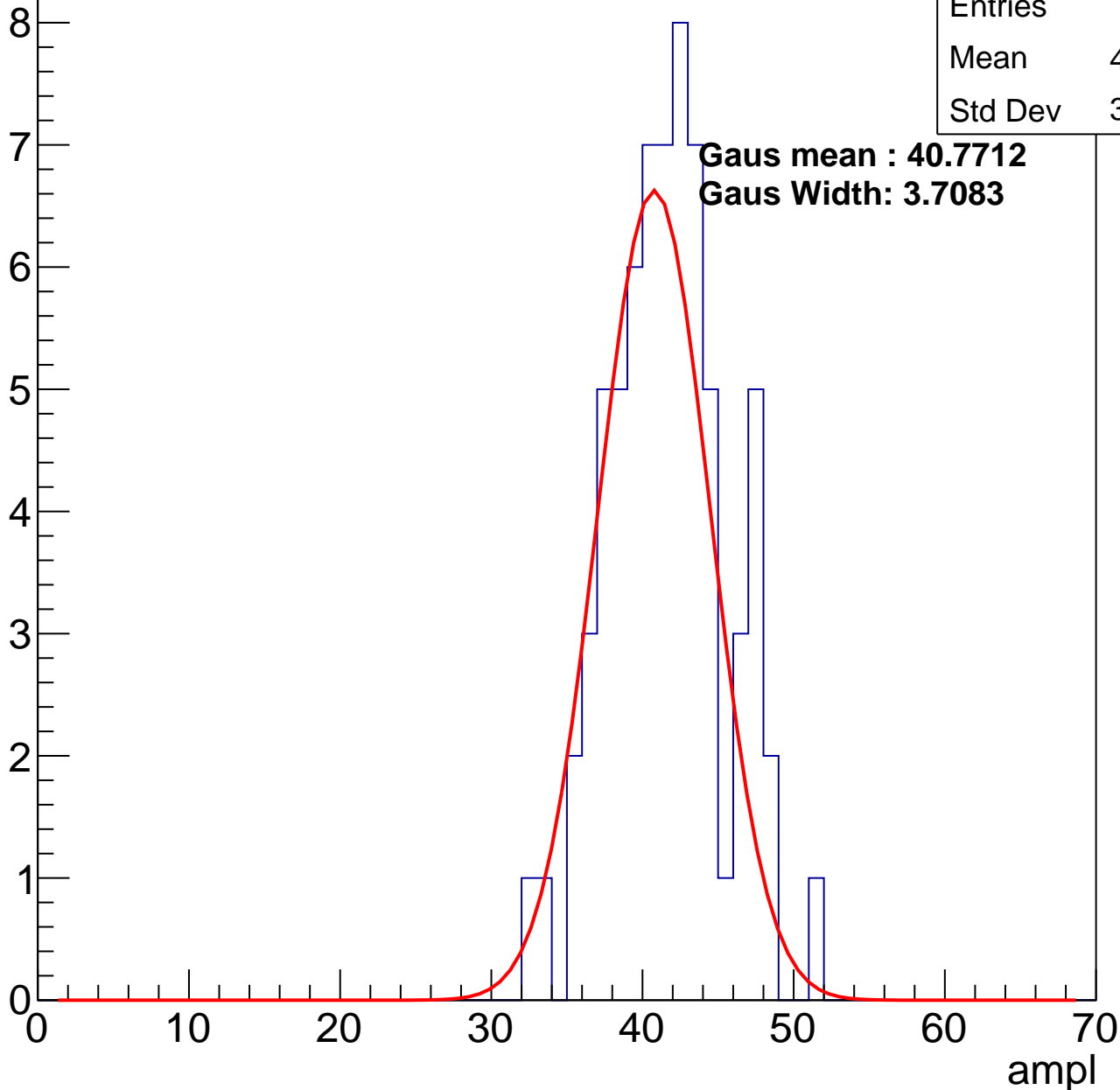
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	41.17
Std Dev	3.799

Gaus mean : 40.7712

Gaus Width: 3.7083

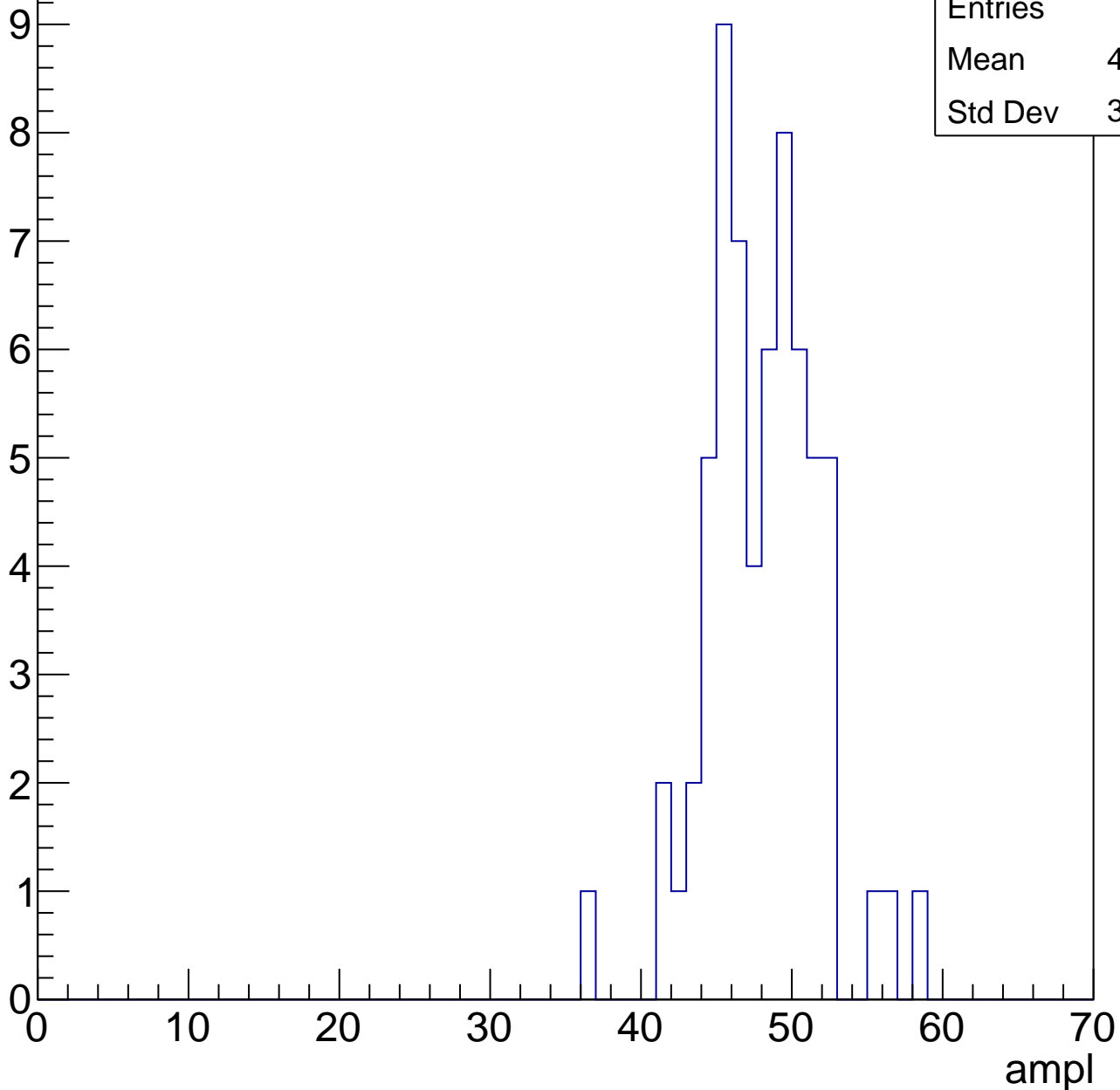


B1L101S, U4-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	47.58
Std Dev	3.707

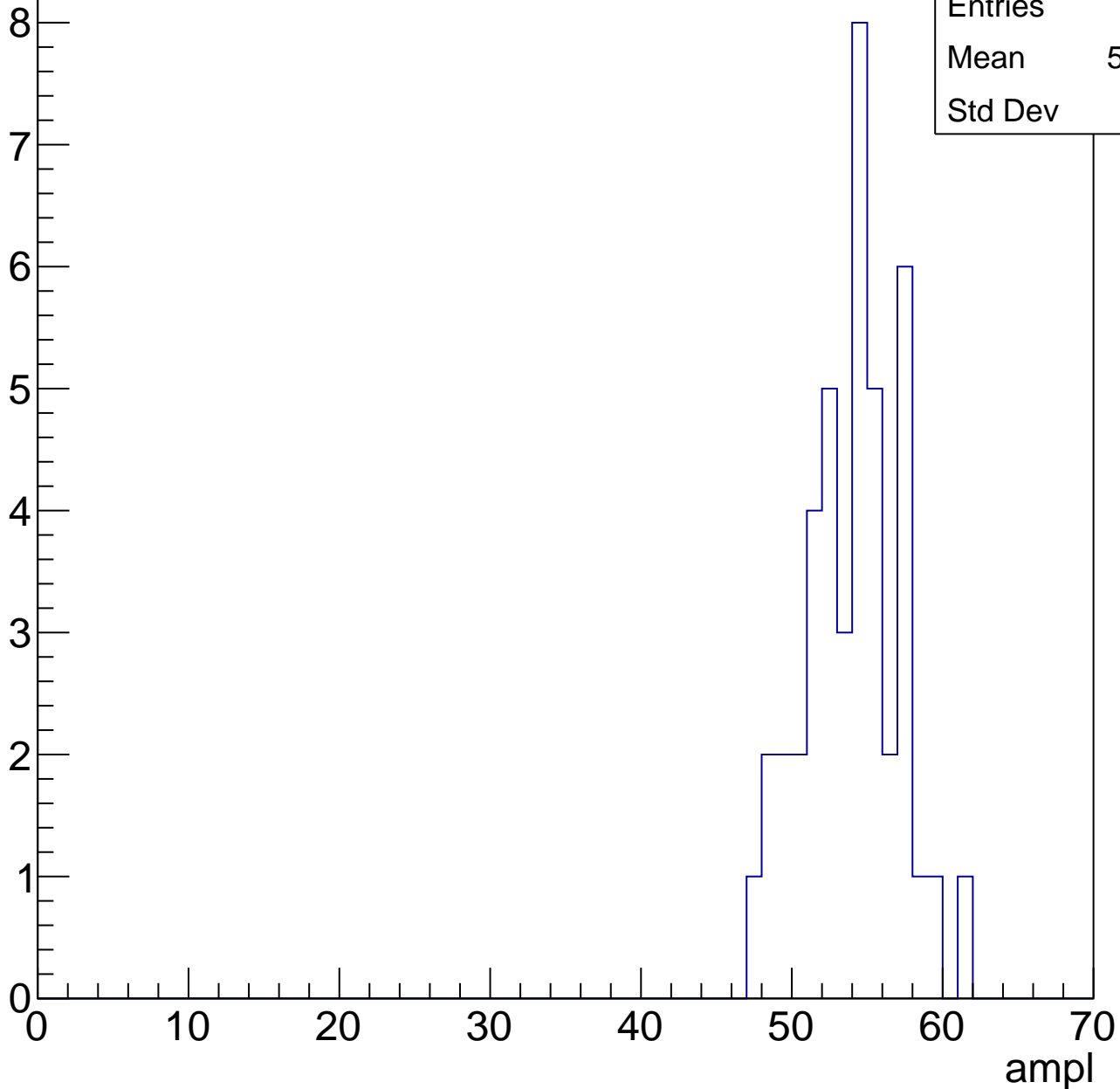


B1L101S, U4-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	53.56
Std Dev	3.09

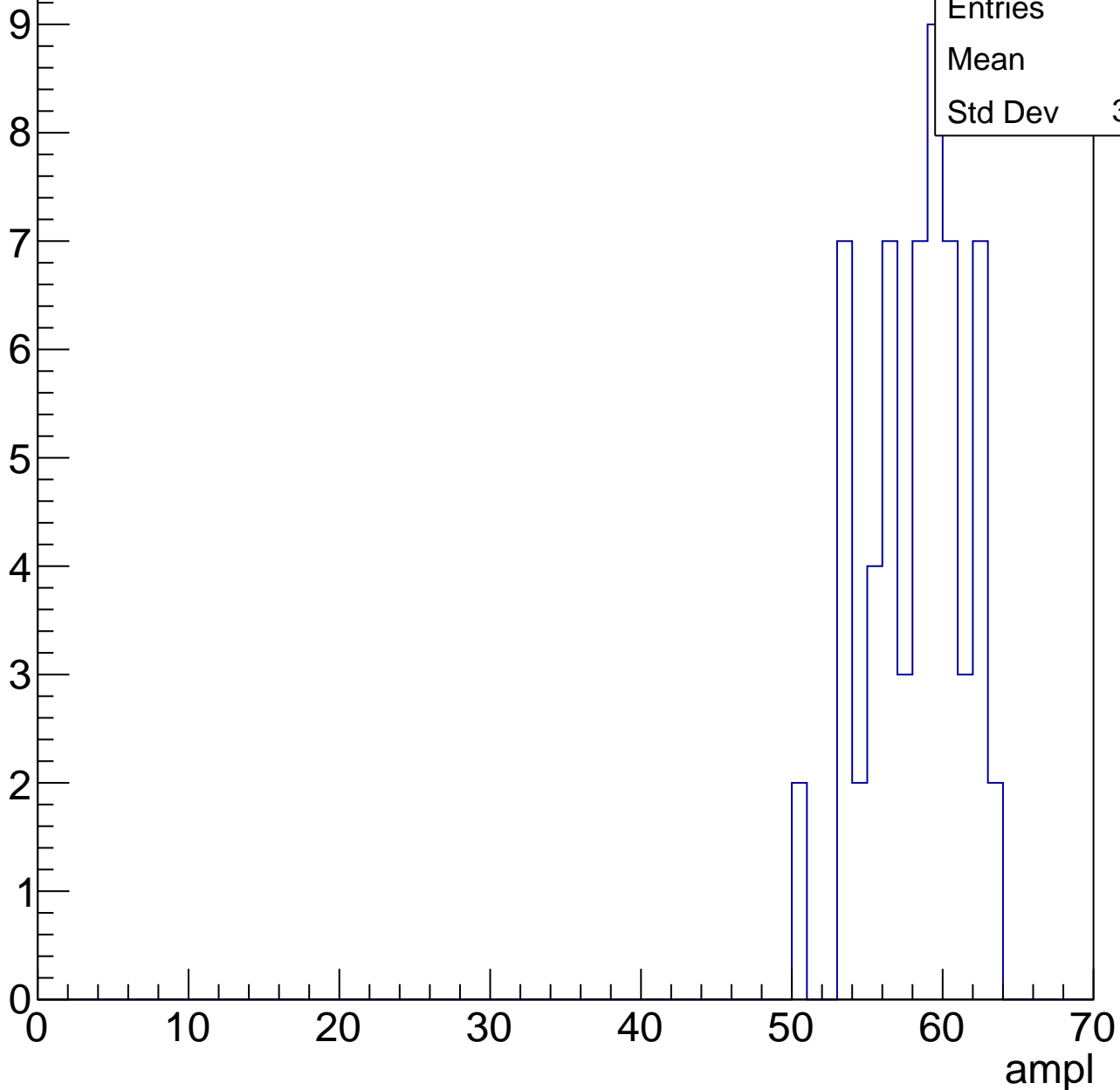


B1L101S, U4-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

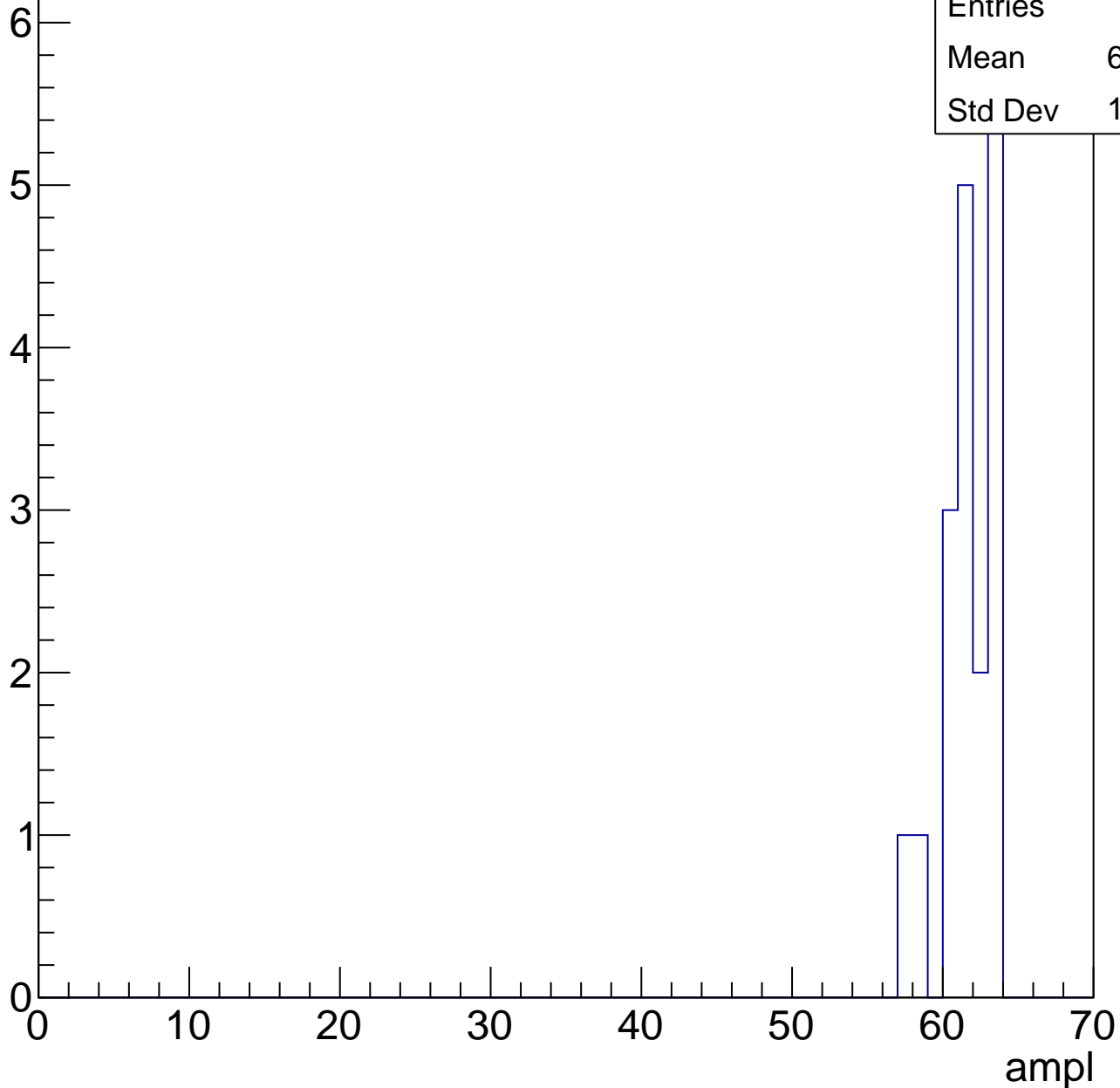
Entries	60
Mean	57.7
Std Dev	3.221



B1L101S, U4-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

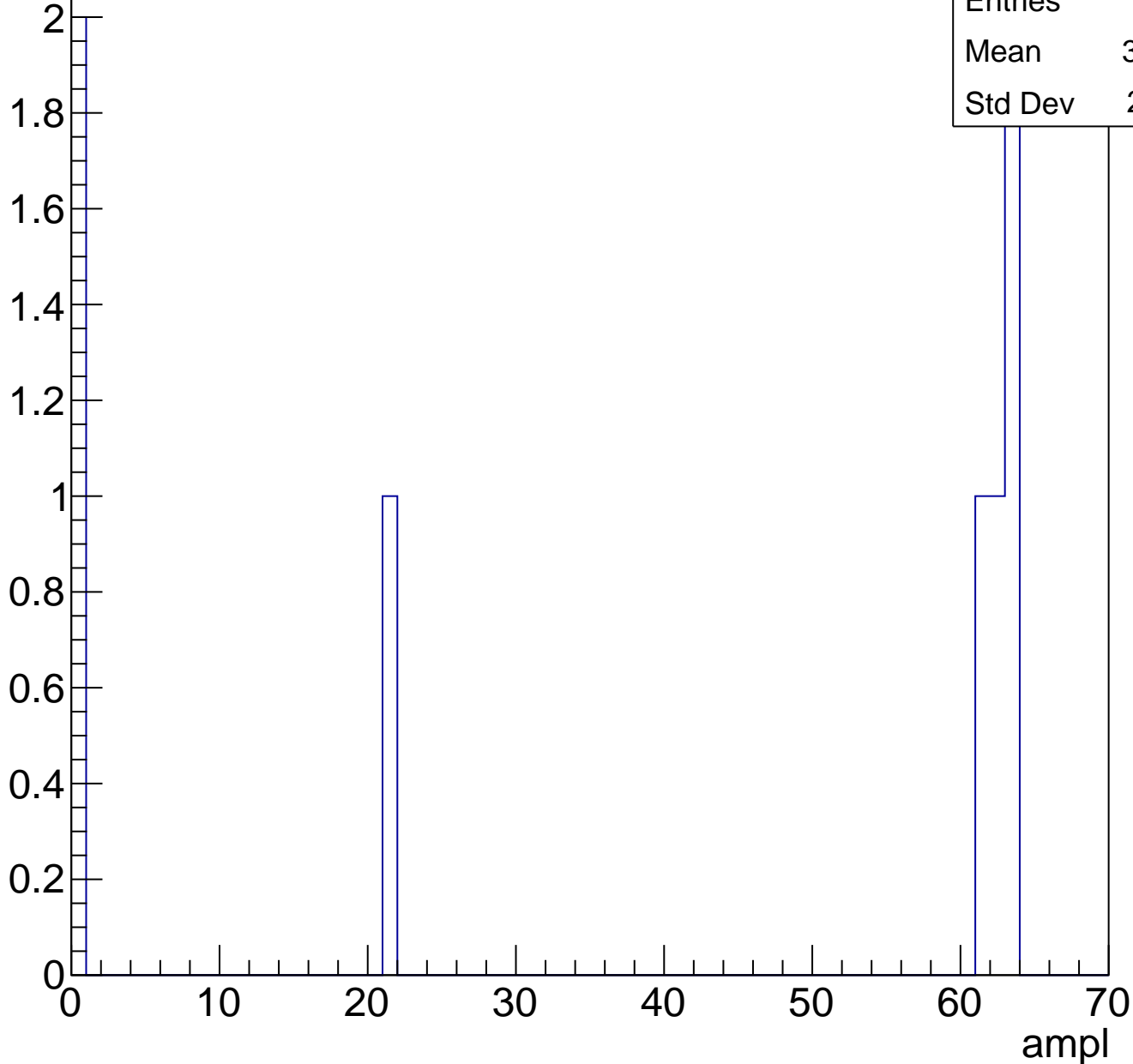
Entry



B1L101S, U4-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch111, adc0

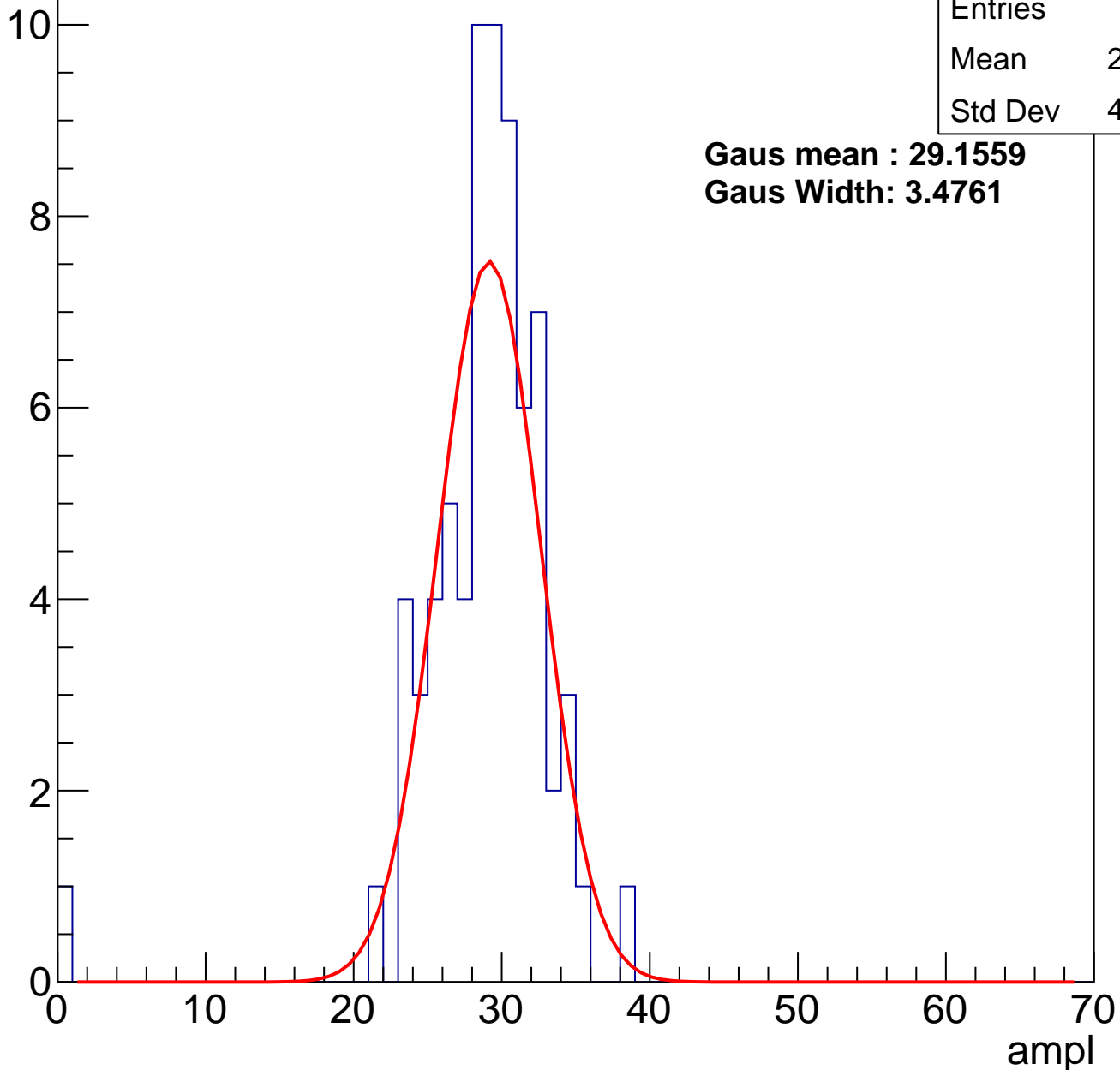
calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	28.37
Std Dev	4.673

Gaus mean : 29.1559

Gaus Width: 3.4761

Entry



B1L101S, U4-ch111, adc1

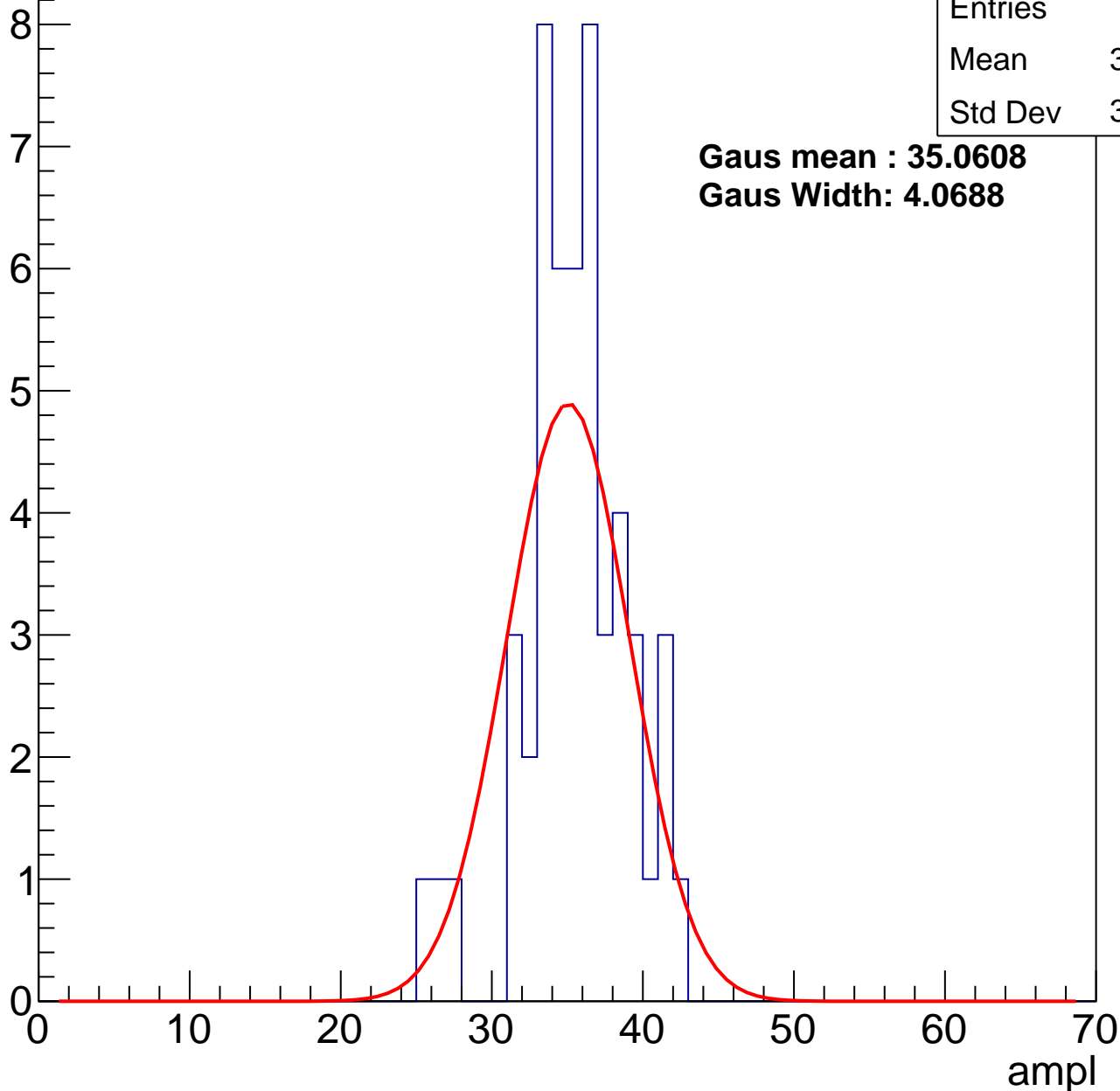
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.02
Std Dev	3.529

Gaus mean : 35.0608

Gaus Width: 4.0688



B1L101S, U4-ch111, adc2

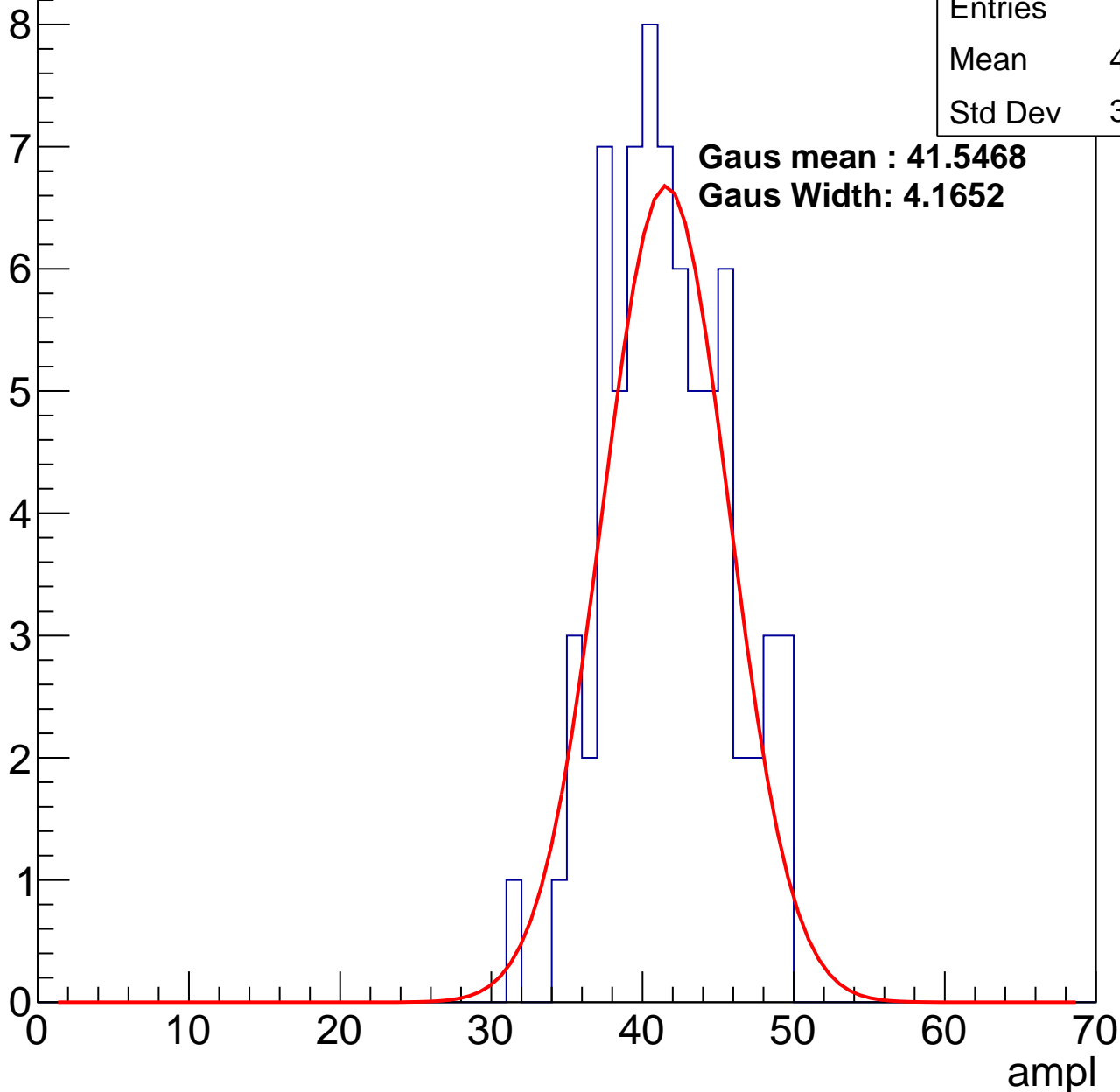
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.16
Std Dev	3.927

Gaus mean : 41.5468

Gaus Width: 4.1652

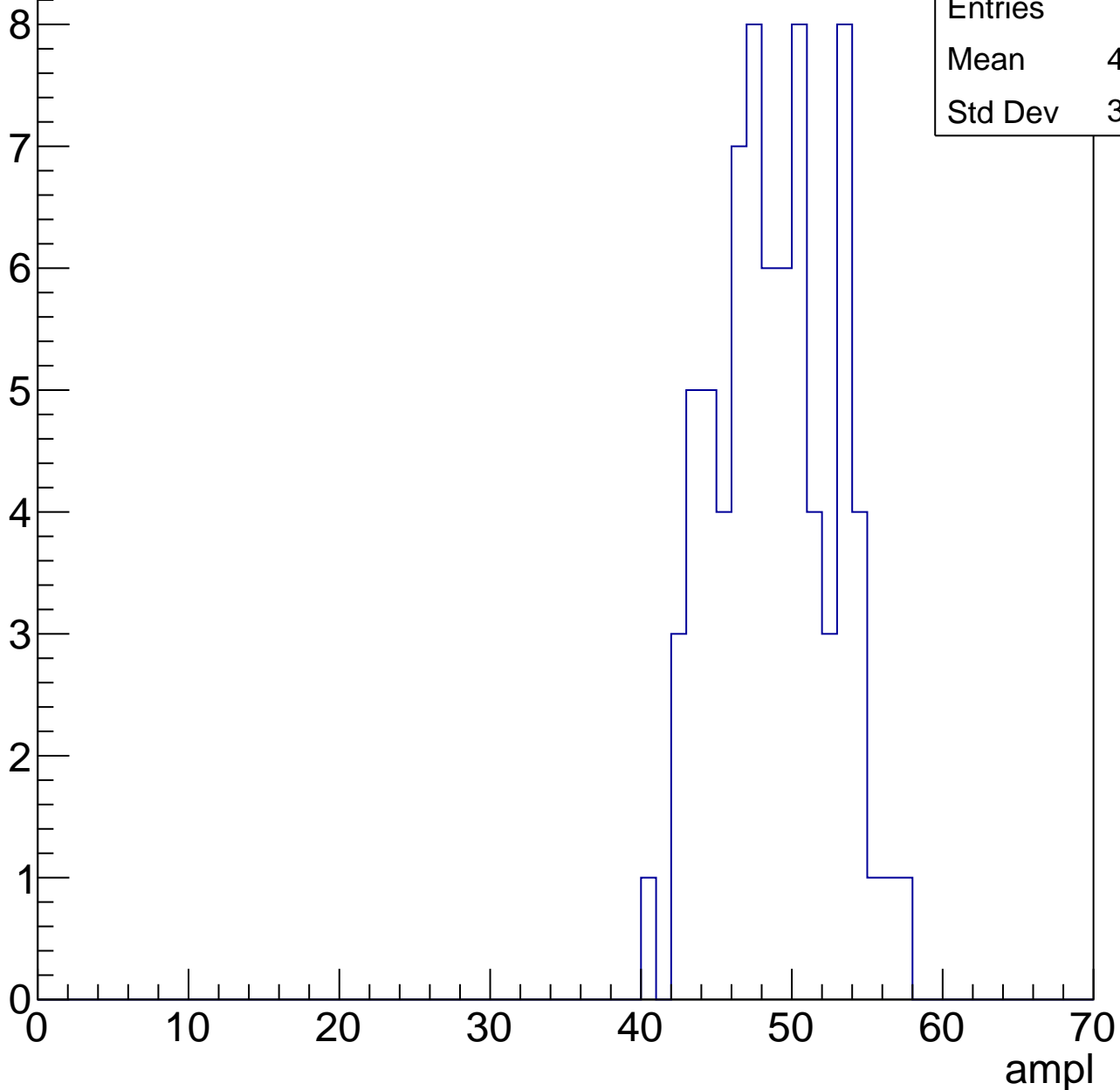


B1L101S, U4-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

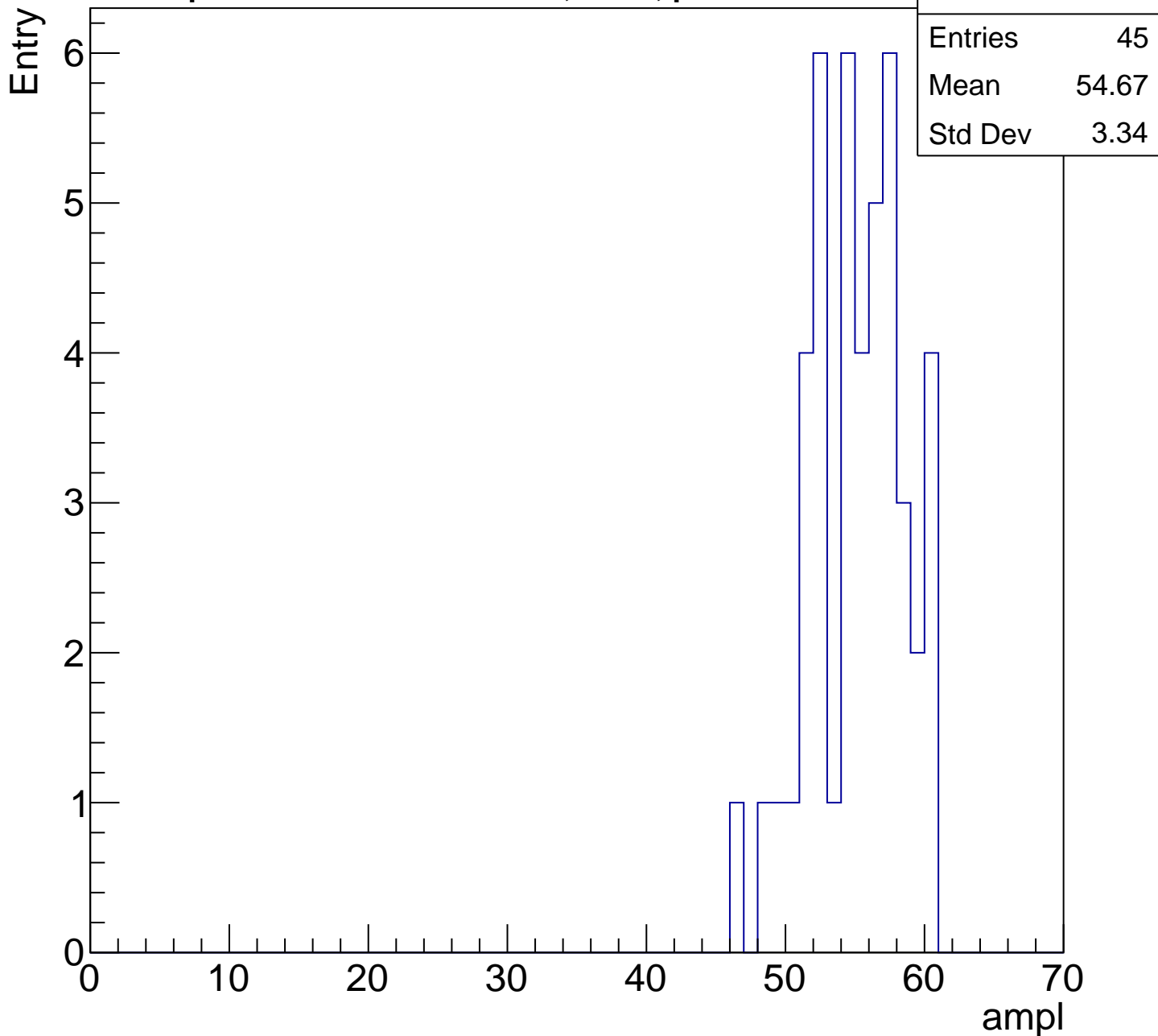
Entry

Entries	75
Mean	48.39
Std Dev	3.826



B1L101S, U4-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

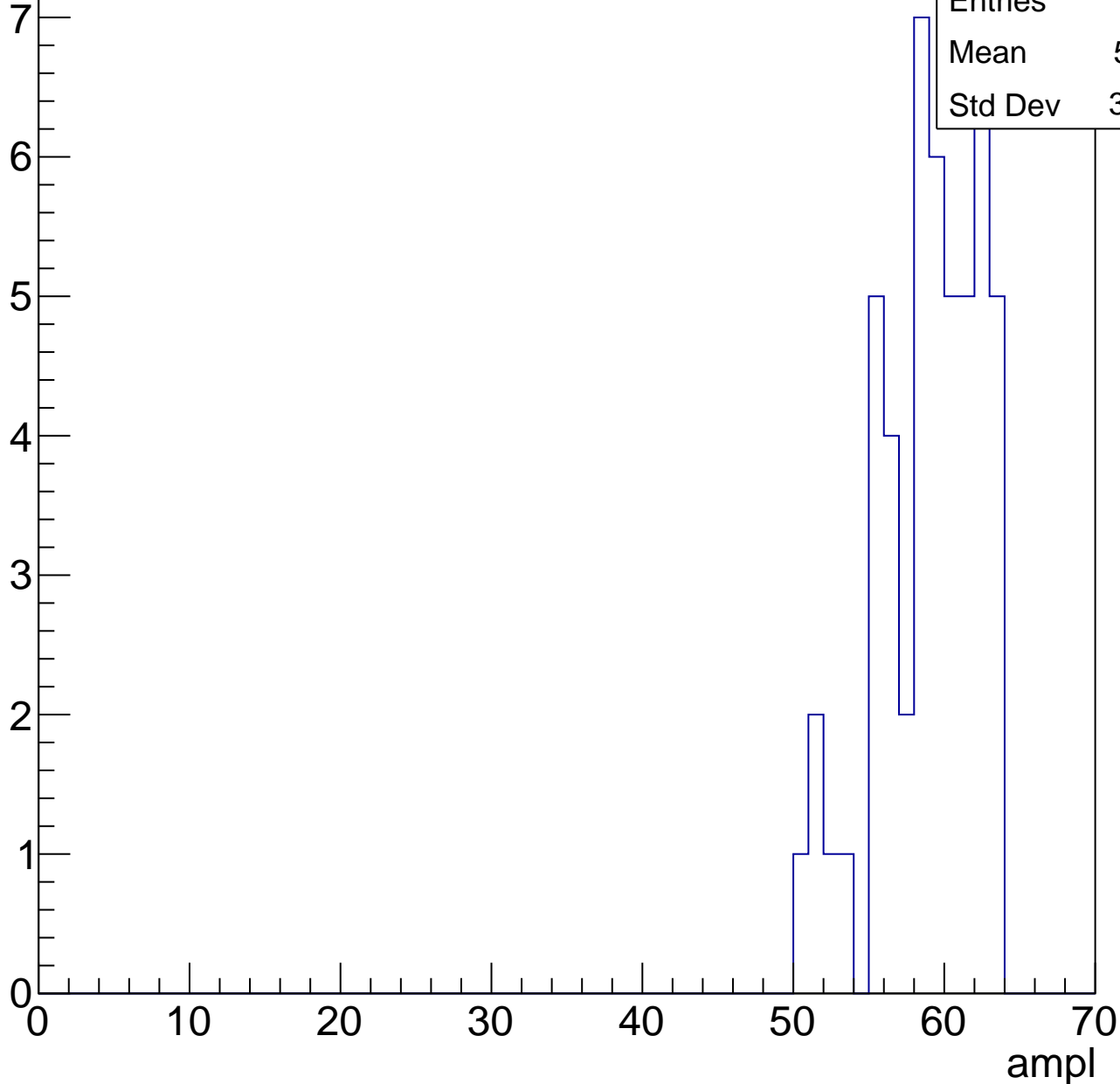


B1L101S, U4-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.51
Std Dev	3.374

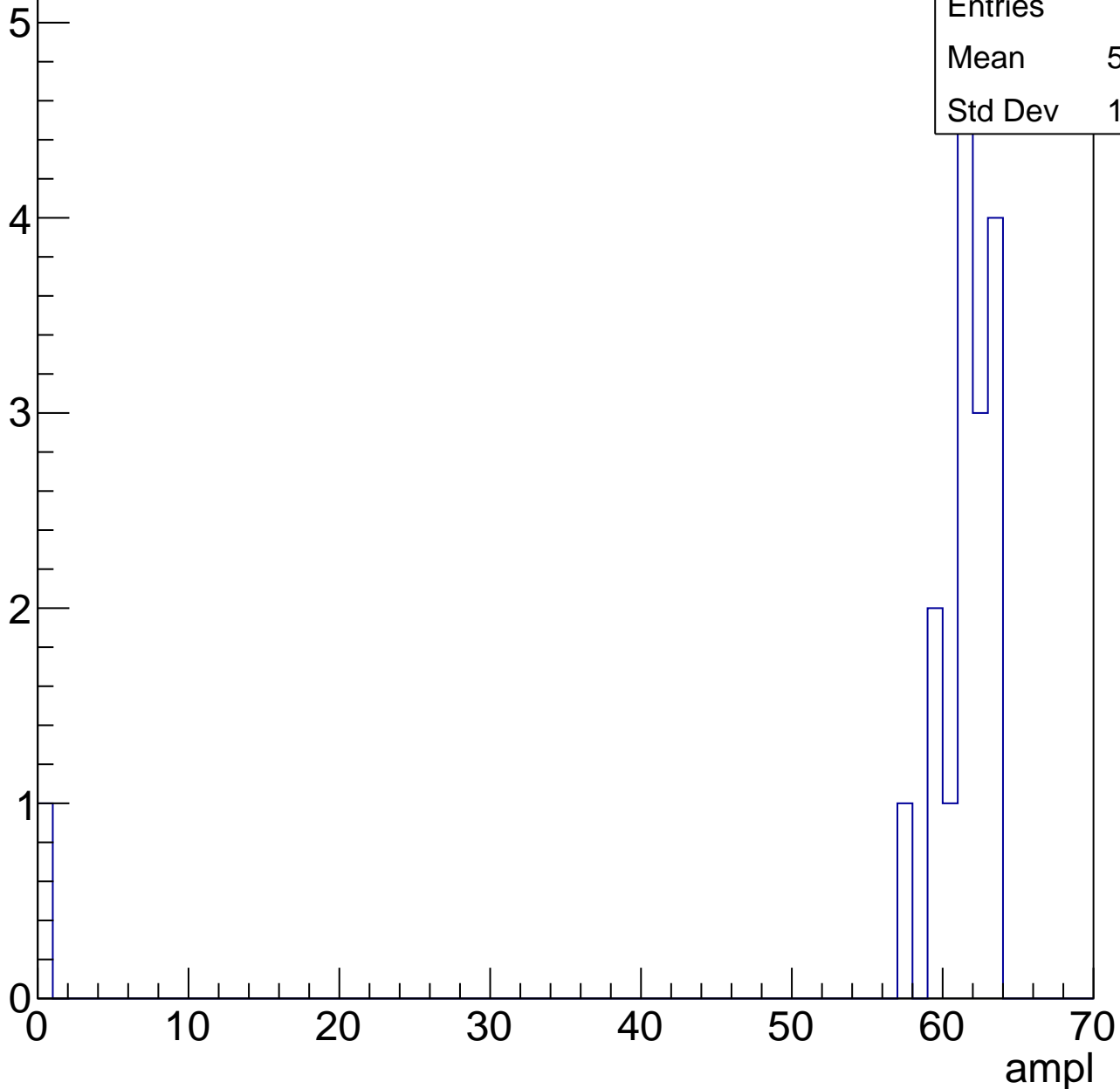


B1L101S, U4-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.53
Std Dev	14.47



B1L101S, U4-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L101S, U4-ch112, adc0

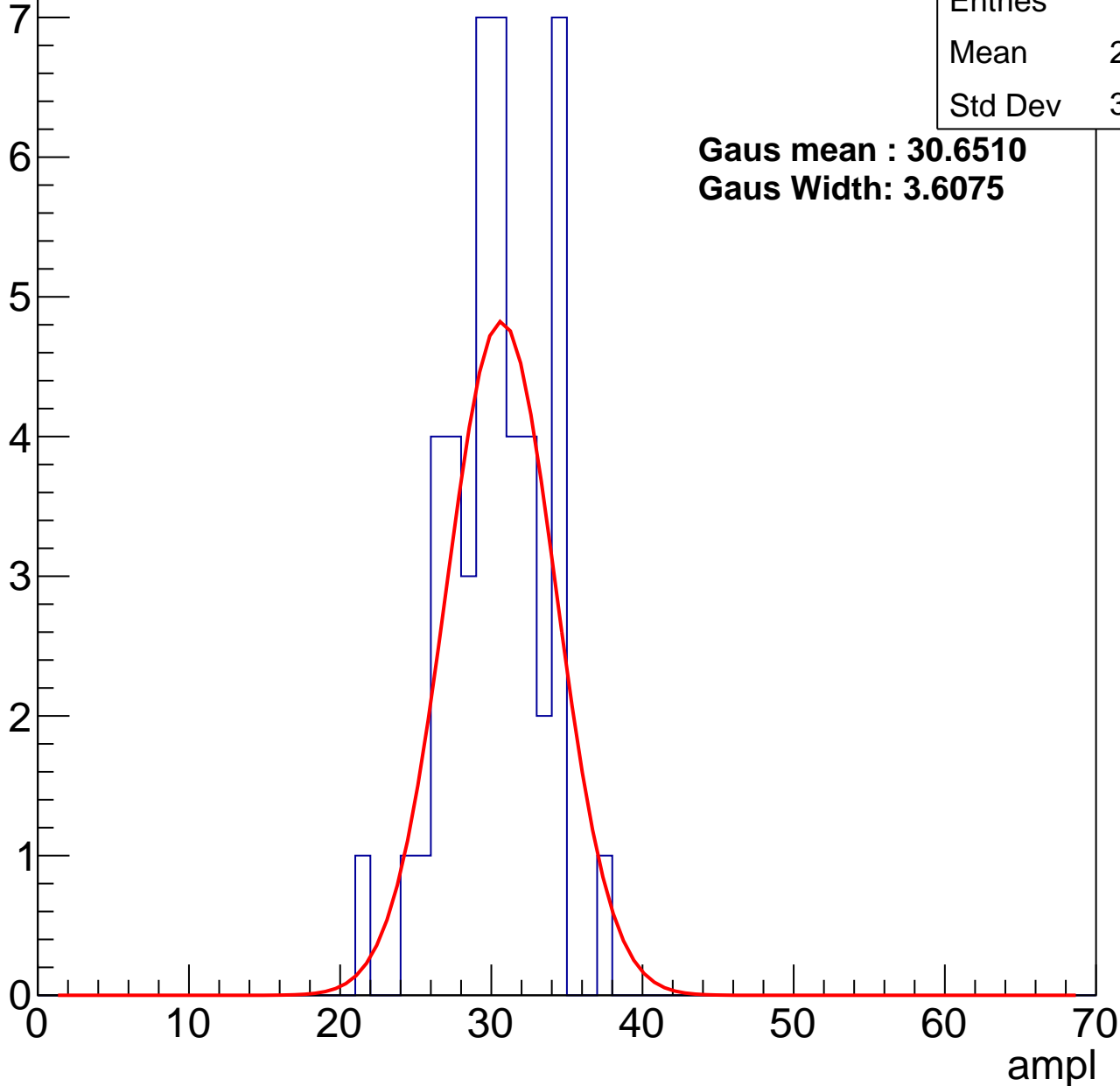
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	29.83
Std Dev	3.157

Gaus mean : 30.6510

Gaus Width: 3.6075



B1L101S, U4-ch112, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	35.93
Std Dev	3.836

Gaus mean : 36.5038

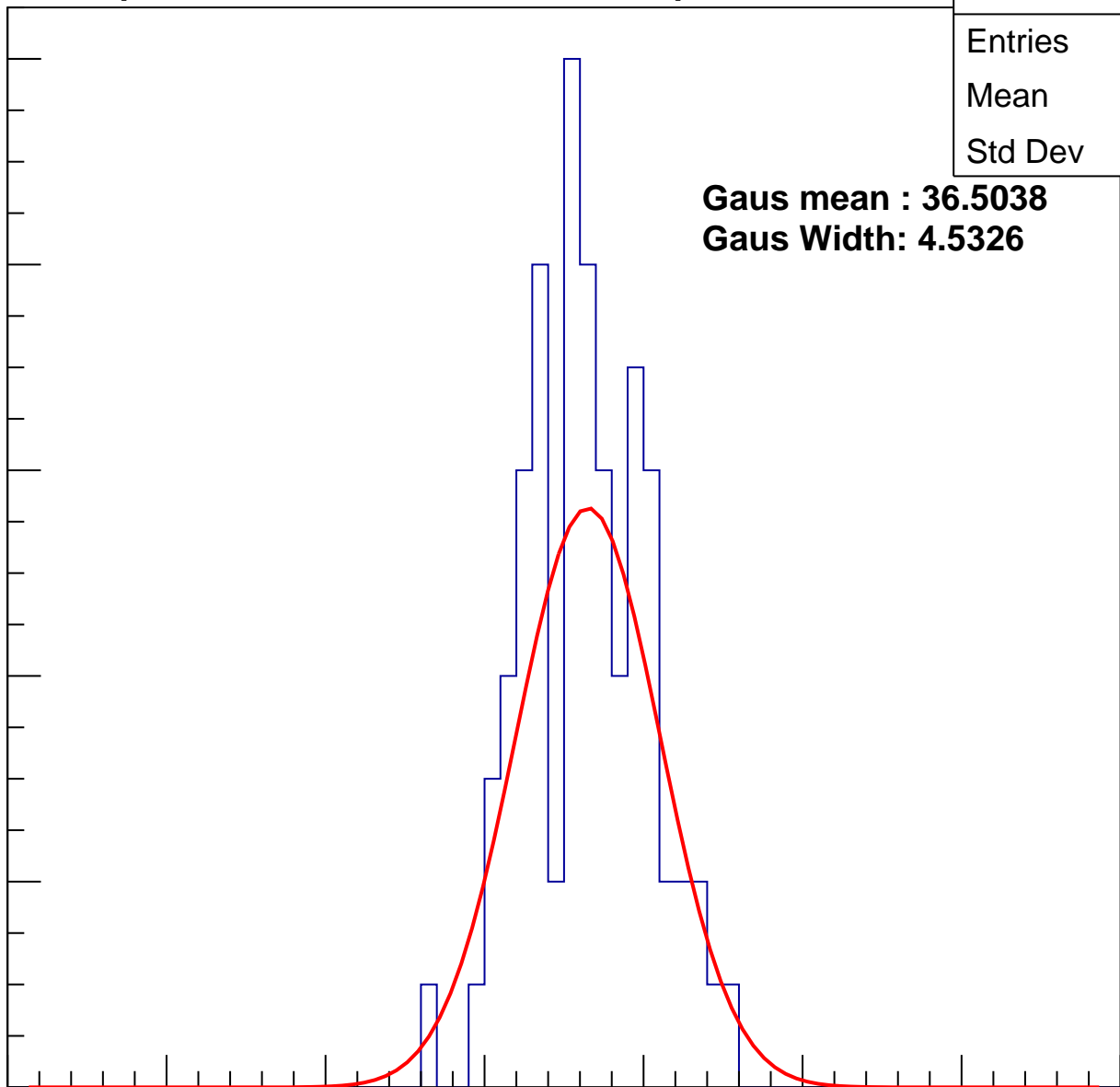
Gaus Width: 4.5326

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch112, adc2

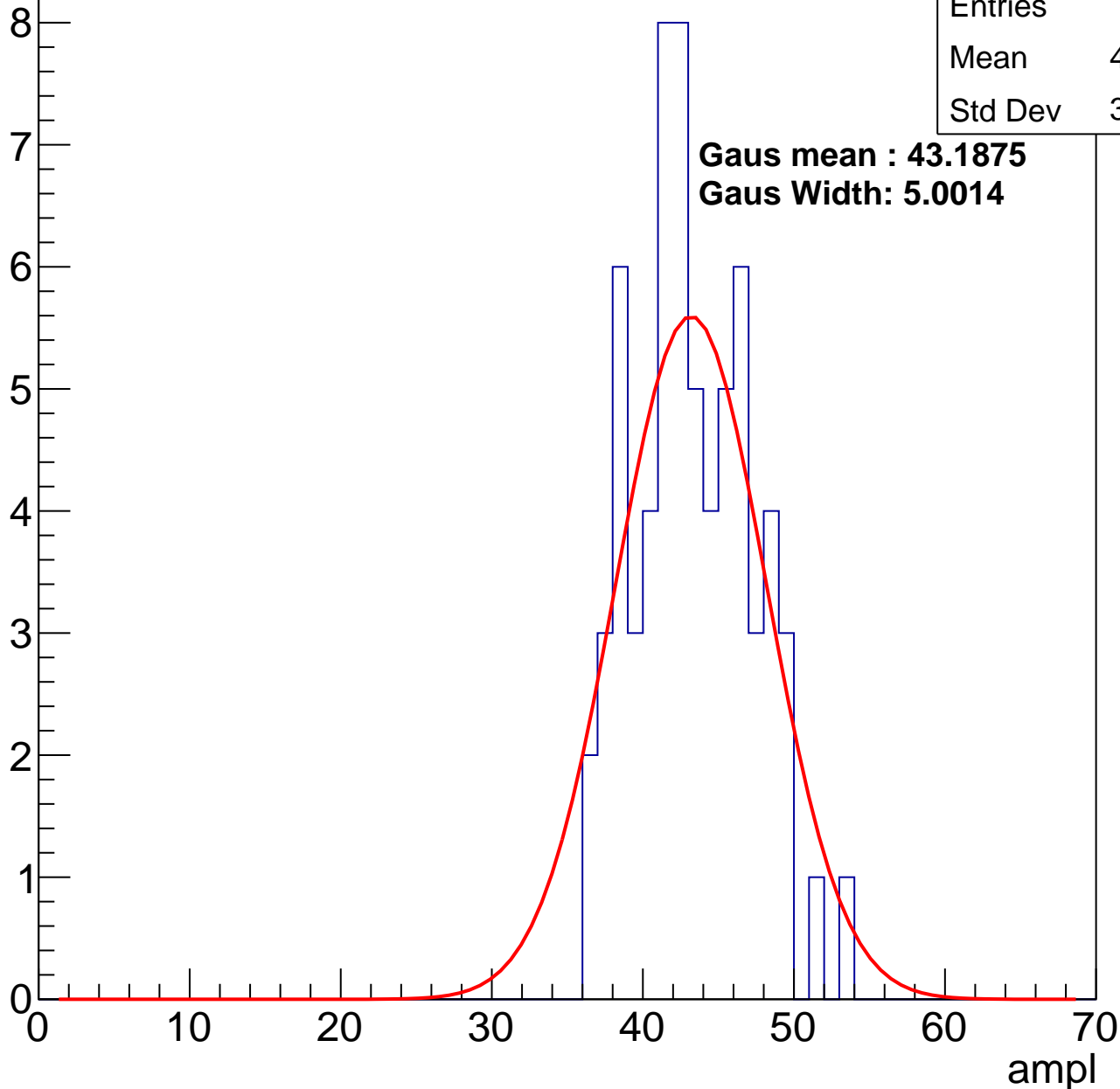
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.85
Std Dev	3.846

Gaus mean : 43.1875

Gaus Width: 5.0014

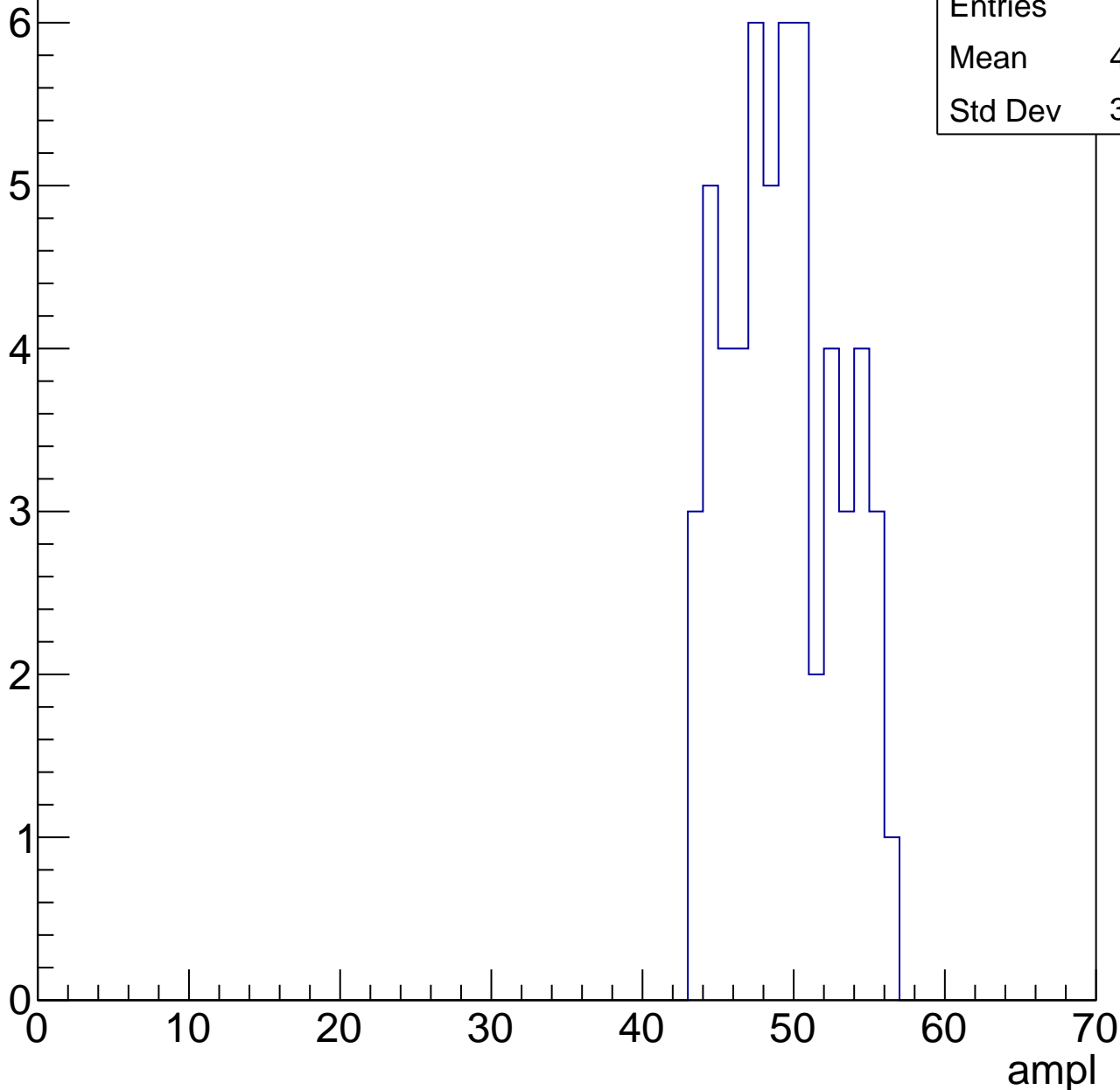


B1L101S, U4-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	48.84
Std Dev	3.575

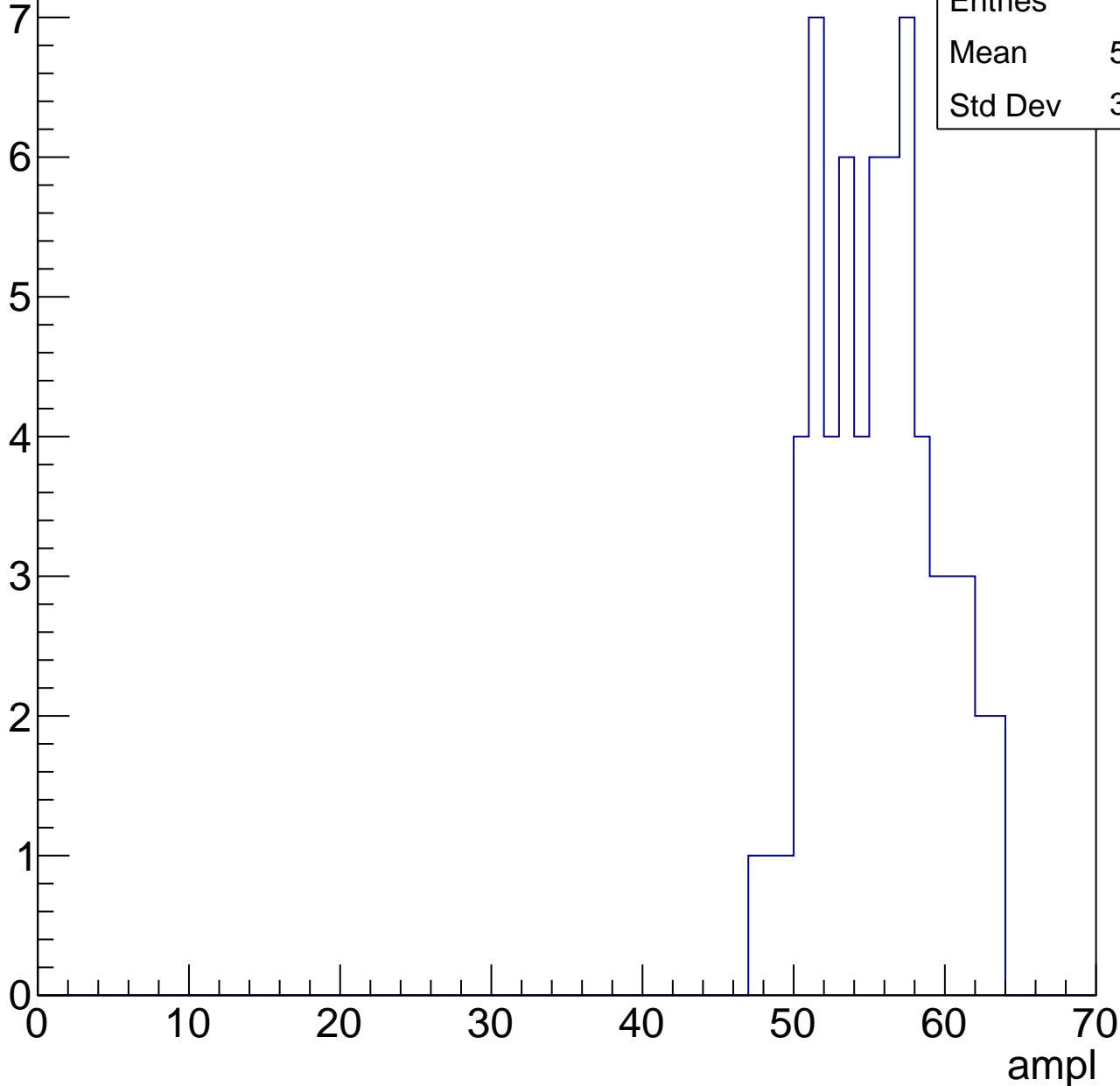


B1L101S, U4-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	55.16
Std Dev	3.854

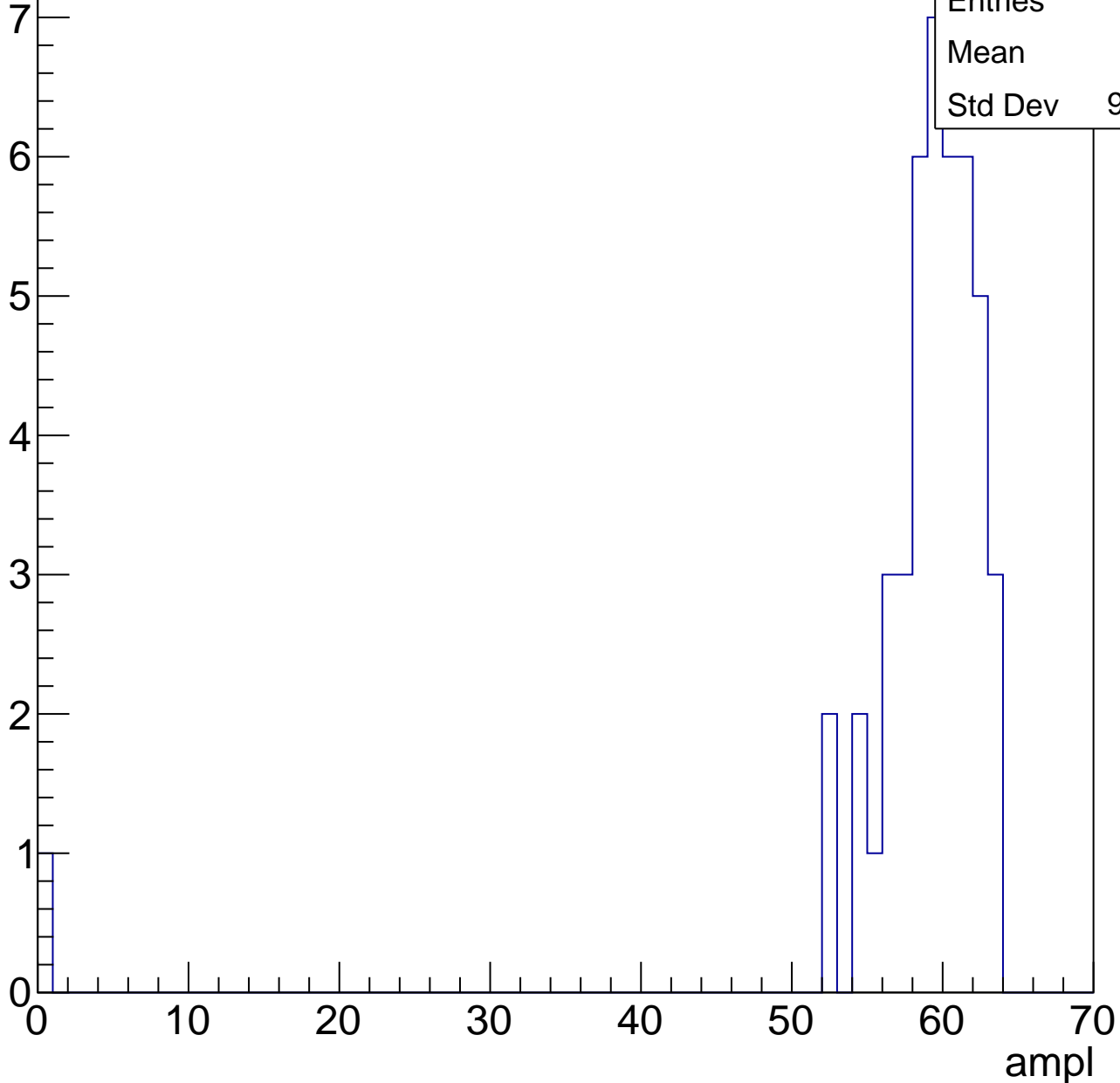


B1L101S, U4-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.6
Std Dev	9.098

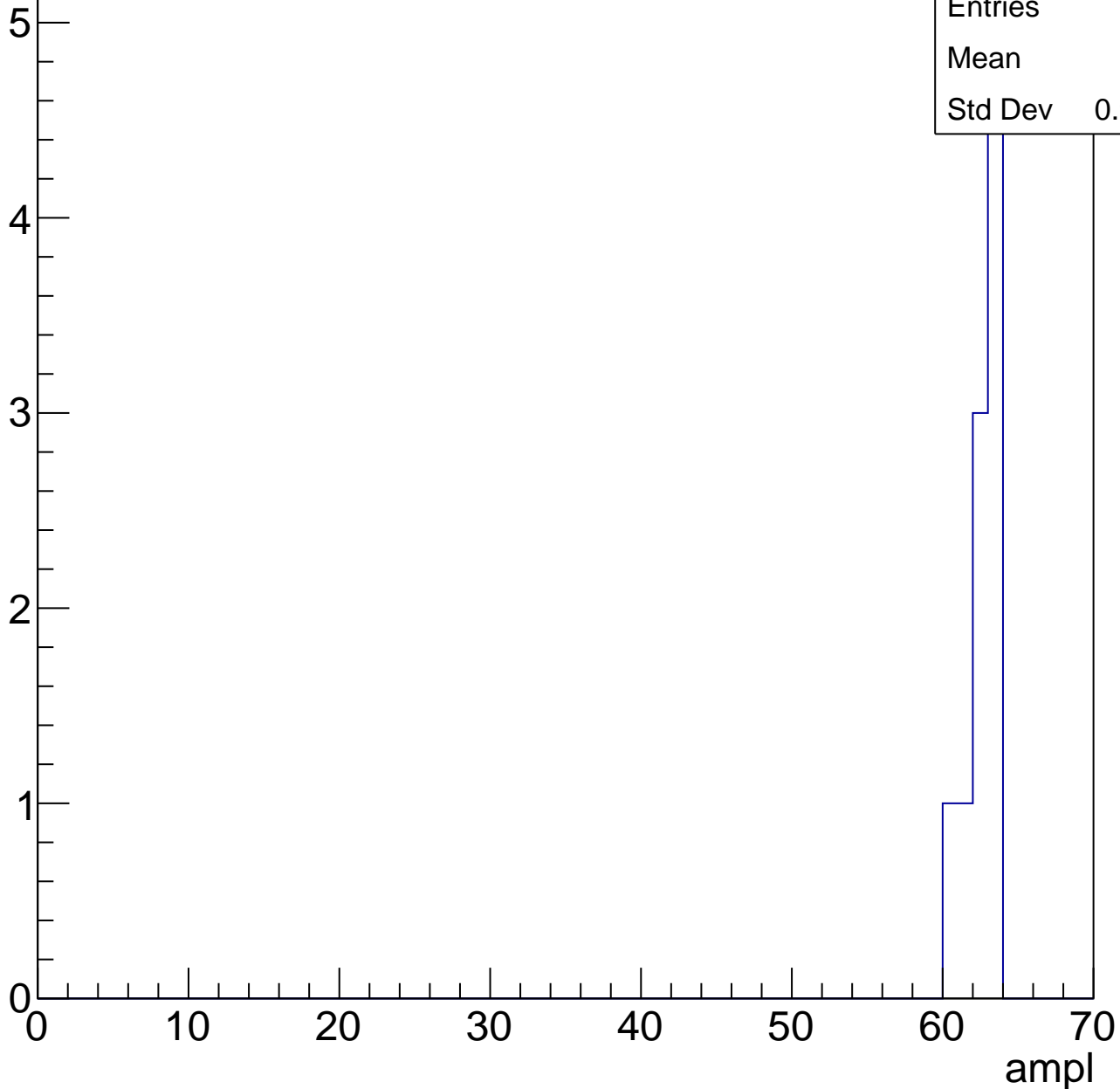


B1L101S, U4-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B1L101S, U4-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch113, adc0

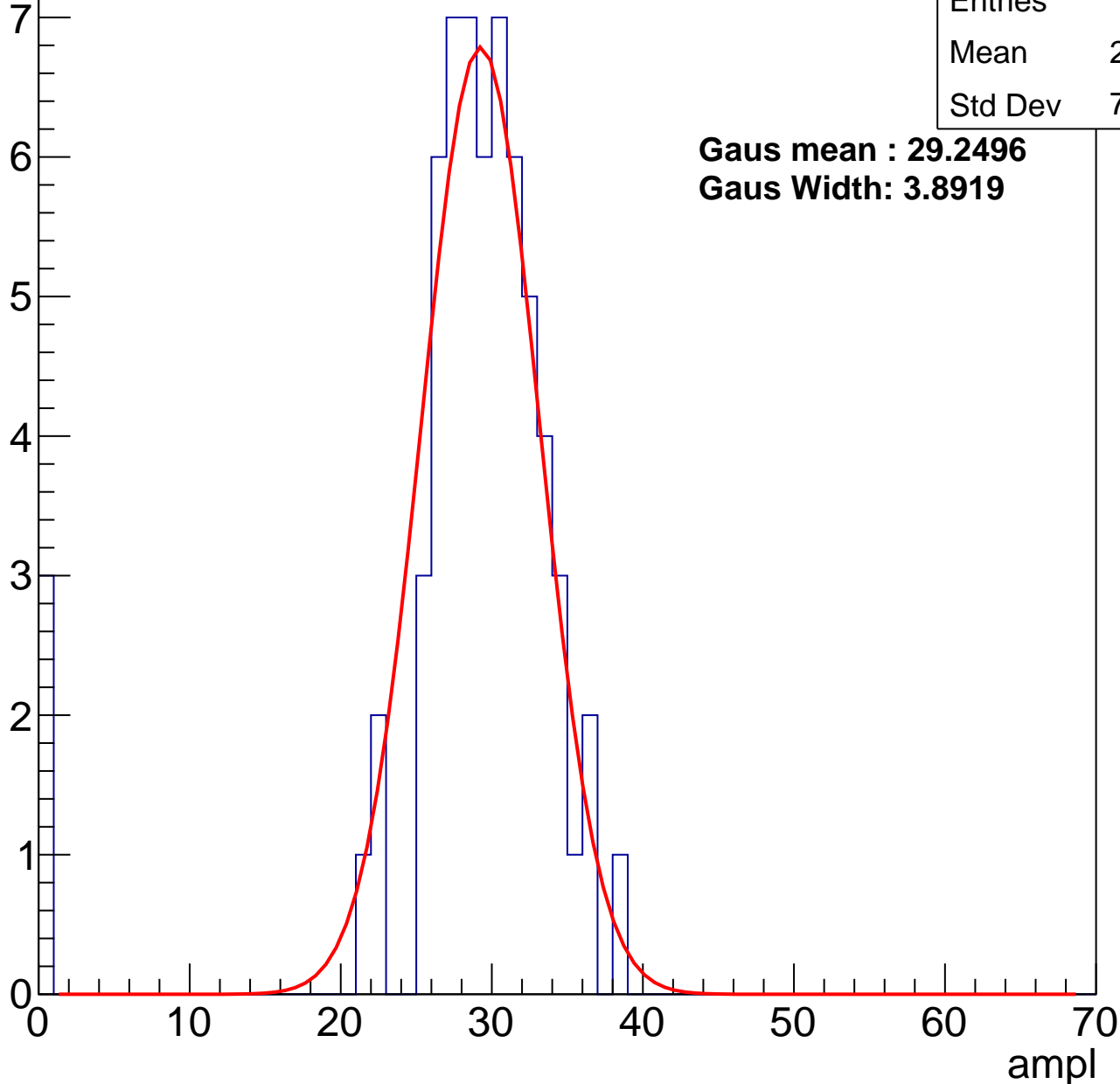
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.97
Std Dev	7.053

Gaus mean : 29.2496

Gaus Width: 3.8919



B1L101S, U4-ch113, adc1

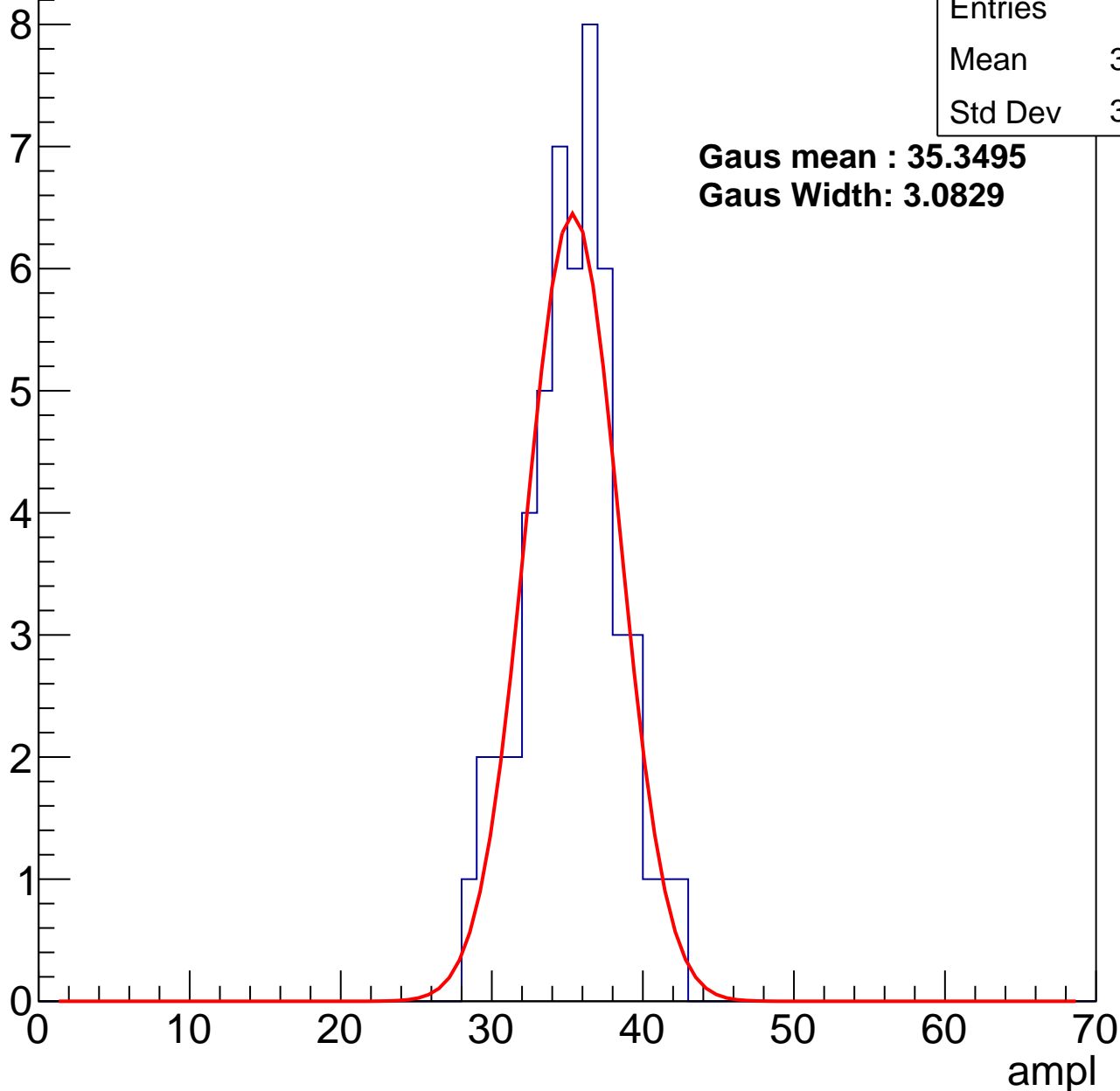
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	34.87
Std Dev	3.045

Gaus mean : 35.3495

Gaus Width: 3.0829



B1L101S, U4-ch113, adc2

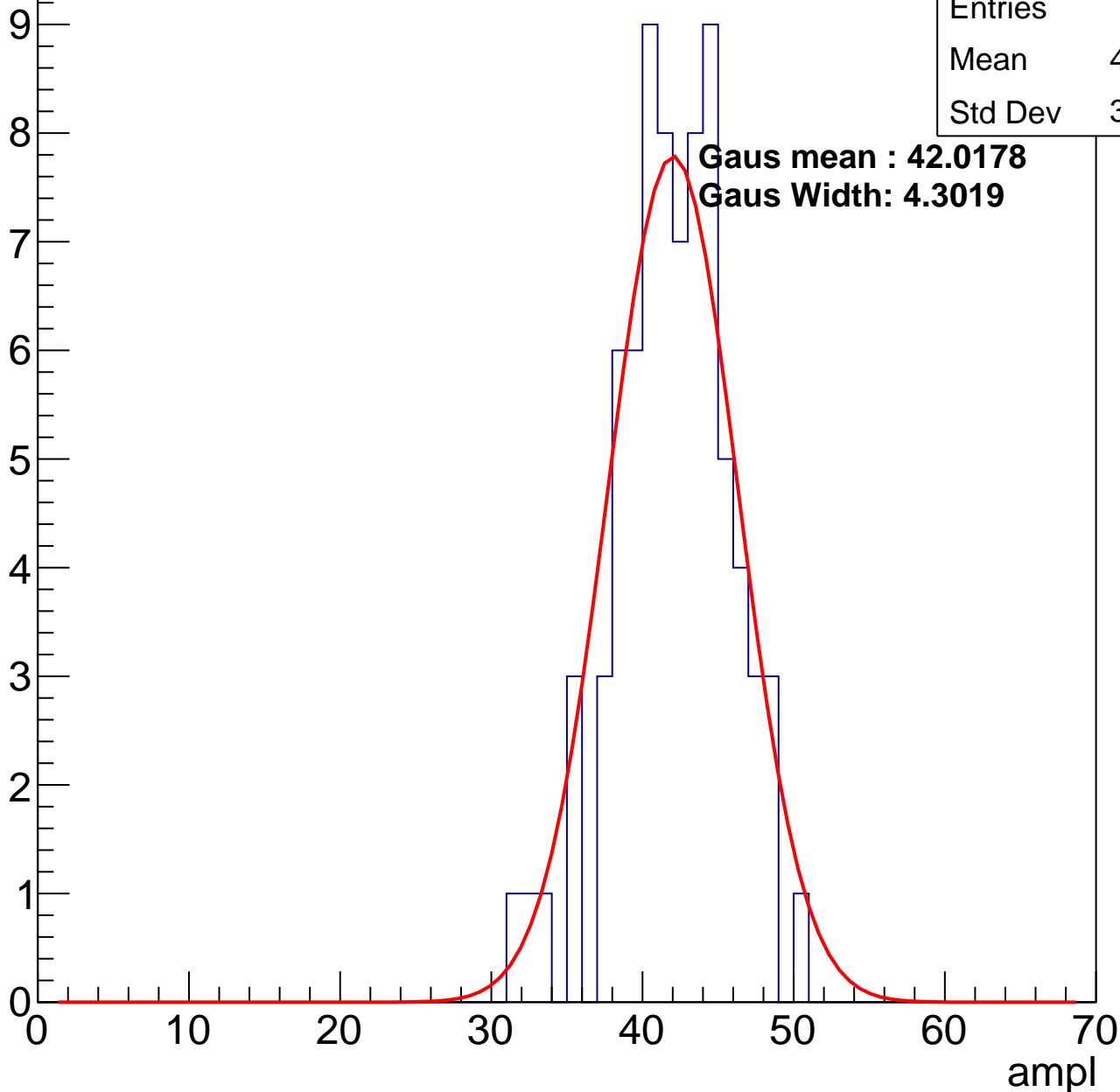
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	41.54
Std Dev	3.758

Gaus mean : 42.0178

Gaus Width: 4.3019



B1L101S, U4-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

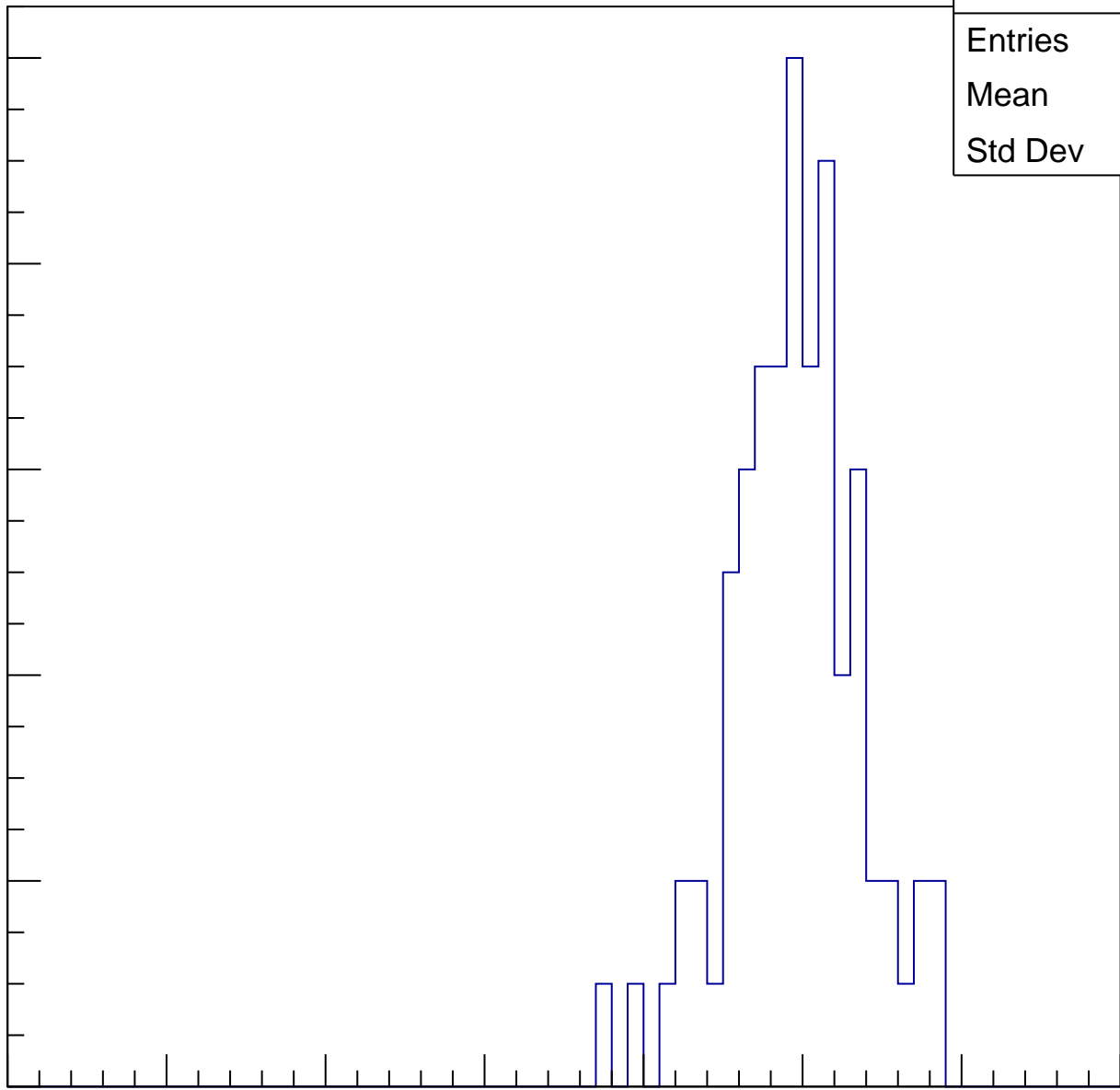
Entries	78
Mean	49.05
Std Dev	4.095

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

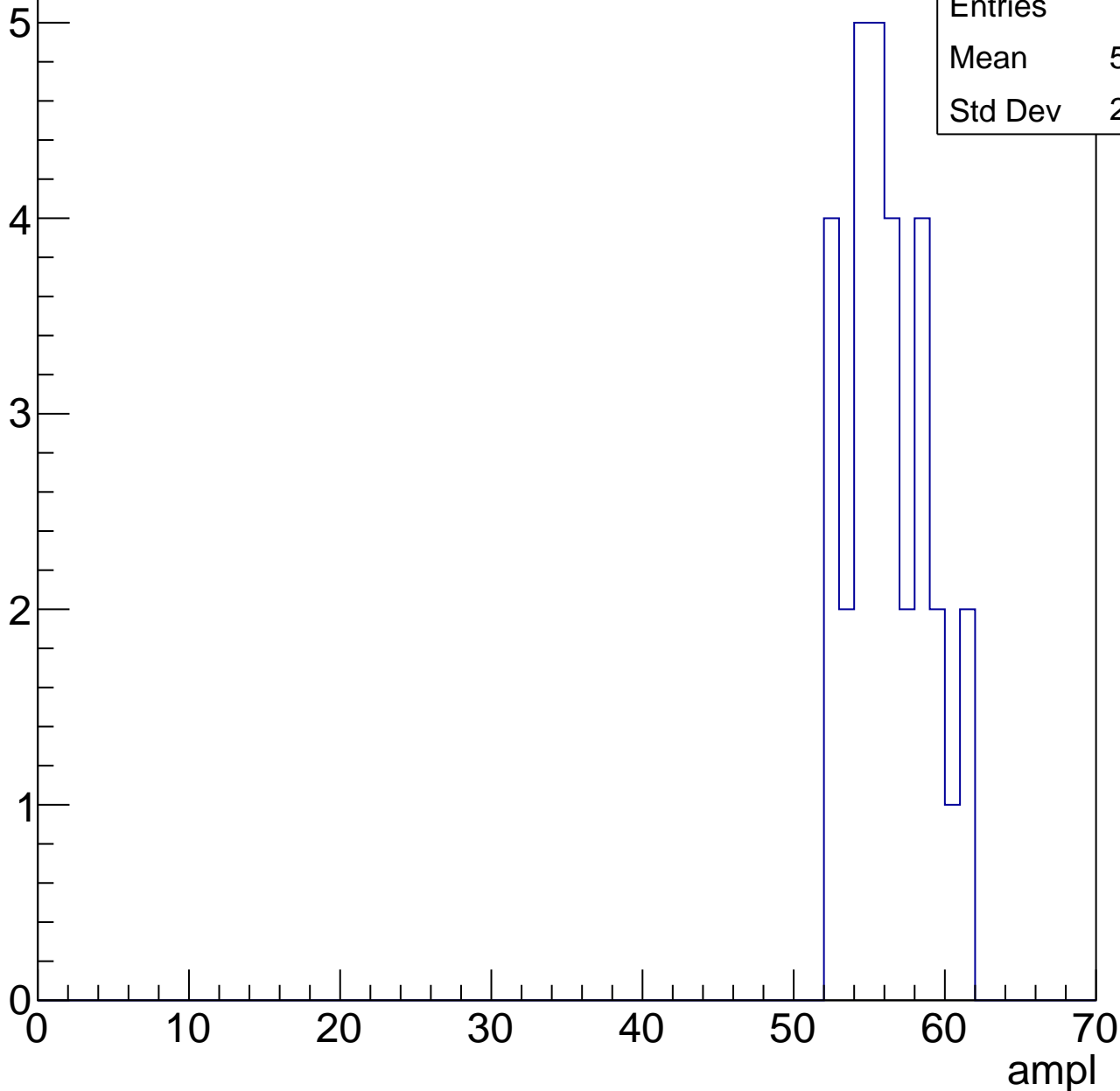


B1L101S, U4-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

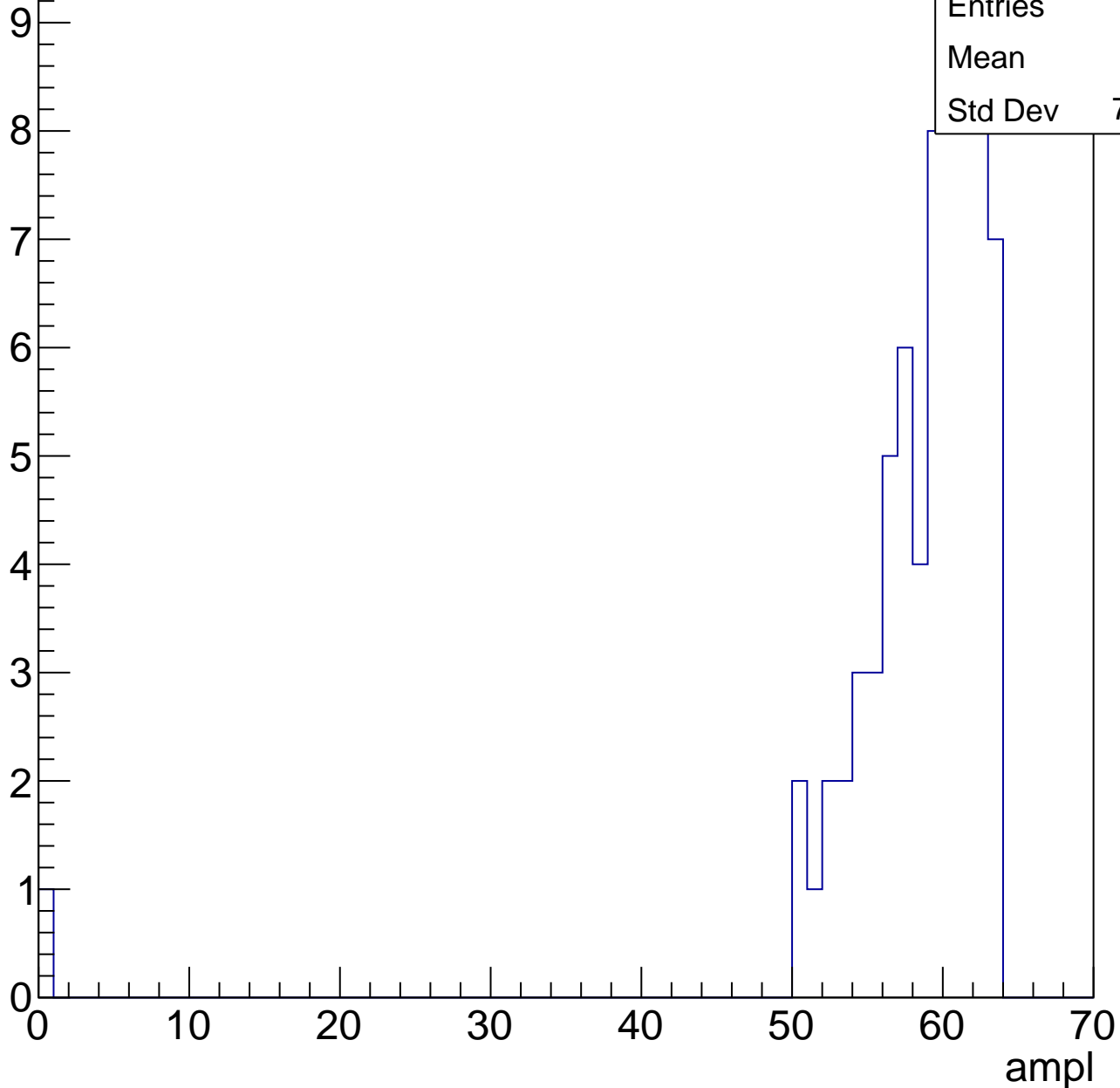
Entries	31
Mean	55.77
Std Dev	2.587



B1L101S, U4-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	62.2
Std Dev	1.166

B1L101S, U4-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch114, adc0

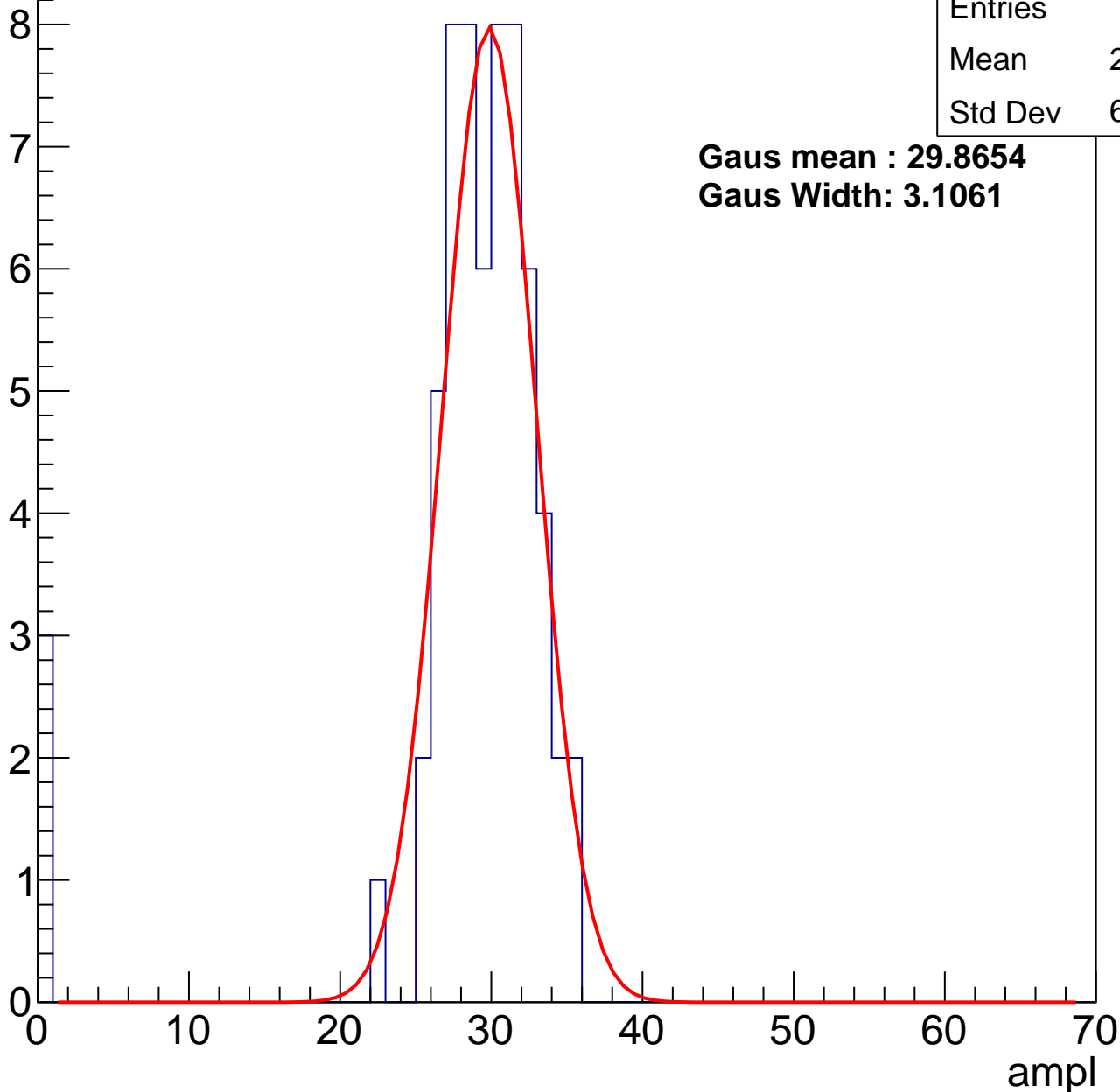
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.03
Std Dev	6.796

Gaus mean : 29.8654

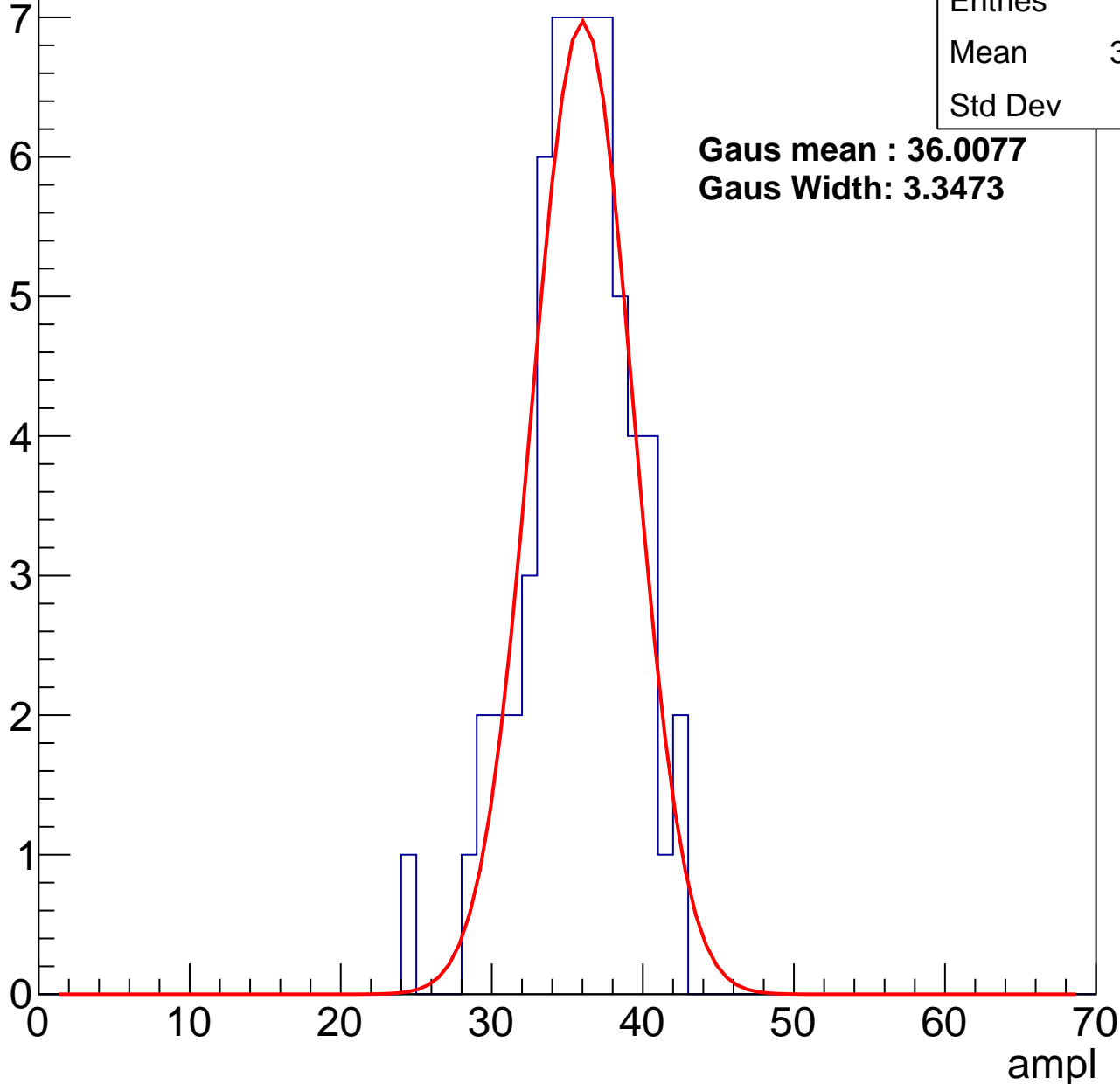
Gaus Width: 3.1061



B1L101S, U4-ch114, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch114, adc2

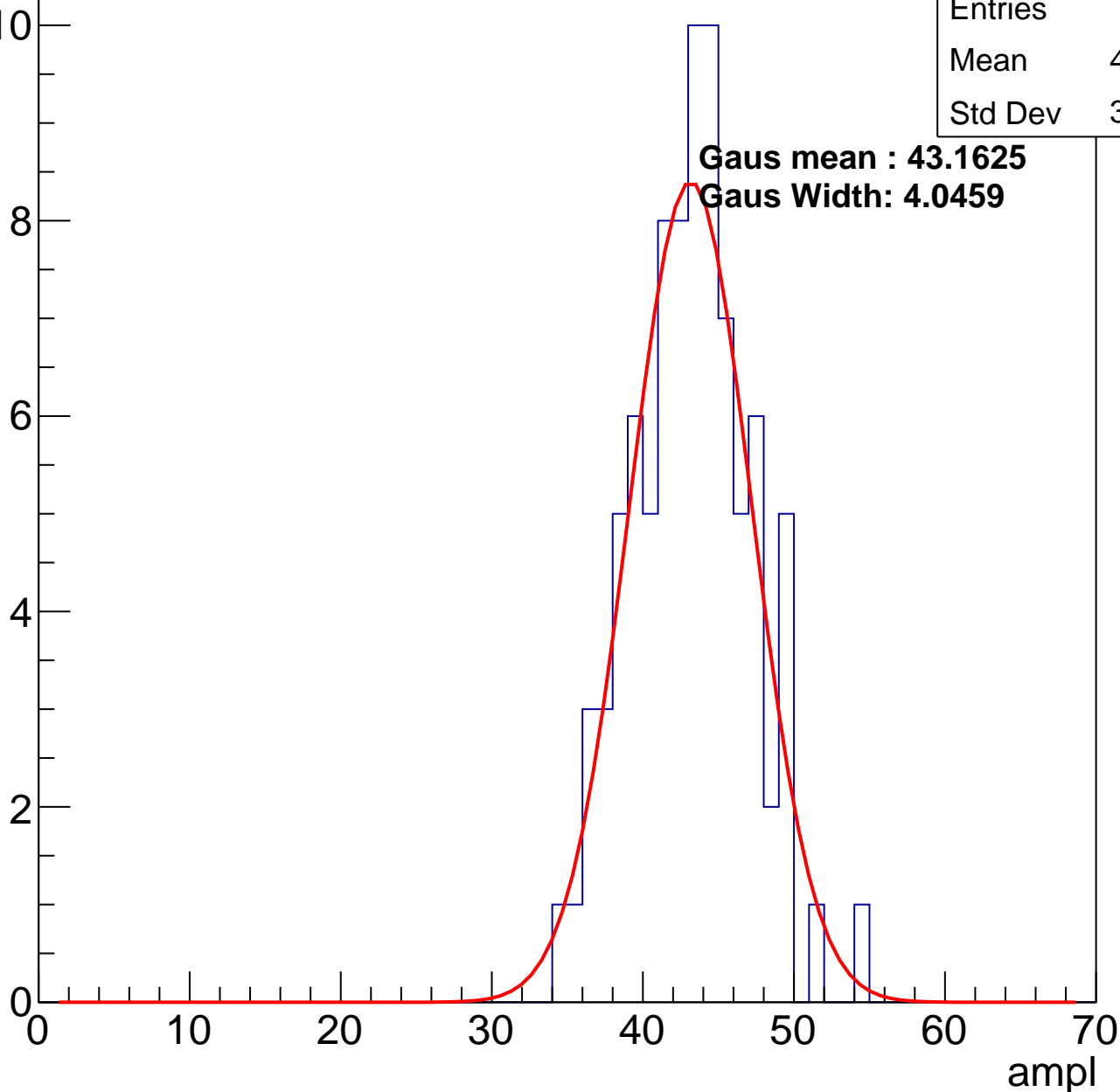
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	42.75
Std Dev	3.857

Gaus mean : 43.1625

Gaus Width: 4.0459

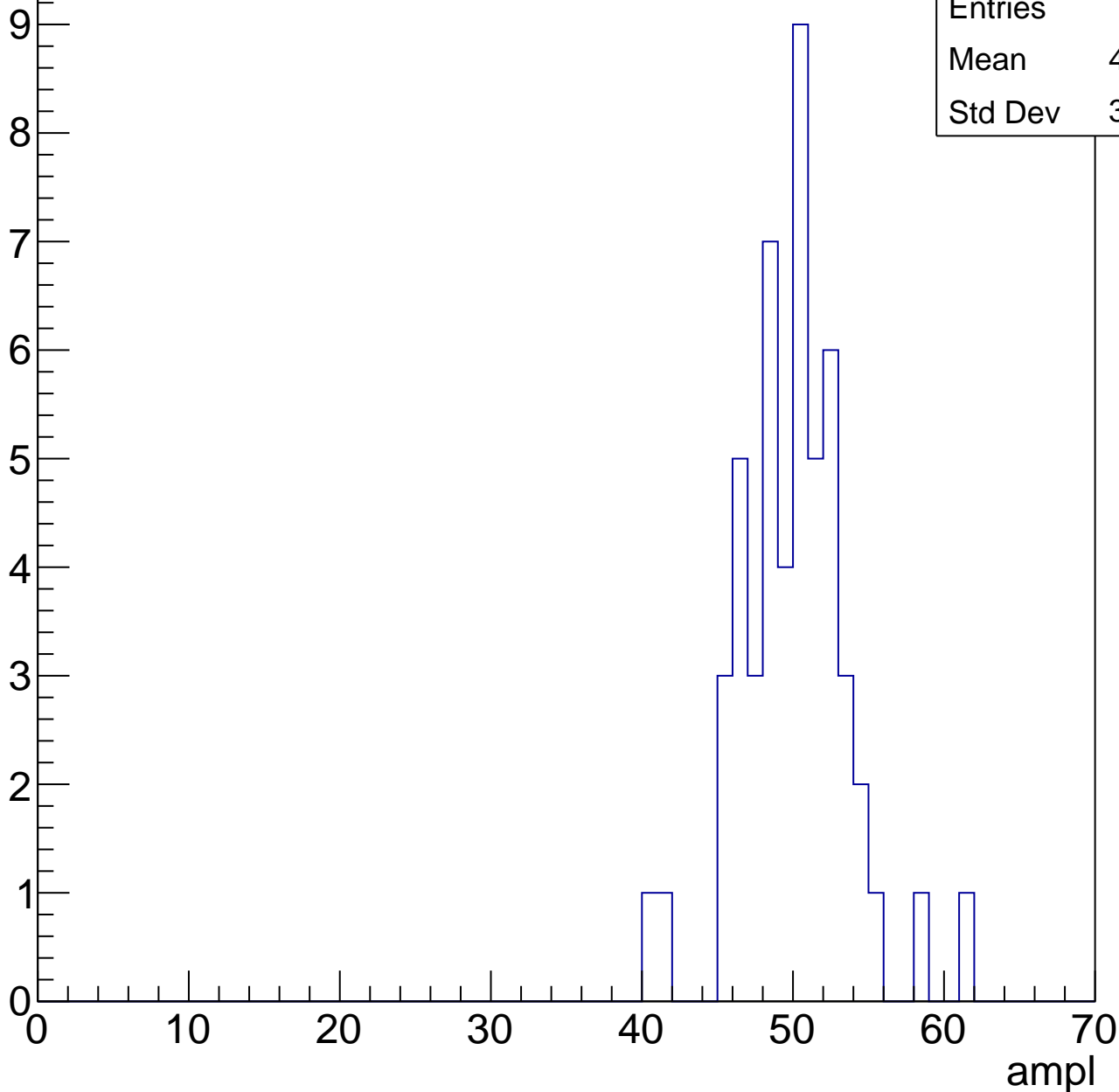


B1L101S, U4-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	49.56
Std Dev	3.624

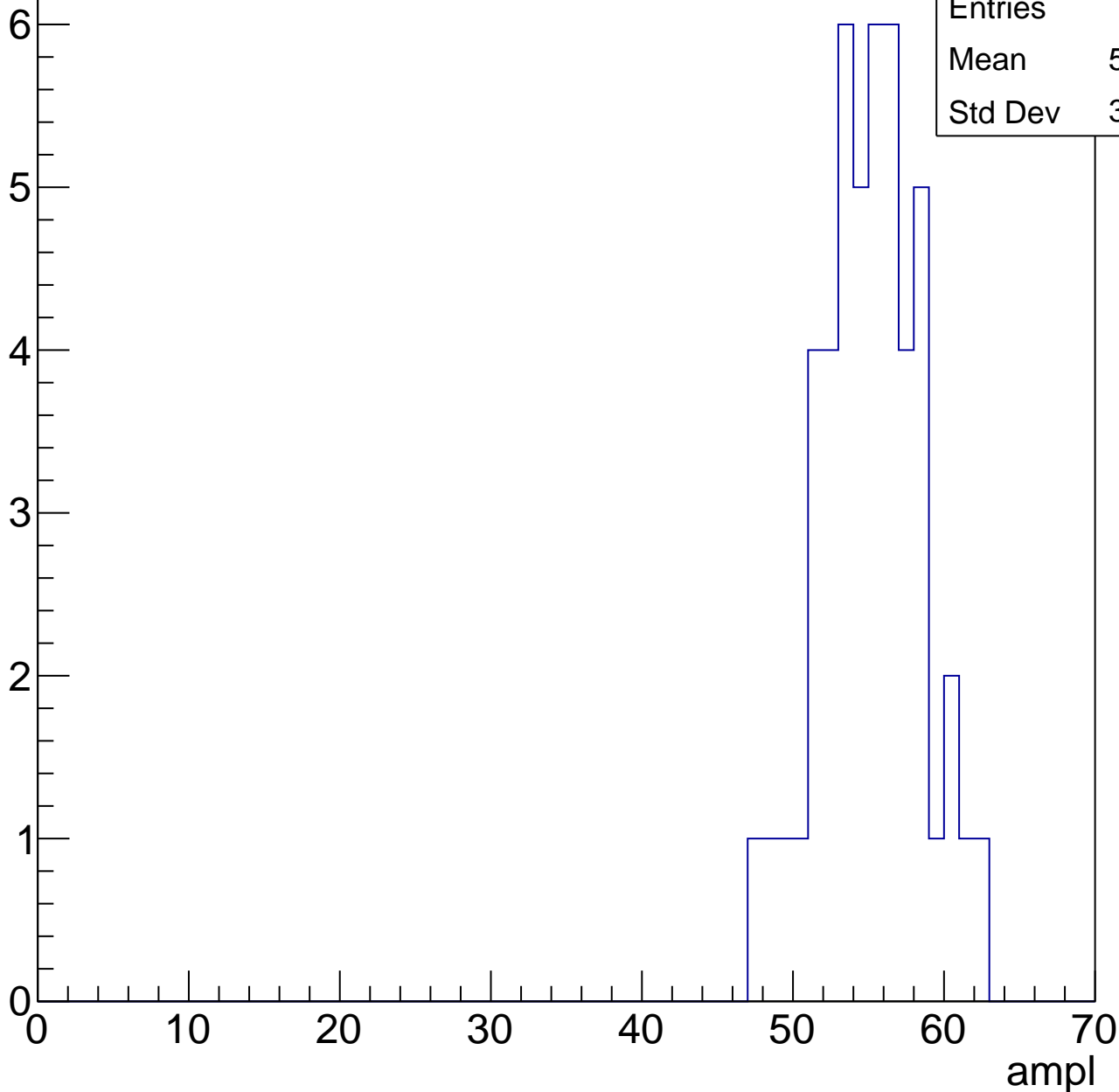


B1L101S, U4-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

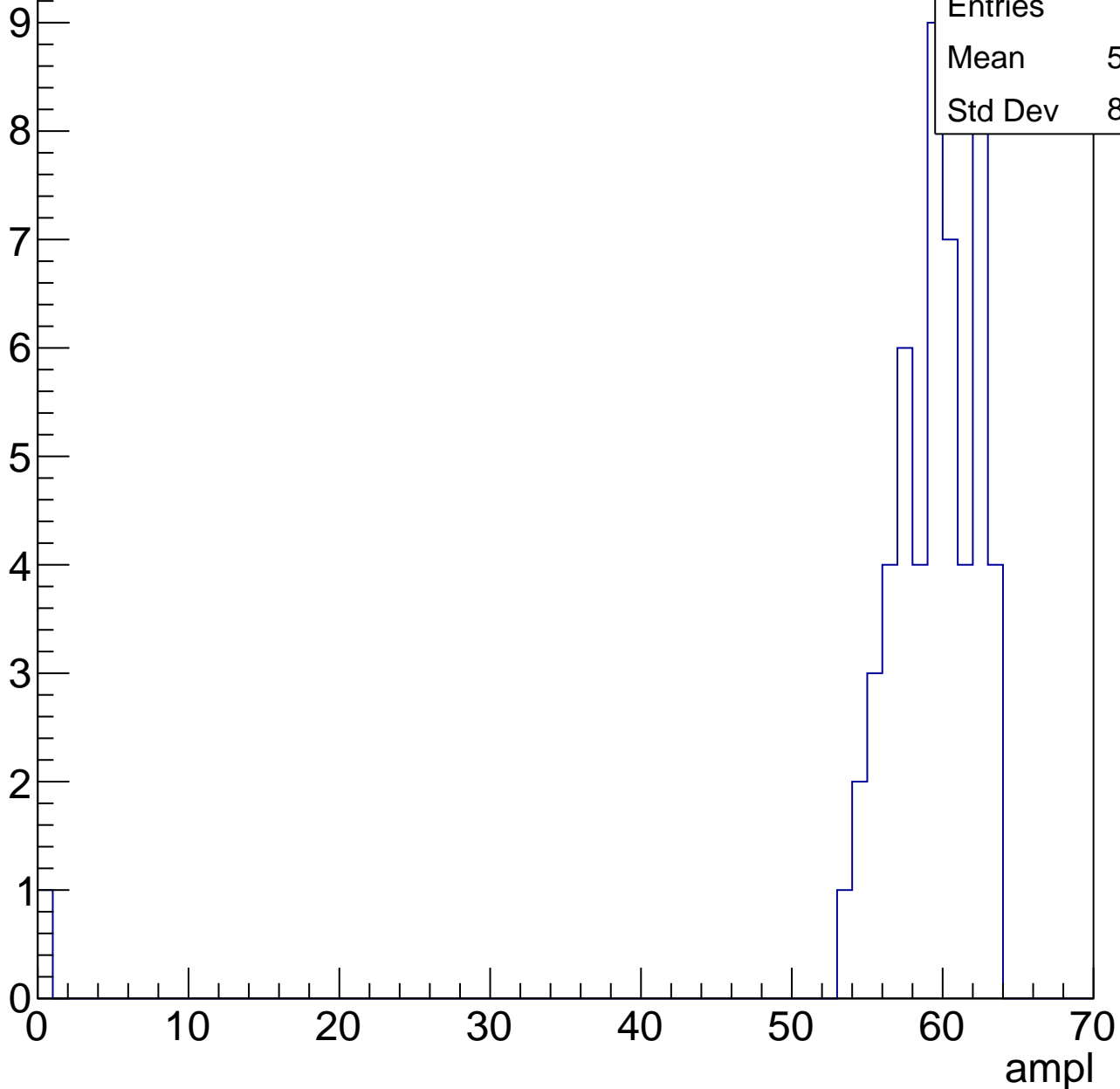
Entries	49
Mean	54.69
Std Dev	3.246



B1L101S, U4-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

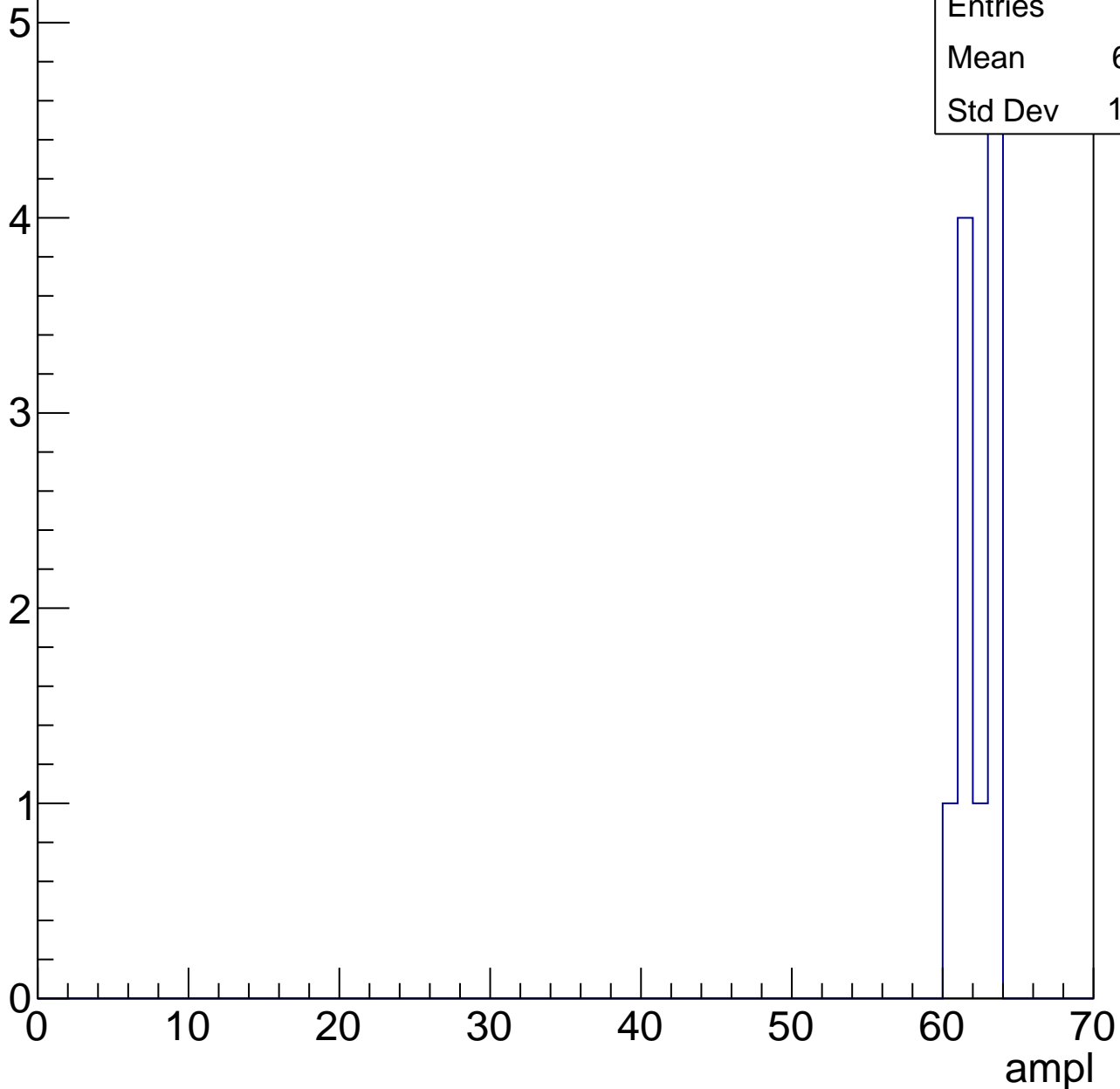


B1L101S, U4-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

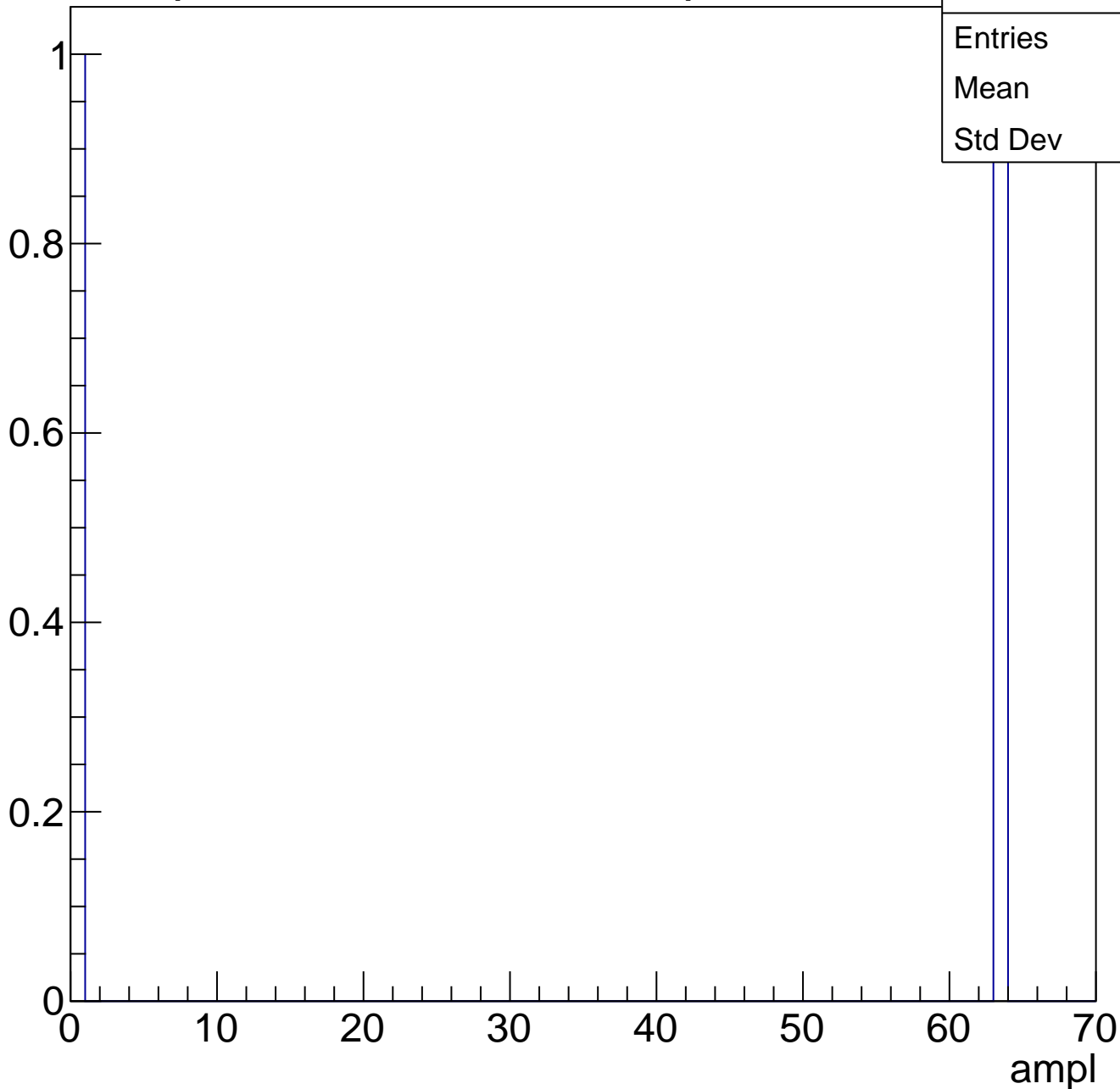
Entries	11
Mean	61.91
Std Dev	1.083



B1L101S, U4-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch115, adc0

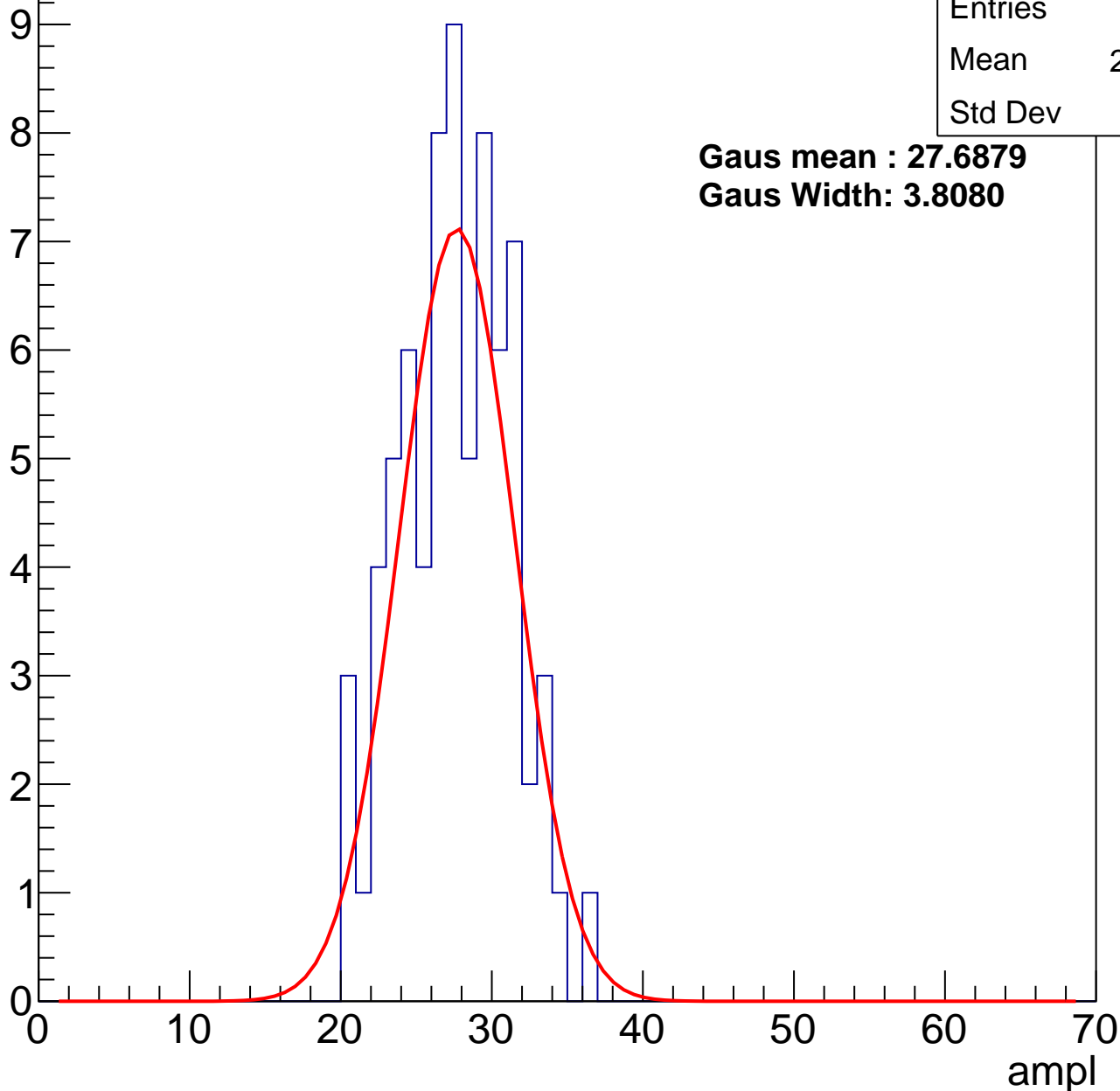
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.14
Std Dev	3.57

Gaus mean : 27.6879

Gaus Width: 3.8080



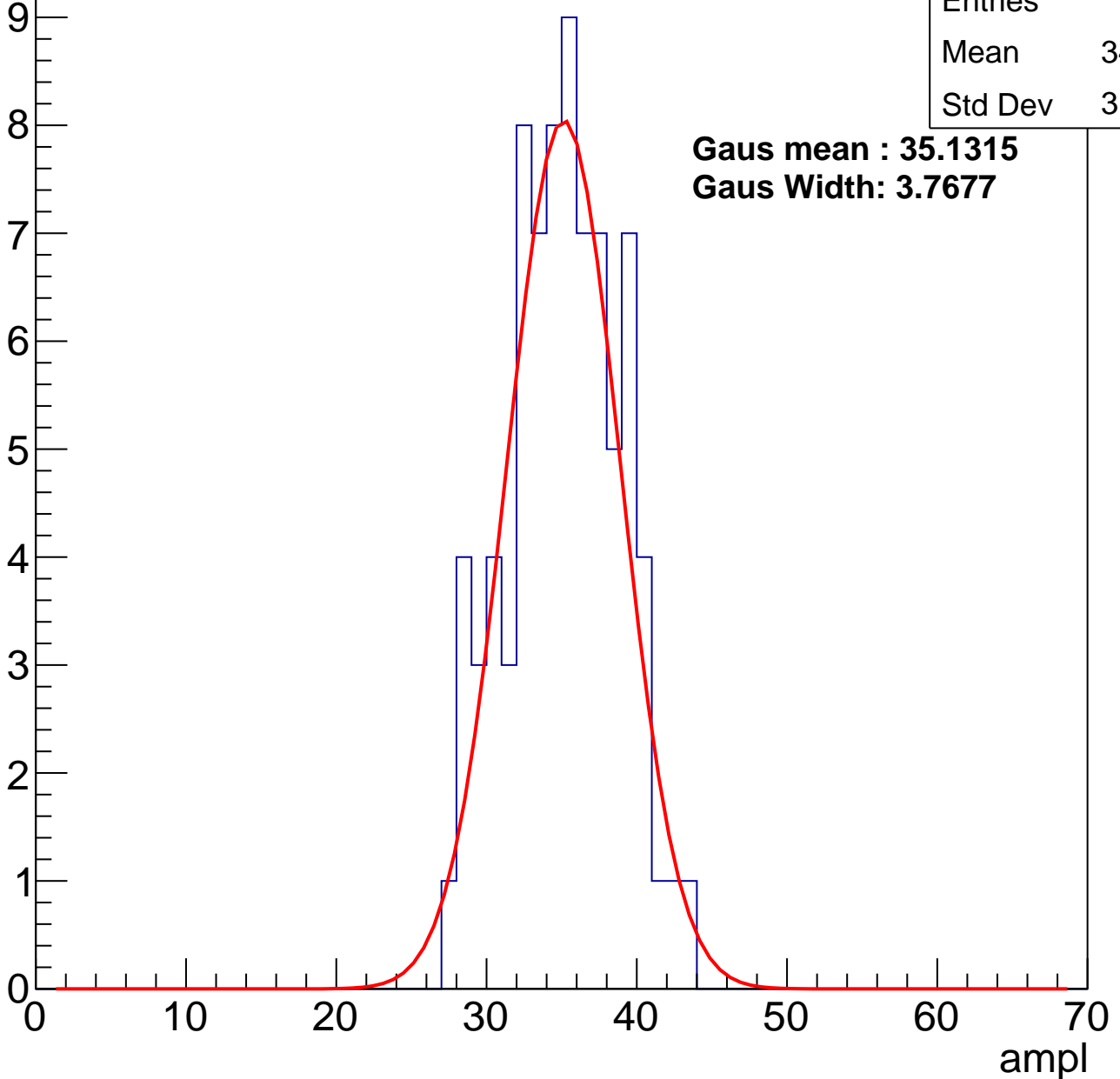
B1L101S, U4-ch115, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	34.66
Std Dev	3.633

Gaus mean : 35.1315
Gaus Width: 3.7677



B1L101S, U4-ch115, adc2

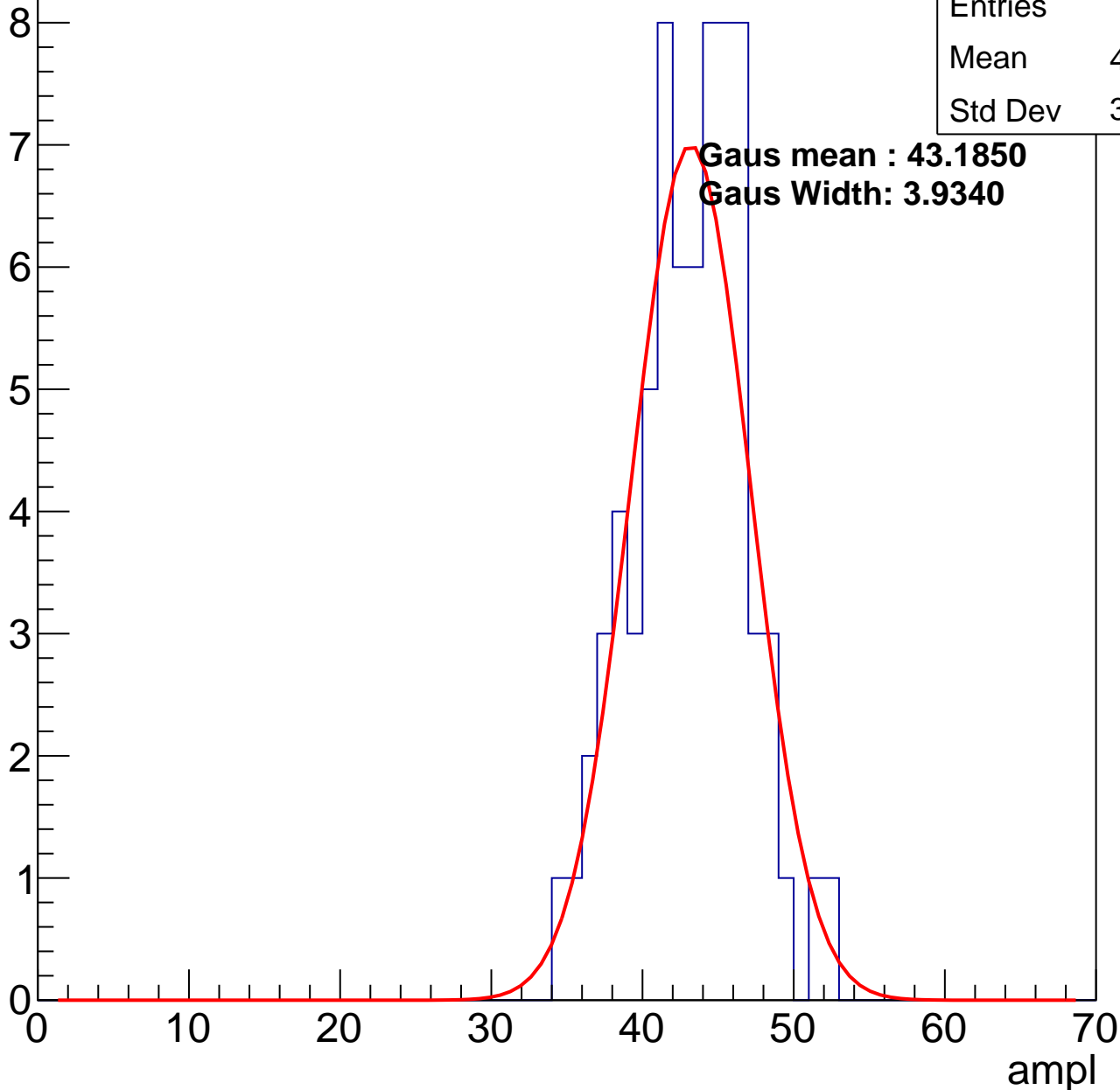
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.72
Std Dev	3.713

Gaus mean : 43.1850

Gaus Width: 3.9340

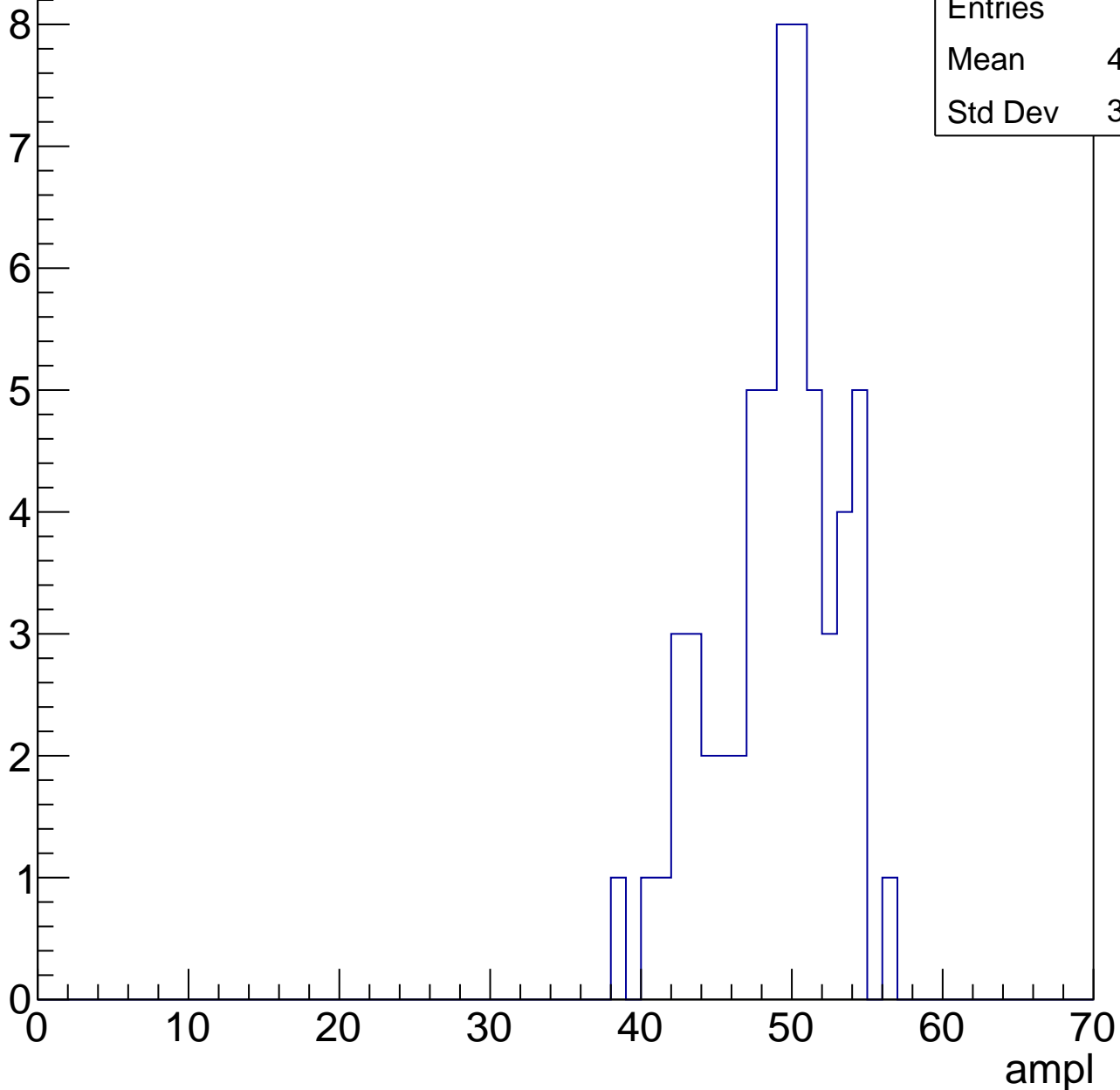


B1L101S, U4-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	48.47
Std Dev	3.955

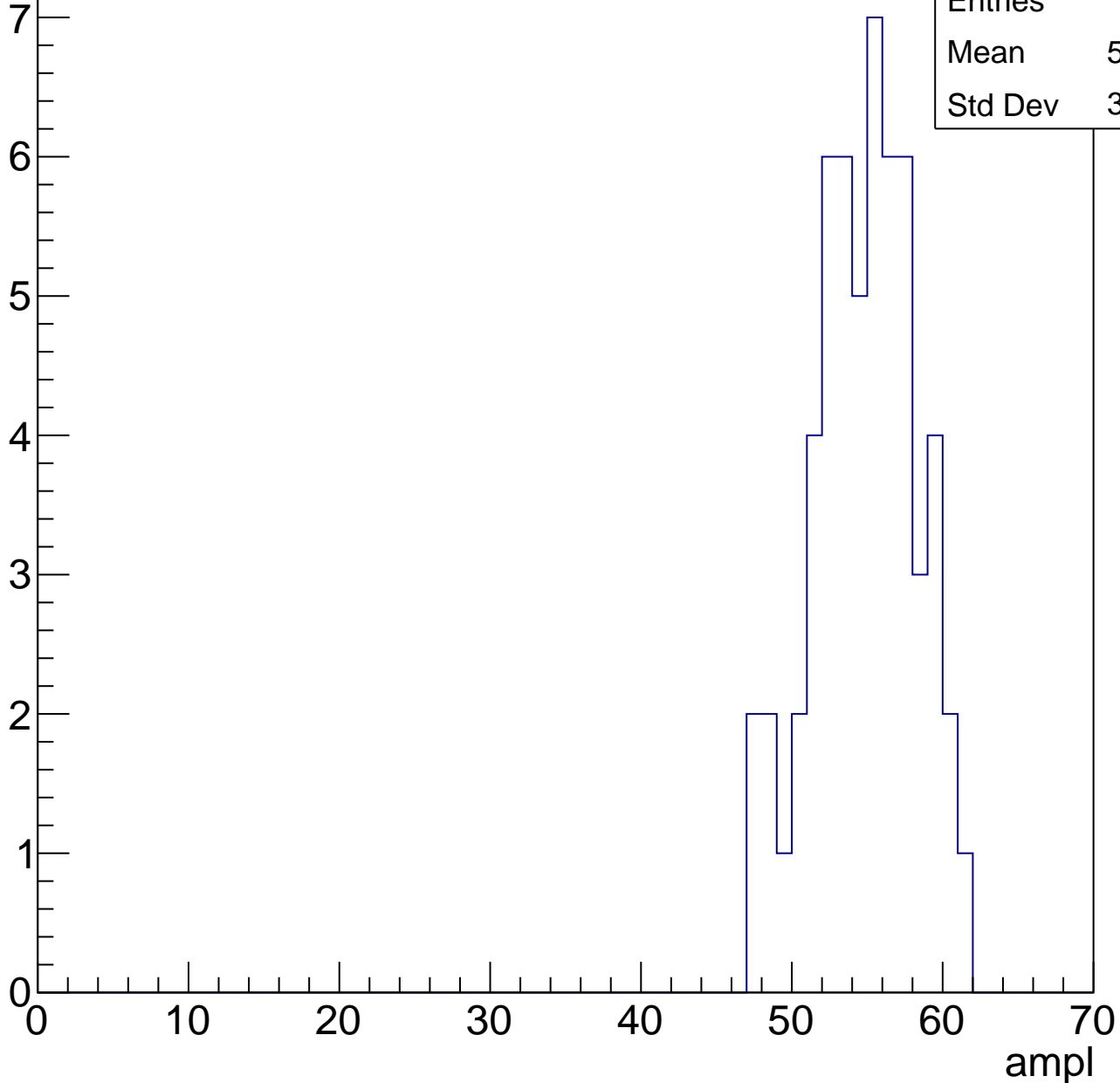


B1L101S, U4-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	54.33
Std Dev	3.353



B1L101S, U4-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	49
Mean	58.47
Std Dev	8.753

0

10

20

30

40

50

60

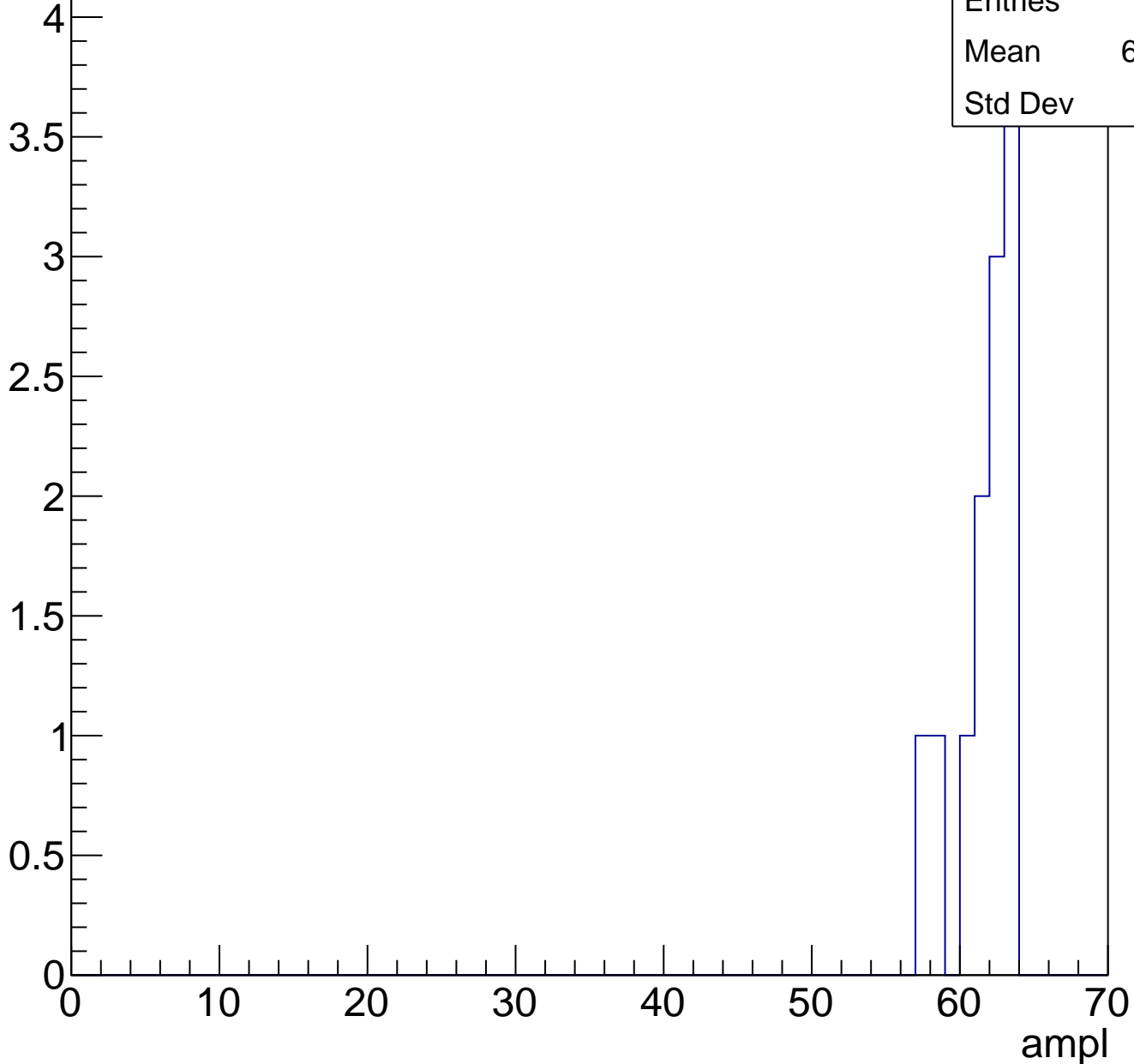
70

ampl

B1L101S, U4-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch116, adc0

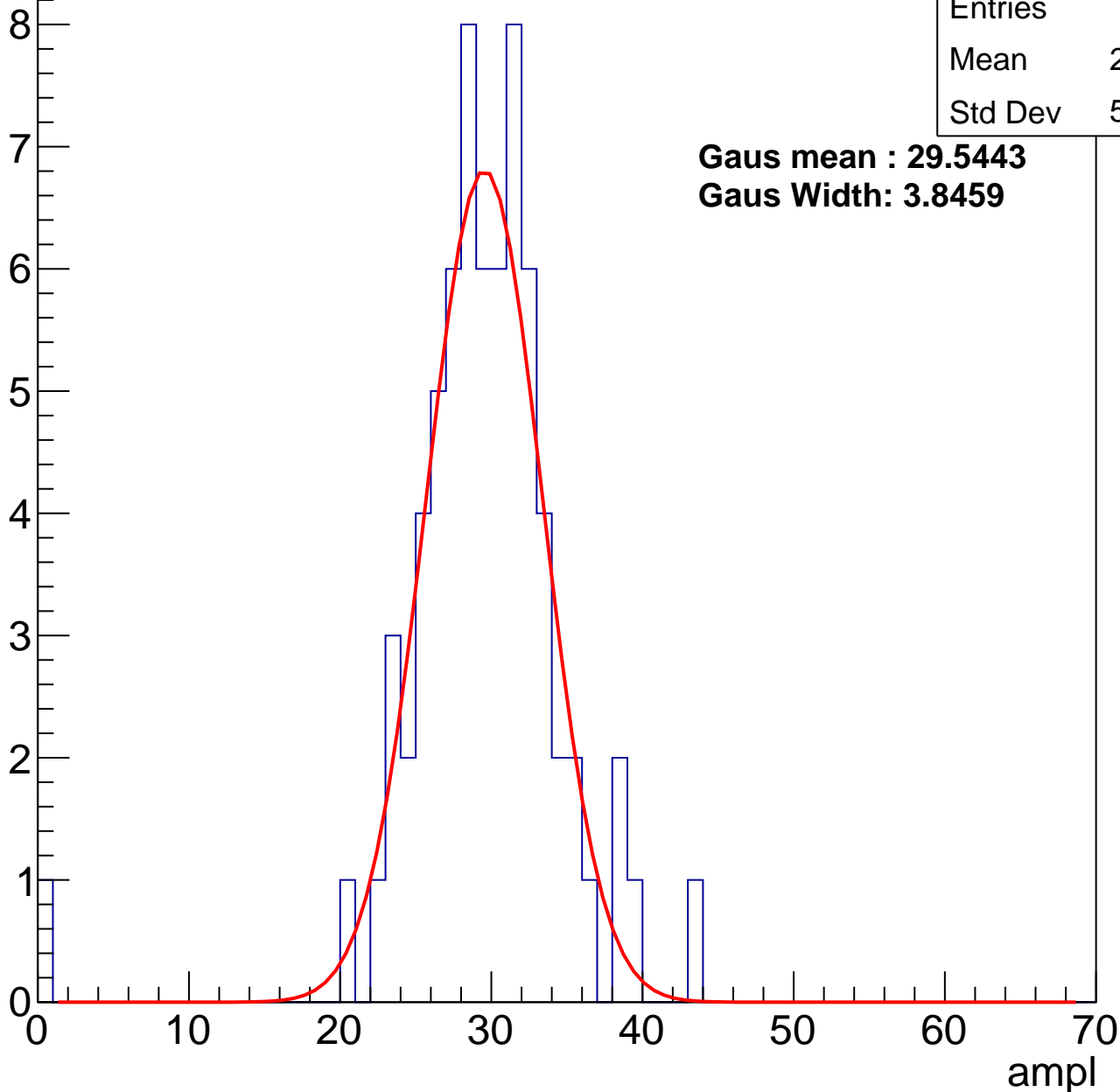
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.04
Std Dev	5.418

Gaus mean : 29.5443

Gaus Width: 3.8459



B1L101S, U4-ch116, adc1

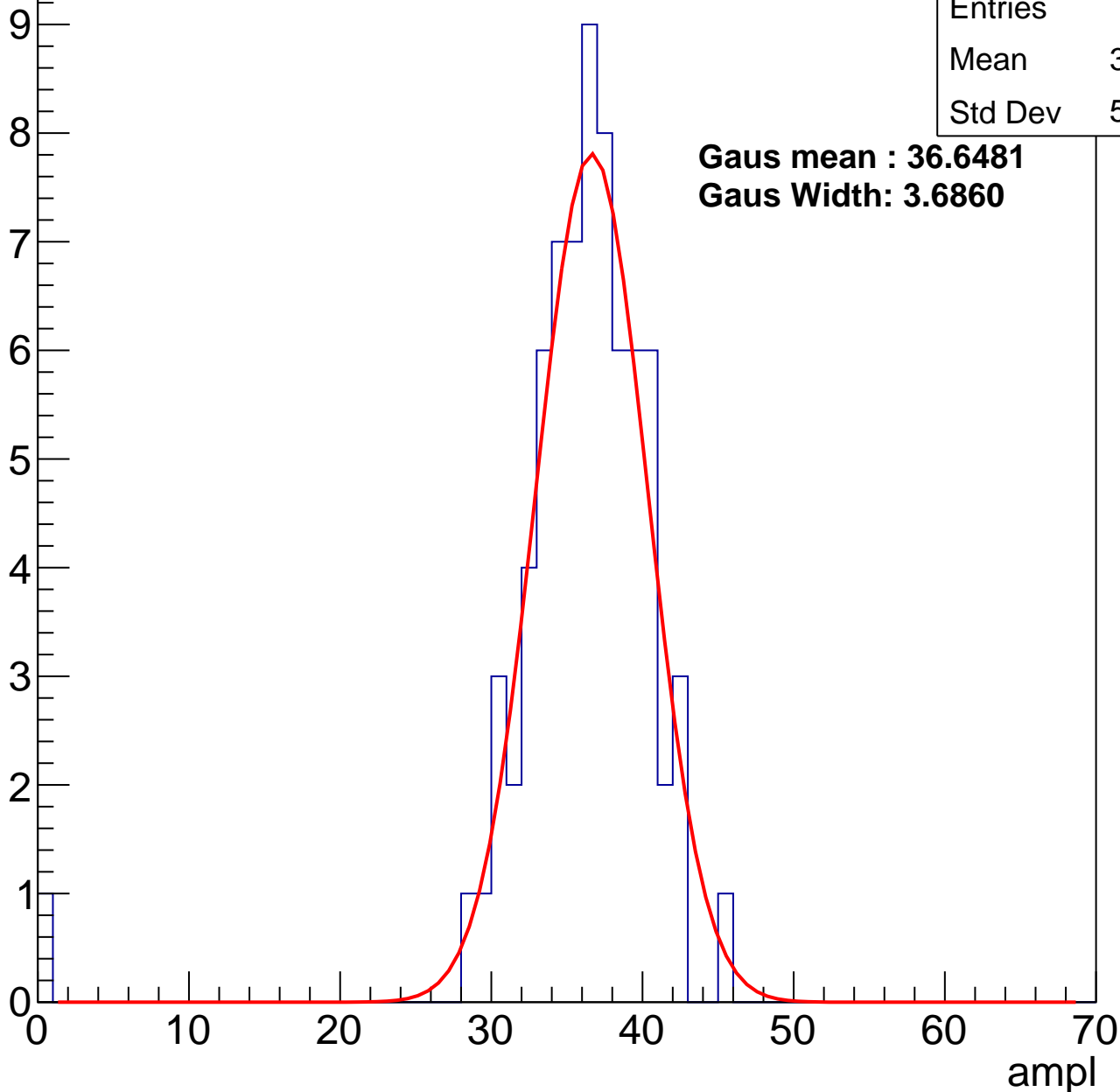
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	35.52
Std Dev	5.394

Gaus mean : 36.6481

Gaus Width: 3.6860



B1L101S, U4-ch116, adc2

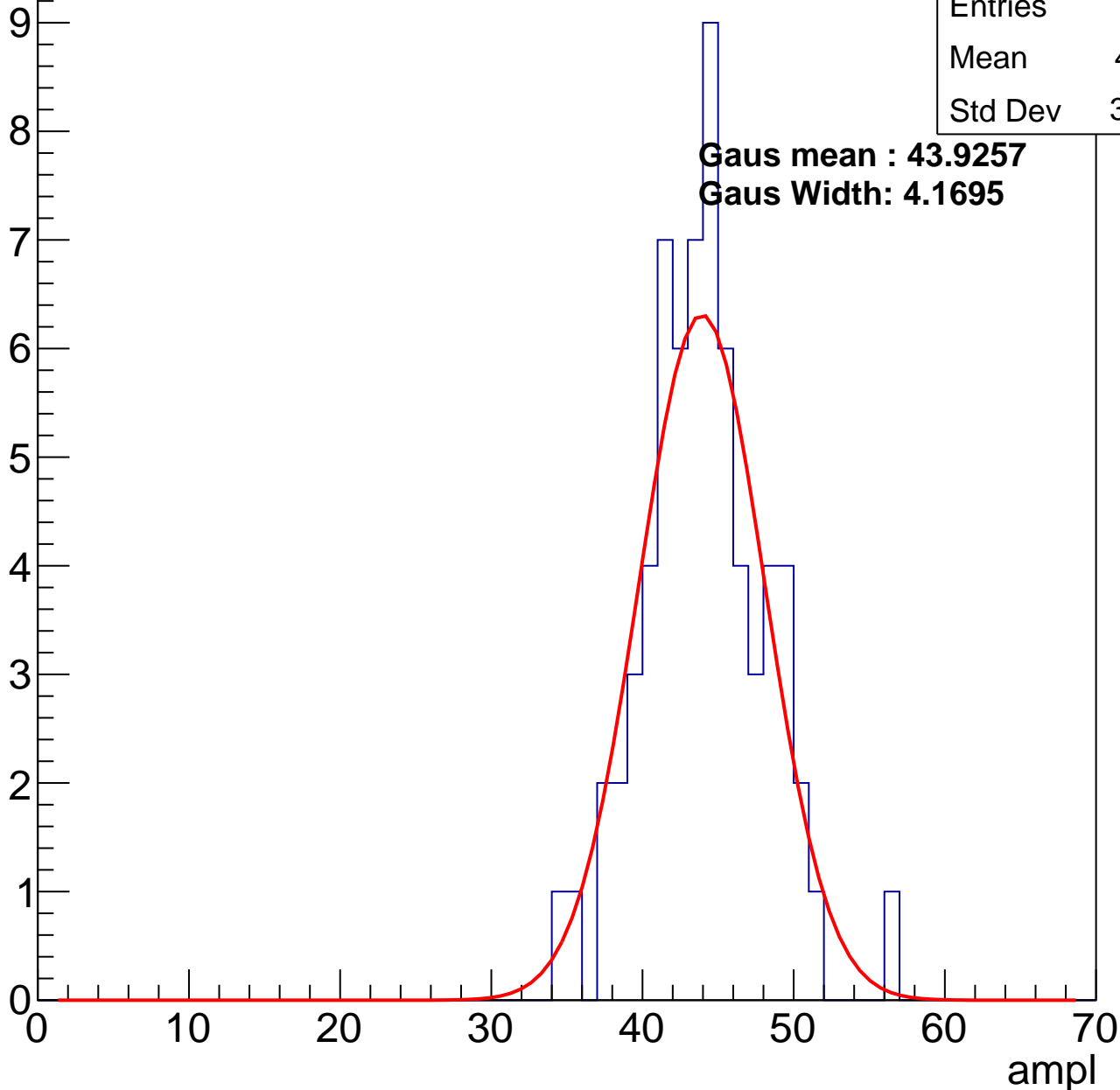
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.61
Std Dev	3.962

Gaus mean : 43.9257

Gaus Width: 4.1695

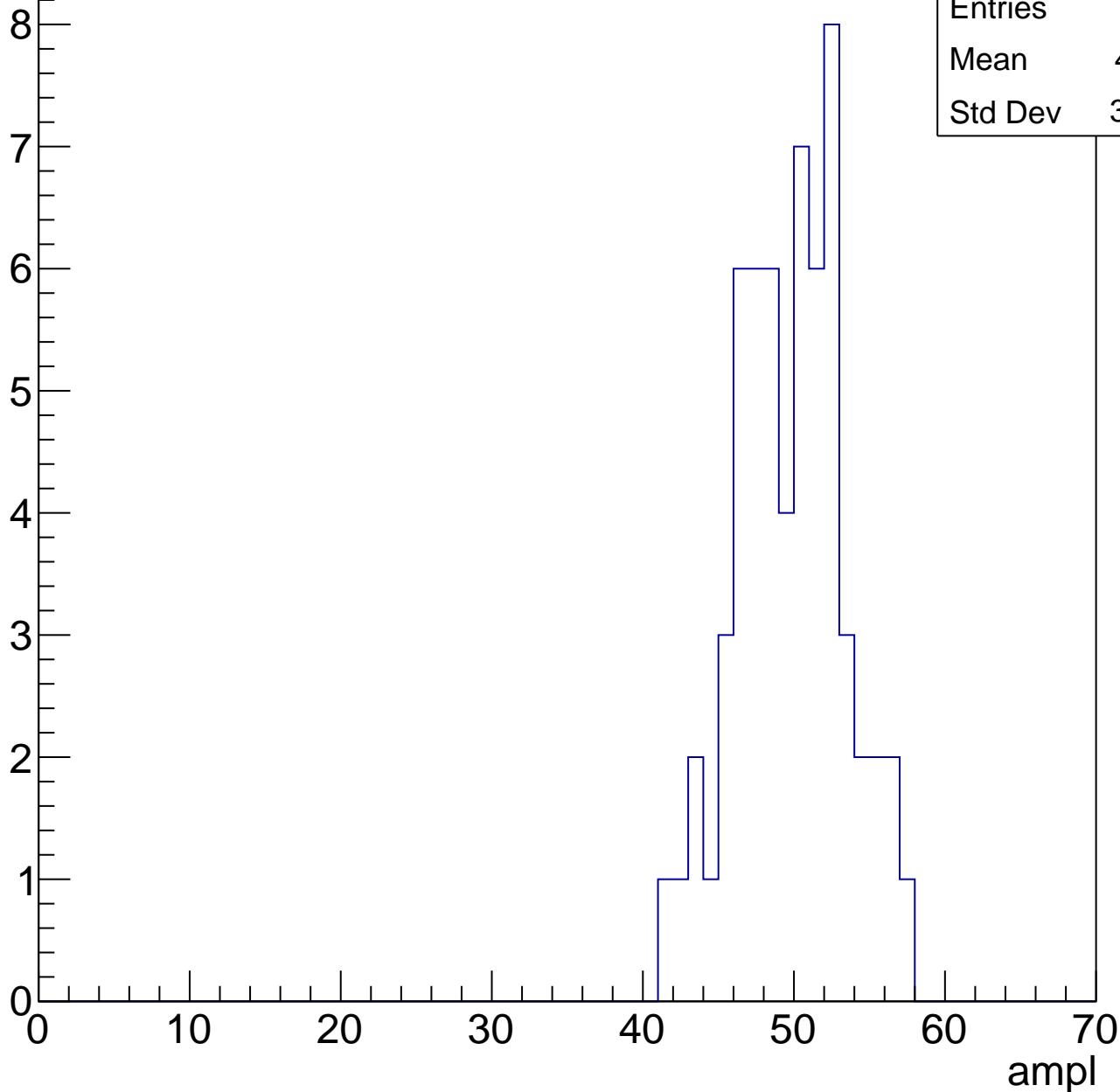


B1L101S, U4-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.31
Std Dev	3.555

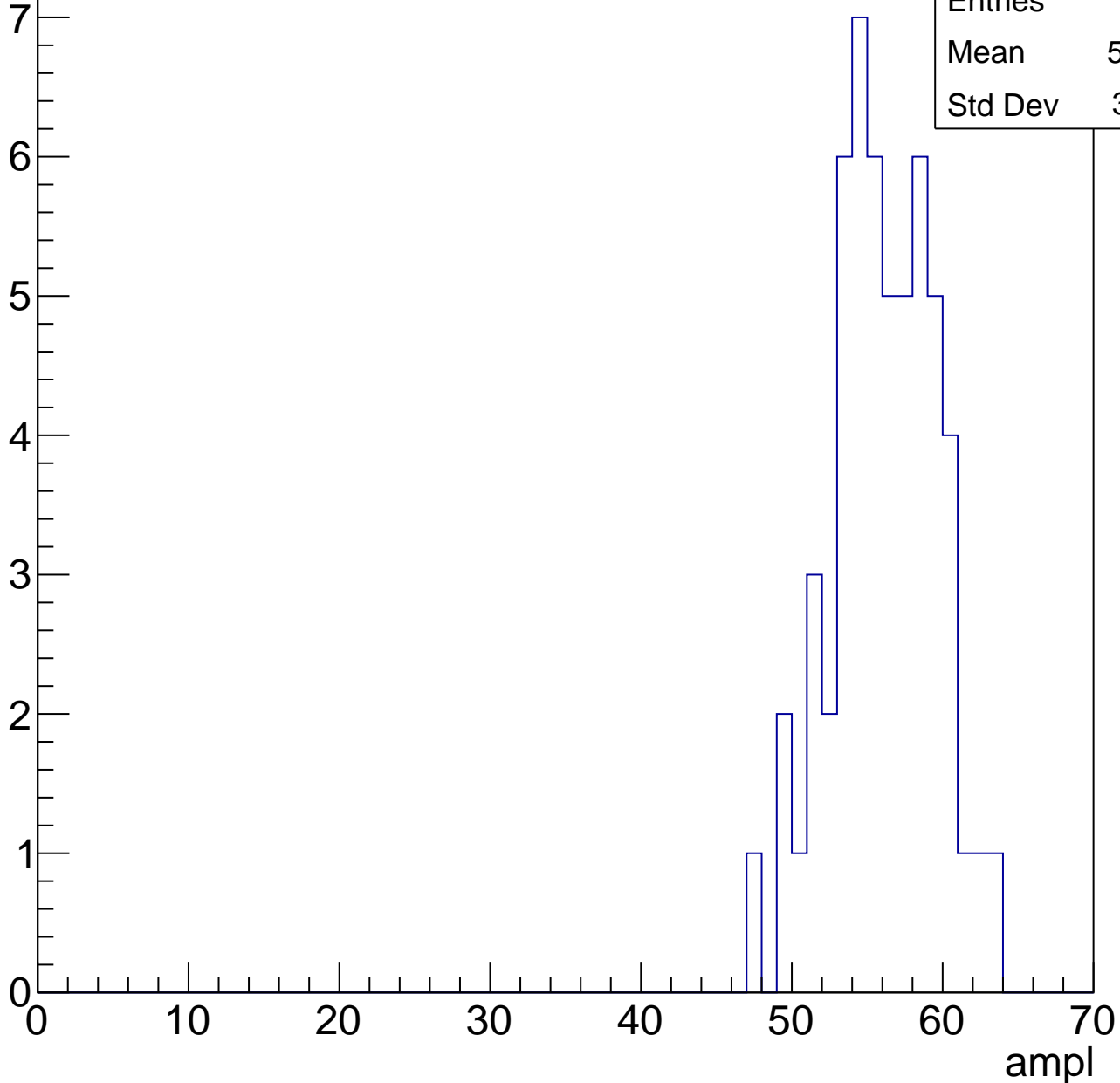


B1L101S, U4-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

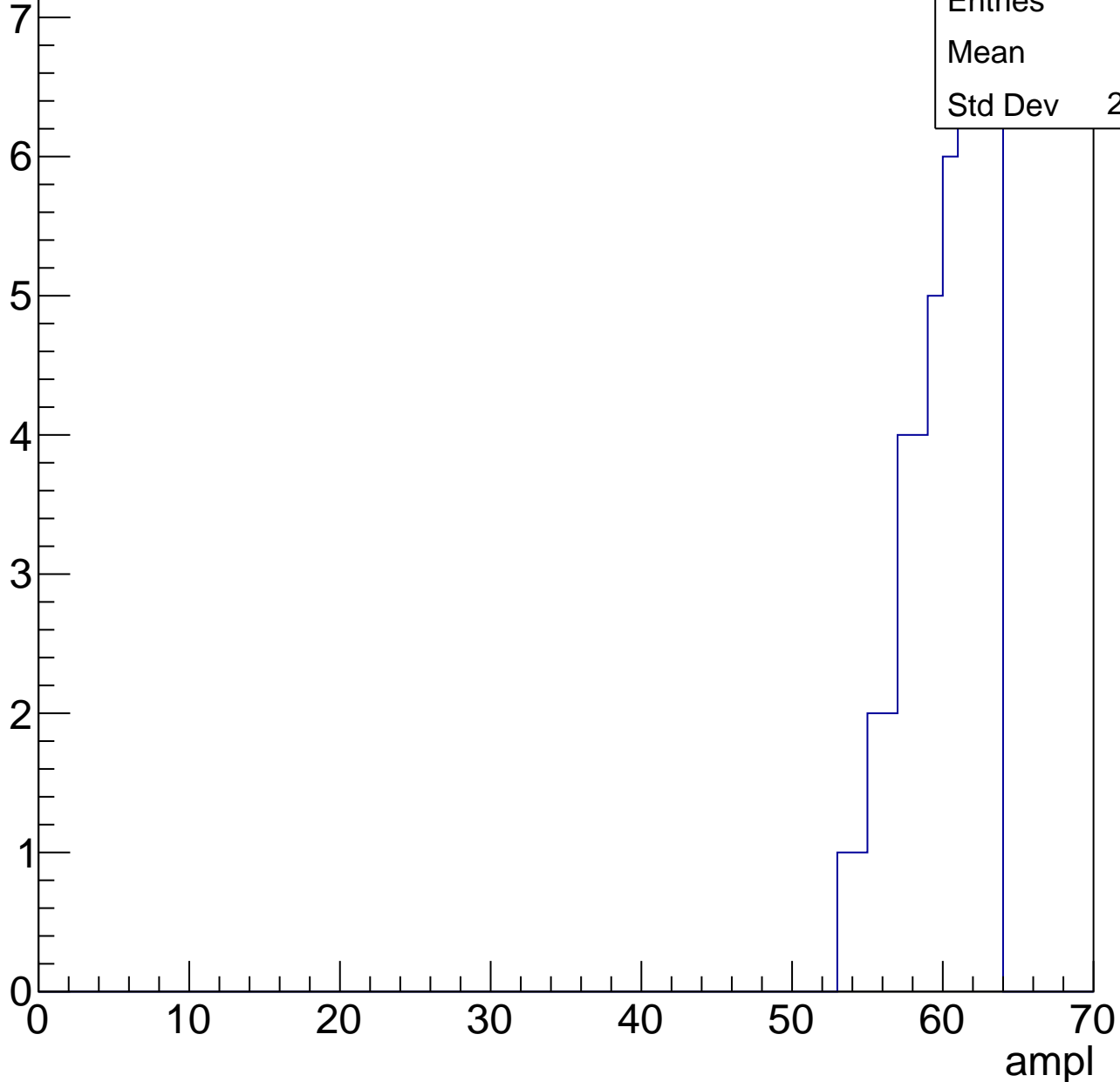
Entries	56
Mean	55.57
Std Dev	3.401



B1L101S, U4-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	46
Mean	59.7
Std Dev	2.628

B1L101S, U4-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	62
Std Dev	0.8165

ampl

B1L101S, U4-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U4-ch117, adc0

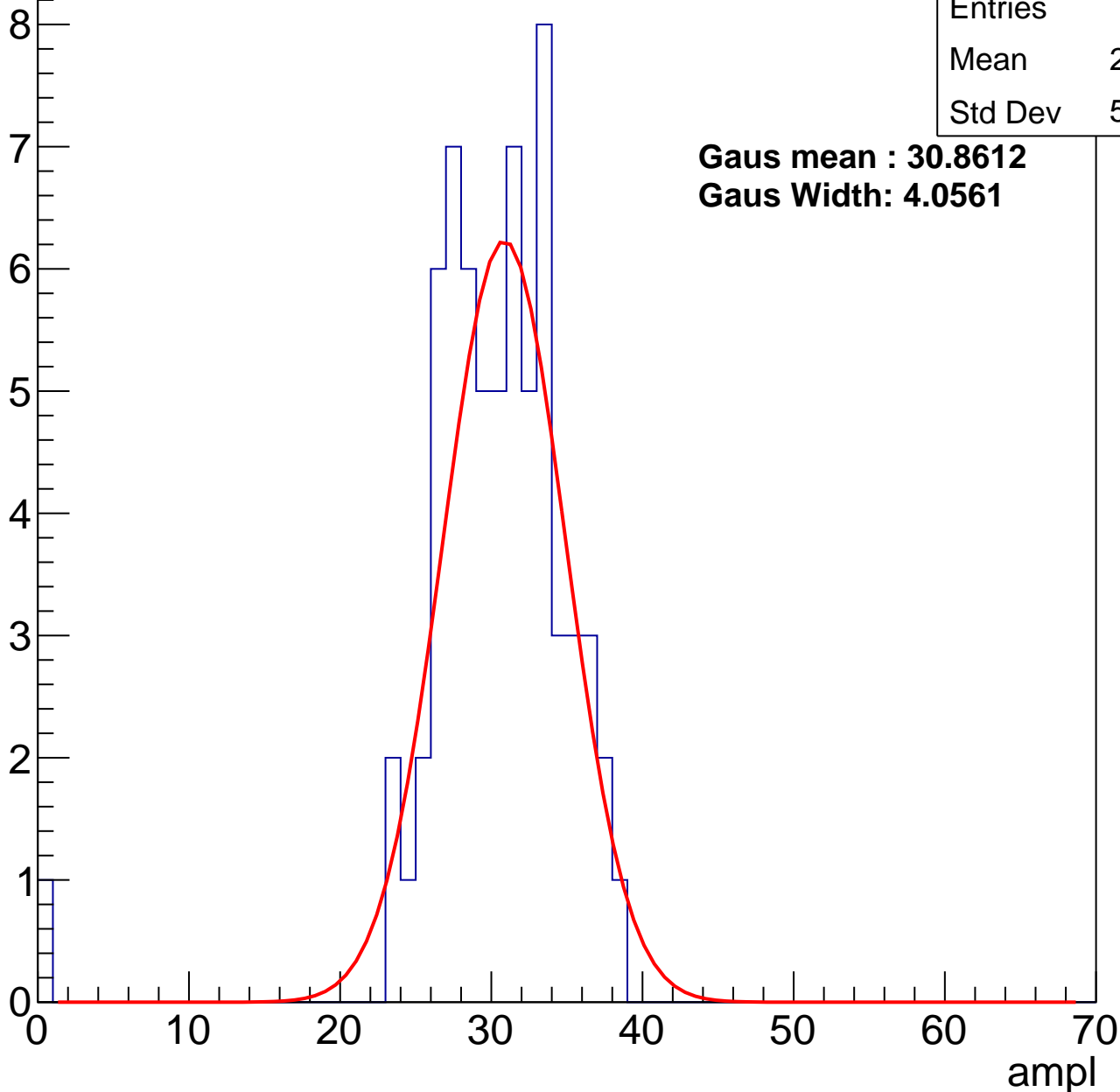
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.79
Std Dev	5.127

Gaus mean : 30.8612

Gaus Width: 4.0561



B1L101S, U4-ch117, adc1

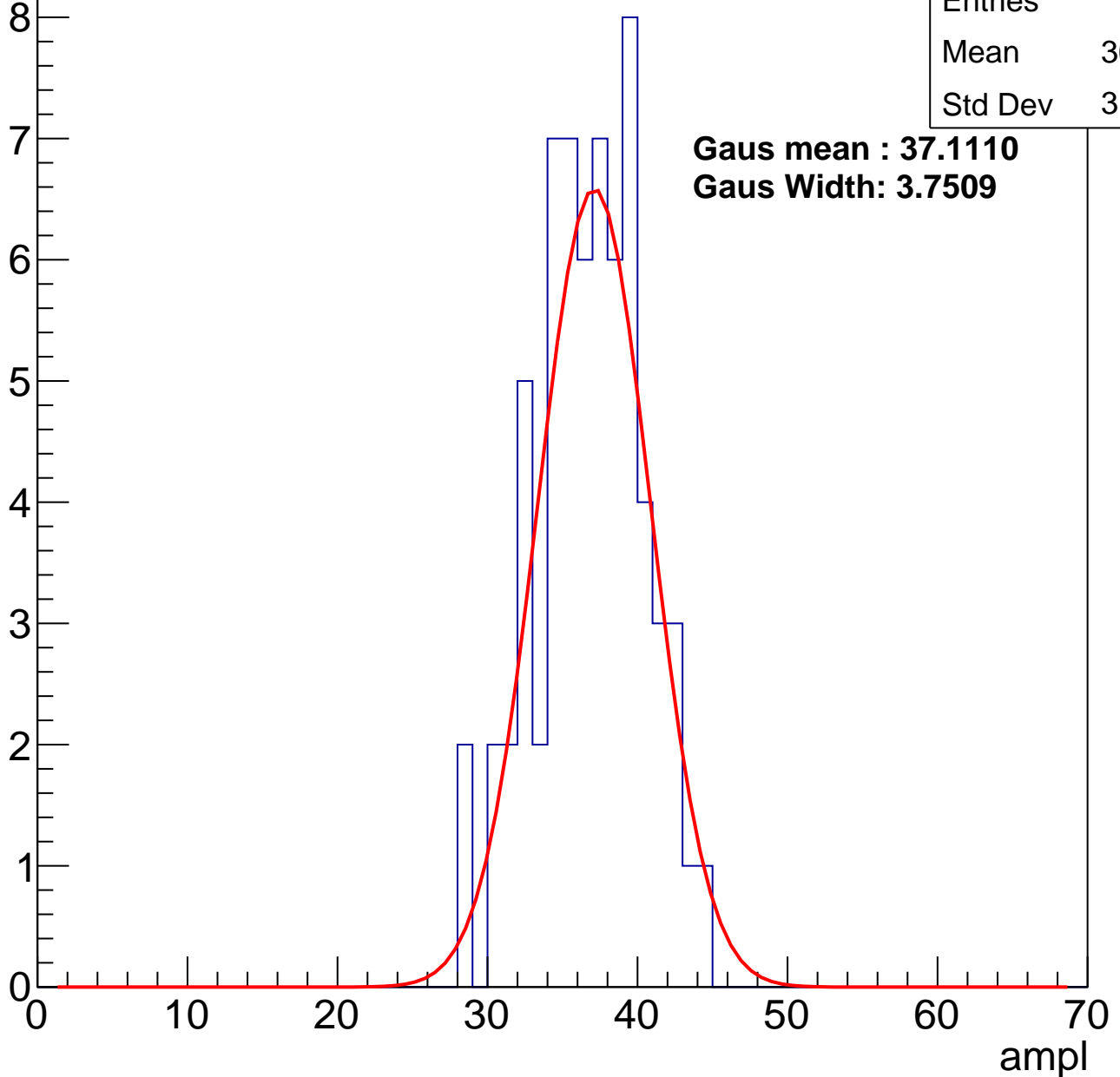
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.33
Std Dev	3.565

Gaus mean : 37.1110

Gaus Width: 3.7509



B1L101S, U4-ch117, adc2

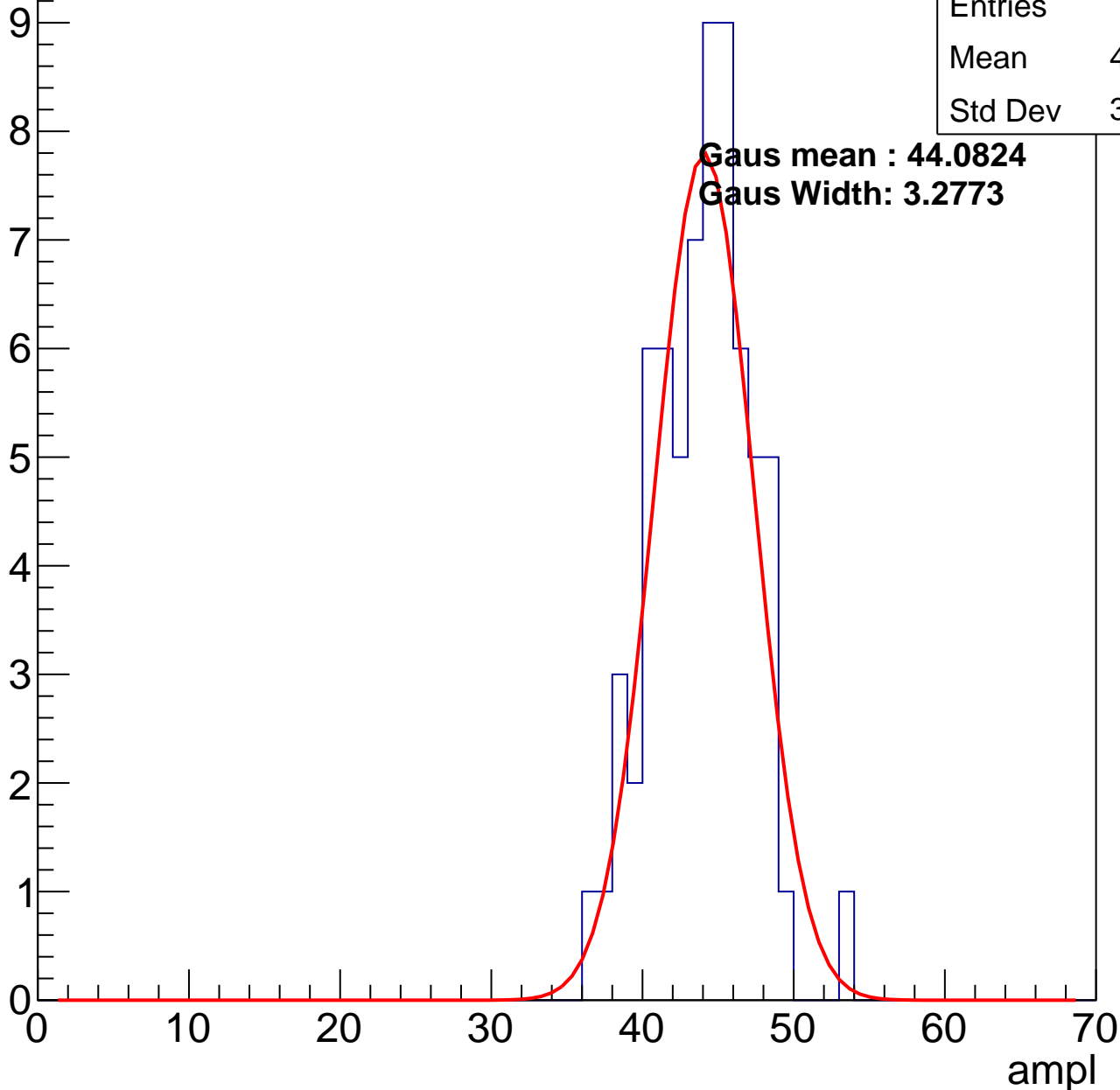
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.52
Std Dev	3.225

Gaus mean : 44.0824

Gaus Width: 3.2773

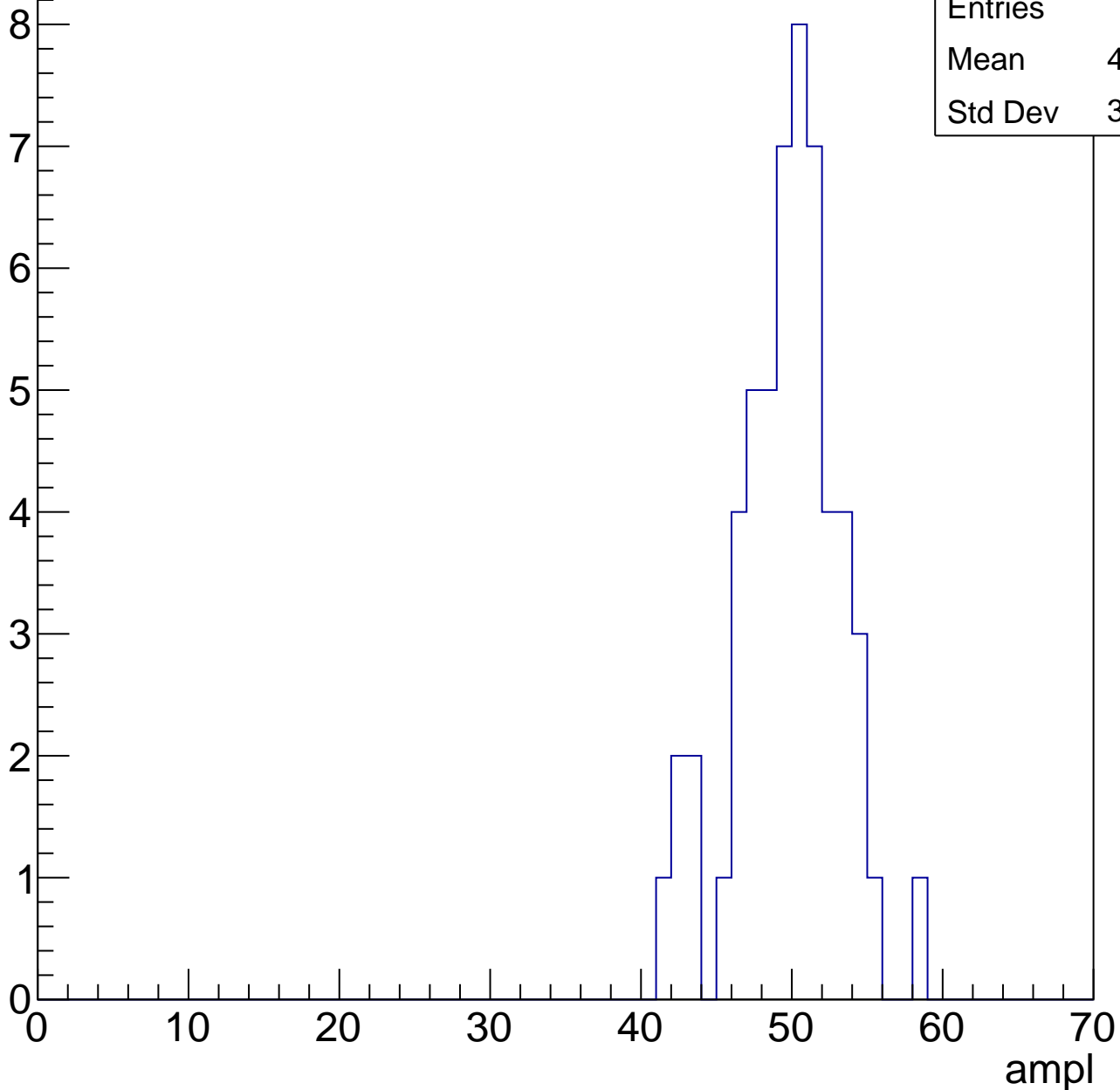


B1L101S, U4-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	49.27
Std Dev	3.398

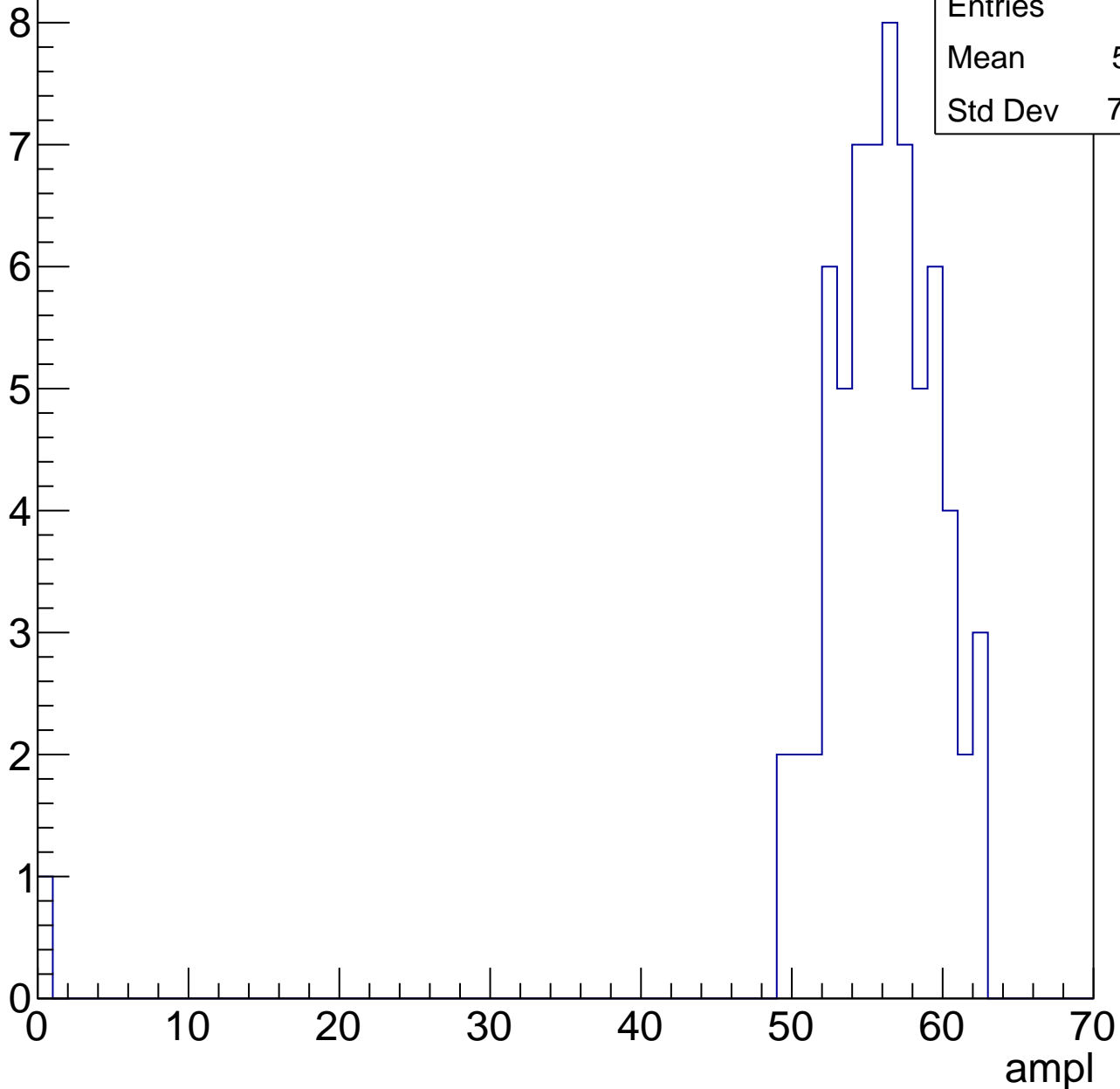


B1L101S, U4-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	54.91
Std Dev	7.487

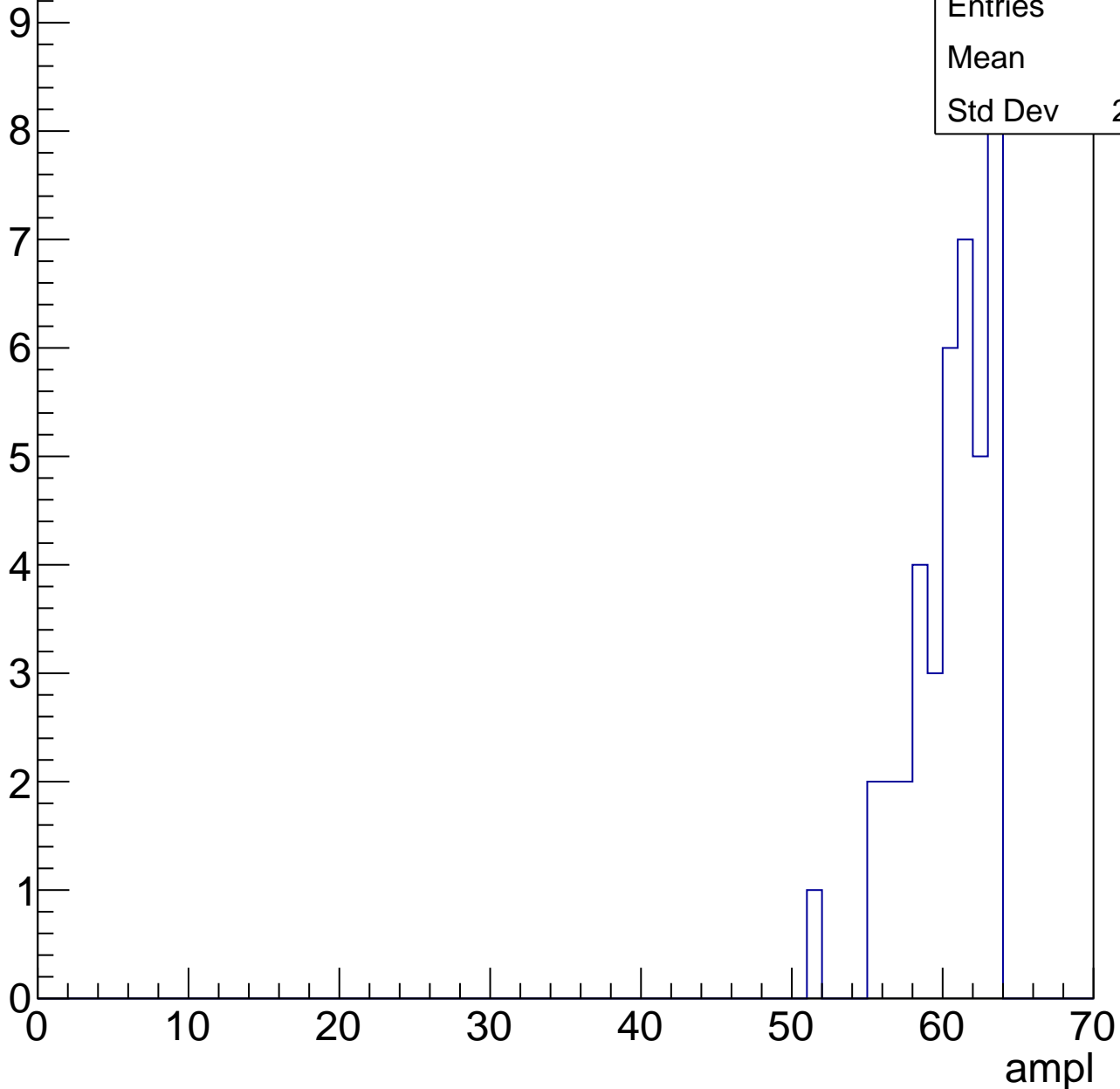


B1L101S, U4-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	60
Std Dev	2.741



B1L101S, U4-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

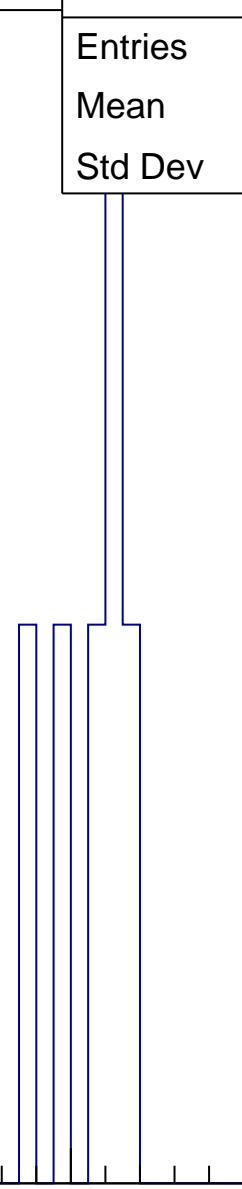
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	2.055

0 10 20 30 40 50 60 70

ampl



B1L101S, U4-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U4-ch118, adc0

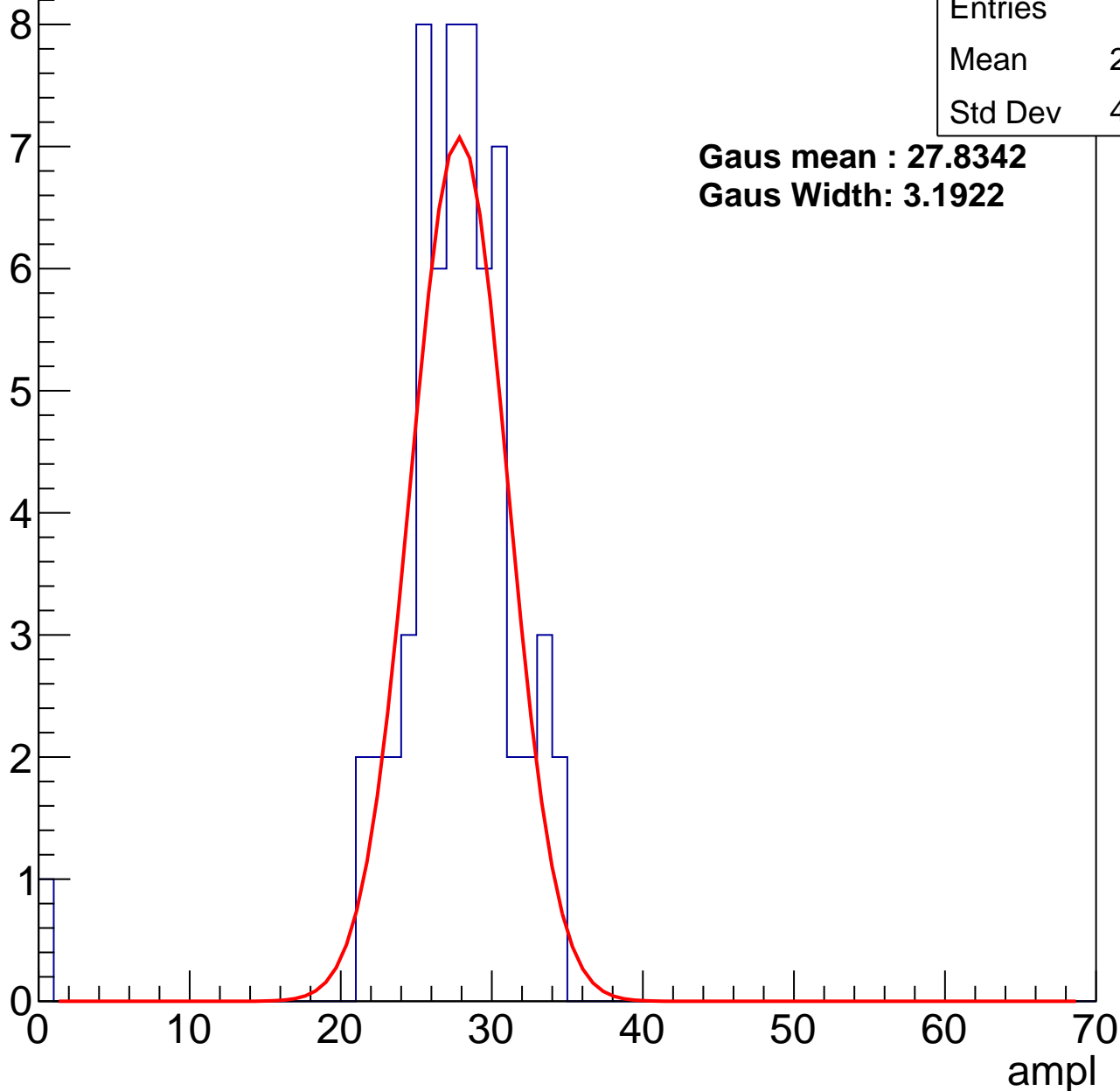
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	27.05
Std Dev	4.633

Gaus mean : 27.8342

Gaus Width: 3.1922



B1L101S, U4-ch118, adc1

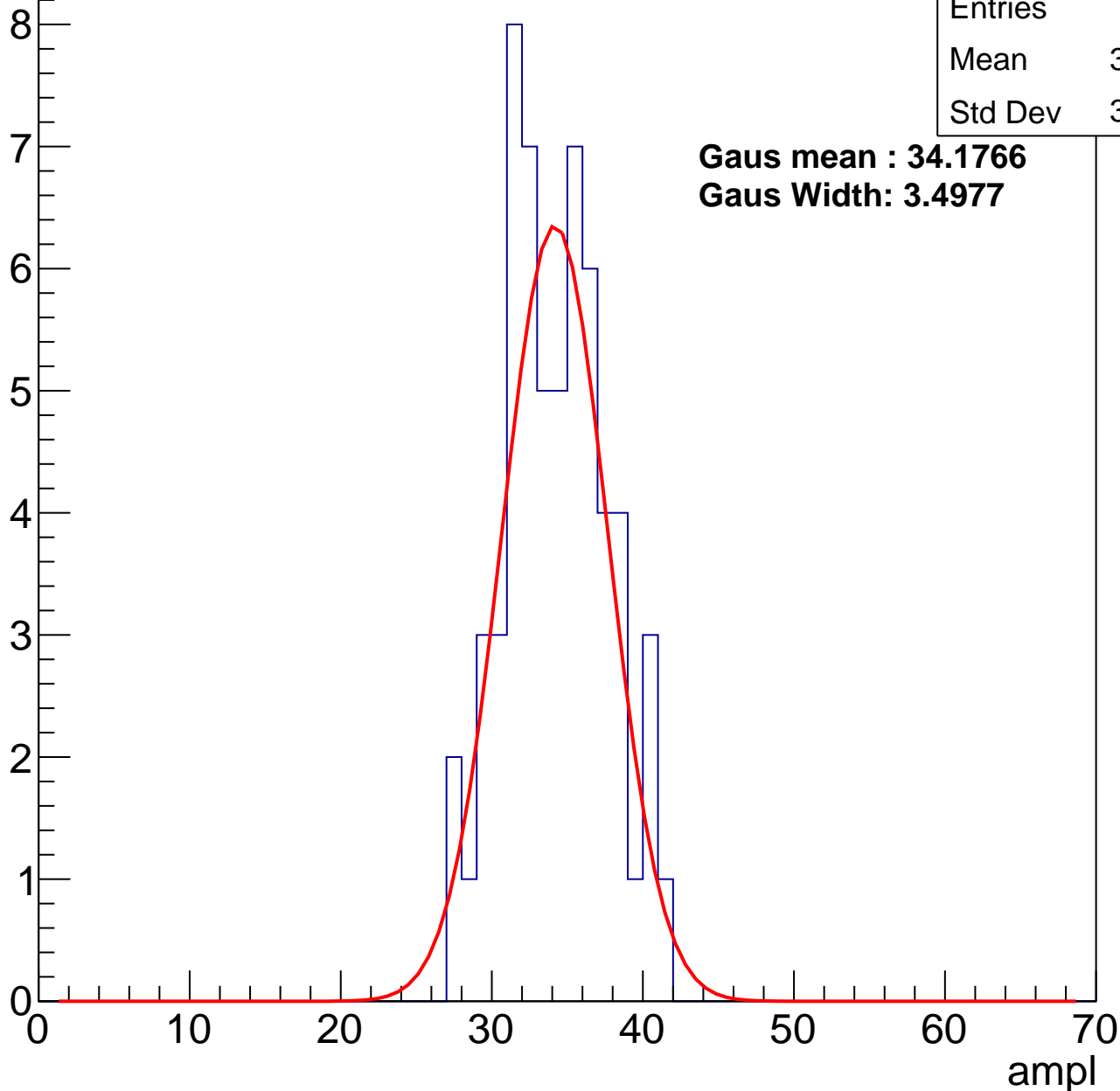
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	33.78
Std Dev	3.347

Gaus mean : 34.1766

Gaus Width: 3.4977



B1L101S, U4-ch118, adc2

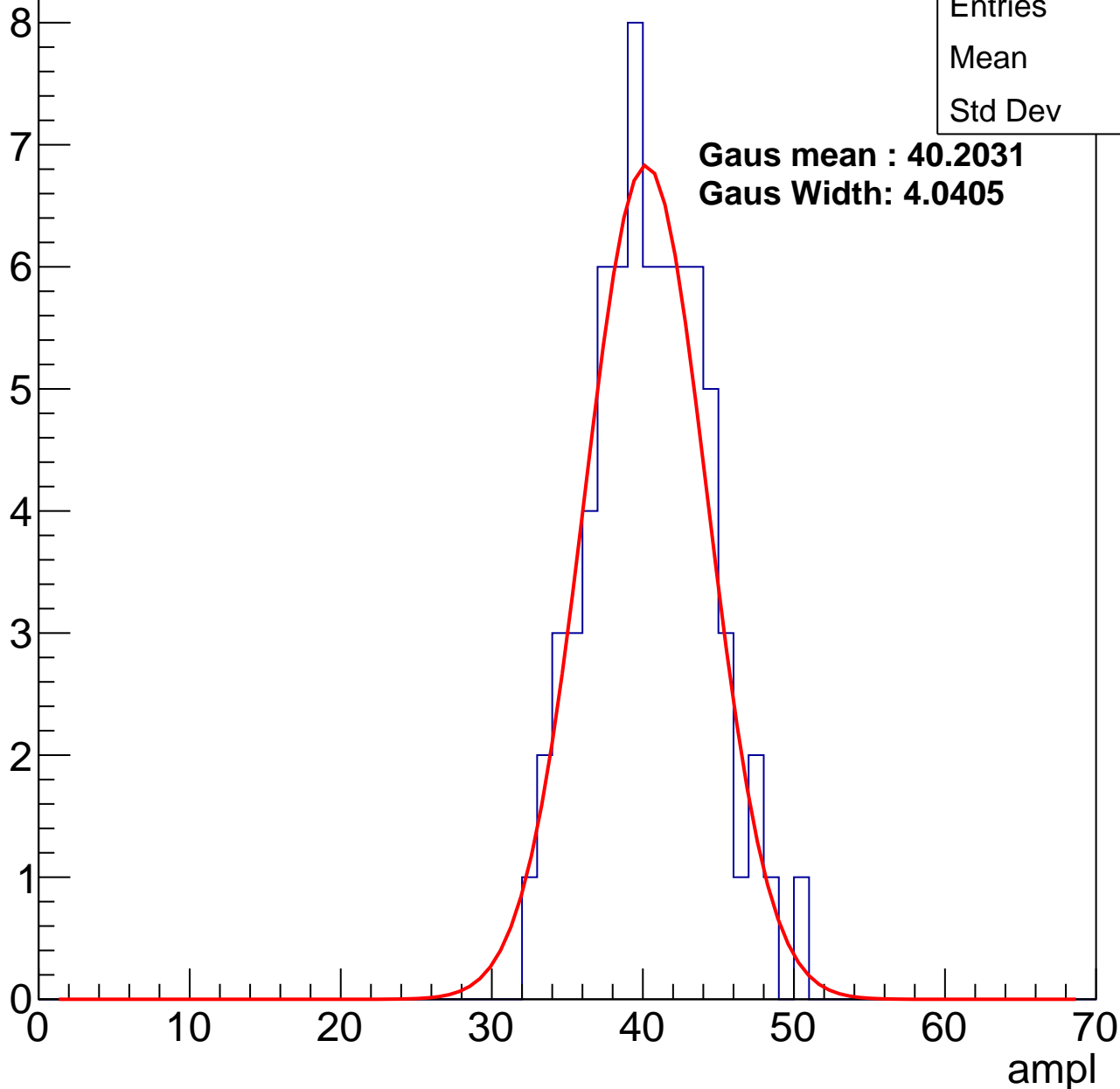
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	40
Std Dev	3.84

Gaus mean : 40.2031

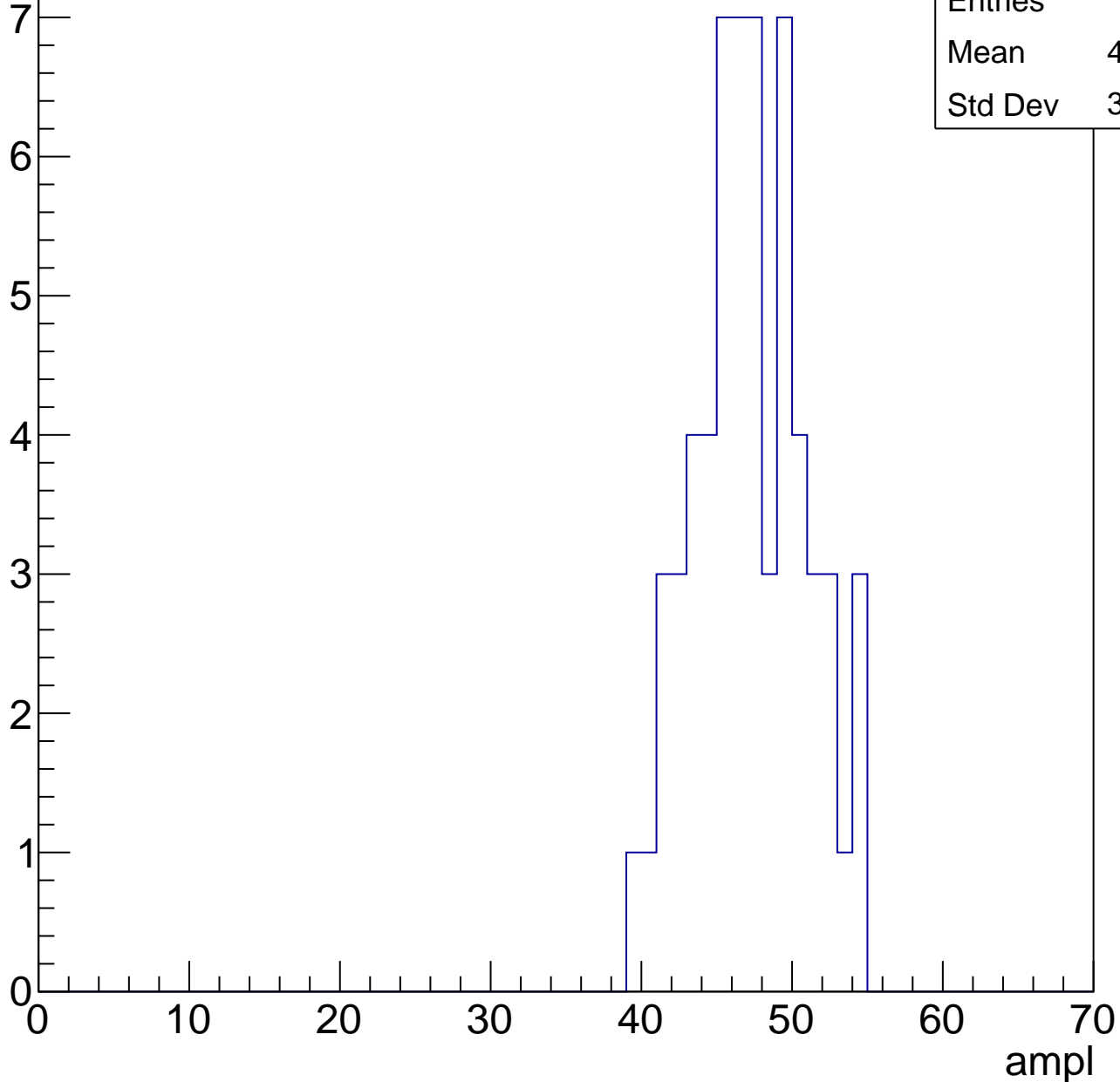
Gaus Width: 4.0405



B1L101S, U4-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



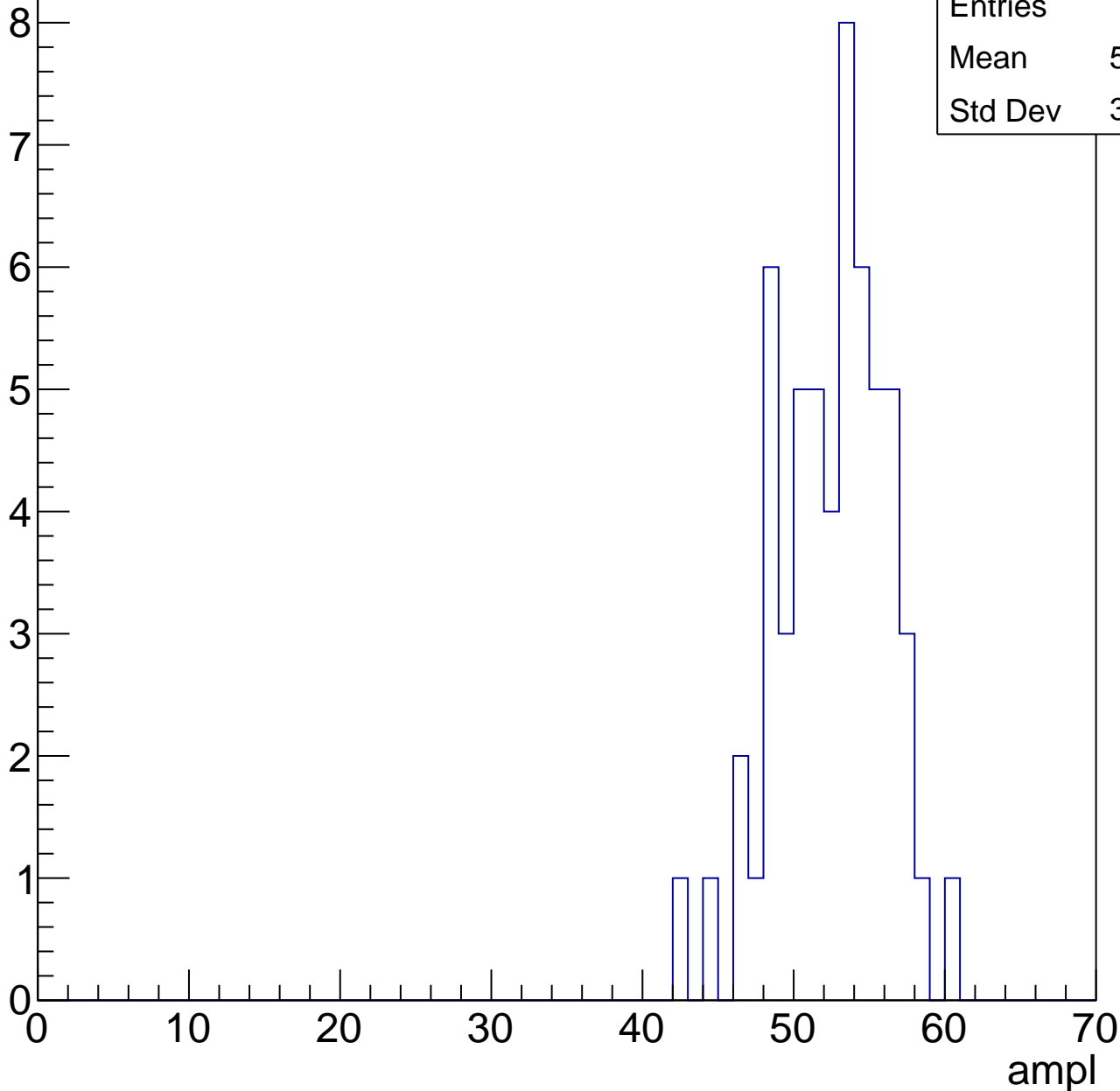
Entries	61
Mean	46.77
Std Dev	3.623

B1L101S, U4-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

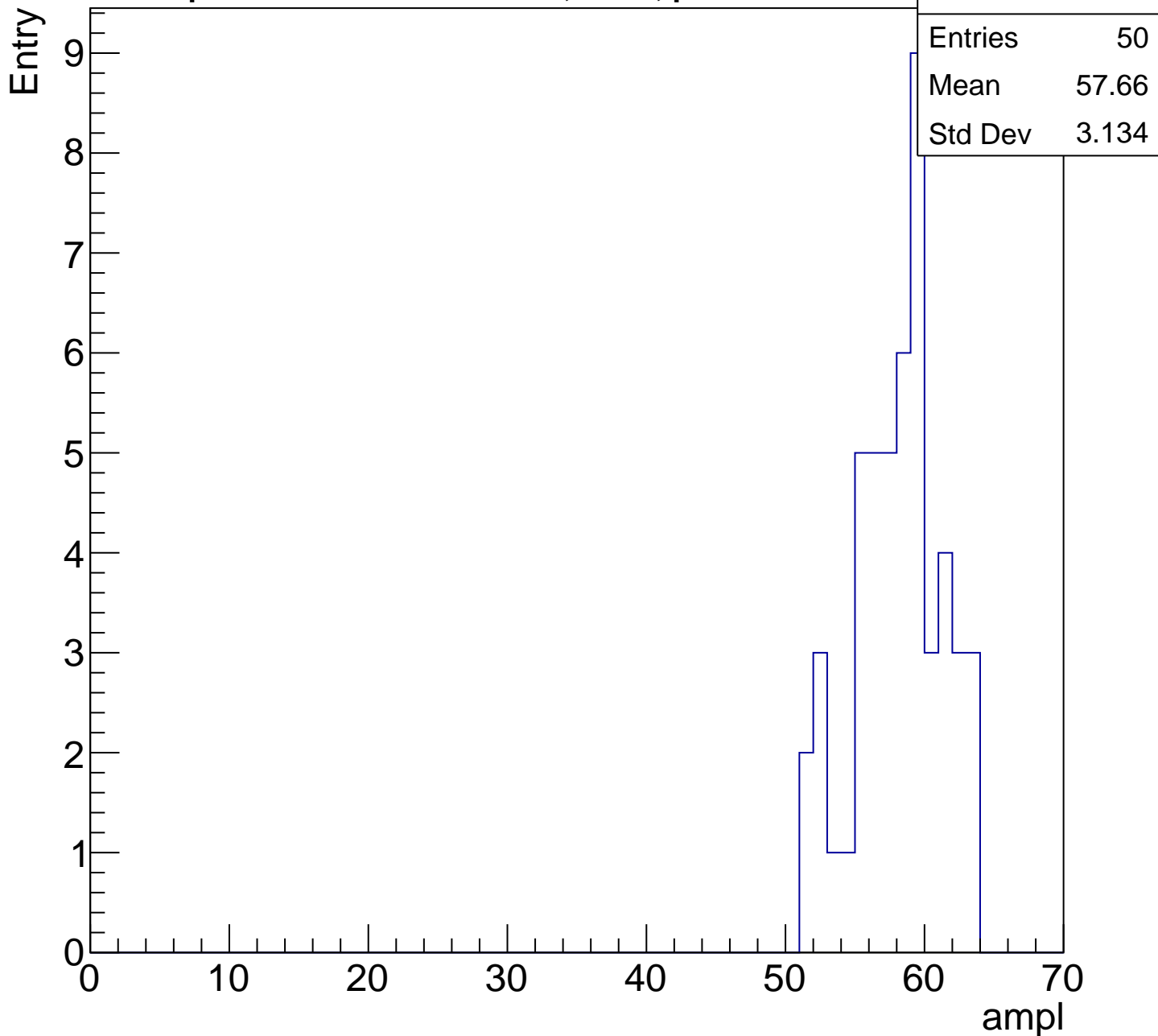
Entry

Entries	57
Mean	52.02
Std Dev	3.606



B1L101S, U4-ch118, adc5

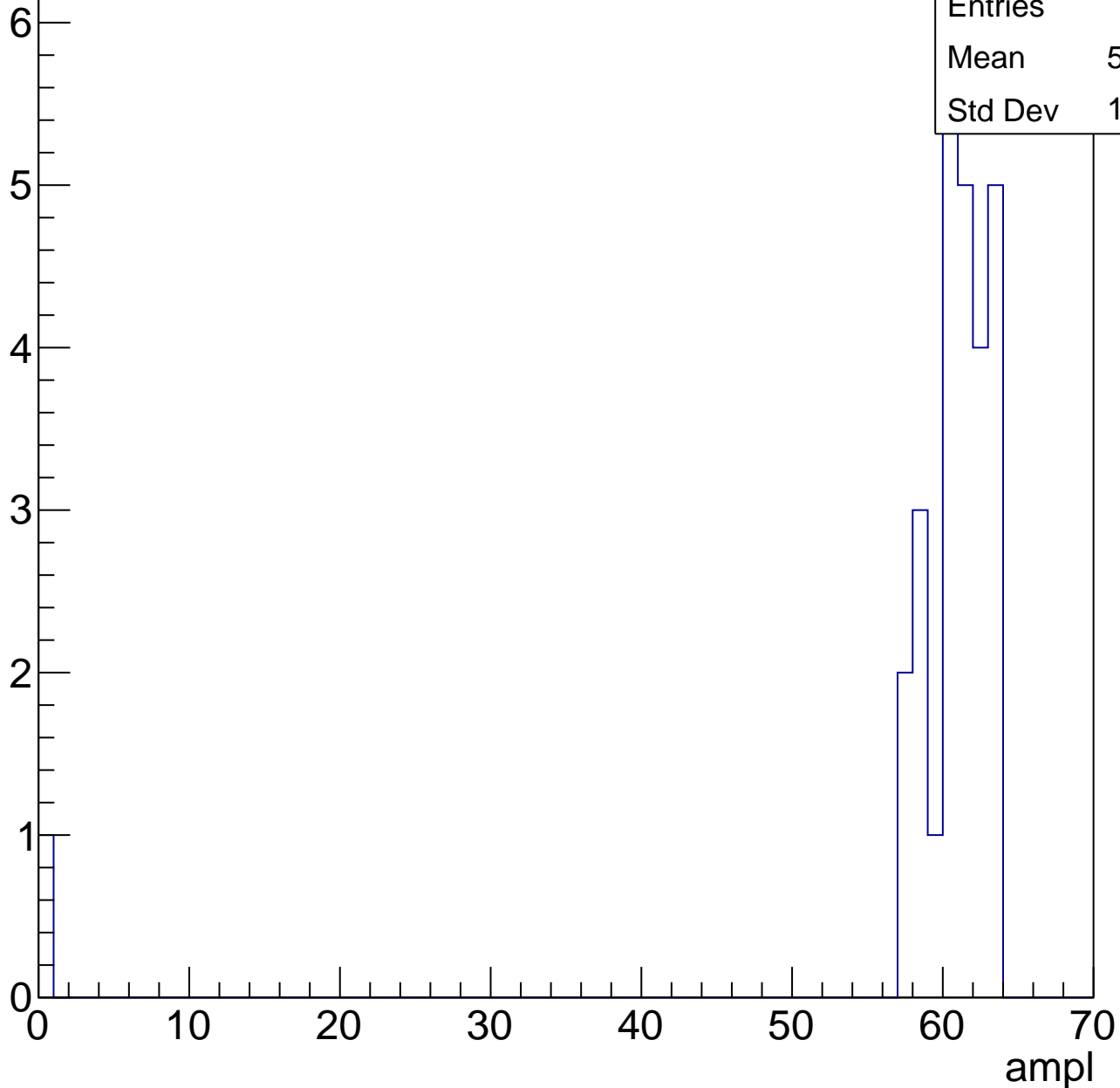
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U4-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

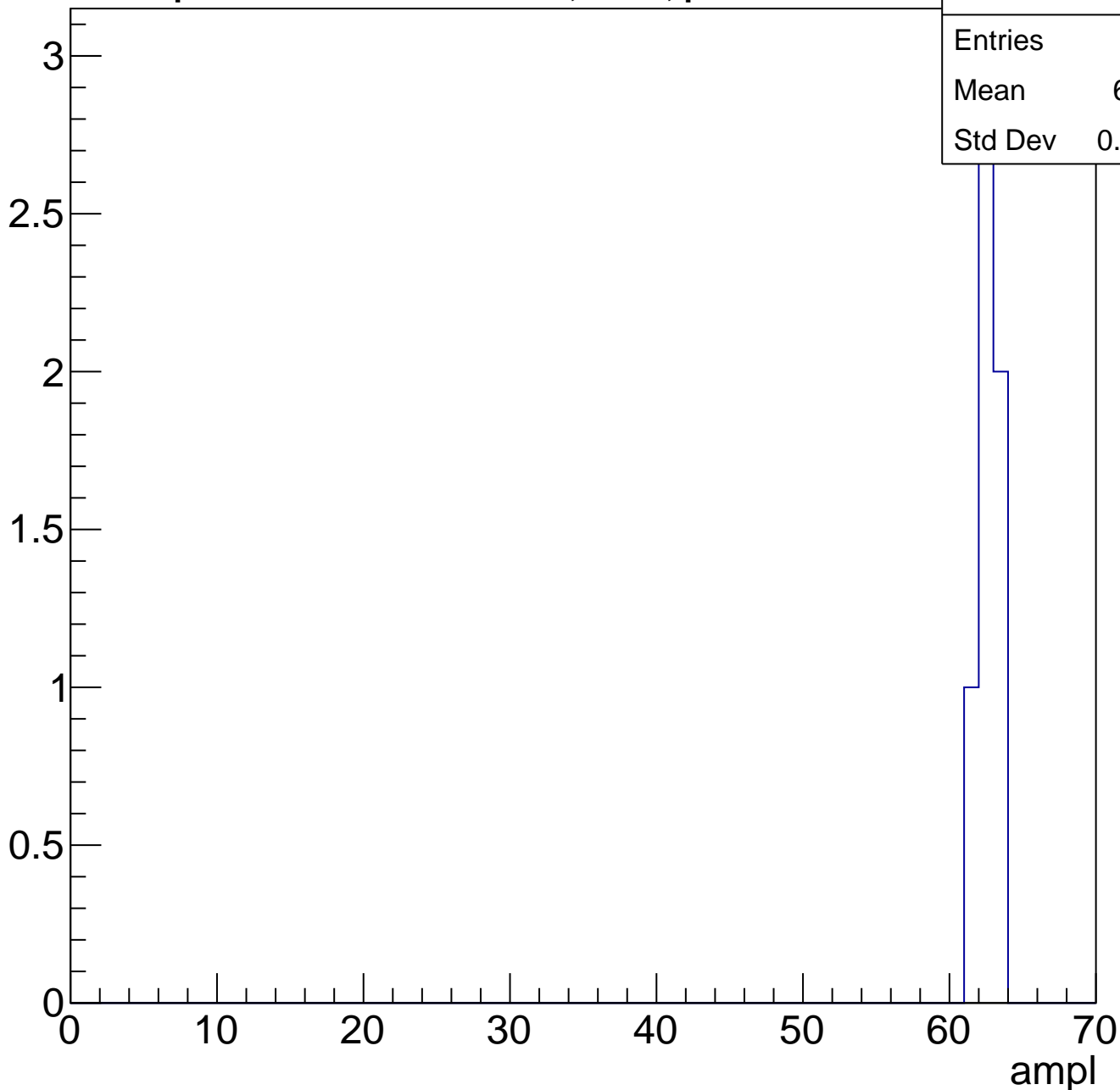
Entry



B1L101S, U4-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch119, adc0

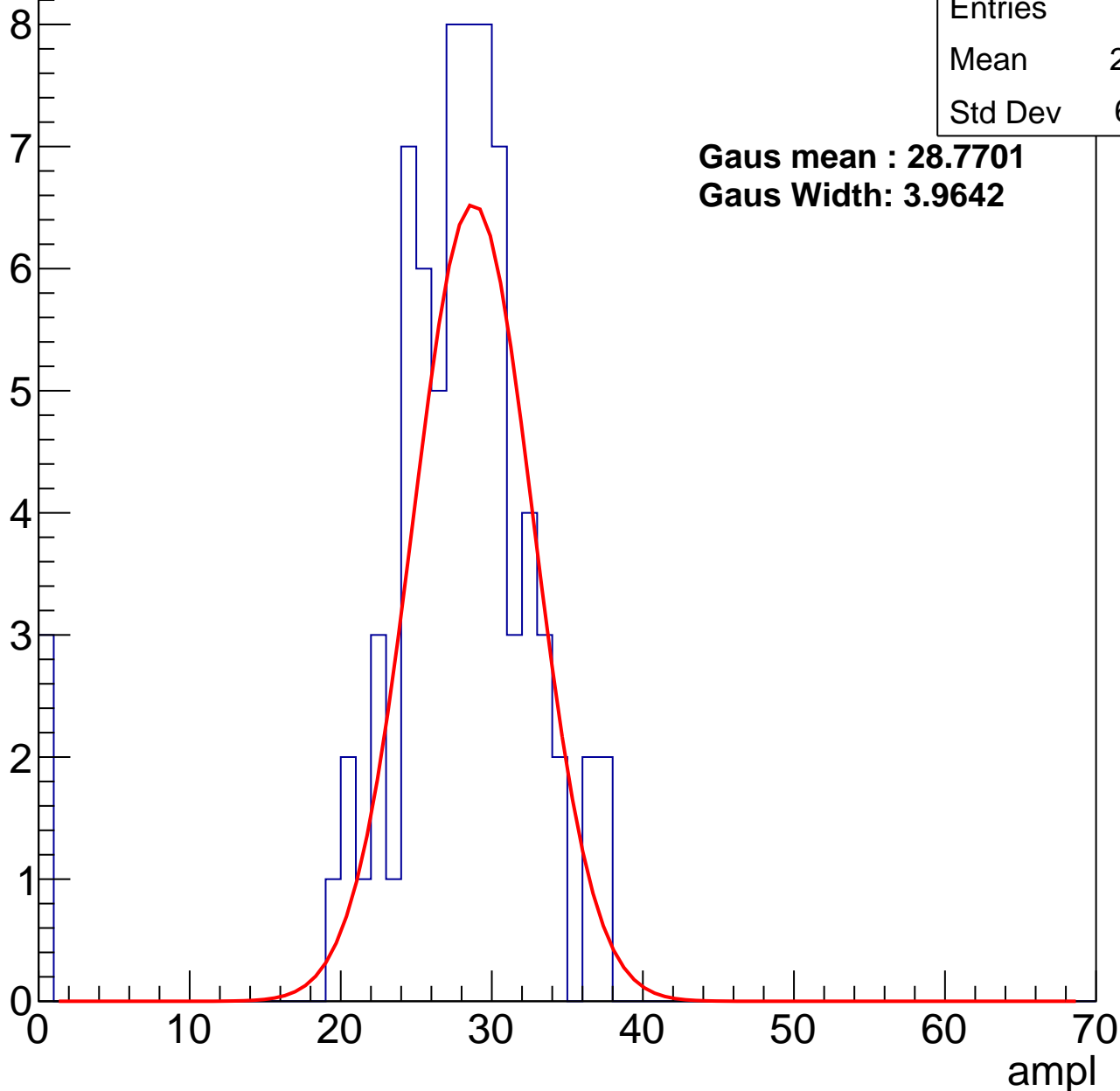
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	26.75
Std Dev	6.671

Gaus mean : 28.7701

Gaus Width: 3.9642



B1L101S, U4-ch119, adc1

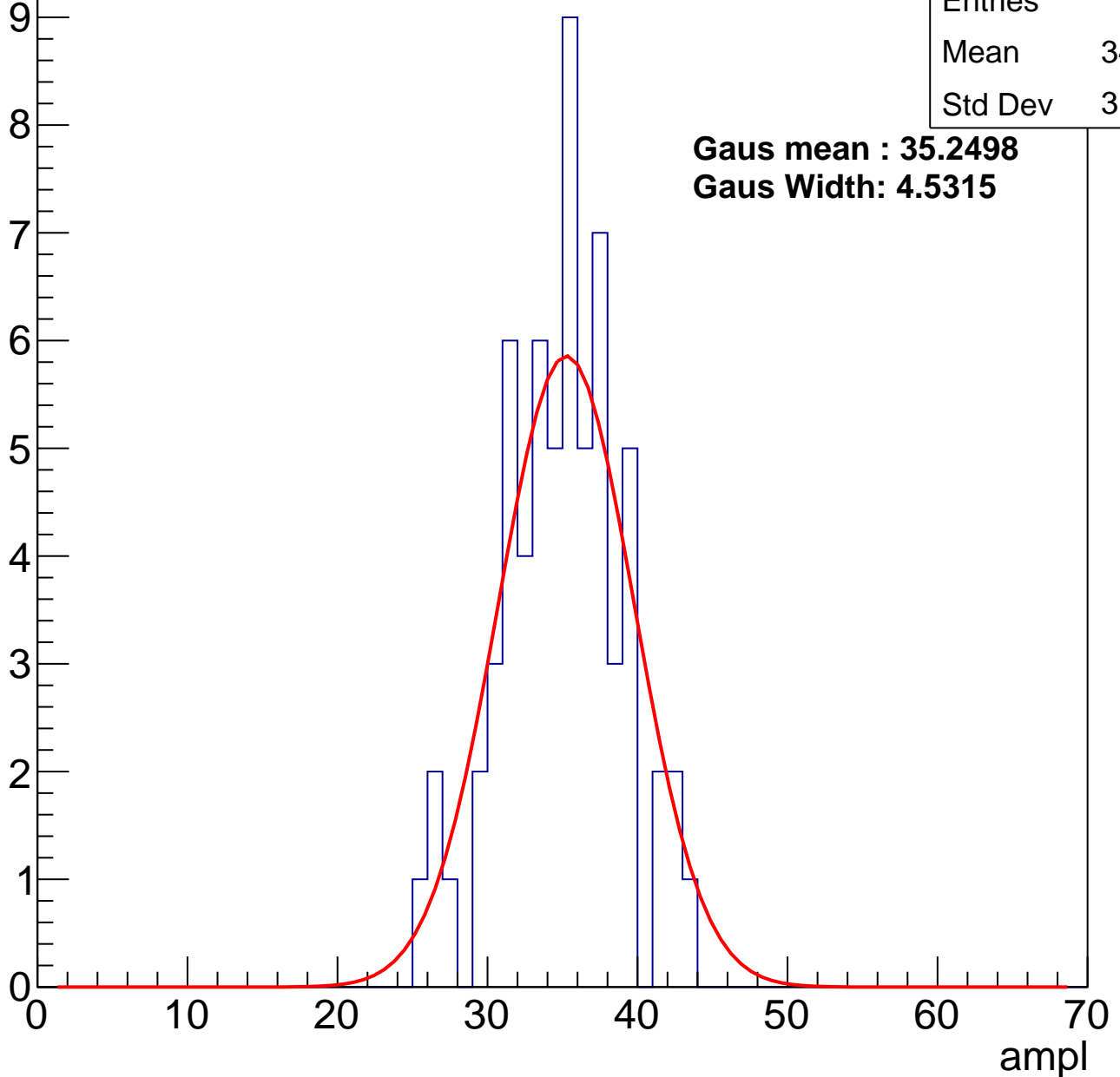
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	34.47
Std Dev	3.933

Gaus mean : 35.2498

Gaus Width: 4.5315



B1L101S, U4-ch119, adc2

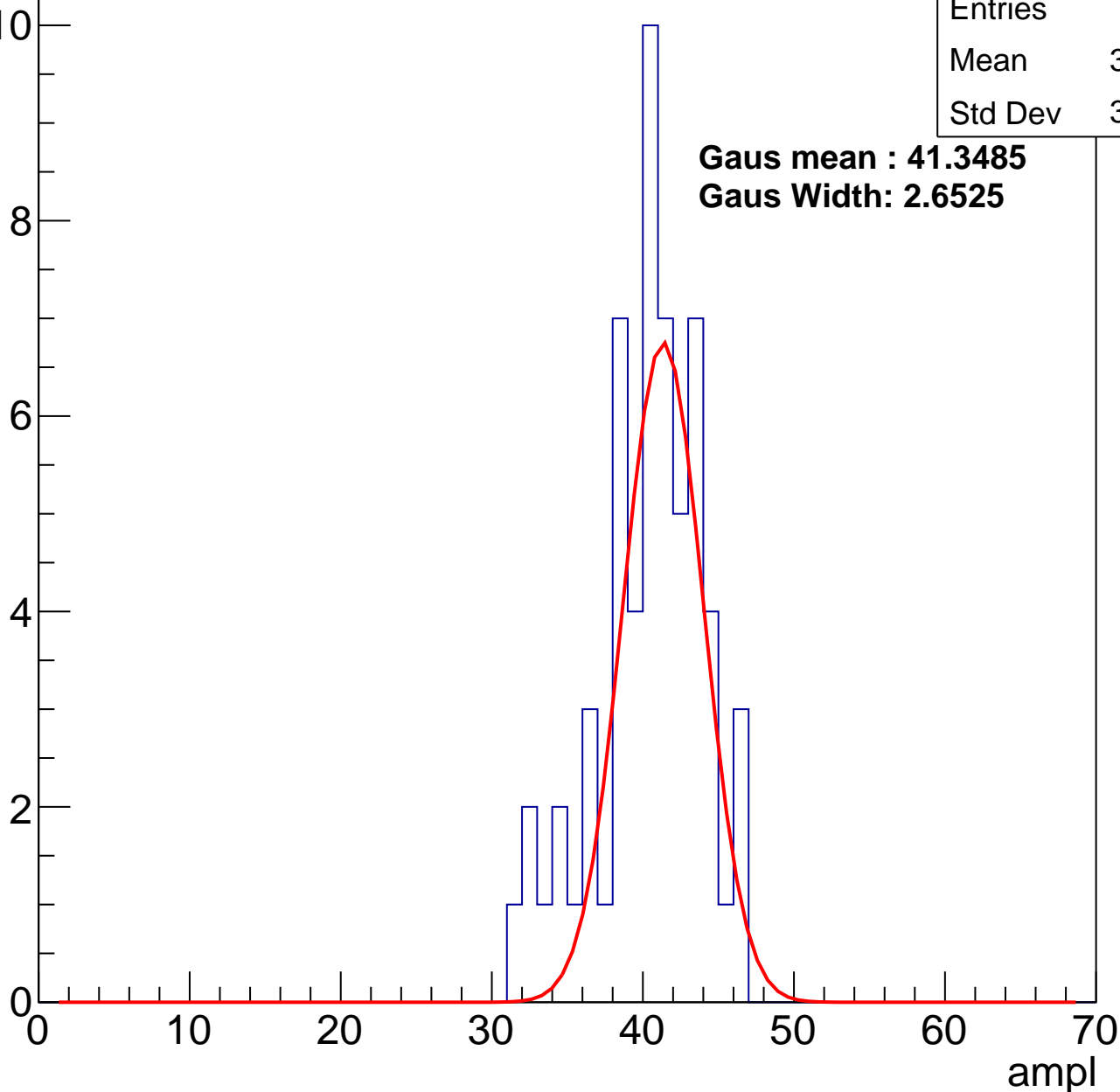
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	39.92
Std Dev	3.519

Gaus mean : 41.3485

Gaus Width: 2.6525



B1L101S, U4-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	47.53
Std Dev	3.456

Entry

10

8

6

4

2

0

0

10

20

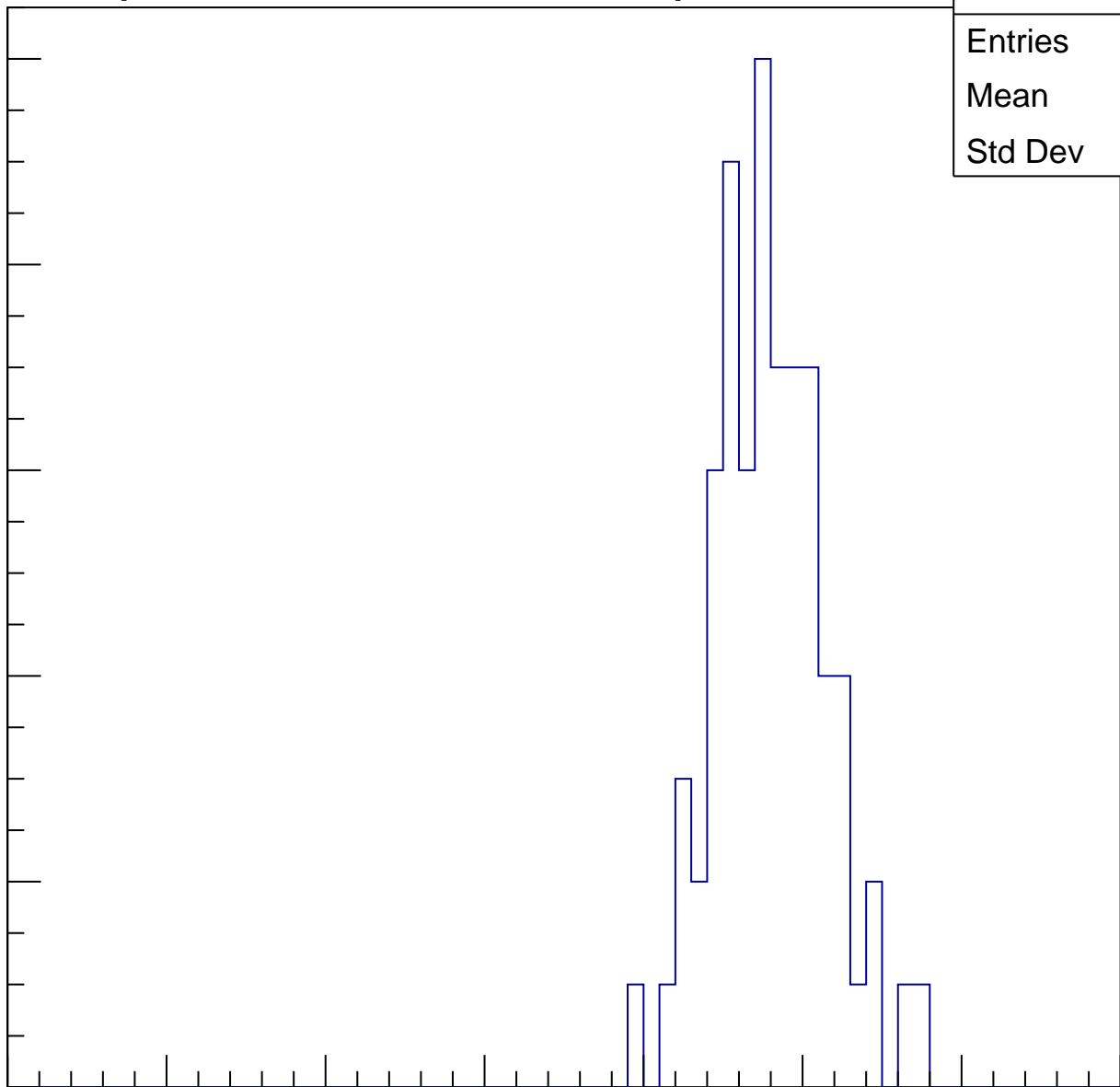
30

40

50

60

ampl

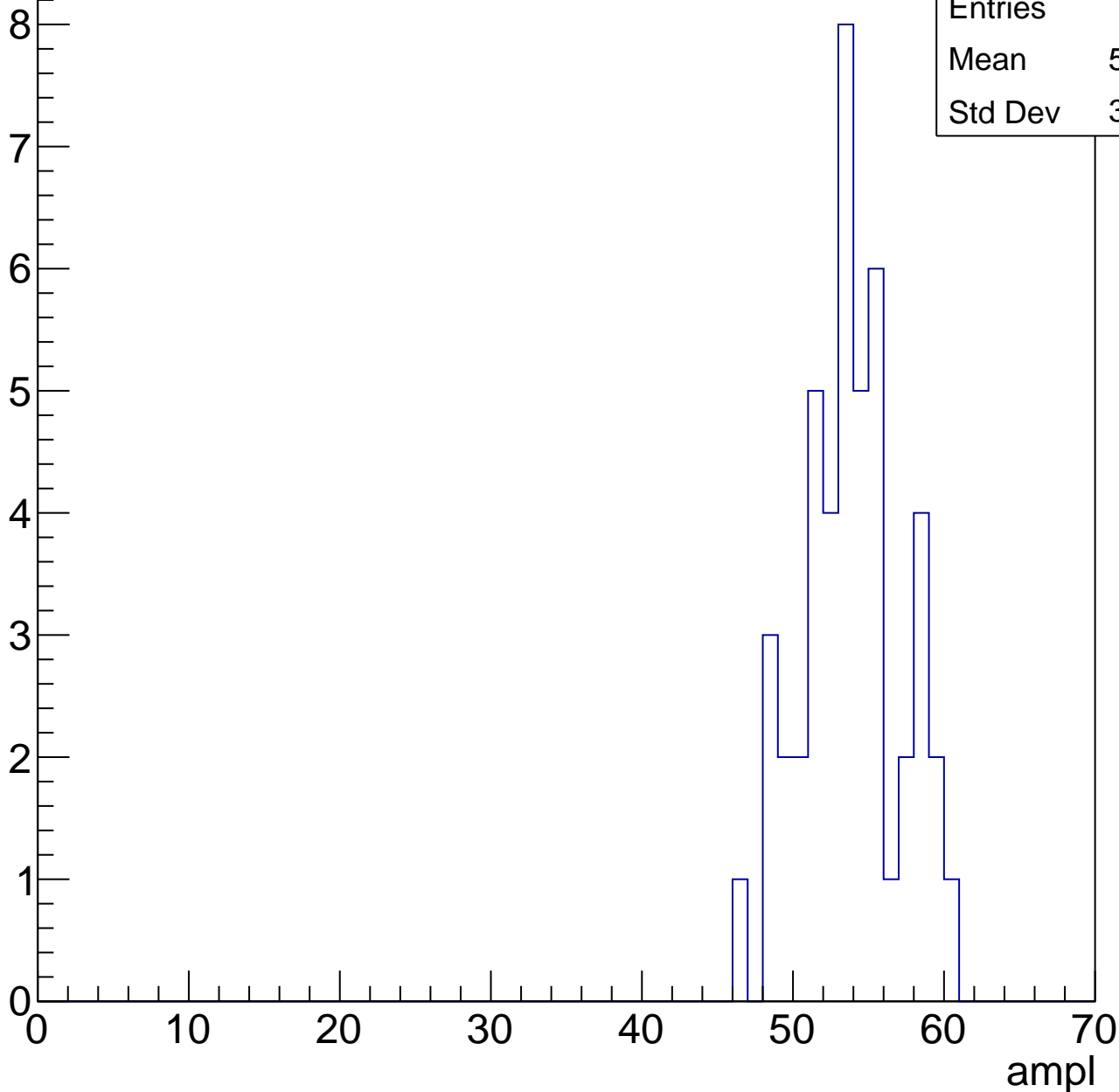


B1L101S, U4-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

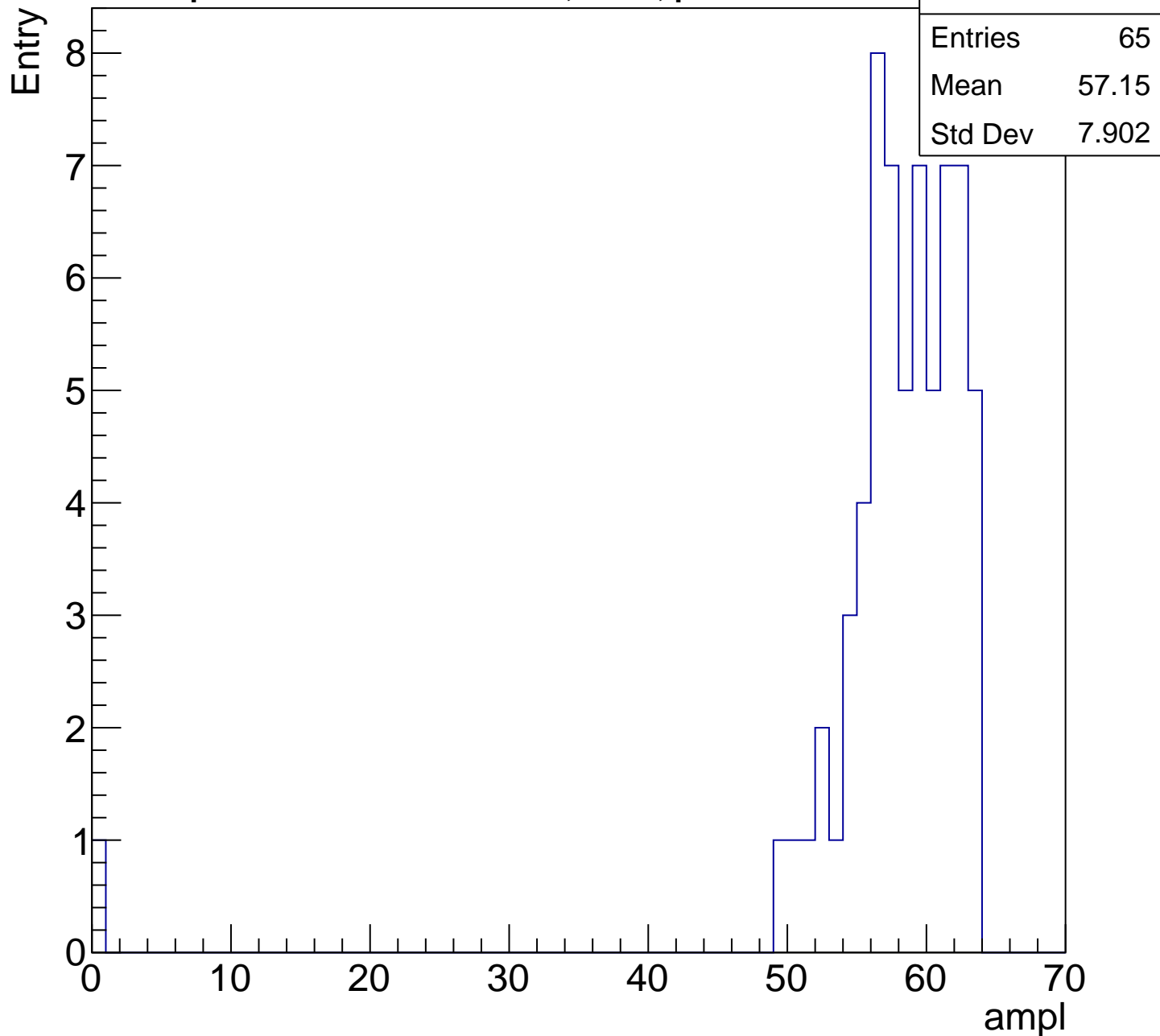
Entry

Entries	46
Mean	53.37
Std Dev	3.239



B1L101S, U4-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

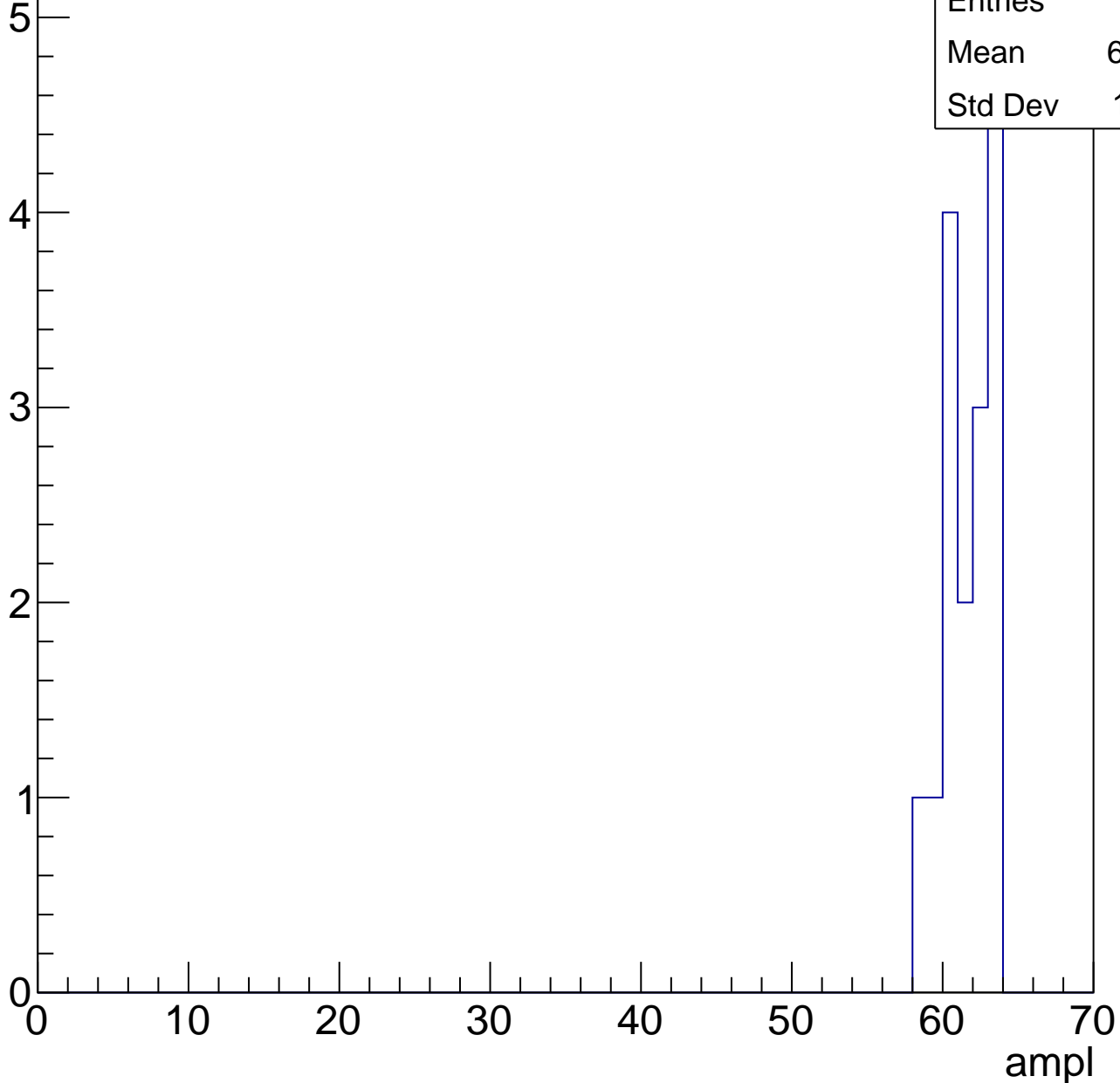


B1L101S, U4-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	61.25
Std Dev	1.561



B1L101S, U4-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	61
Std Dev	0

B1L101S, U4-ch120, adc0

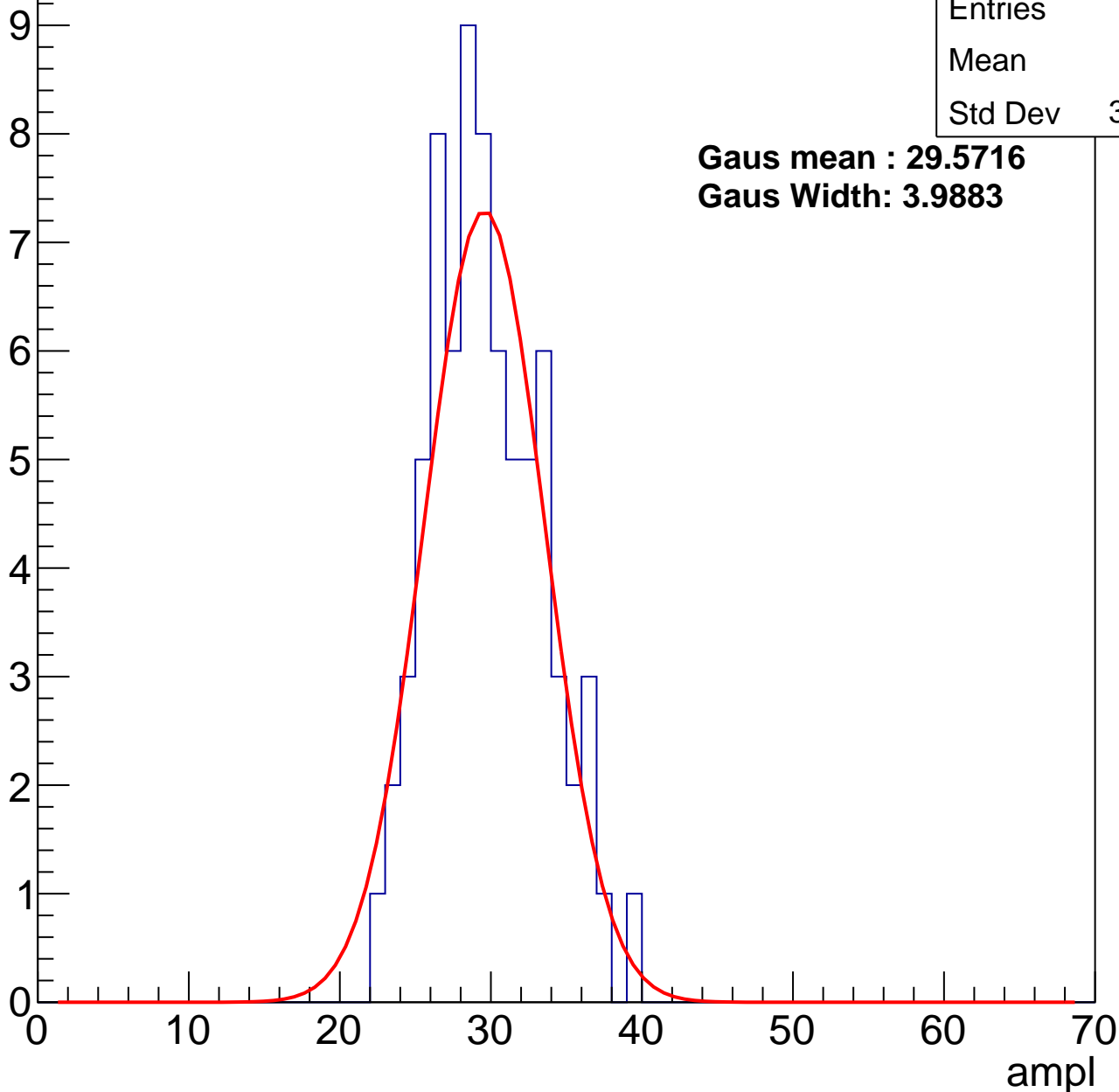
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.3
Std Dev	3.675

Gaus mean : 29.5716

Gaus Width: 3.9883



B1L101S, U4-ch120, adc1

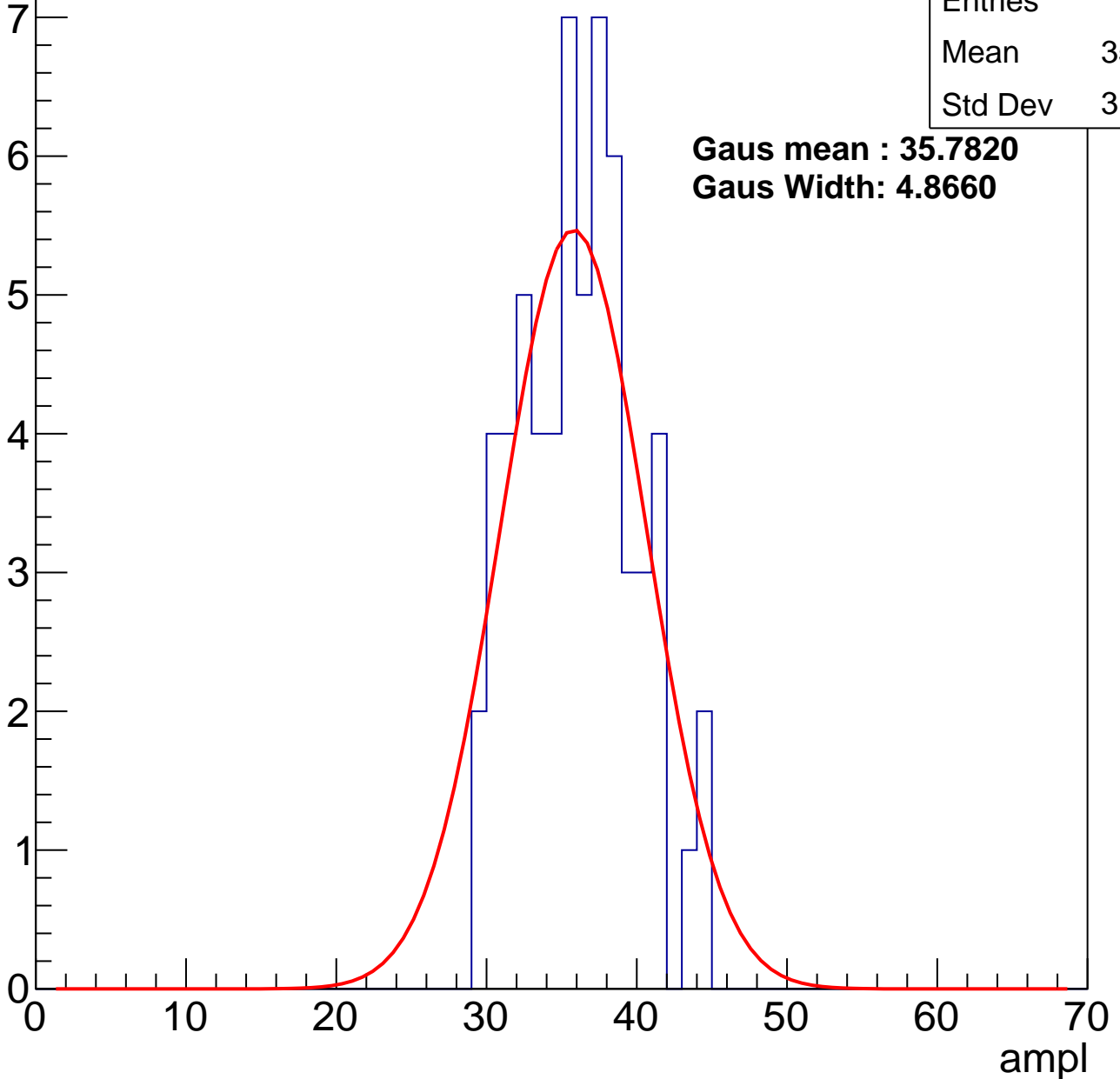
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	35.64
Std Dev	3.746

Gaus mean : 35.7820

Gaus Width: 4.8660



B1L101S, U4-ch120, adc2

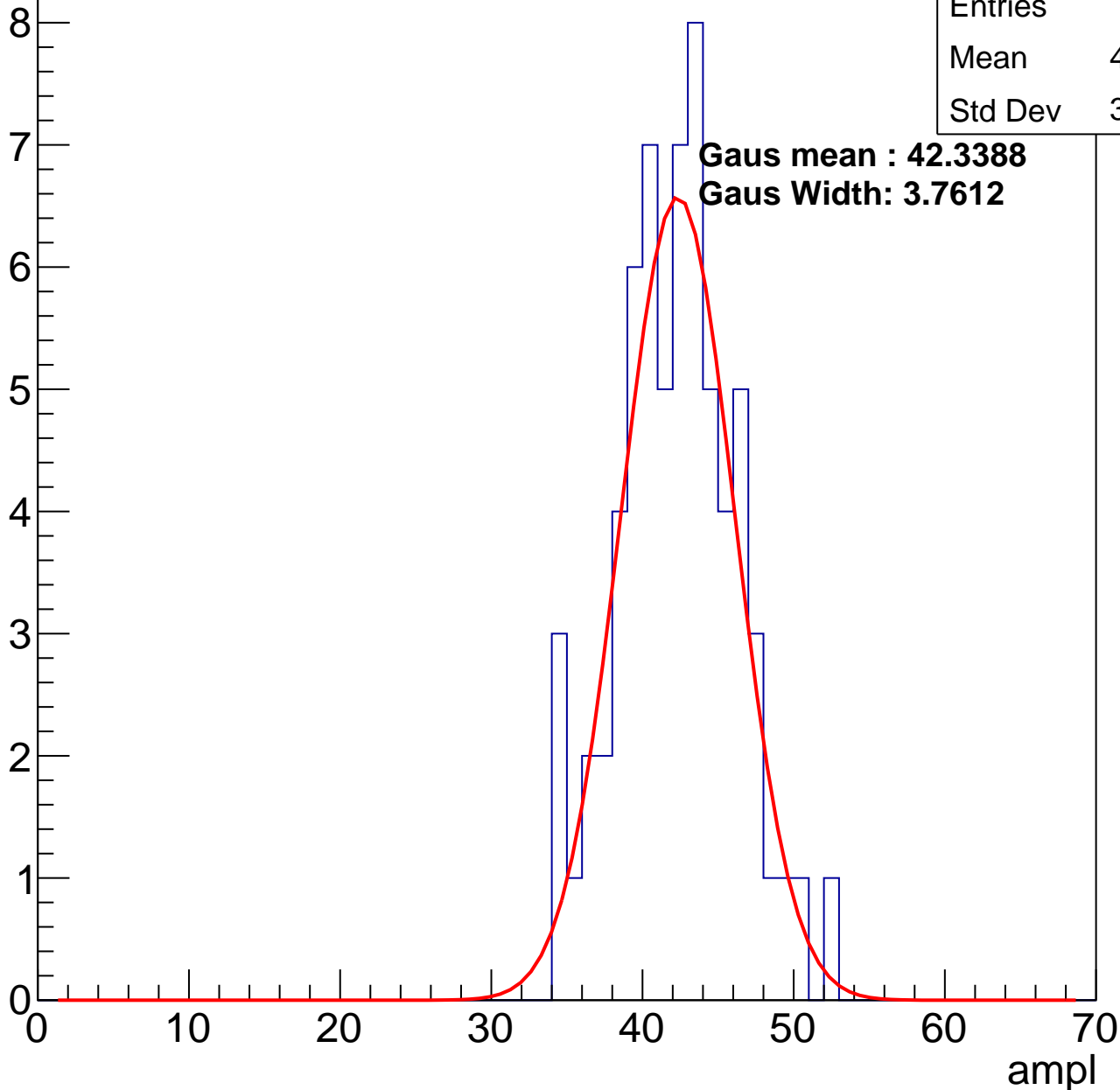
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.85
Std Dev	3.854

Gaus mean : 42.3388

Gaus Width: 3.7612

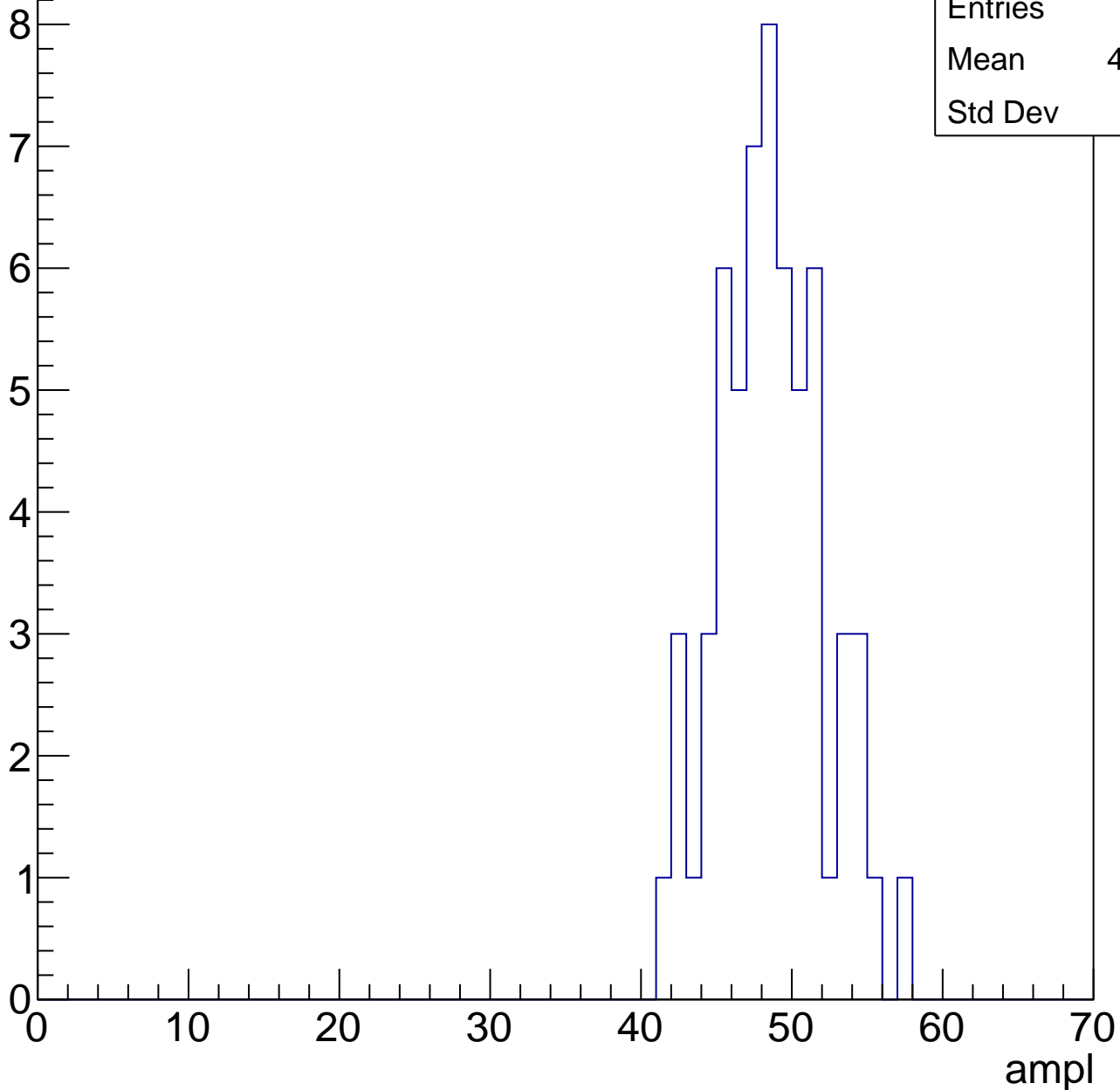


B1L101S, U4-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

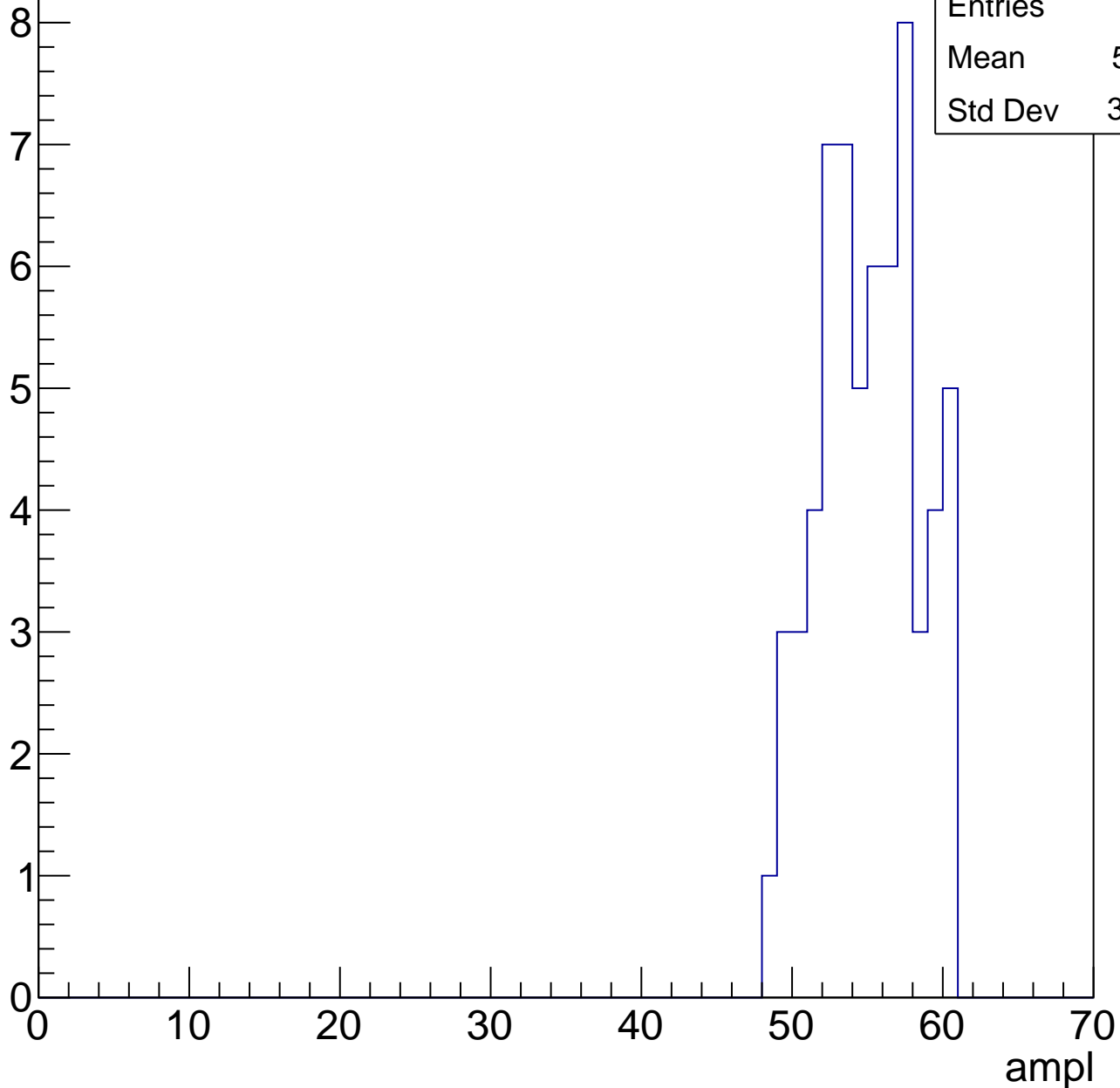
Entries	60
Mean	48.17
Std Dev	3.46



B1L101S, U4-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

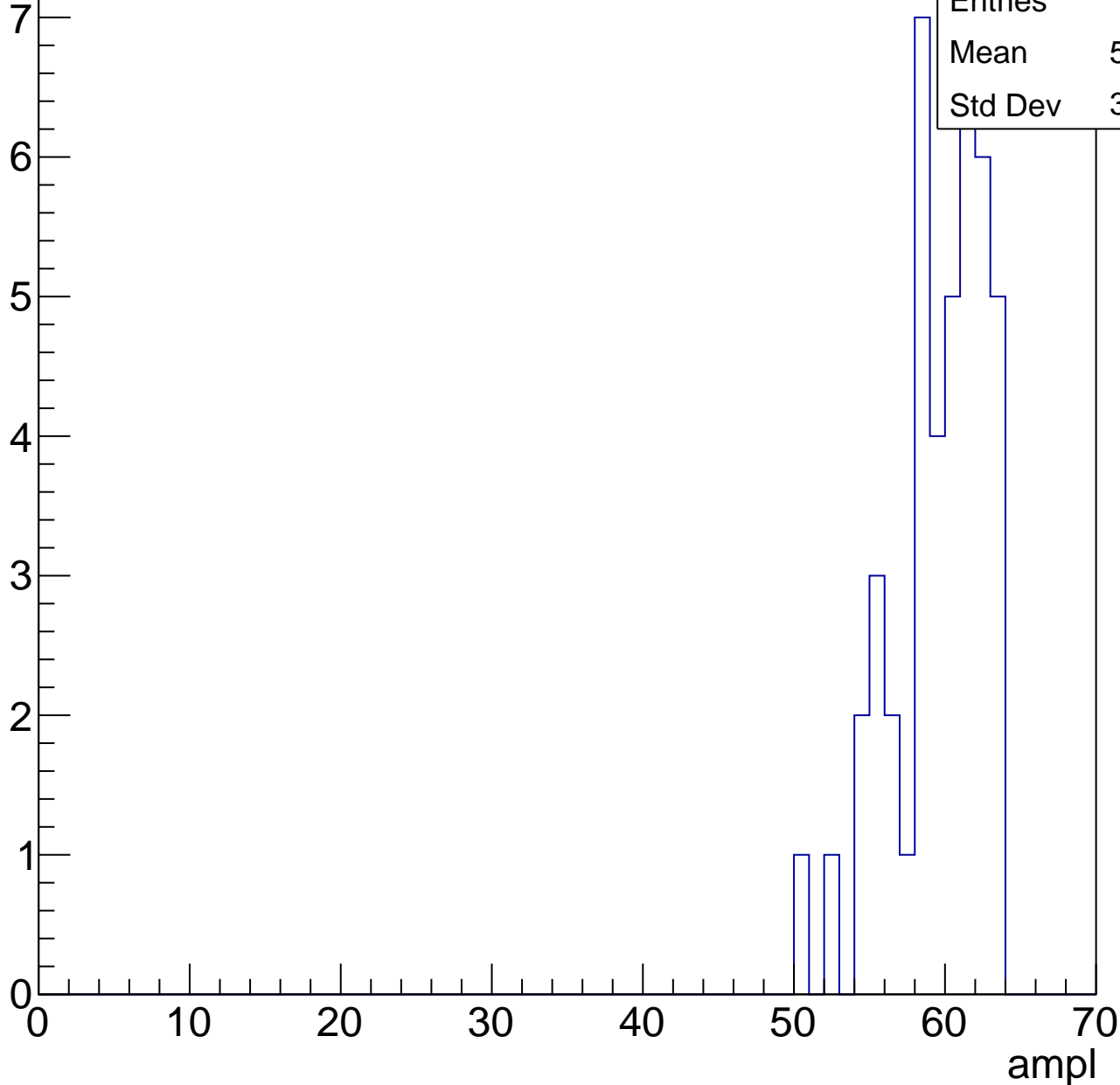
Entry



B1L101S, U4-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

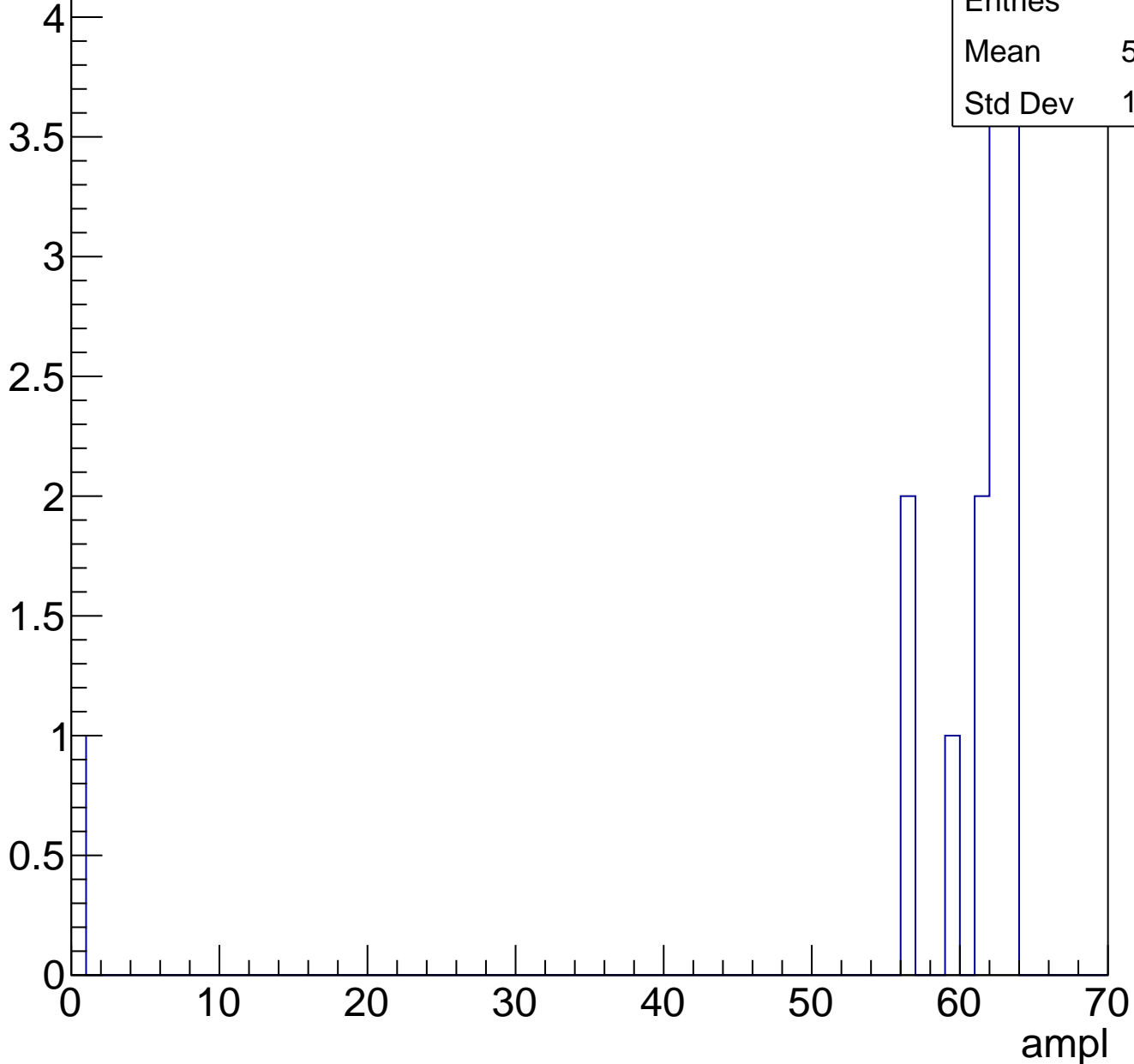
Entry



B1L101S, U4-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

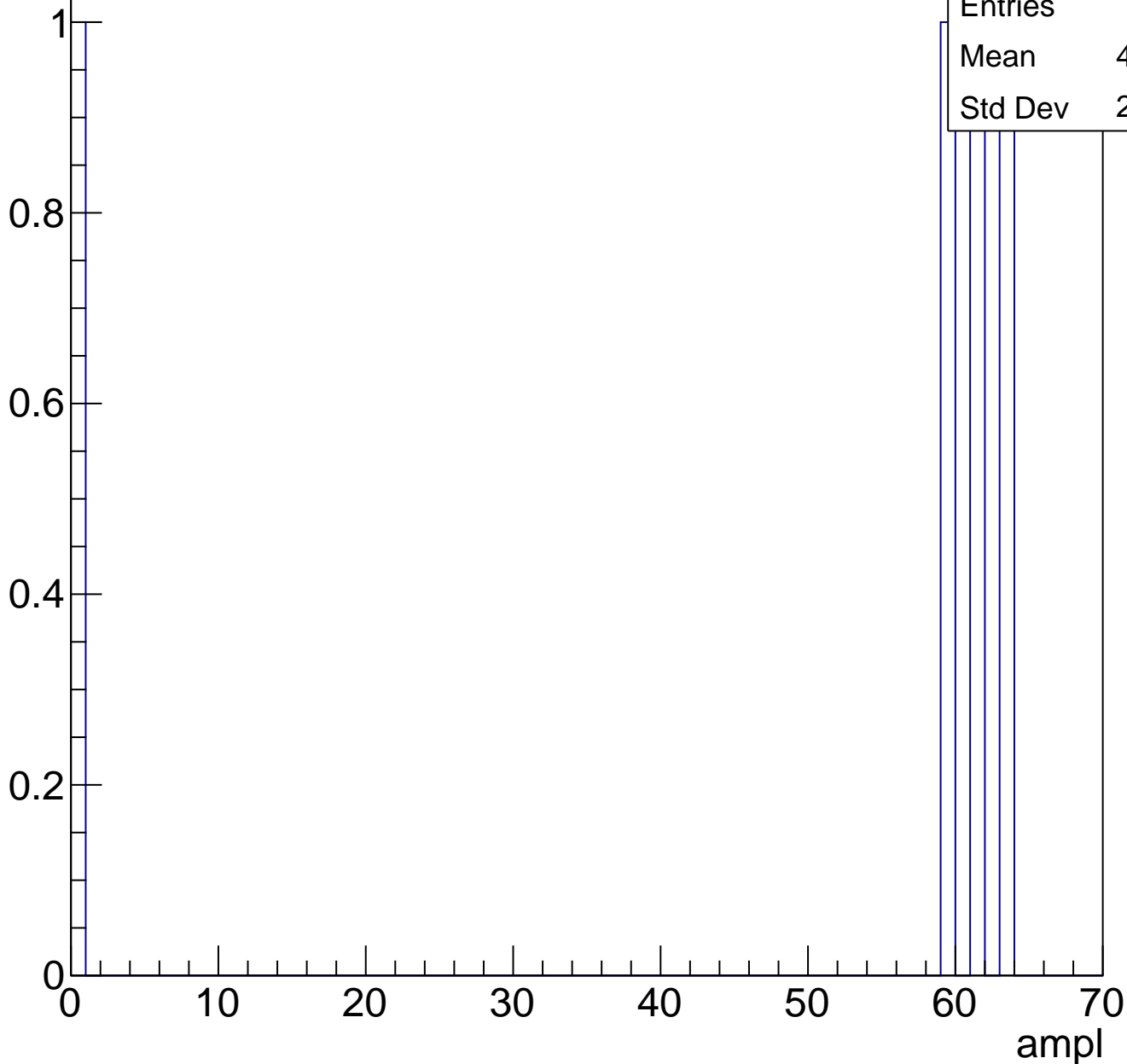
Entry



B1L101S, U4-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch121, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	26.68
Std Dev	5.586

Gaus mean : 27.8014

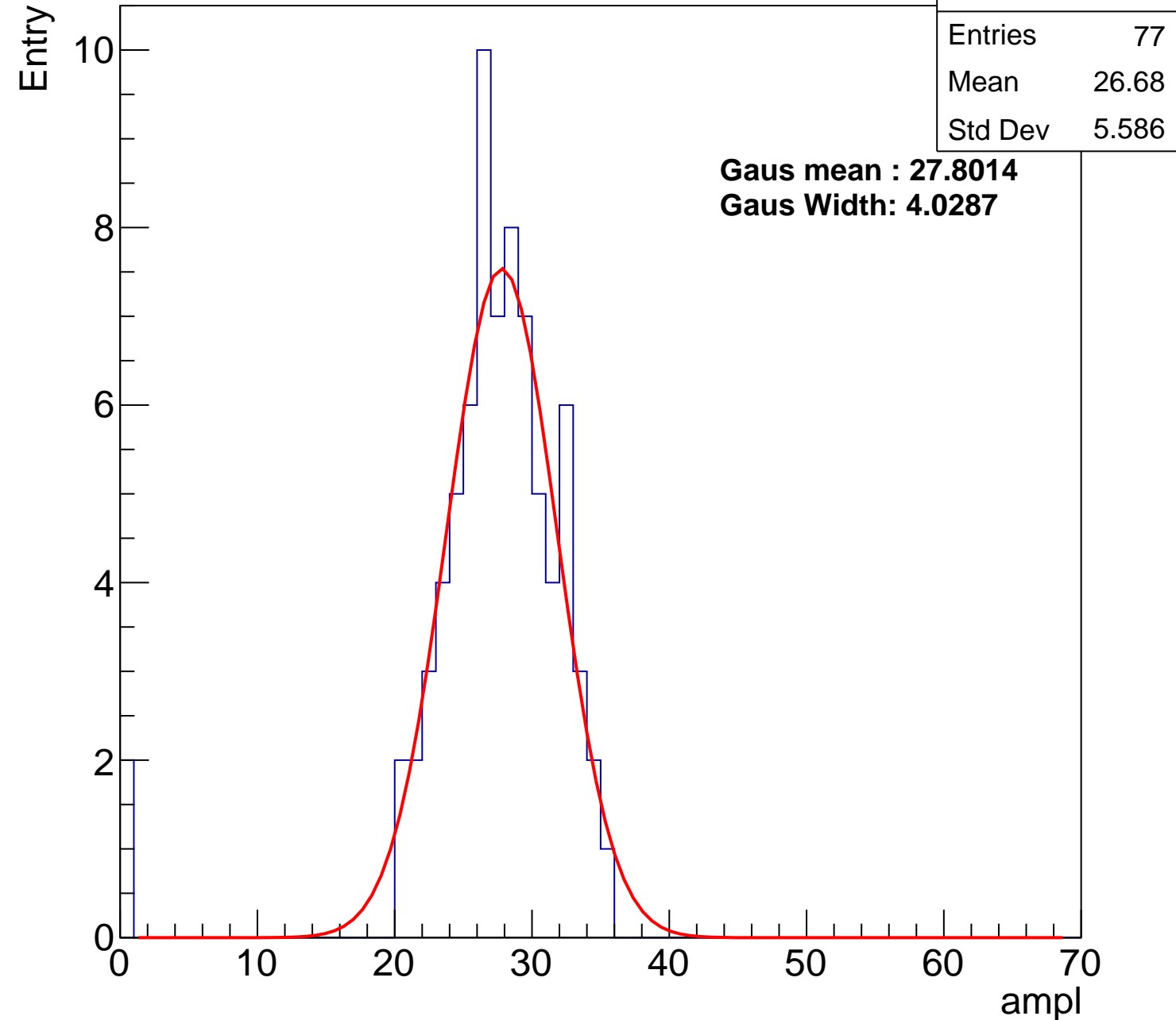
Gaus Width: 4.0287

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U4-ch121, adc1

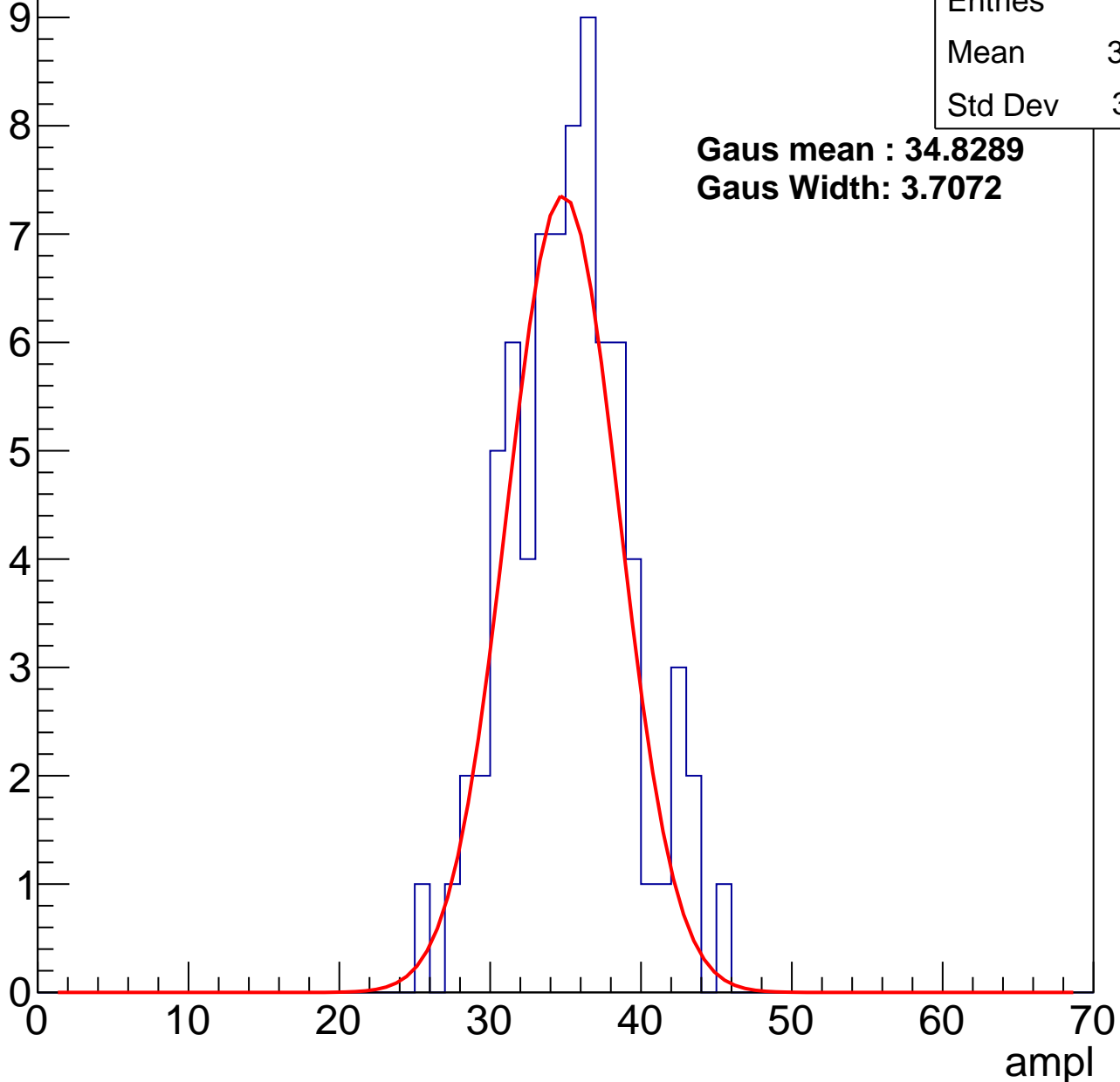
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	34.83
Std Dev	3.991

Gaus mean : 34.8289

Gaus Width: 3.7072



B1L101S, U4-ch121, adc2

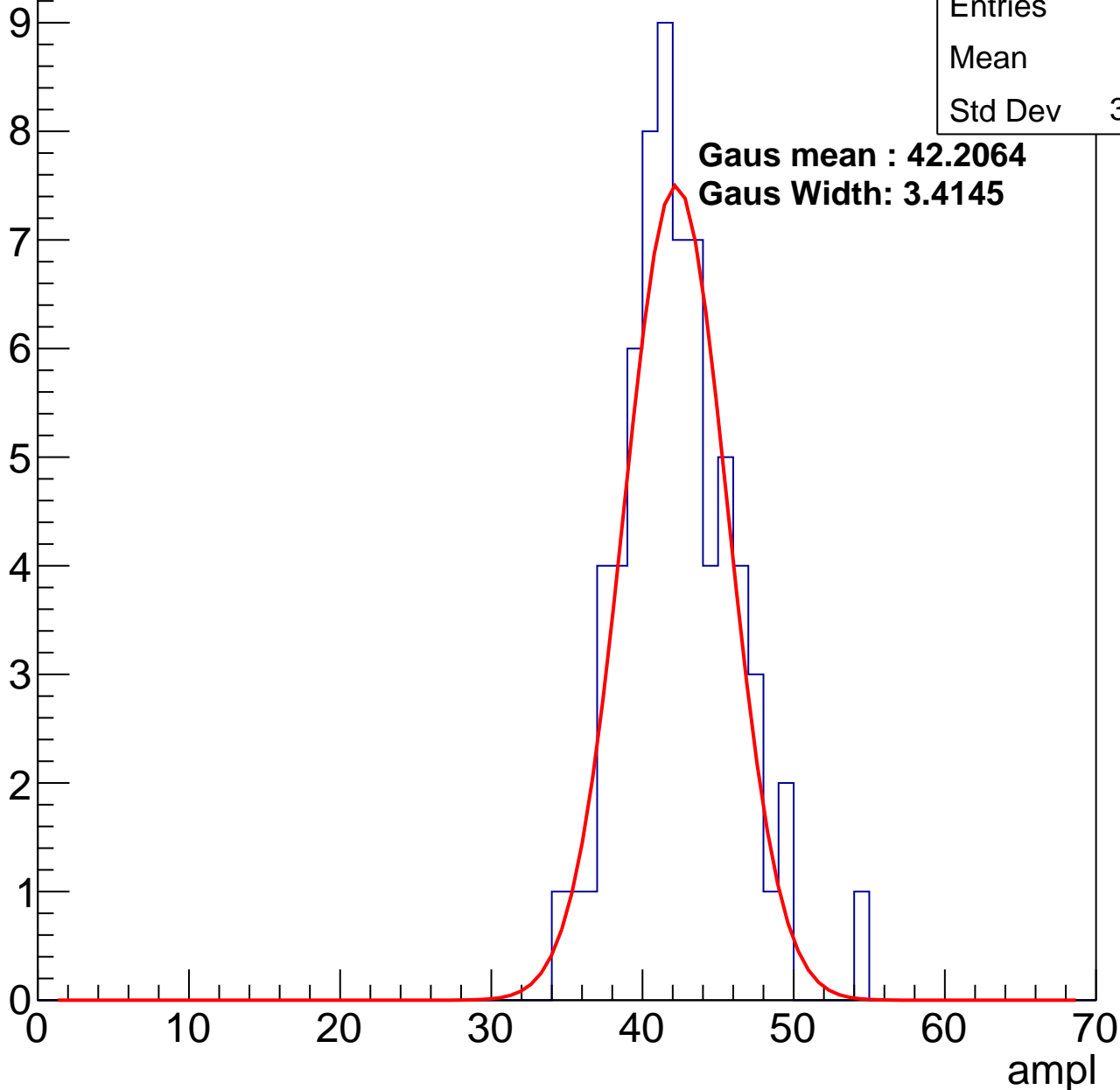
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.9
Std Dev	3.622

Gaus mean : 42.2064

Gaus Width: 3.4145

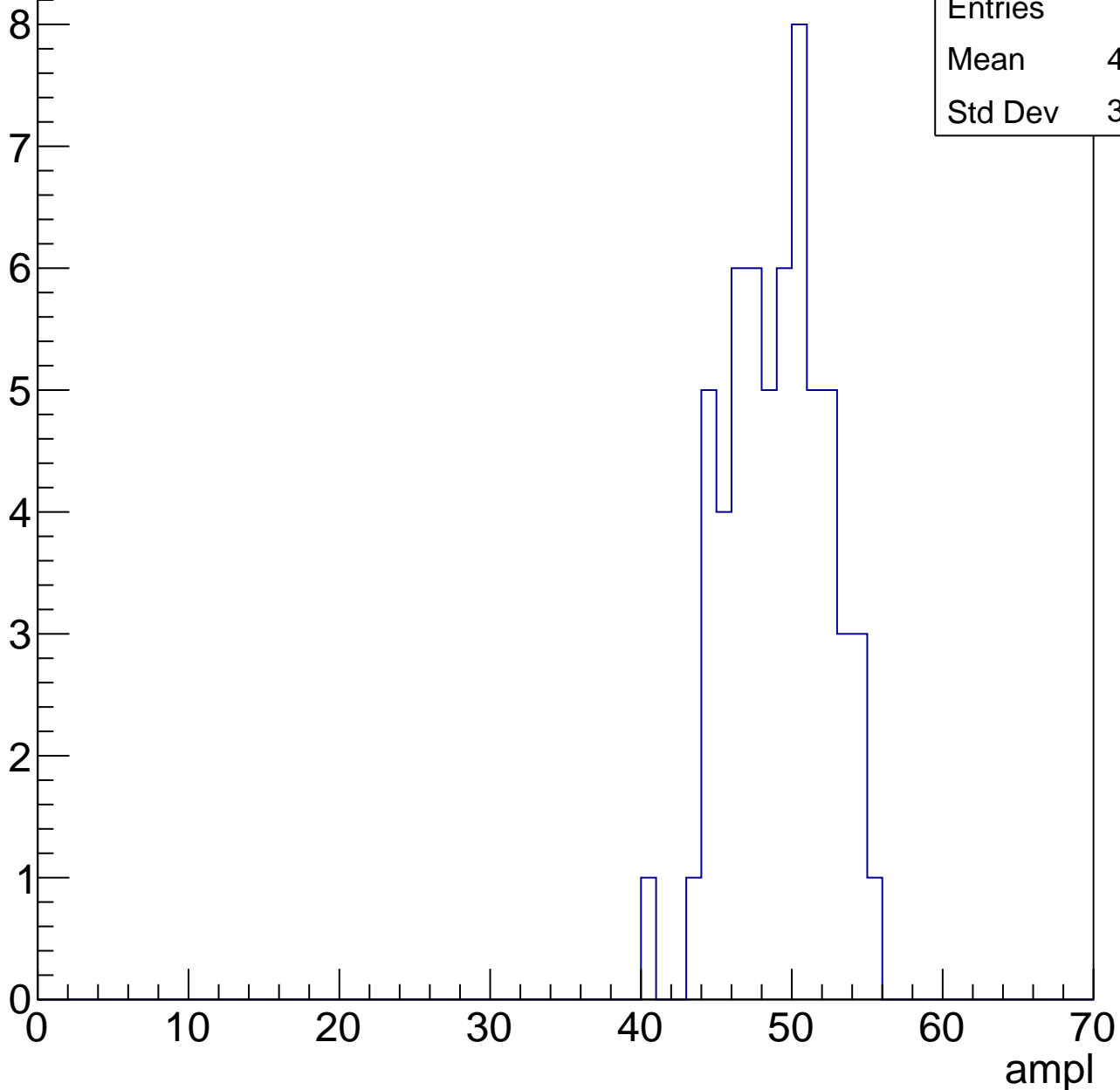


B1L101S, U4-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	48.58
Std Dev	3.217

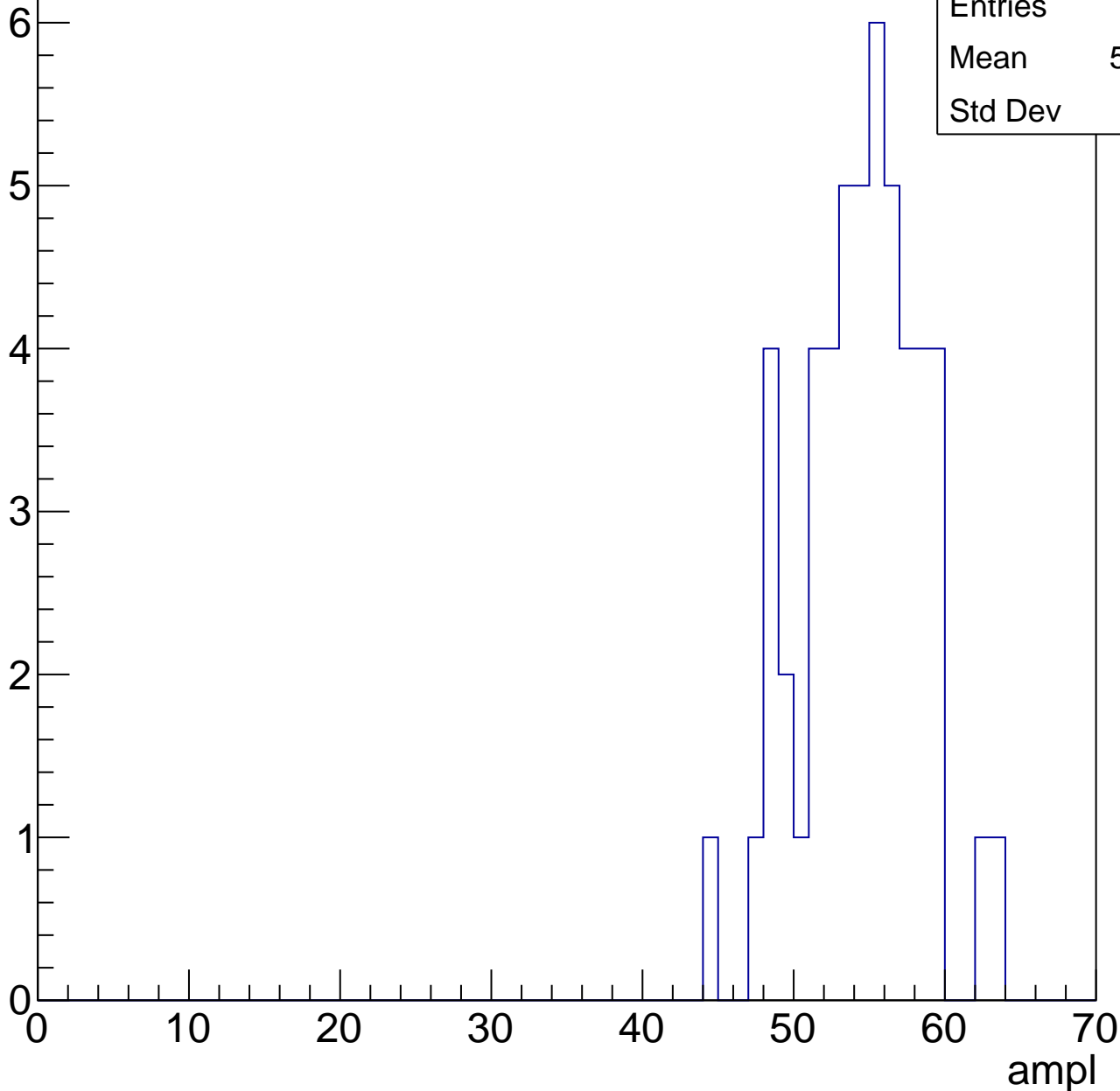


B1L101S, U4-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	54.02
Std Dev	3.89

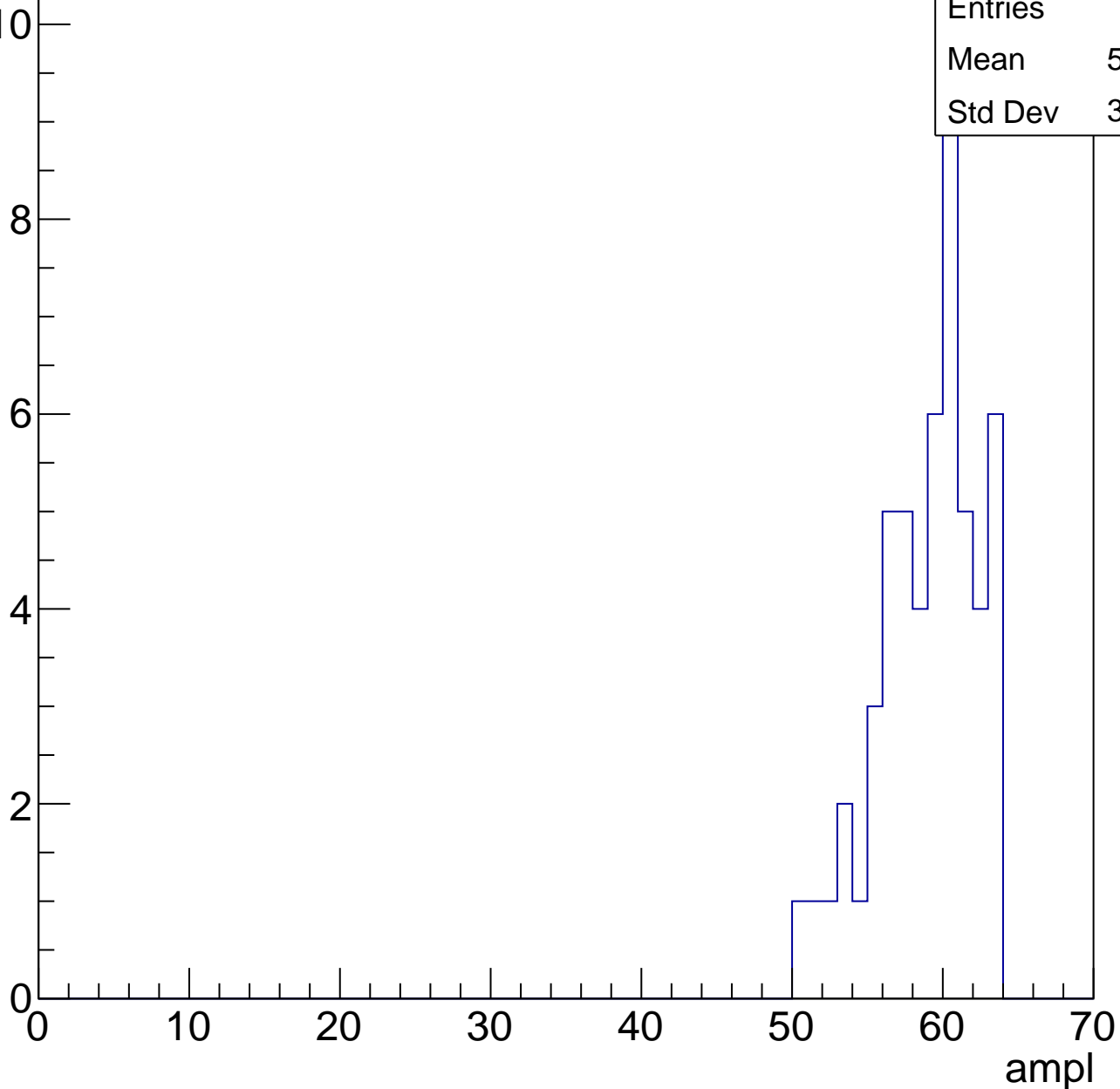


B1L101S, U4-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	58.52
Std Dev	3.213

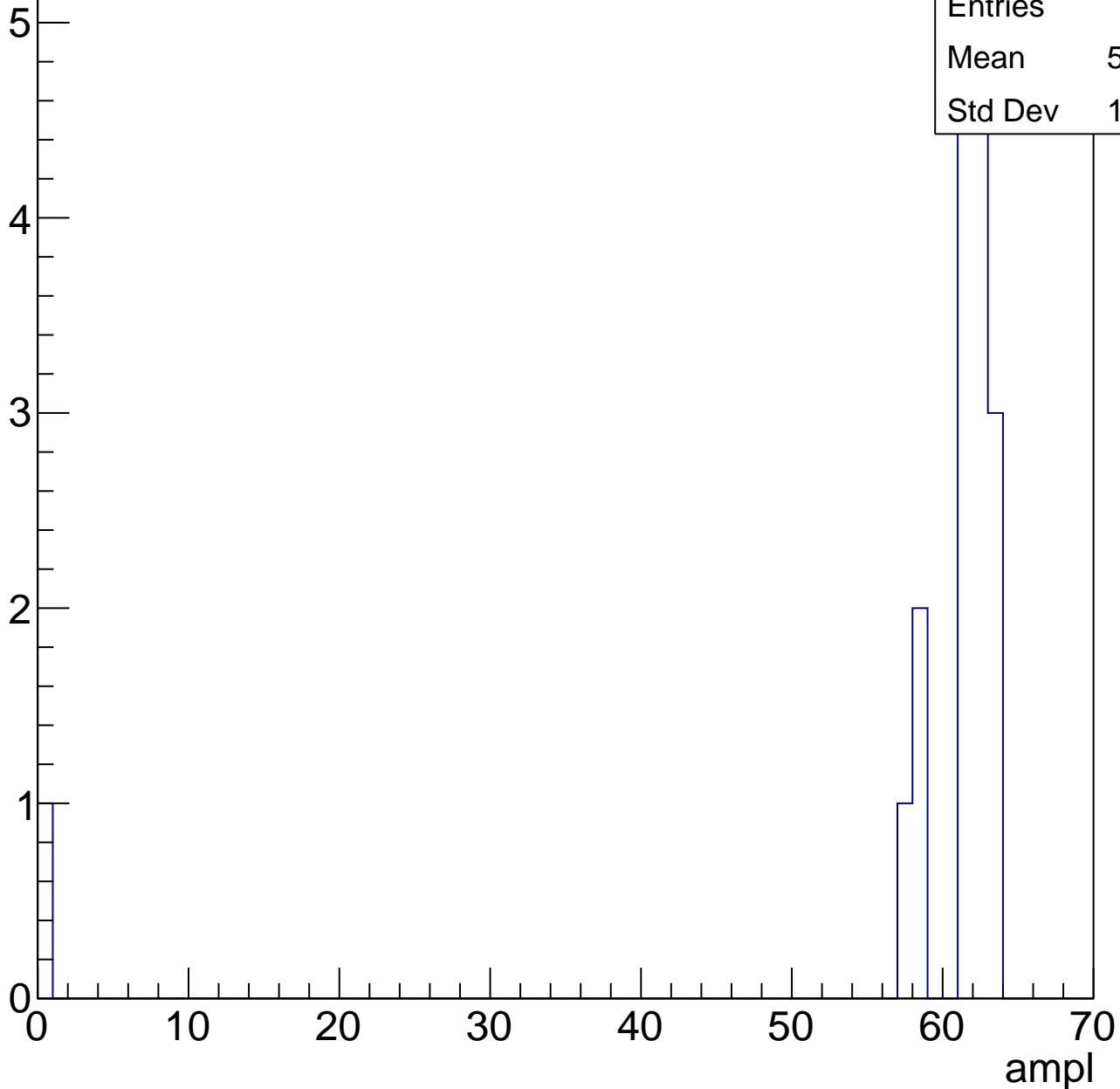


B1L101S, U4-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	57.47
Std Dev	14.47



B1L101S, U4-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U4-ch122, adc0

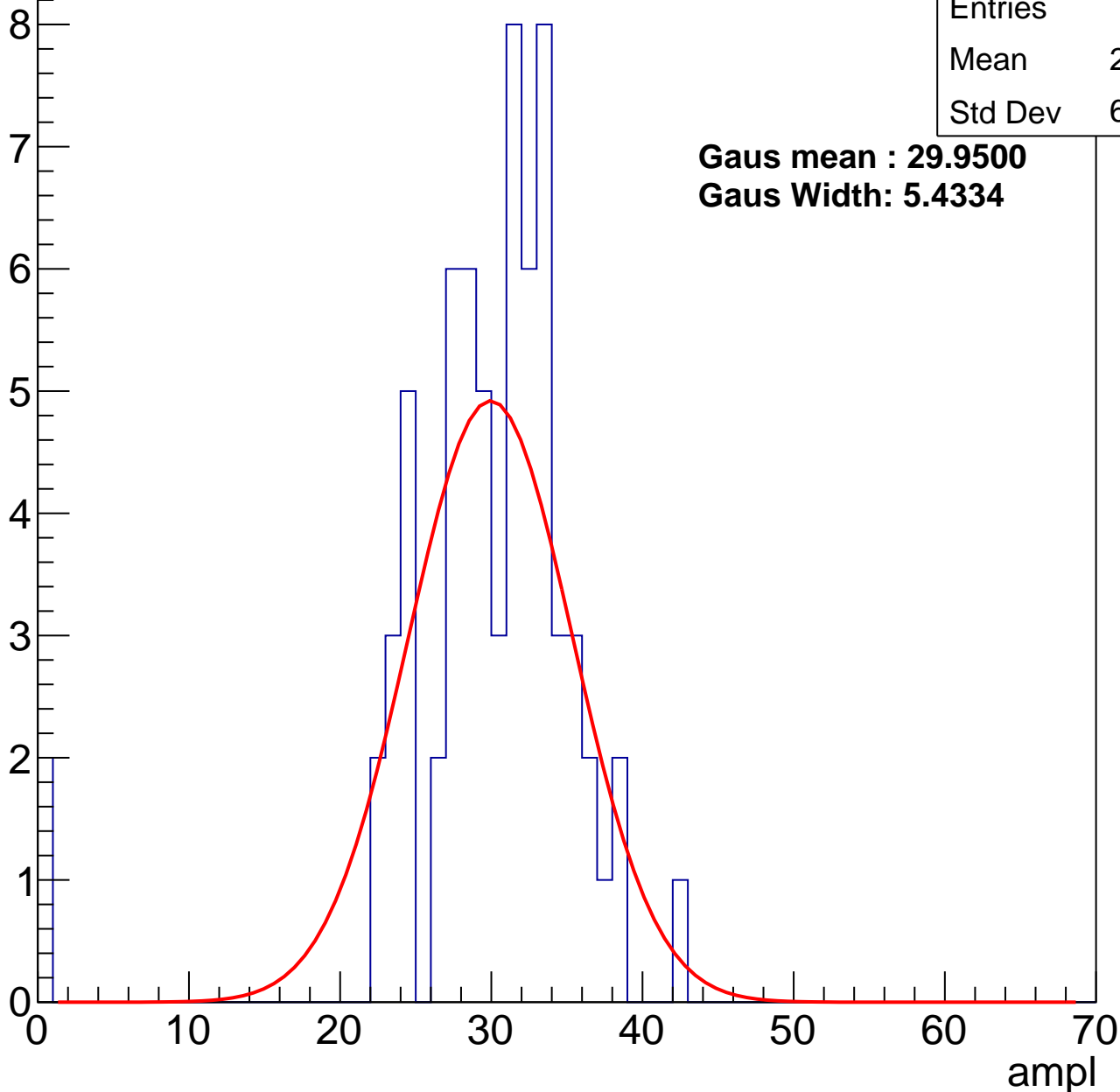
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.24
Std Dev	6.587

Gaus mean : 29.9500

Gaus Width: 5.4334



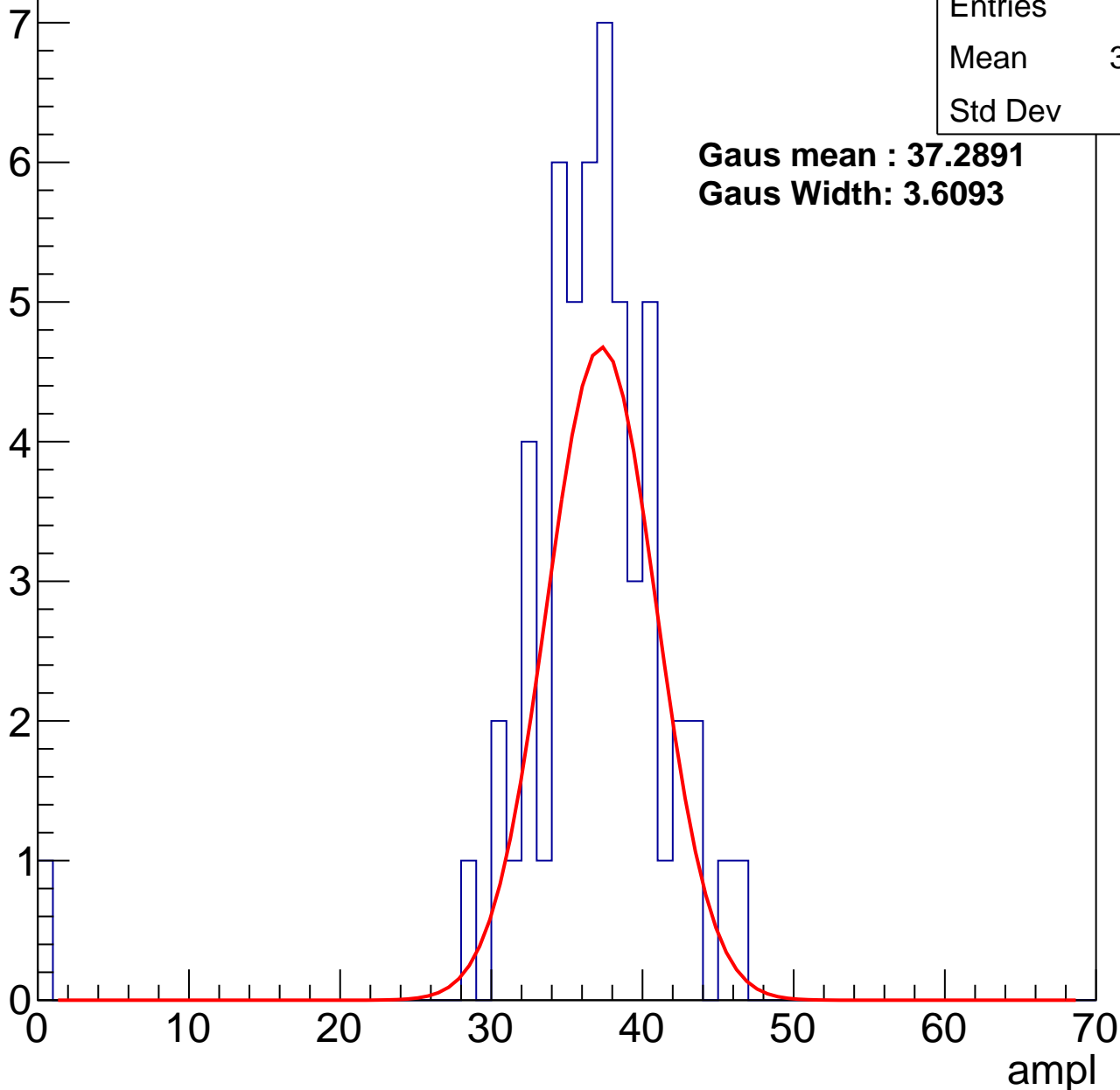
B1L101S, U4-ch122, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	35.98
Std Dev	6.19

Gaus mean : 37.2891
Gaus Width: 3.6093



B1L101S, U4-ch122, adc2

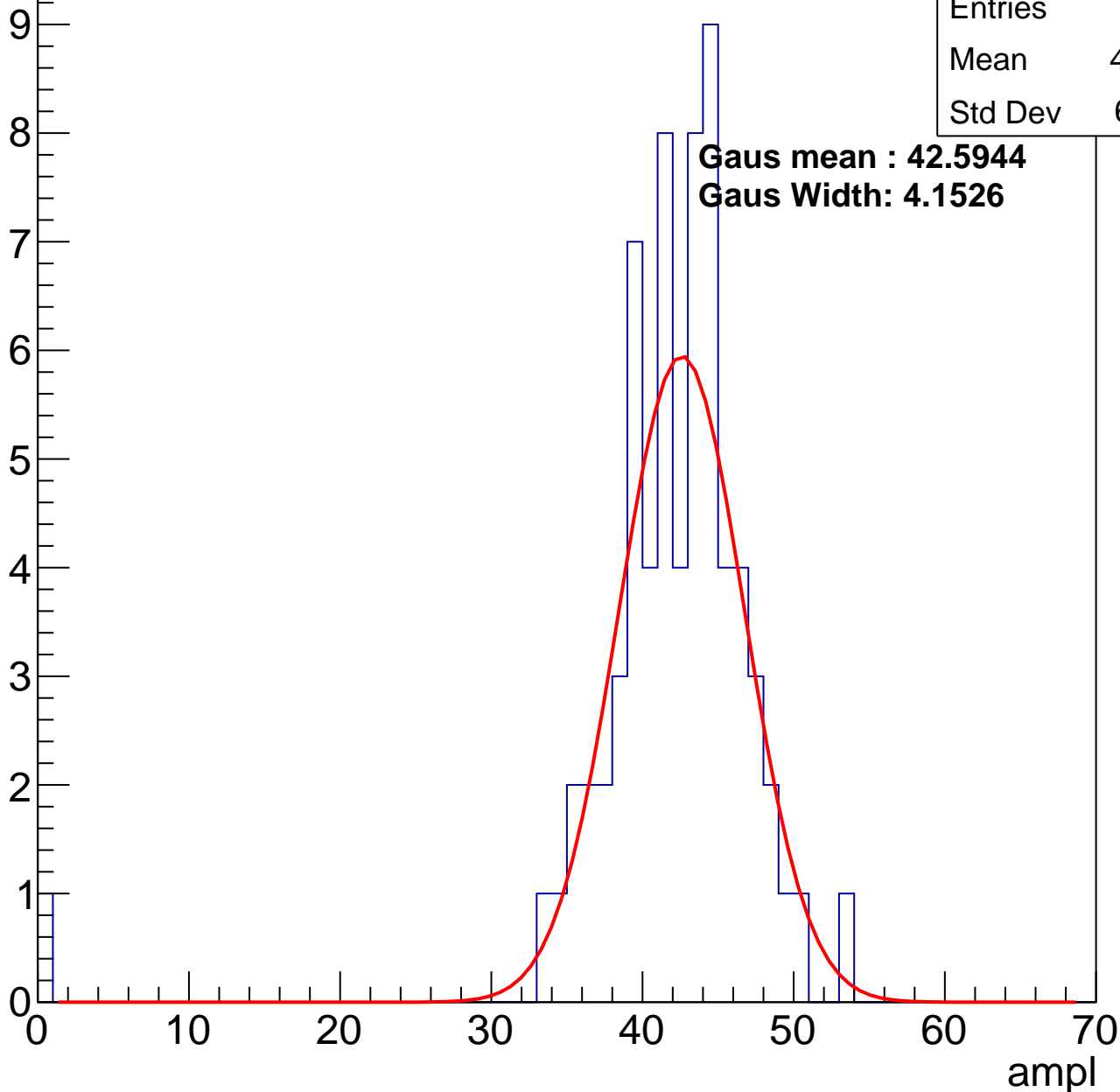
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	41.46
Std Dev	6.381

Gaus mean : 42.5944

Gaus Width: 4.1526

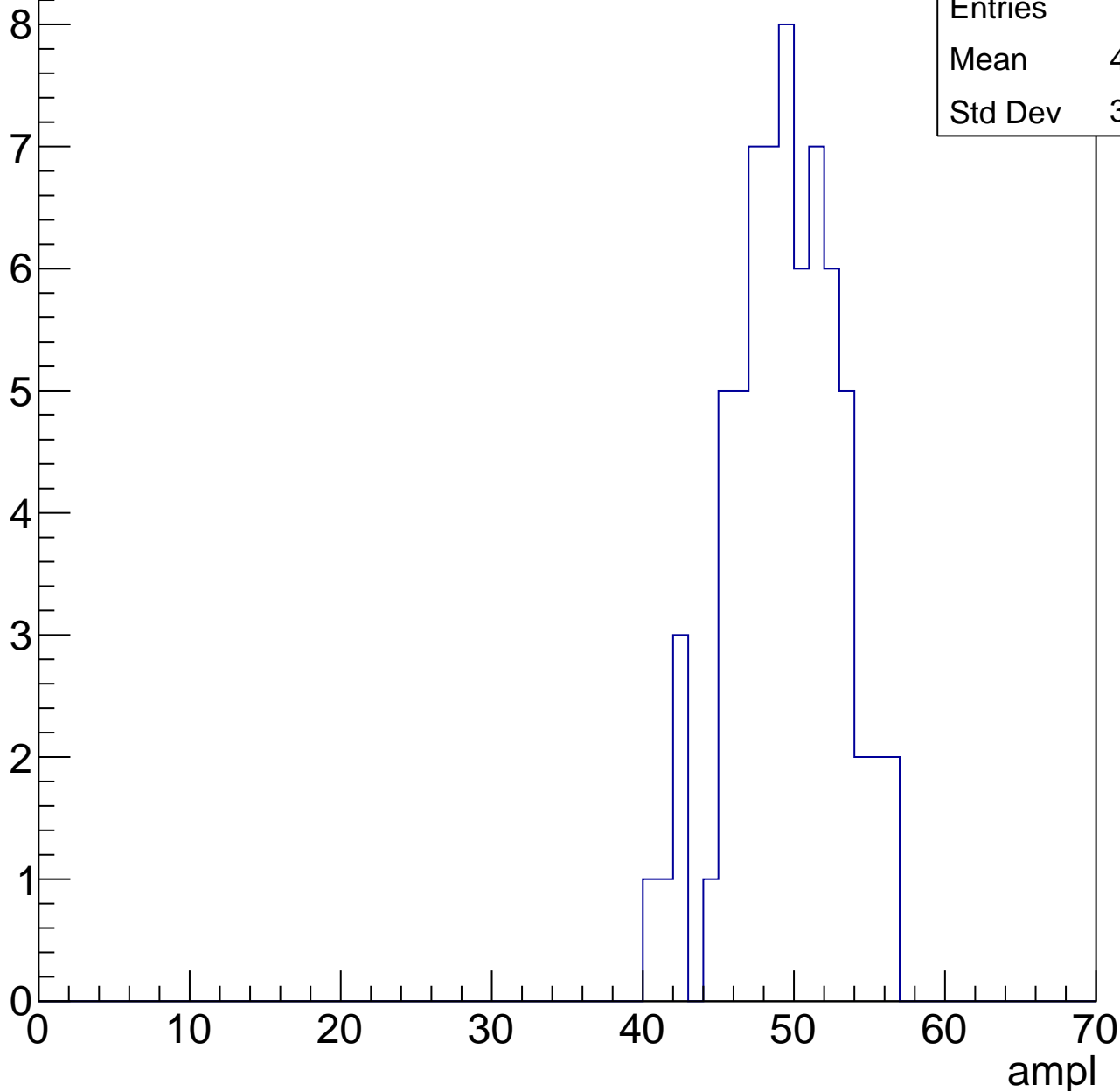


B1L101S, U4-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.93
Std Dev	3.566

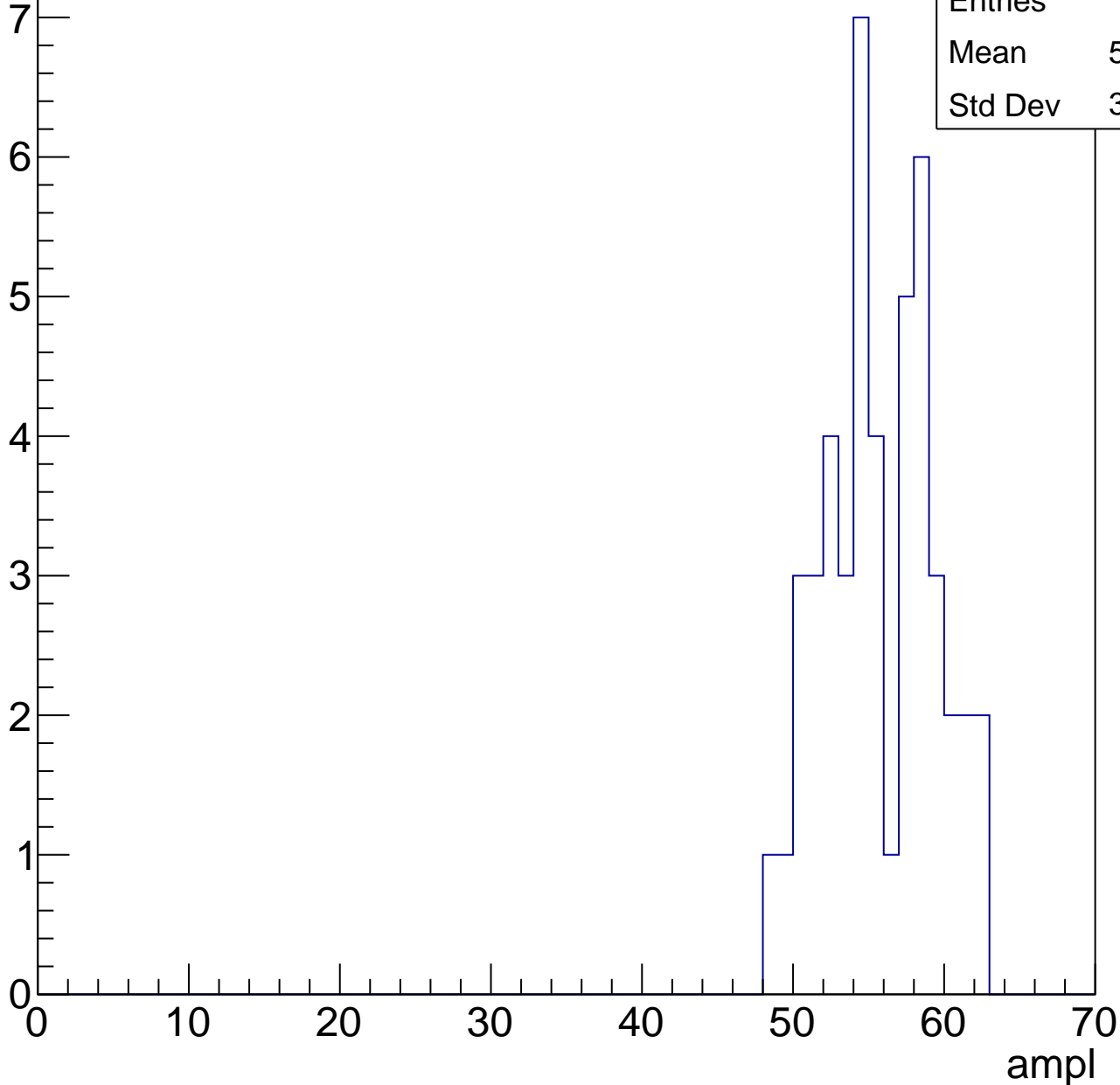


B1L101S, U4-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	55.26
Std Dev	3.582

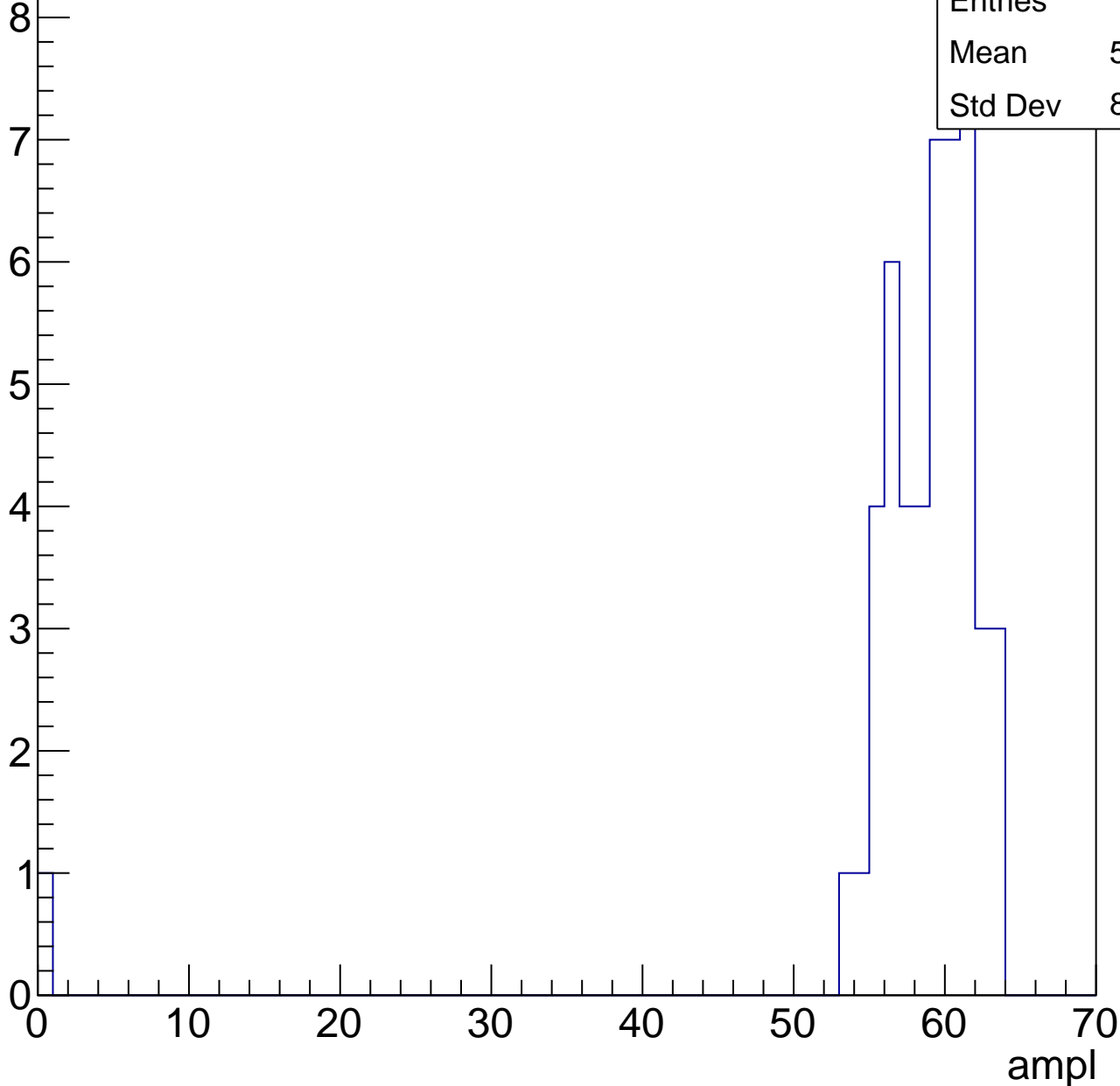


B1L101S, U4-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.53
Std Dev	8.676

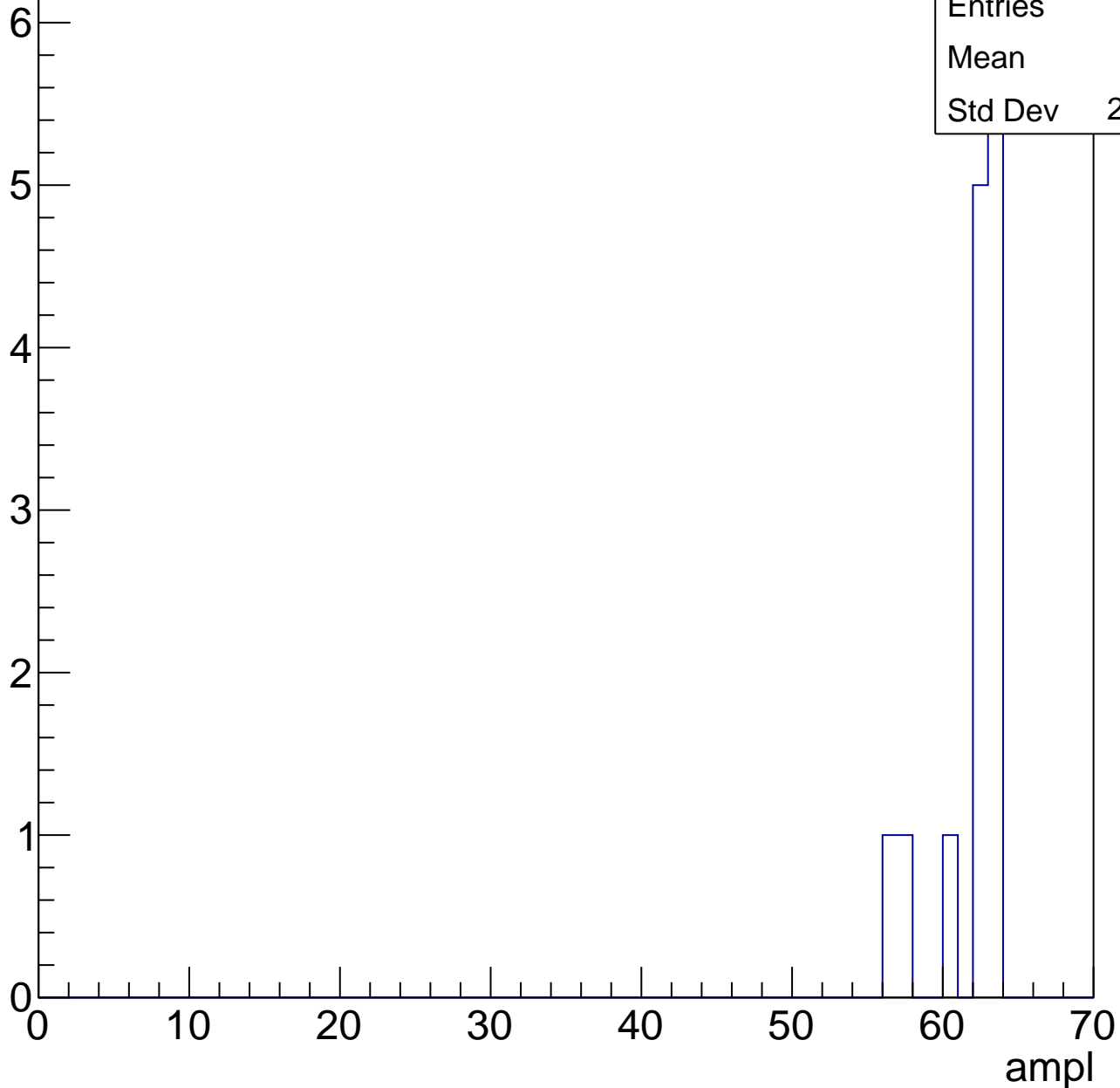


B1L101S, U4-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.5
Std Dev	2.196



B1L101S, U4-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U4-ch123, adc0

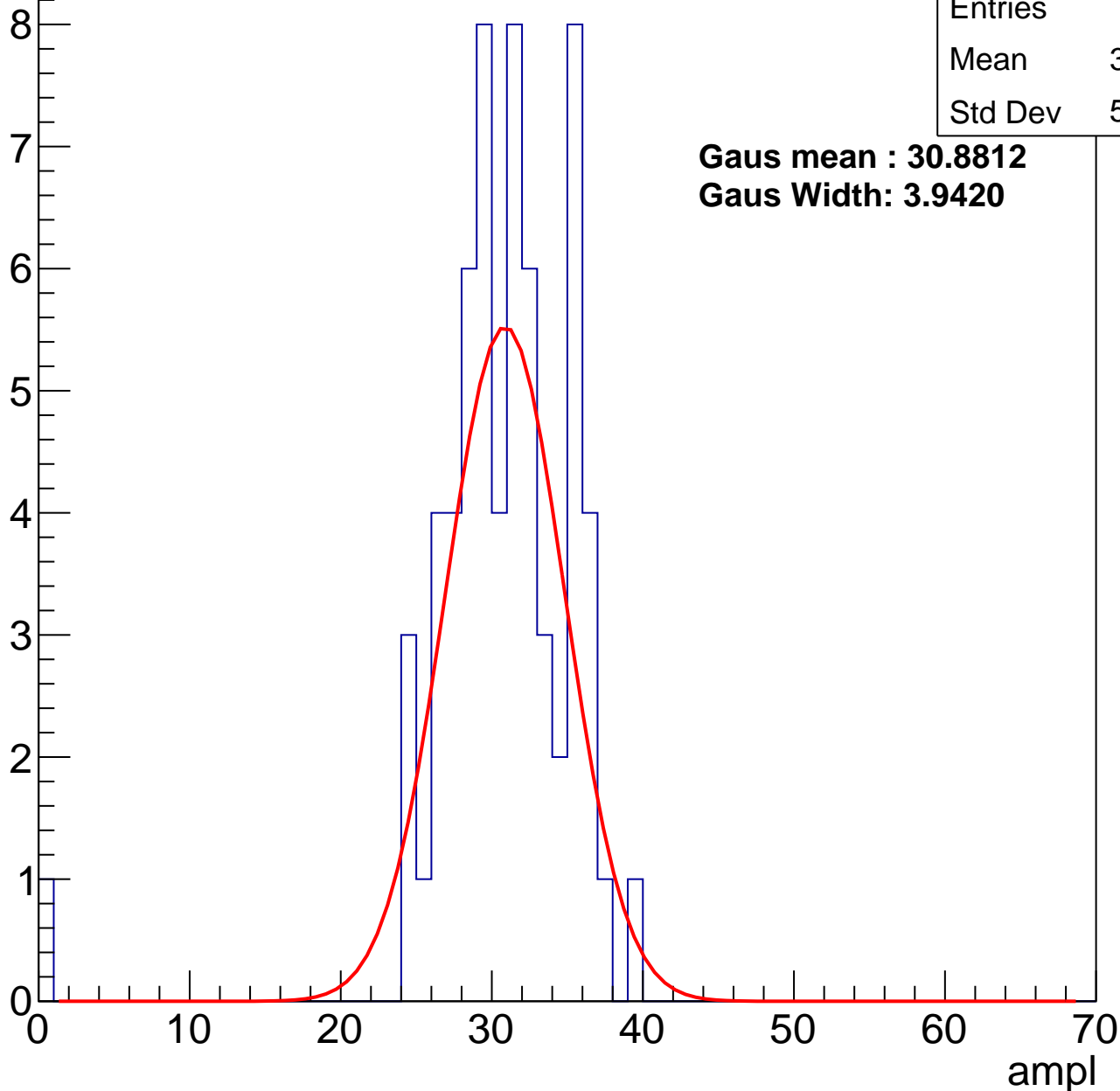
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.25
Std Dev	5.193

Gaus mean : 30.8812

Gaus Width: 3.9420



B1L101S, U4-ch123, adc1

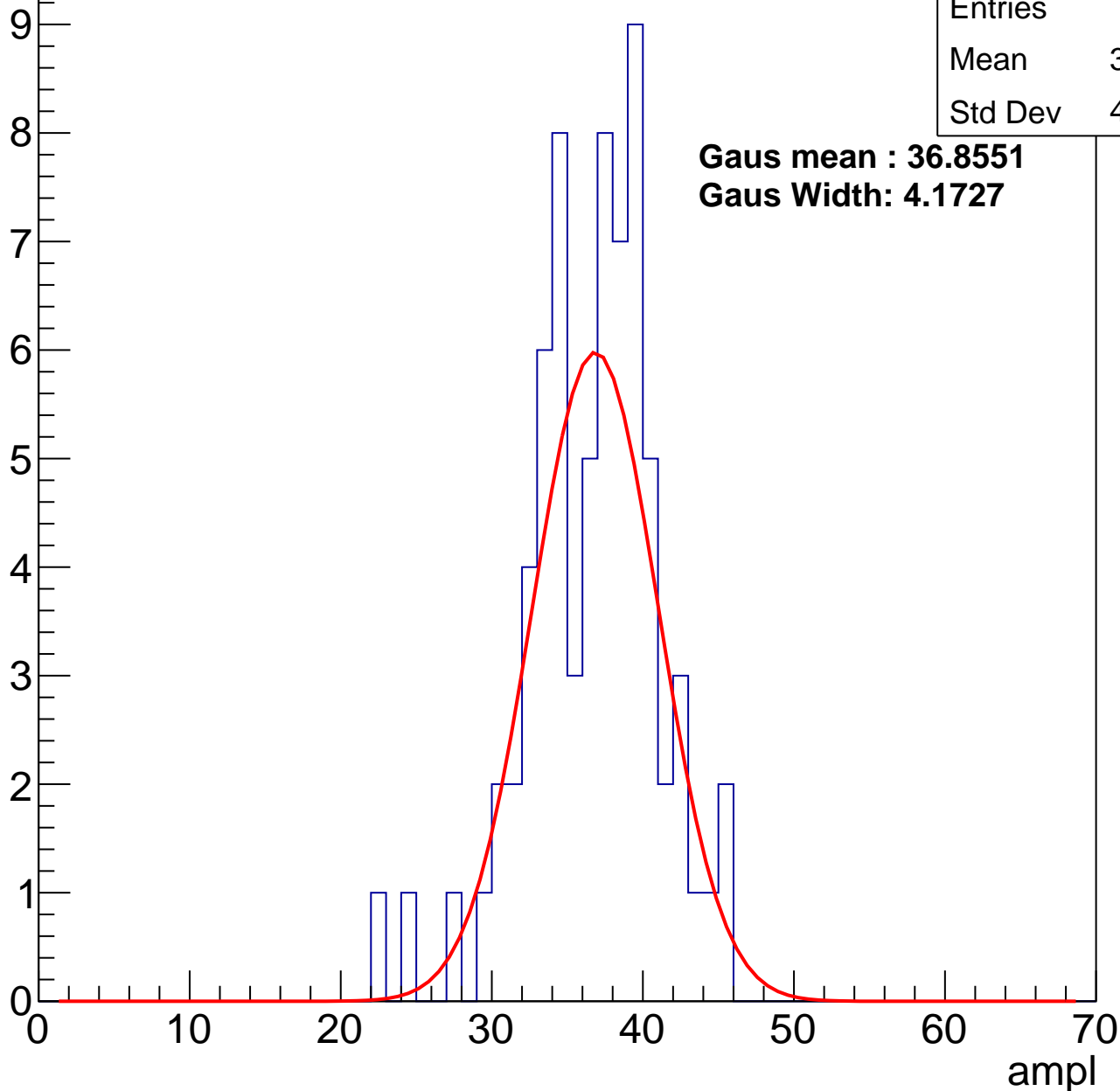
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.18
Std Dev	4.366

Gaus mean : 36.8551

Gaus Width: 4.1727



B1L101S, U4-ch123, adc2

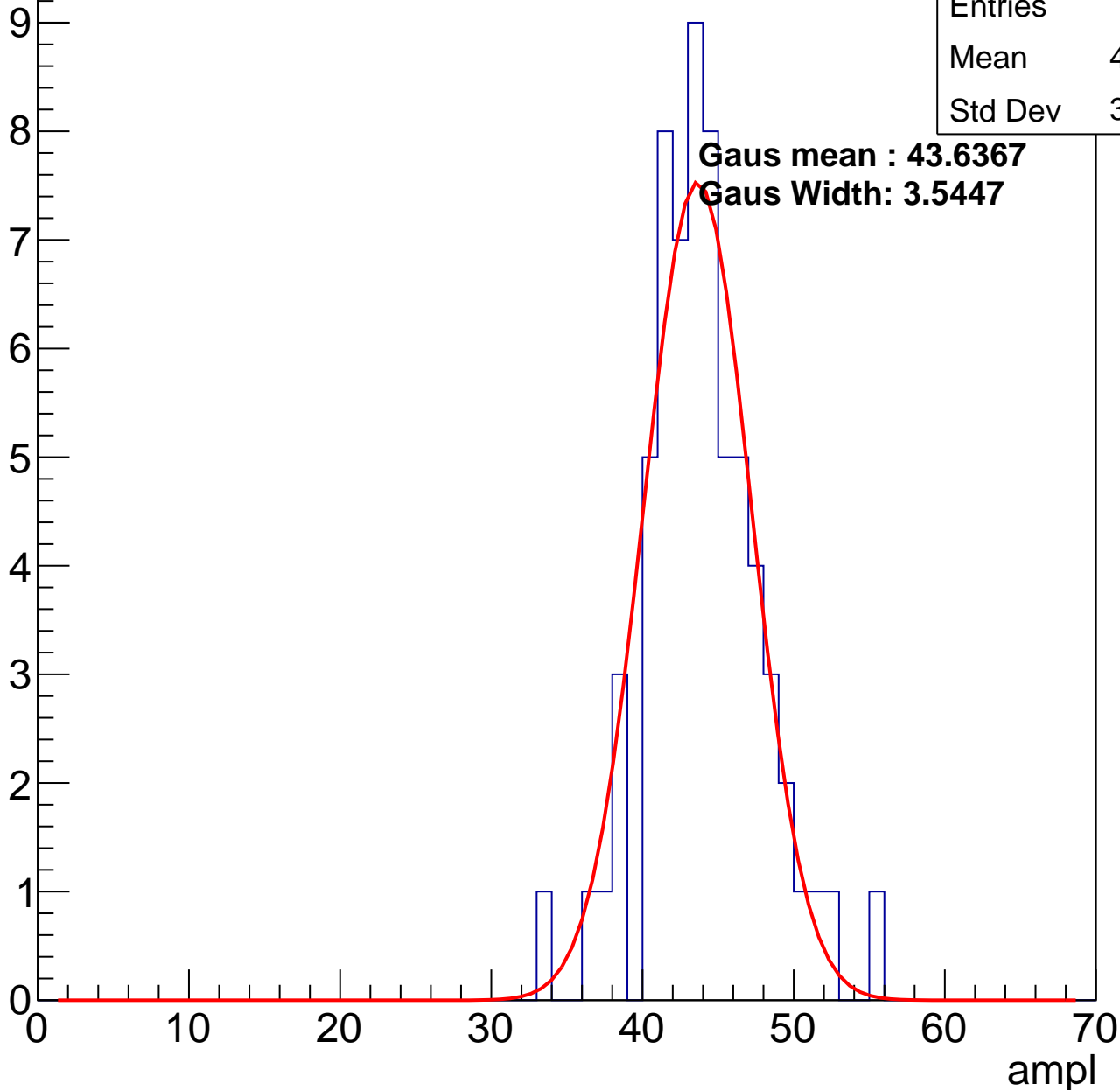
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.55
Std Dev	3.787

Gaus mean : 43.6367

Gaus Width: 3.5447

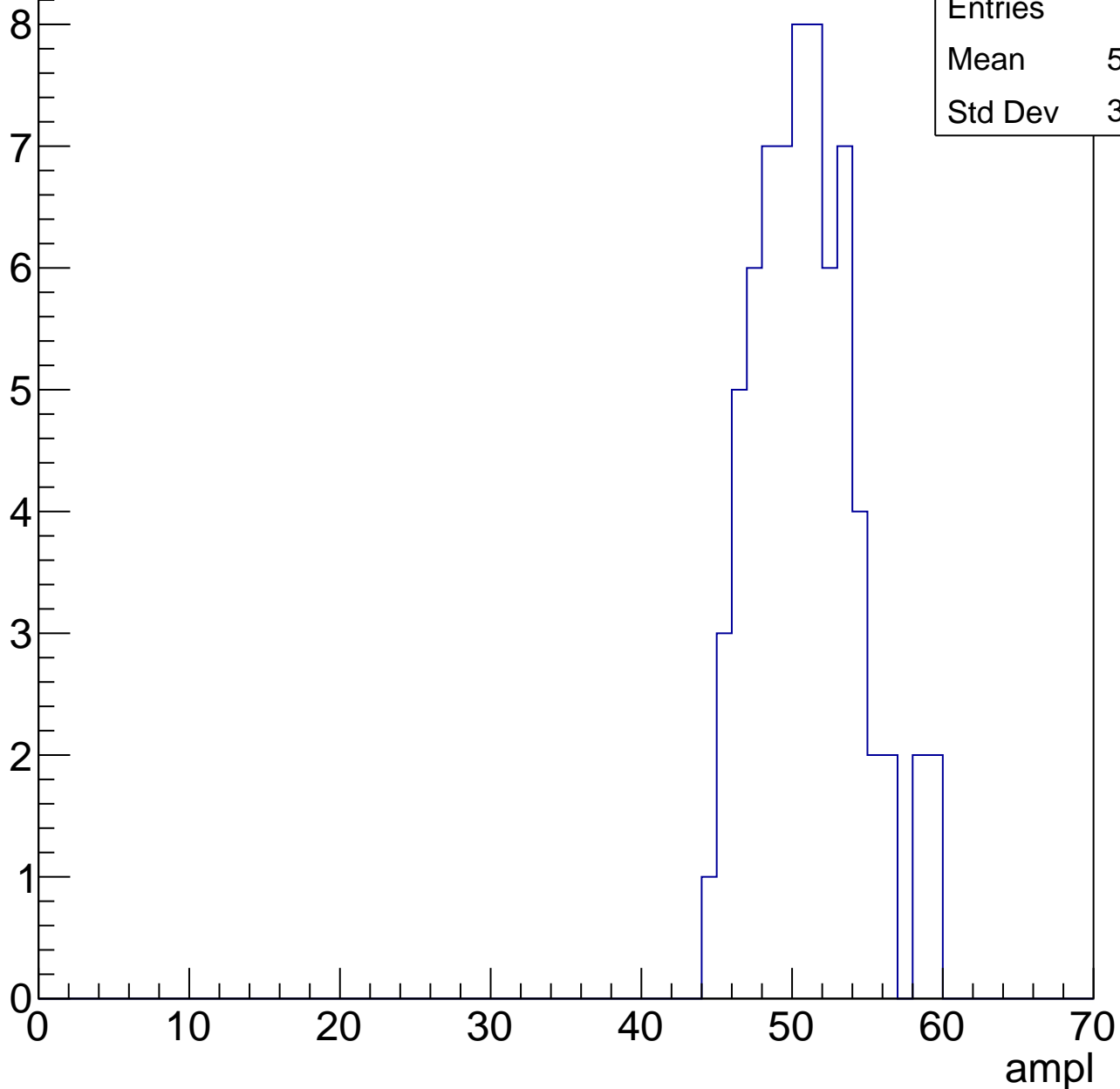


B1L101S, U4-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

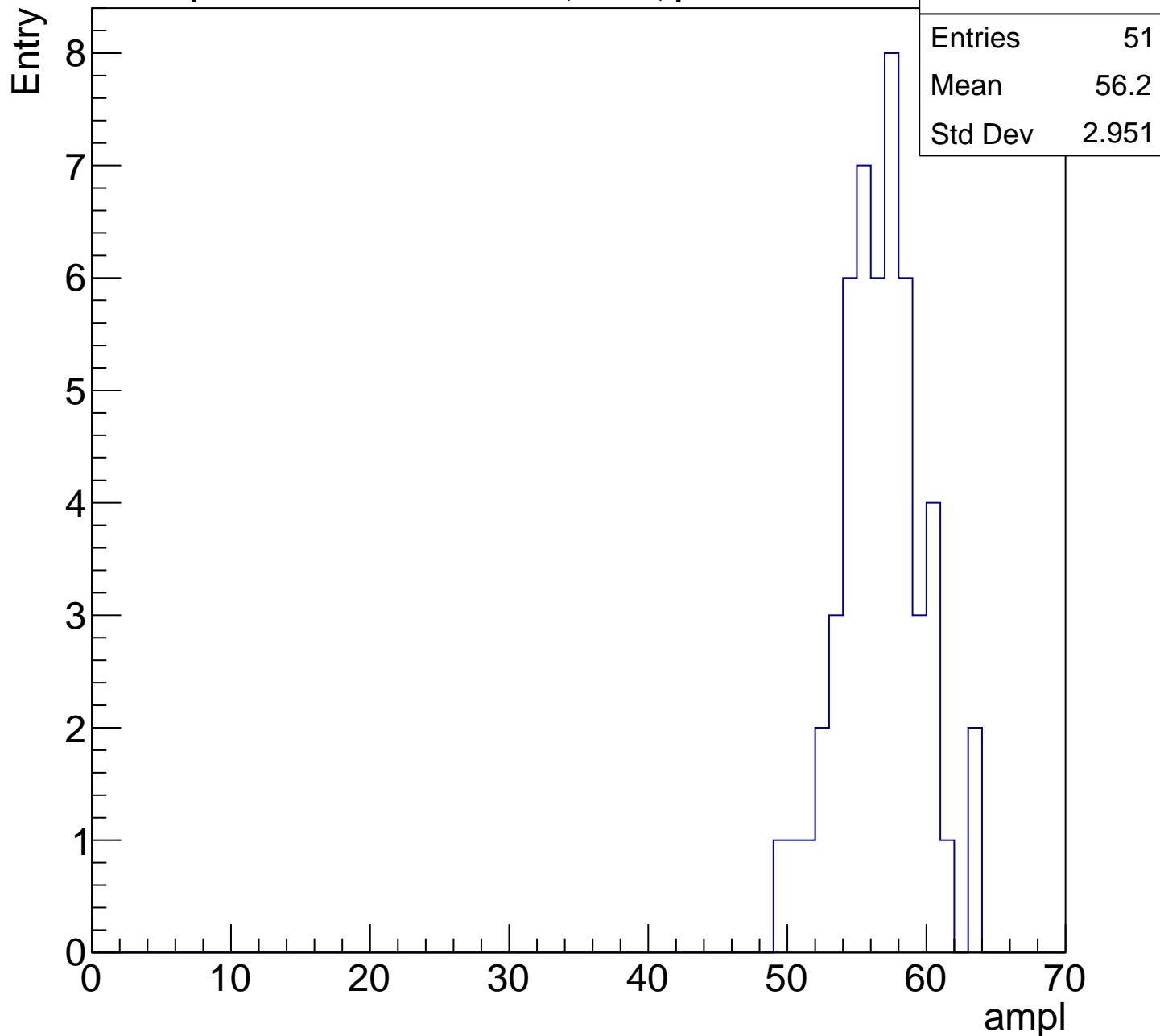
Entry

Entries	70
Mean	50.47
Std Dev	3.455



B1L101S, U4-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

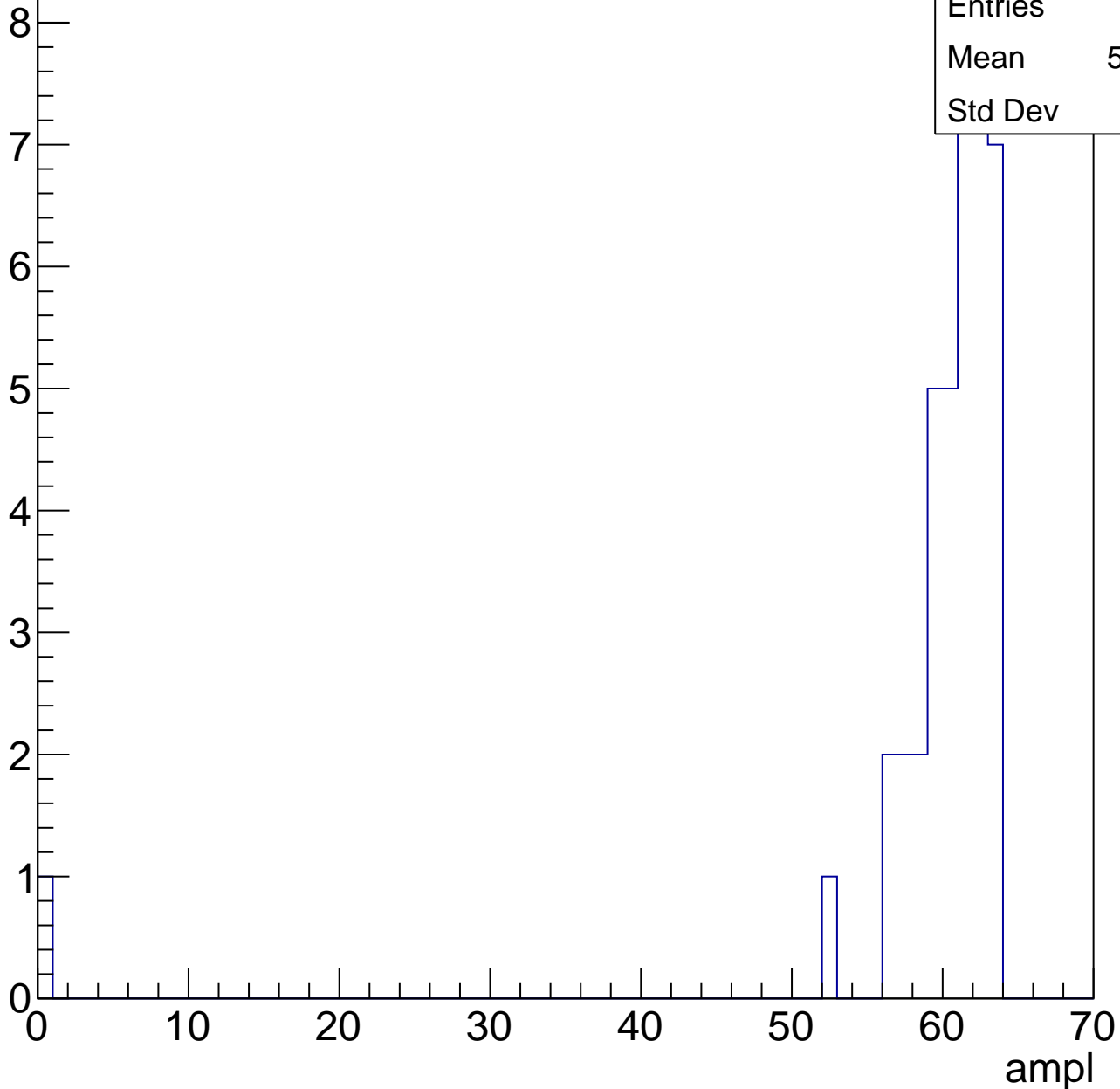


B1L101S, U4-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.88
Std Dev	9.6



B1L101S, U4-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

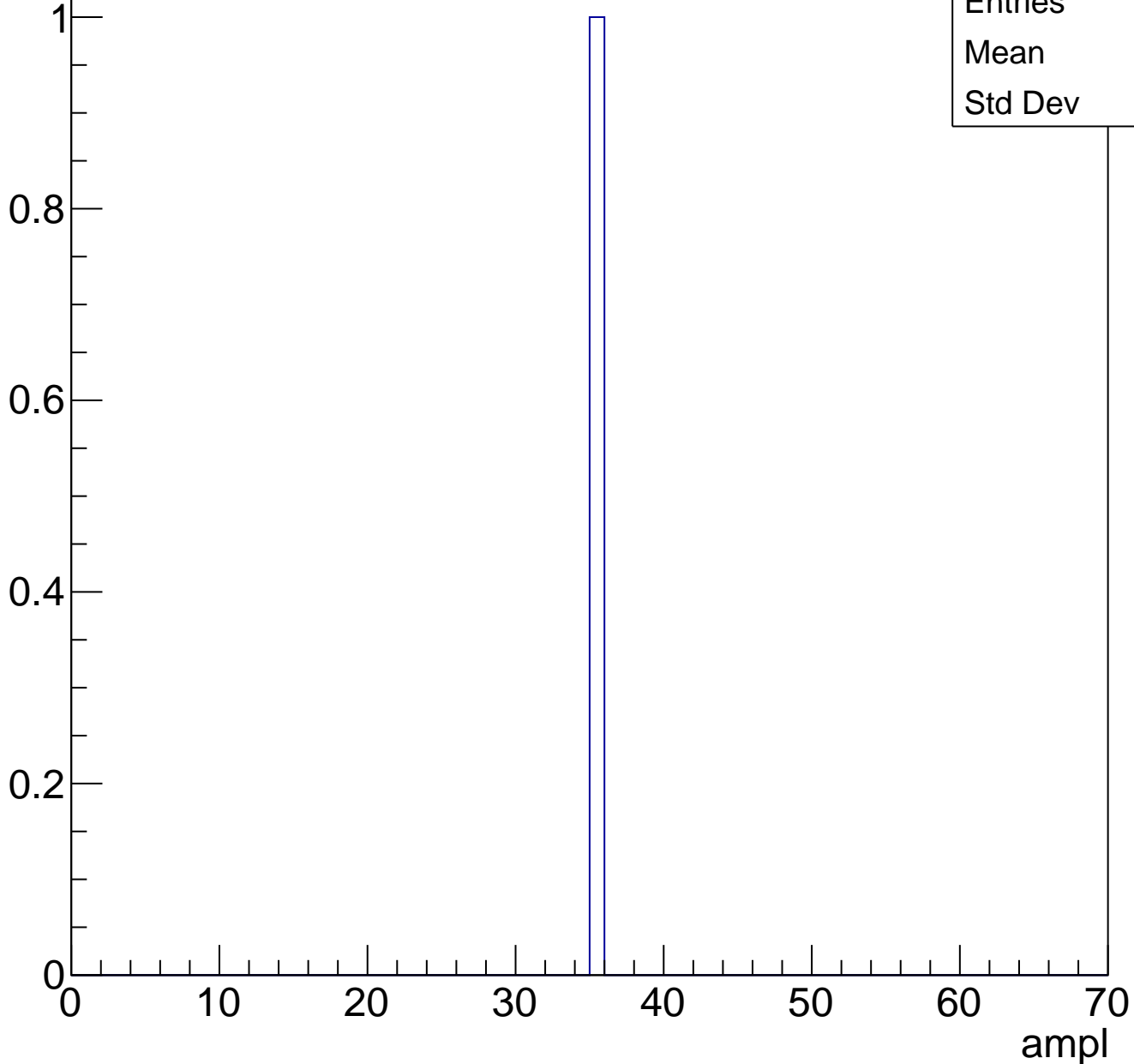


Entries	5
Mean	61.6
Std Dev	1.02

B1L101S, U4-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch124, adc0

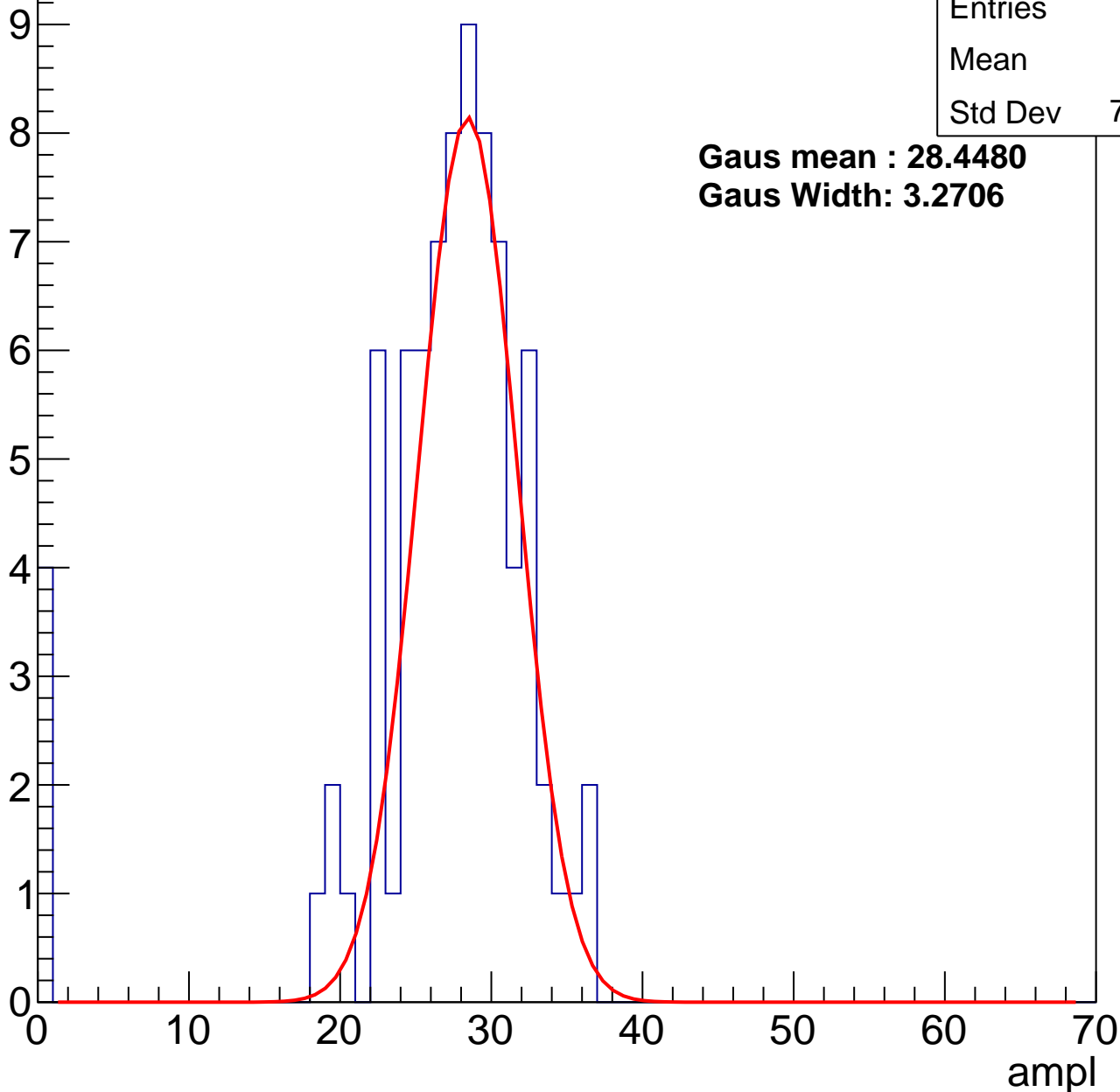
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	26.1
Std Dev	7.005

Gaus mean : 28.4480

Gaus Width: 3.2706



B1L101S, U4-ch124, adc1

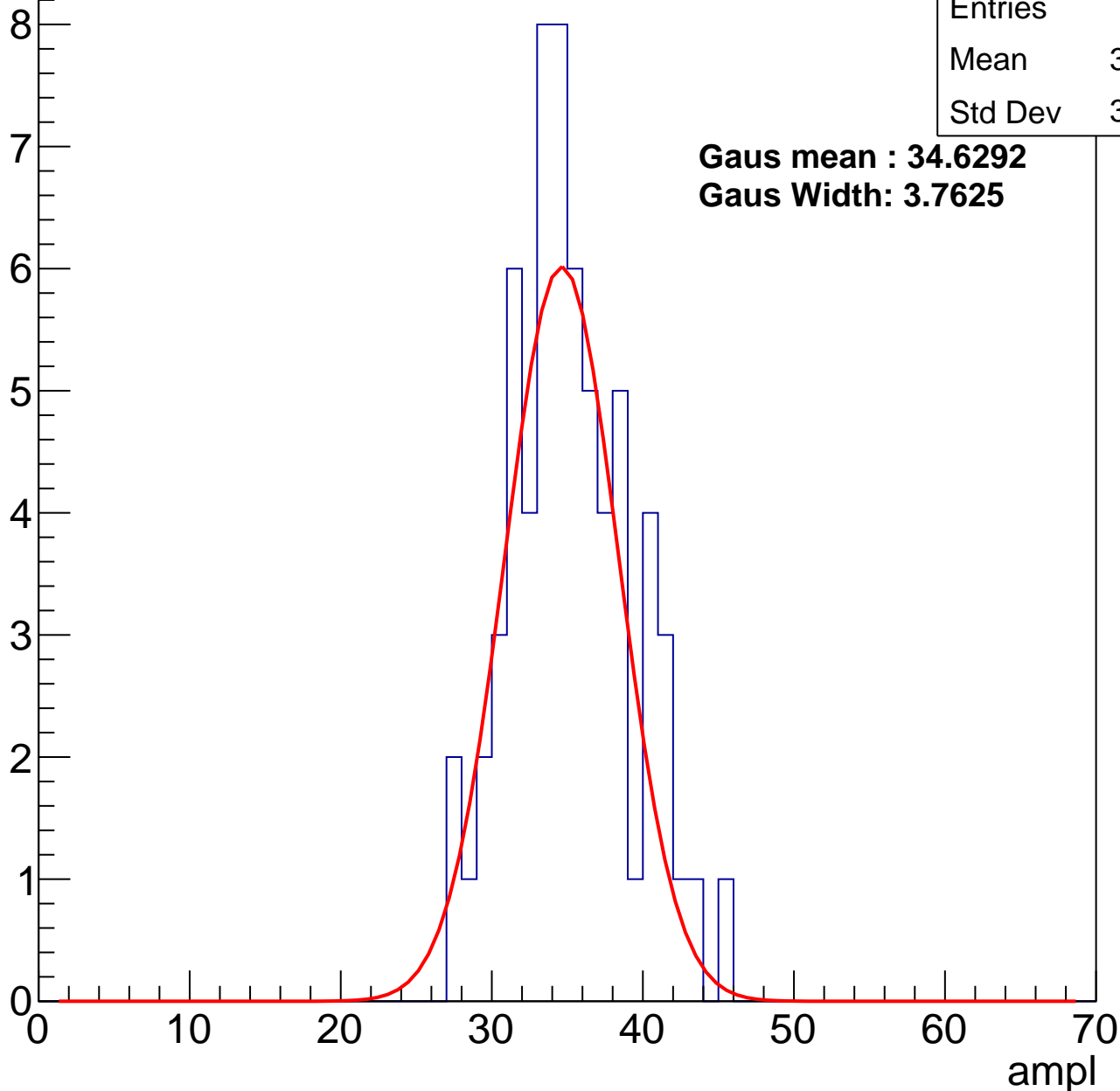
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	34.77
Std Dev	3.914

Gaus mean : 34.6292

Gaus Width: 3.7625



B1L101S, U4-ch124, adc2

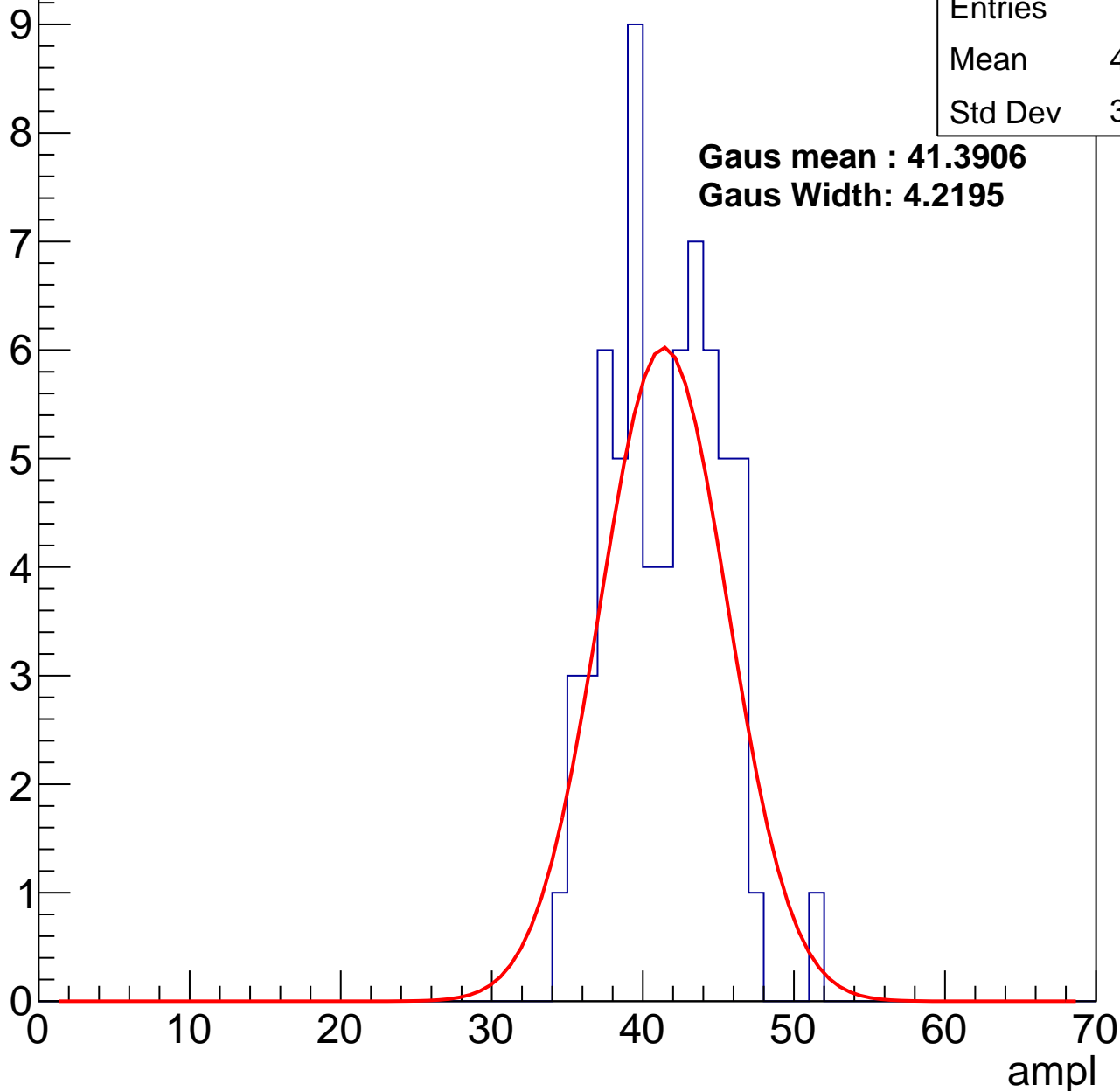
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	40.97
Std Dev	3.572

Gaus mean : 41.3906

Gaus Width: 4.2195

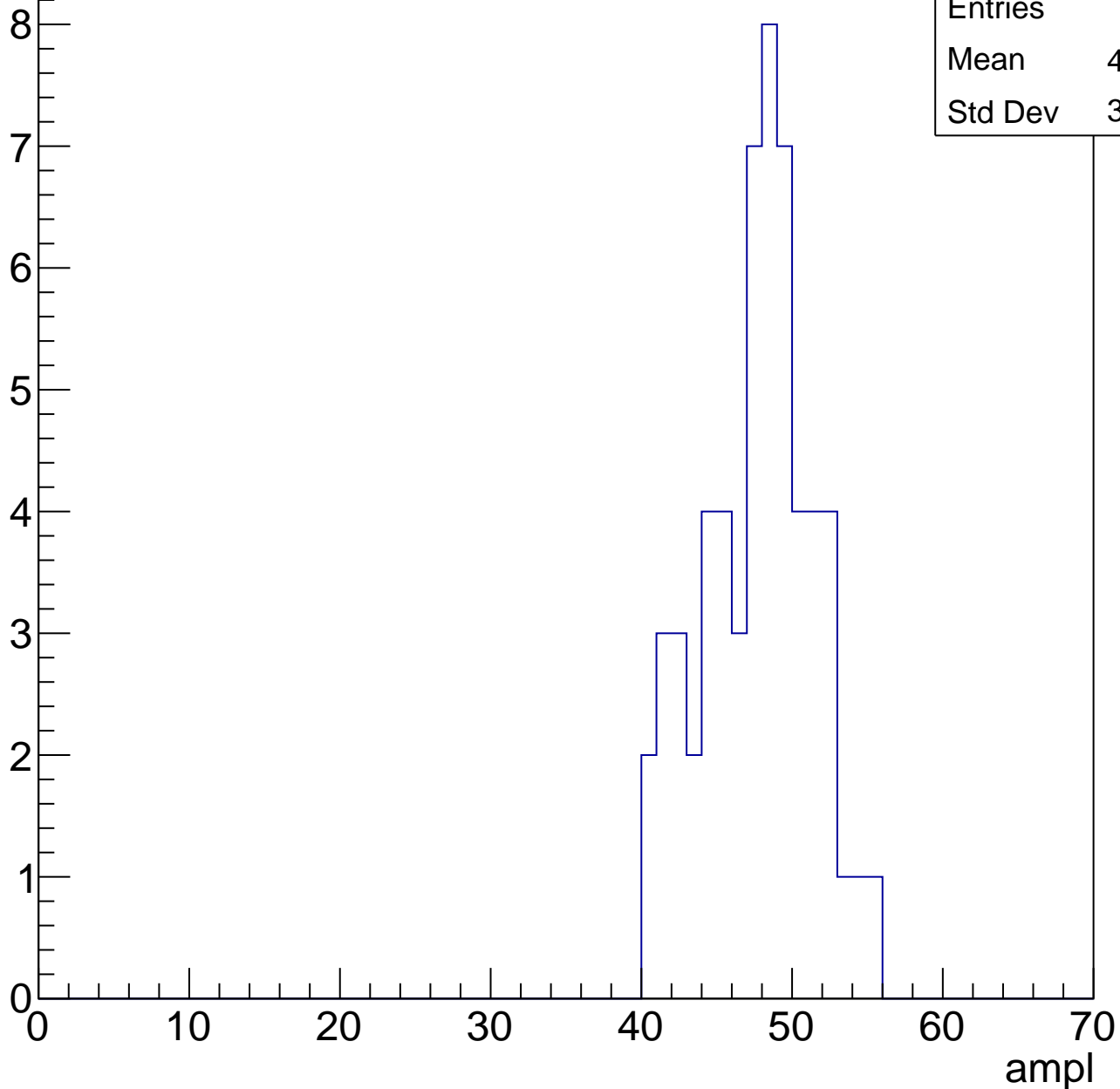


B1L101S, U4-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	47.22
Std Dev	3.596

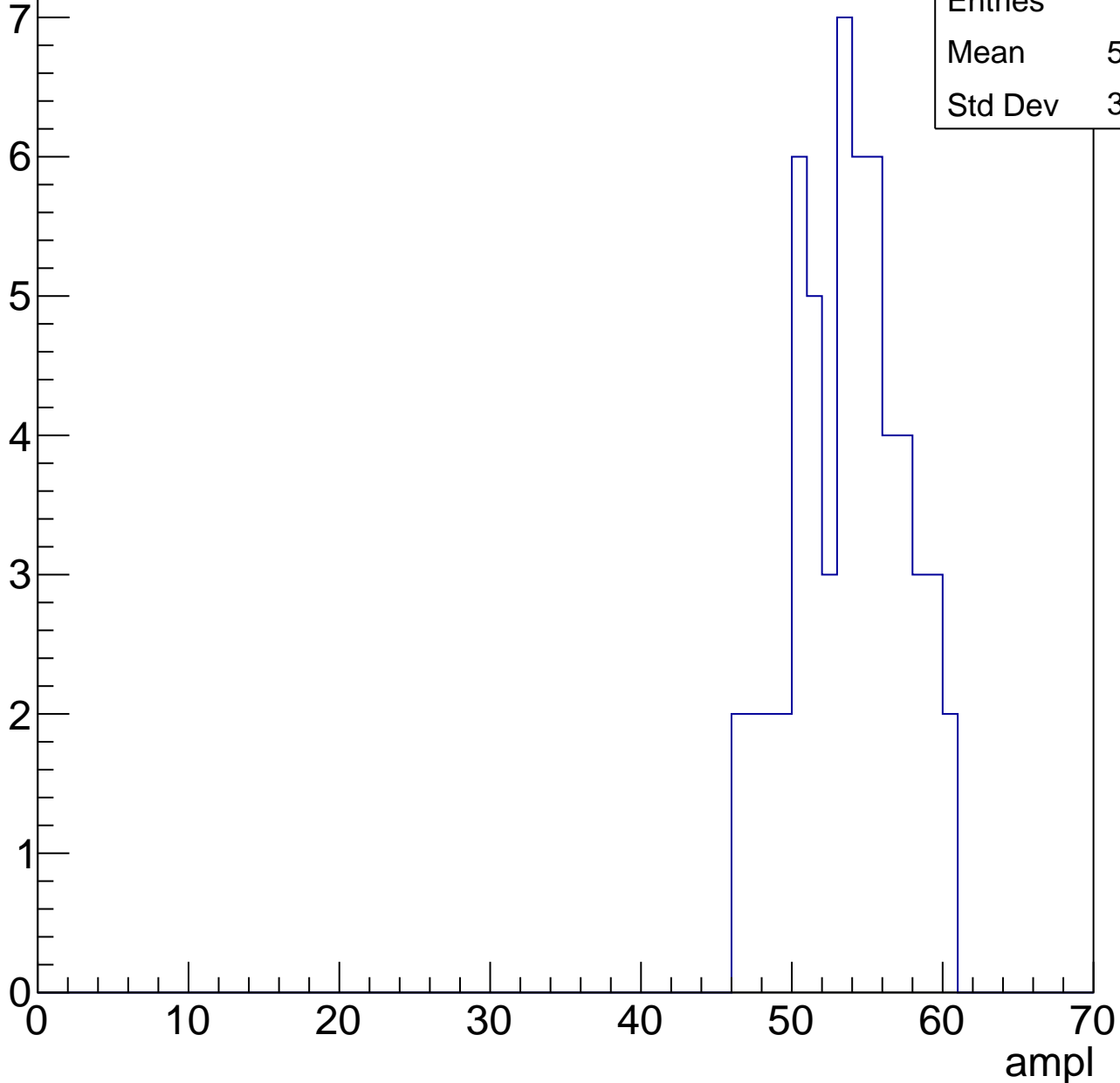


B1L101S, U4-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	53.32
Std Dev	3.589

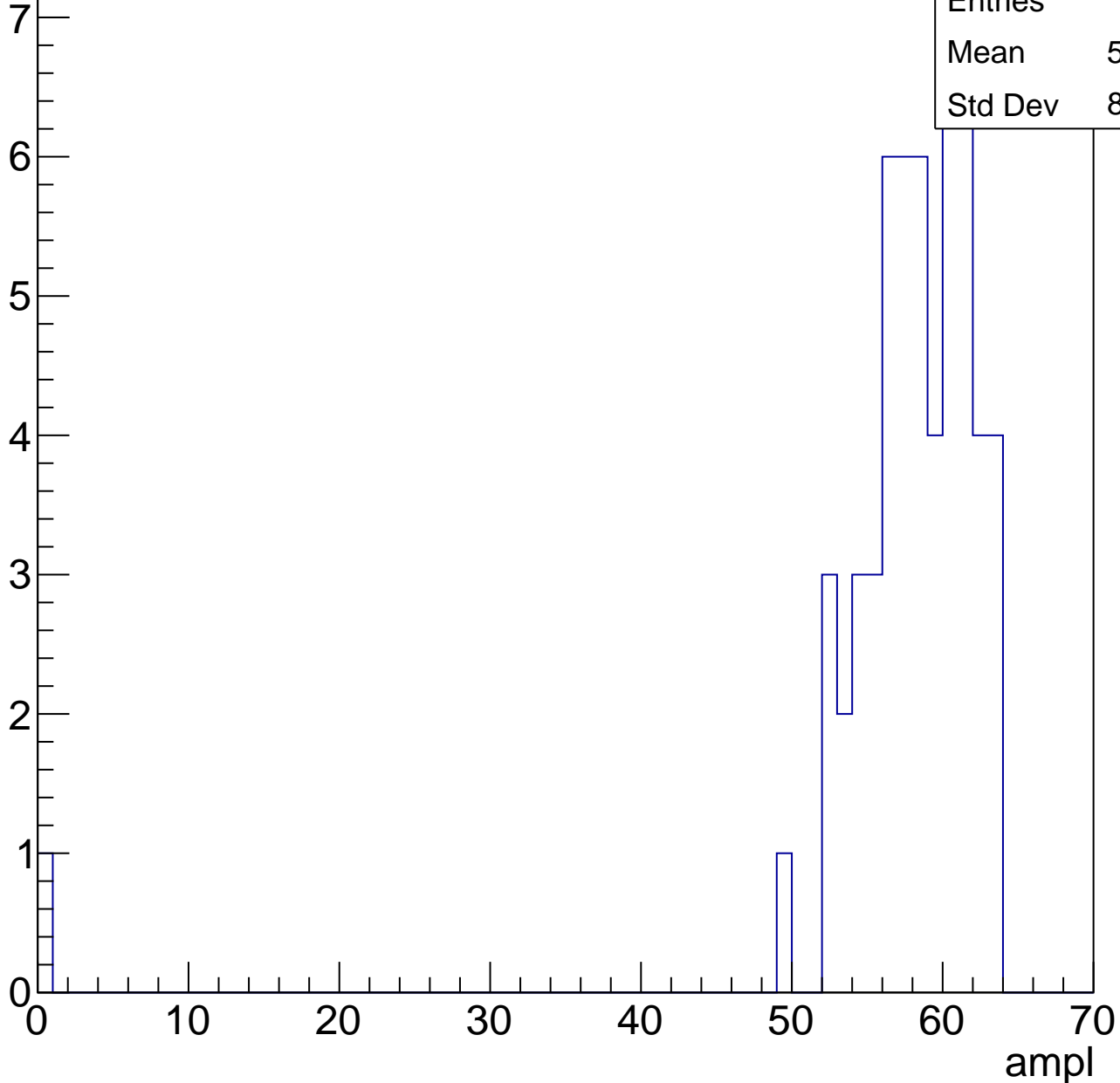


B1L101S, U4-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.96
Std Dev	8.278

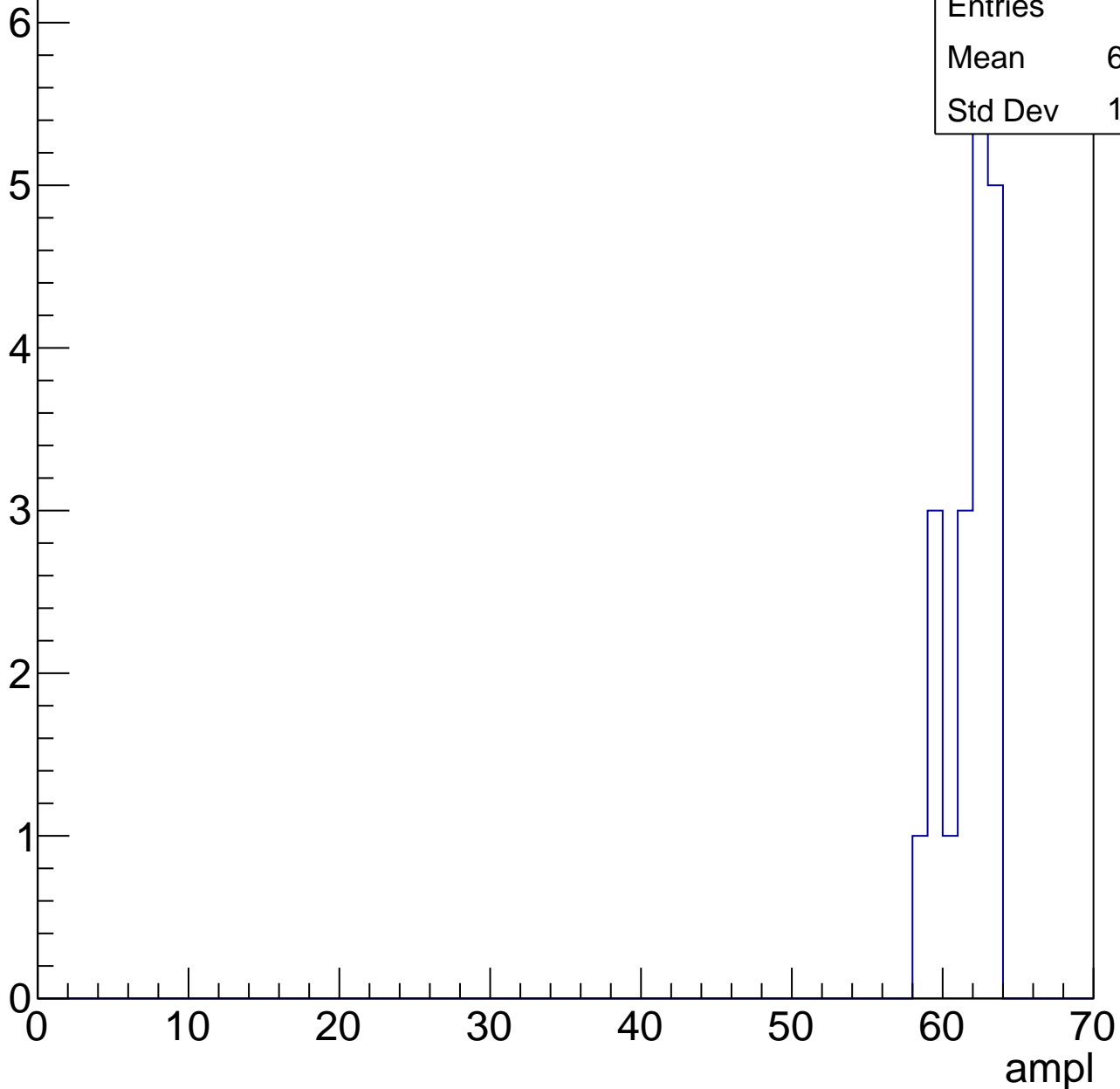


B1L101S, U4-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61.32
Std Dev	1.558



B1L101S, U4-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch125, adc0

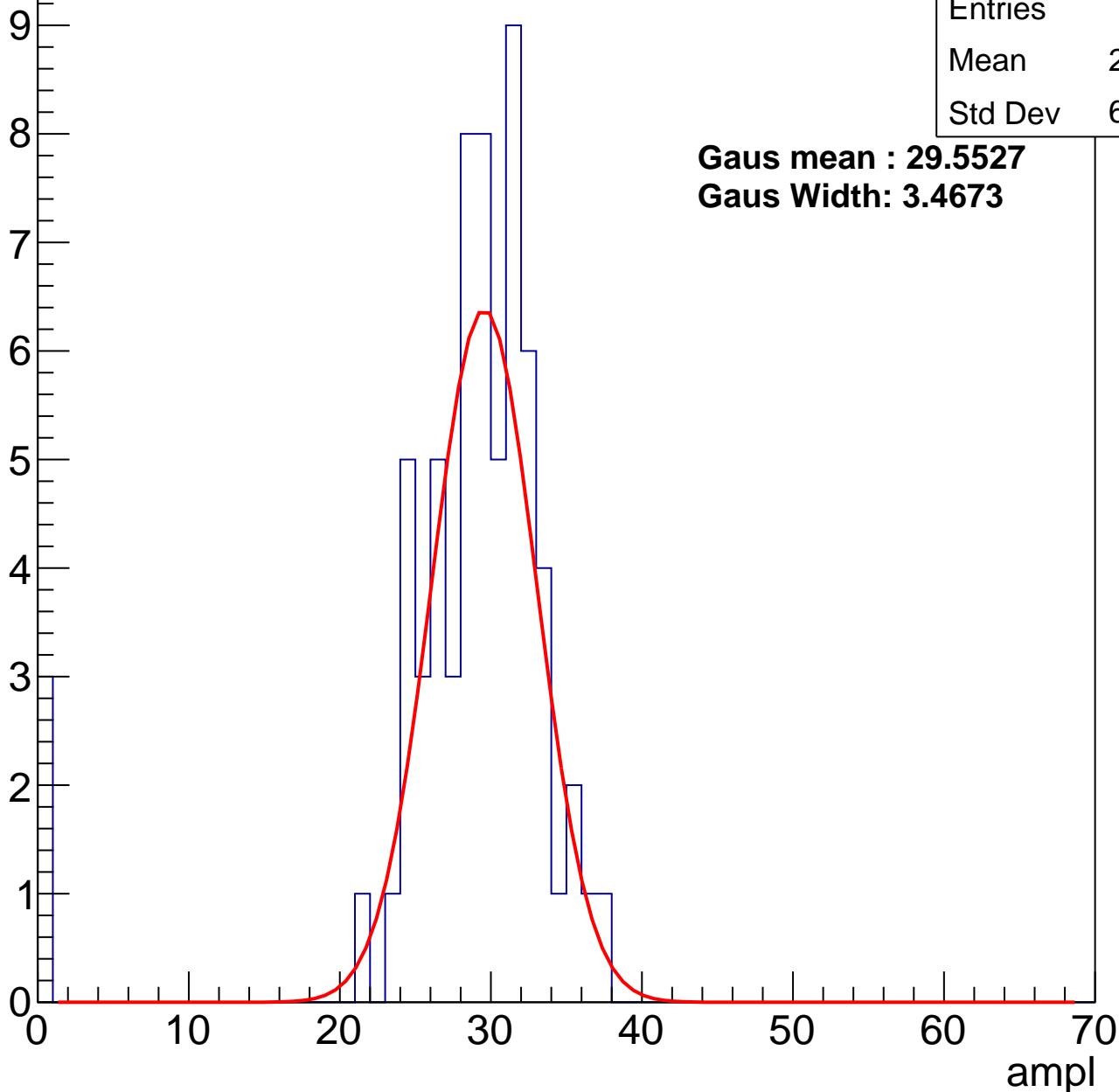
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	27.82
Std Dev	6.895

Gaus mean : 29.5527

Gaus Width: 3.4673



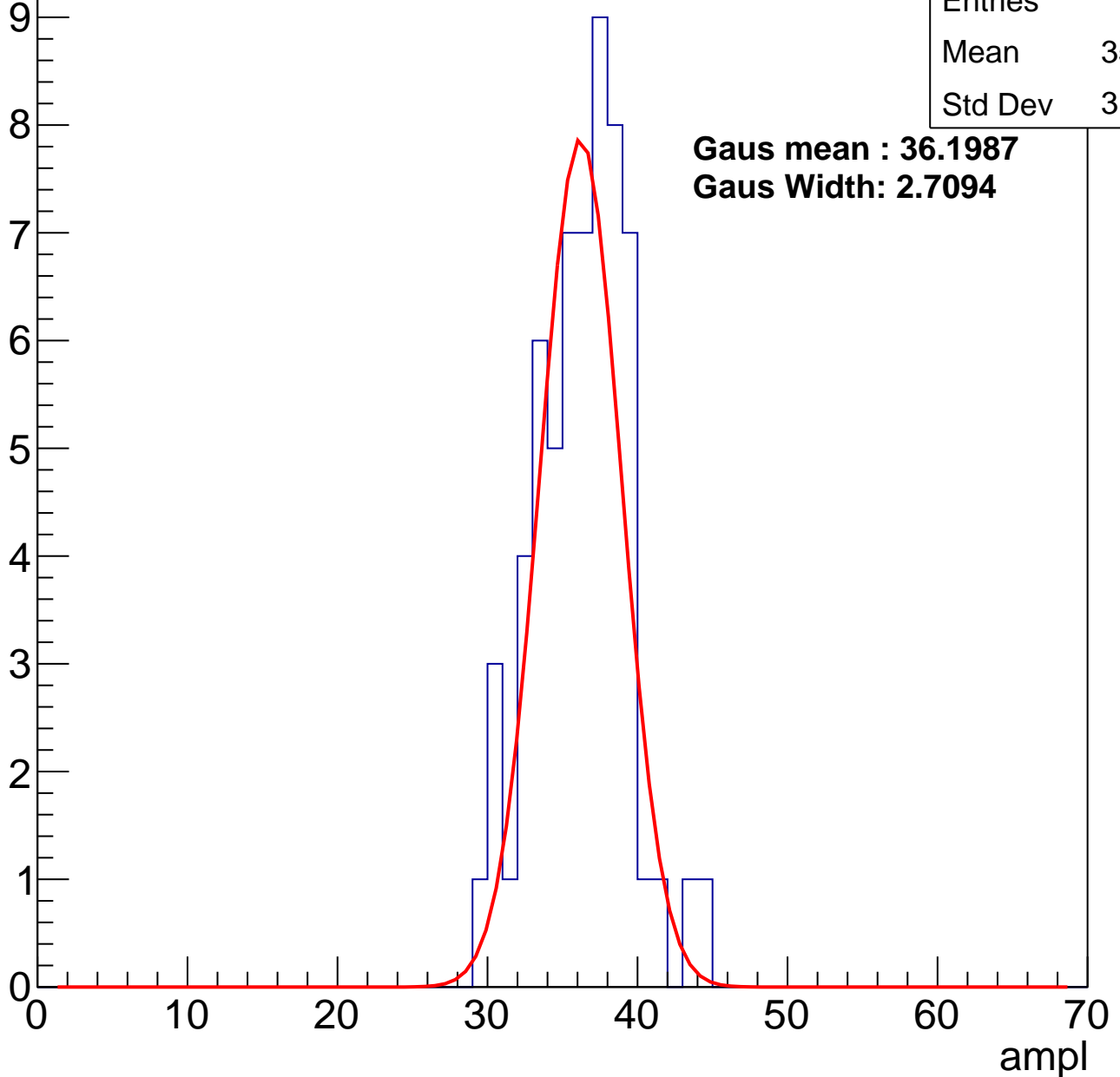
B1L101S, U4-ch125, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	35.82
Std Dev	3.067

Gaus mean : 36.1987
Gaus Width: 2.7094



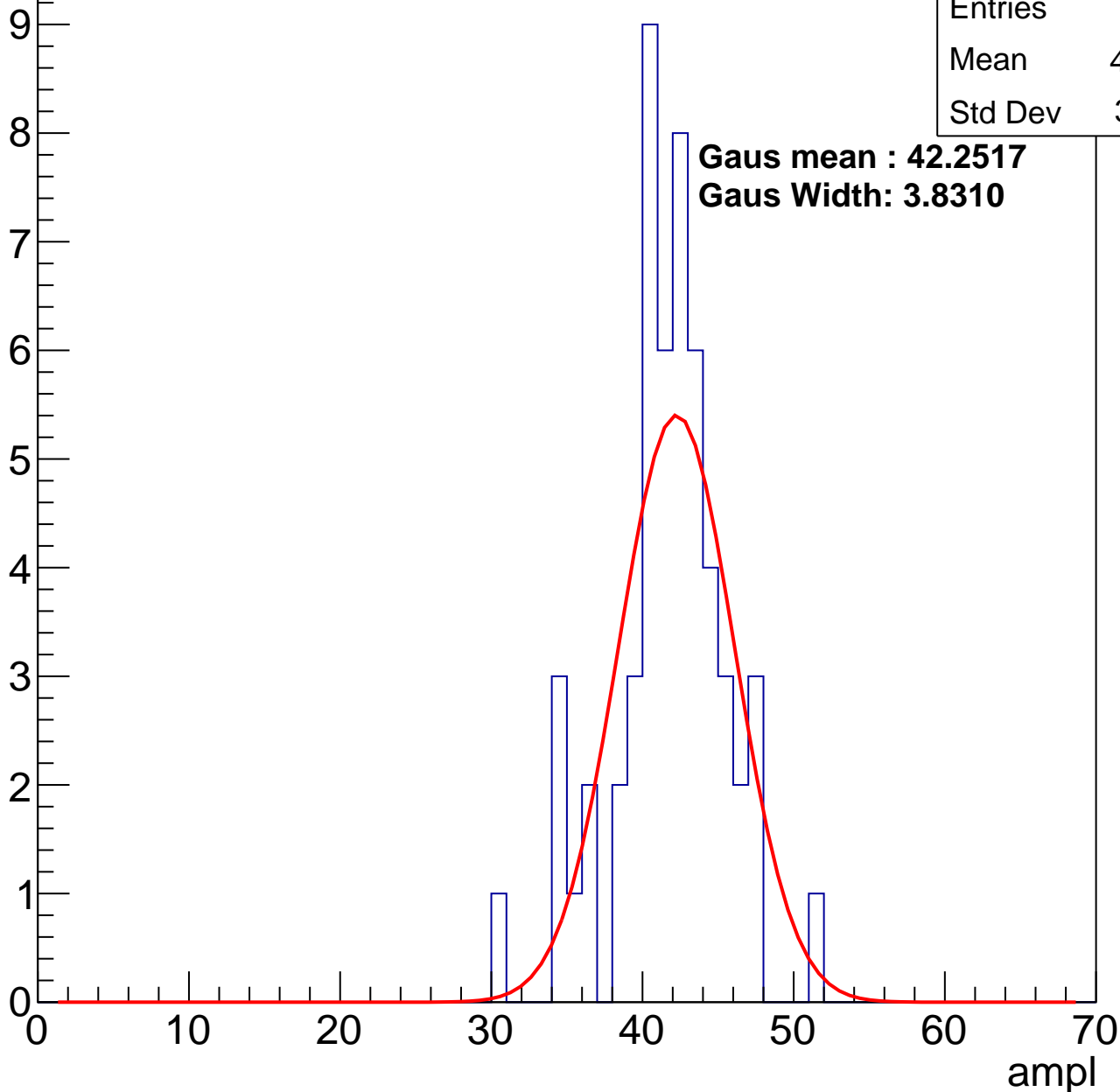
B1L101S, U4-ch125, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.24
Std Dev	3.761

Gaus mean : 42.2517
Gaus Width: 3.8310



B1L101S, U4-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

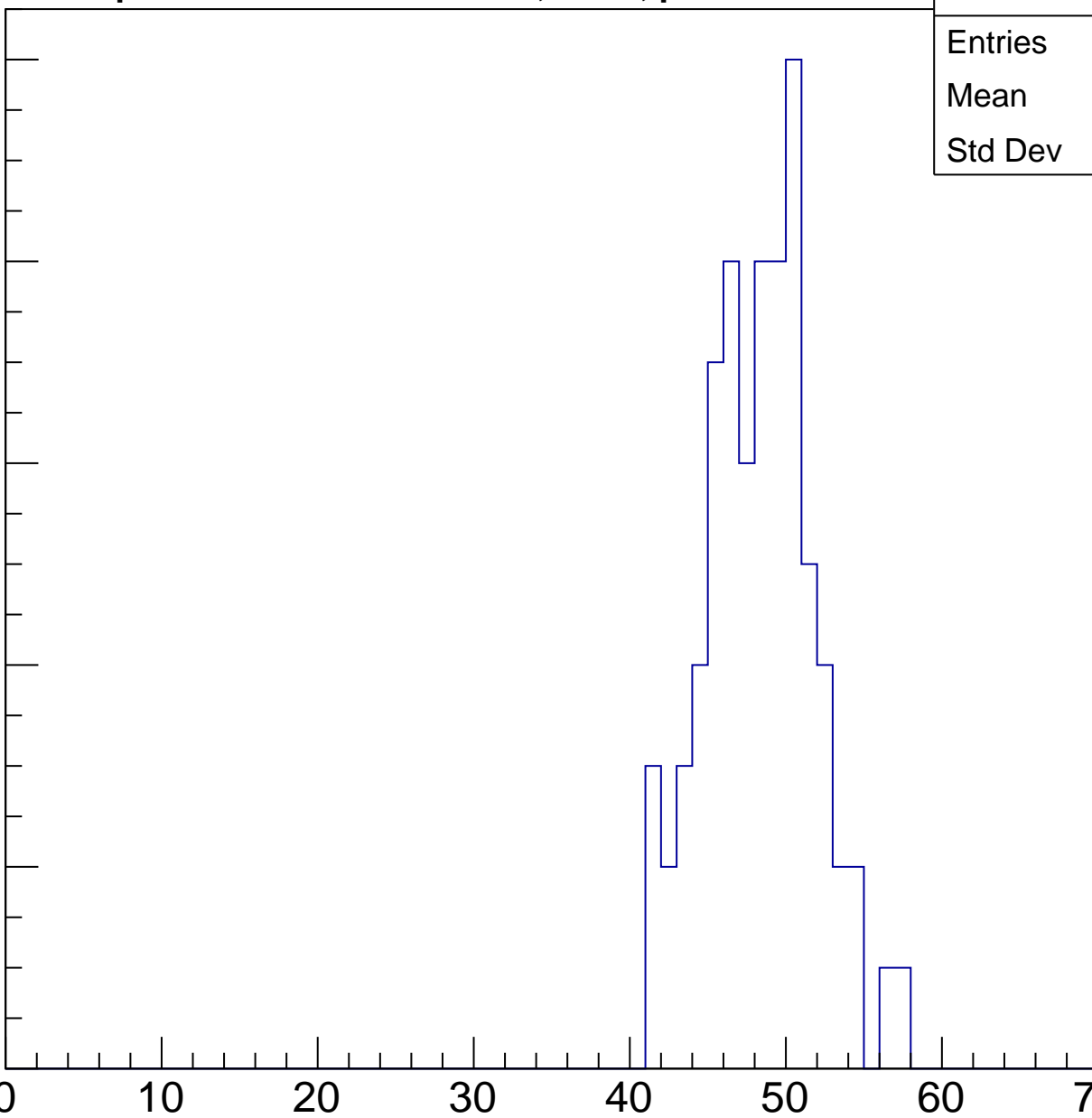
Entries	74
Mean	47.88
Std Dev	3.452

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

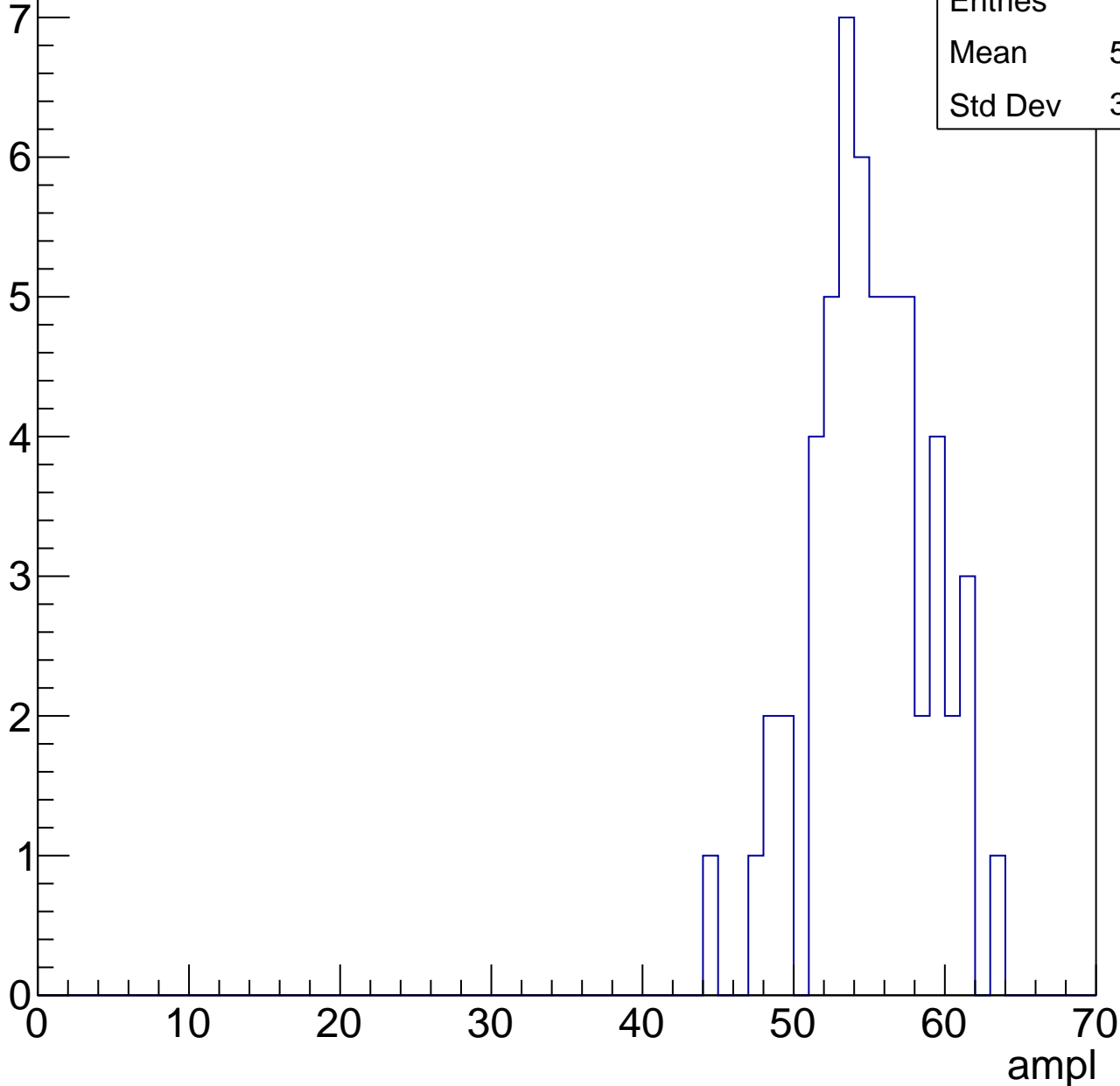


B1L101S, U4-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

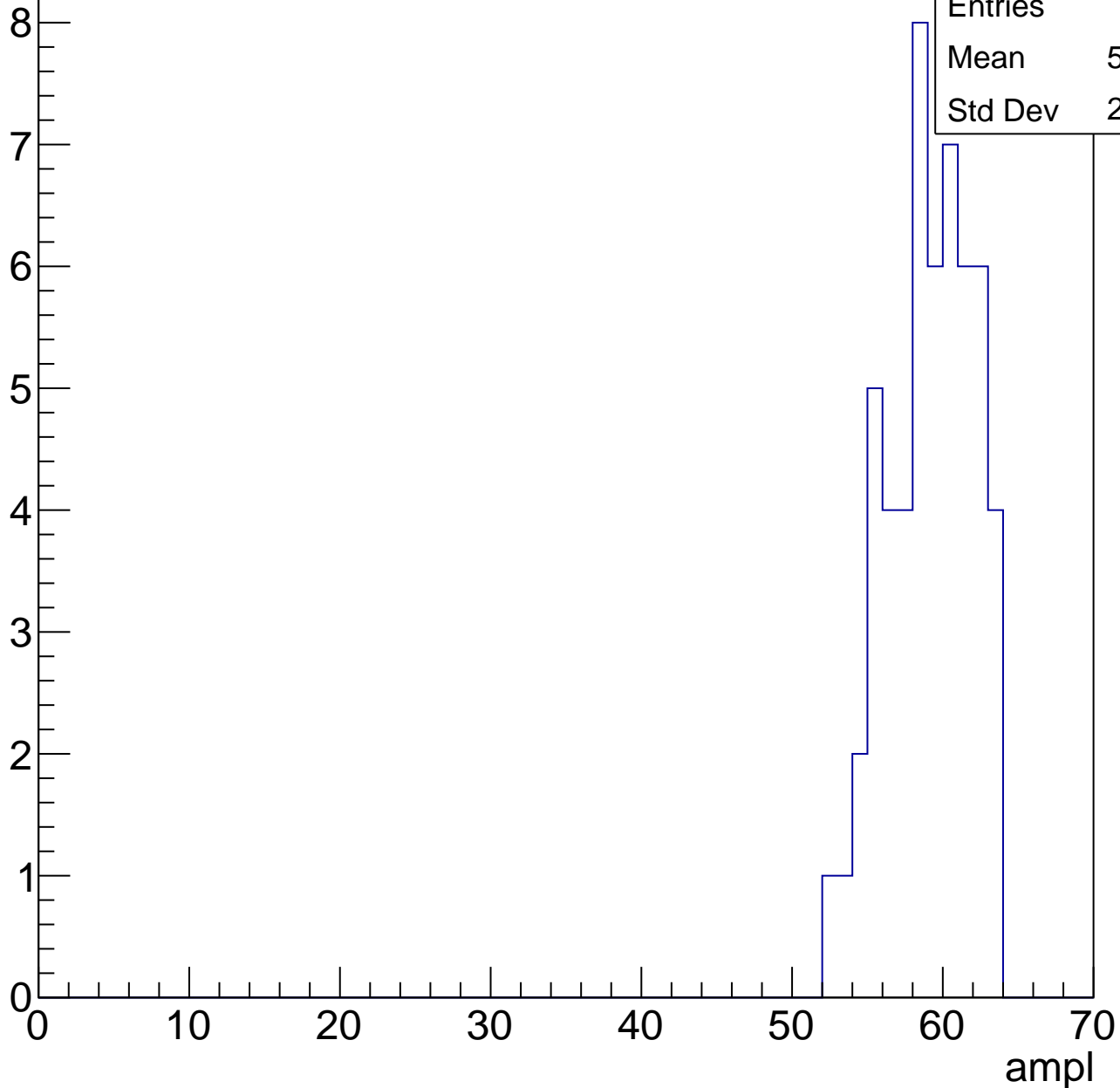
Entries	55
Mean	54.58
Std Dev	3.855



B1L101S, U4-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



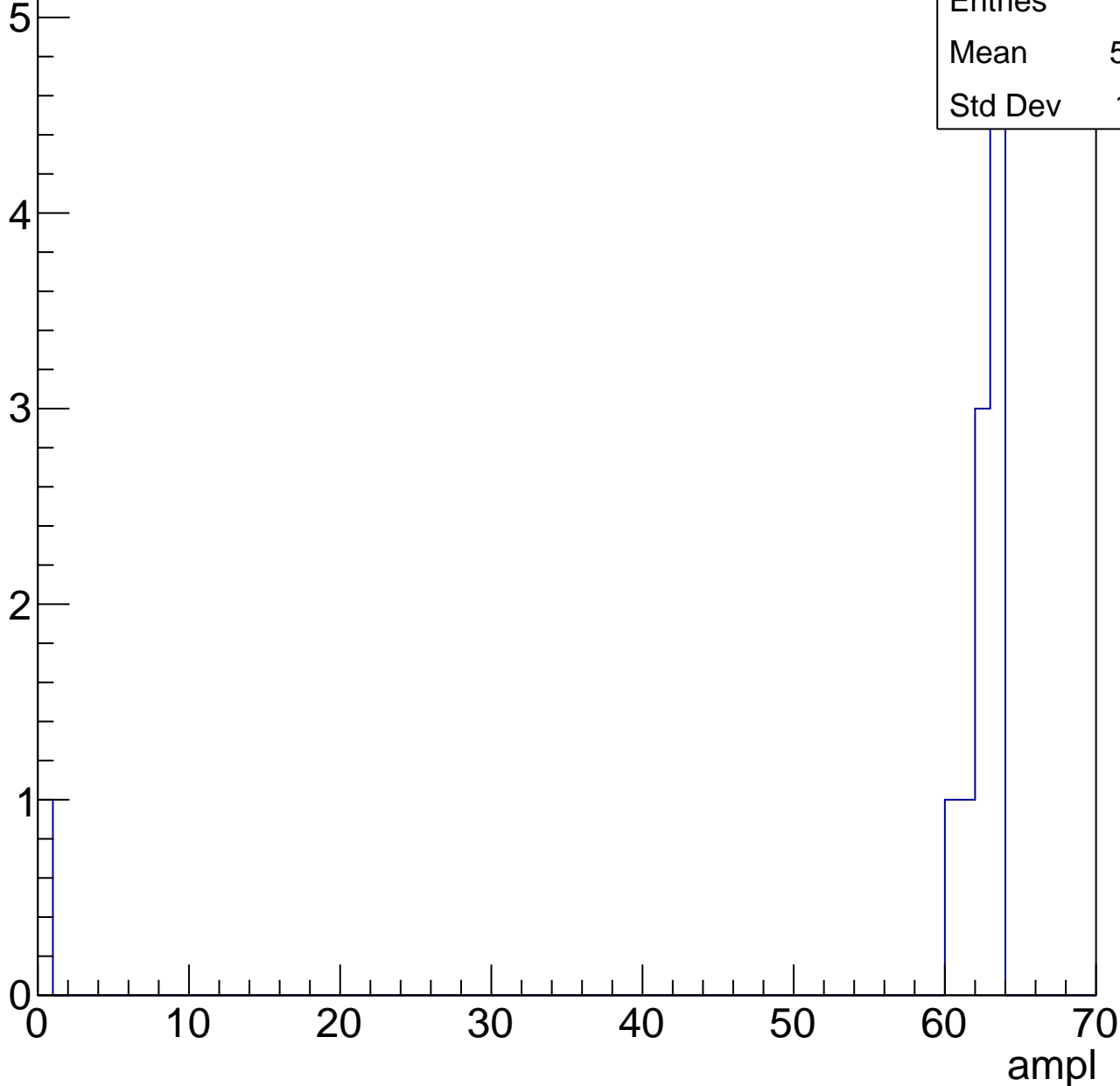
Entries	54
Mean	58.67
Std Dev	2.782

B1L101S, U4-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

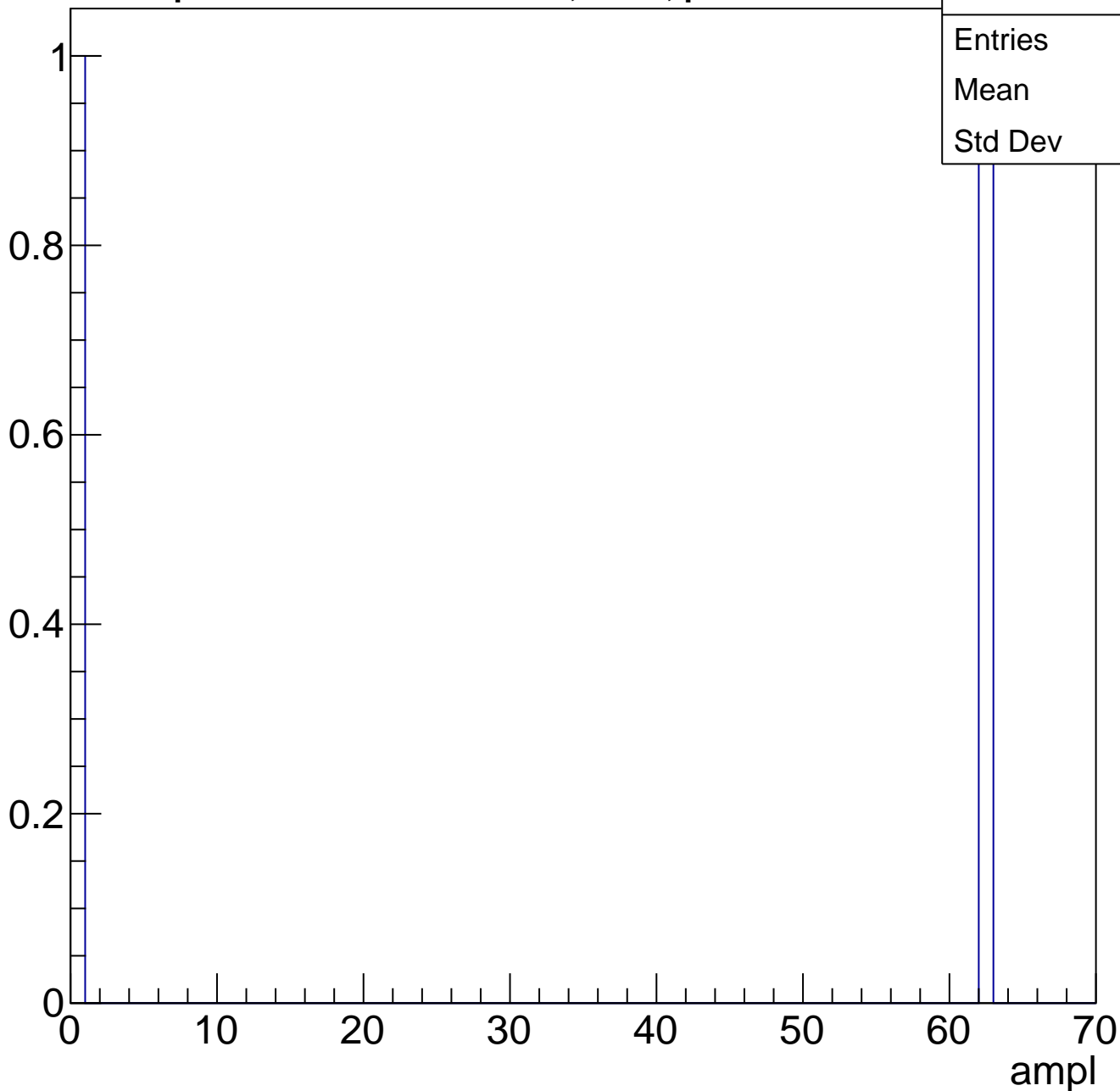
Entries	11
Mean	56.55
Std Dev	17.91



B1L101S, U4-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch126, adc0

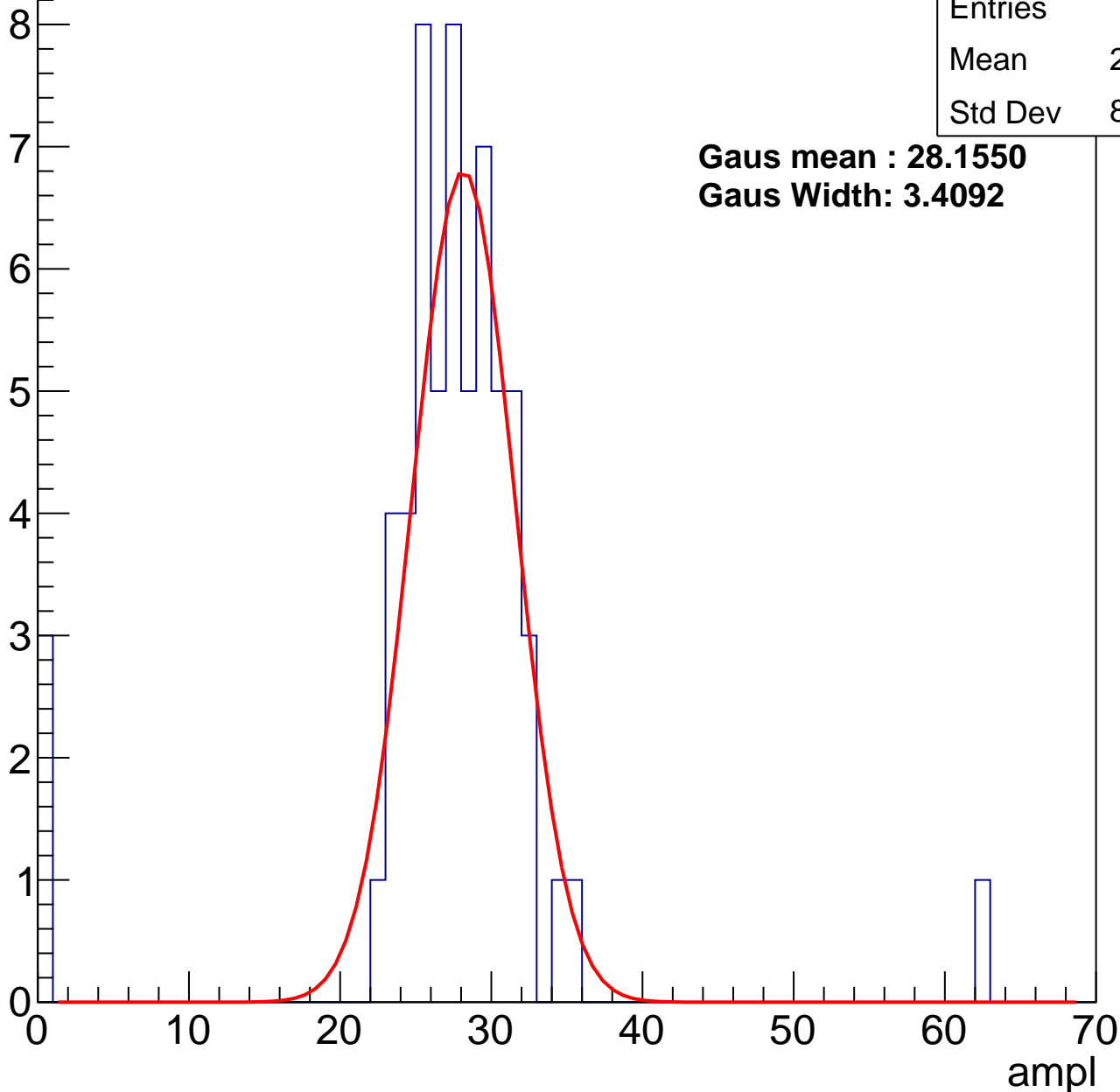
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	26.74
Std Dev	8.012

Gaus mean : 28.1550

Gaus Width: 3.4092



B1L101S, U4-ch126, adc1

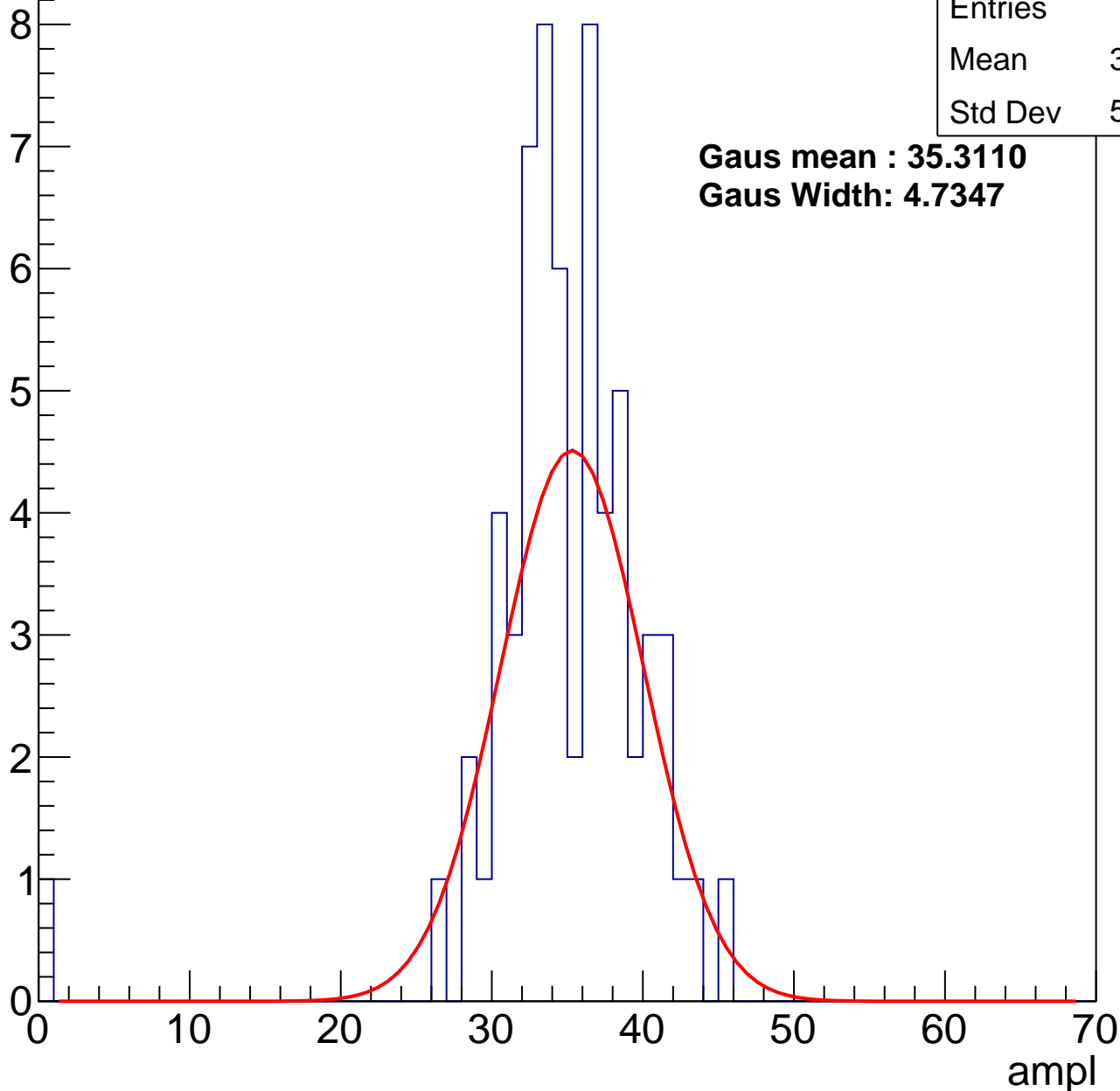
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	34.33
Std Dev	5.853

Gaus mean : 35.3110

Gaus Width: 4.7347



B1L101S, U4-ch126, adc2

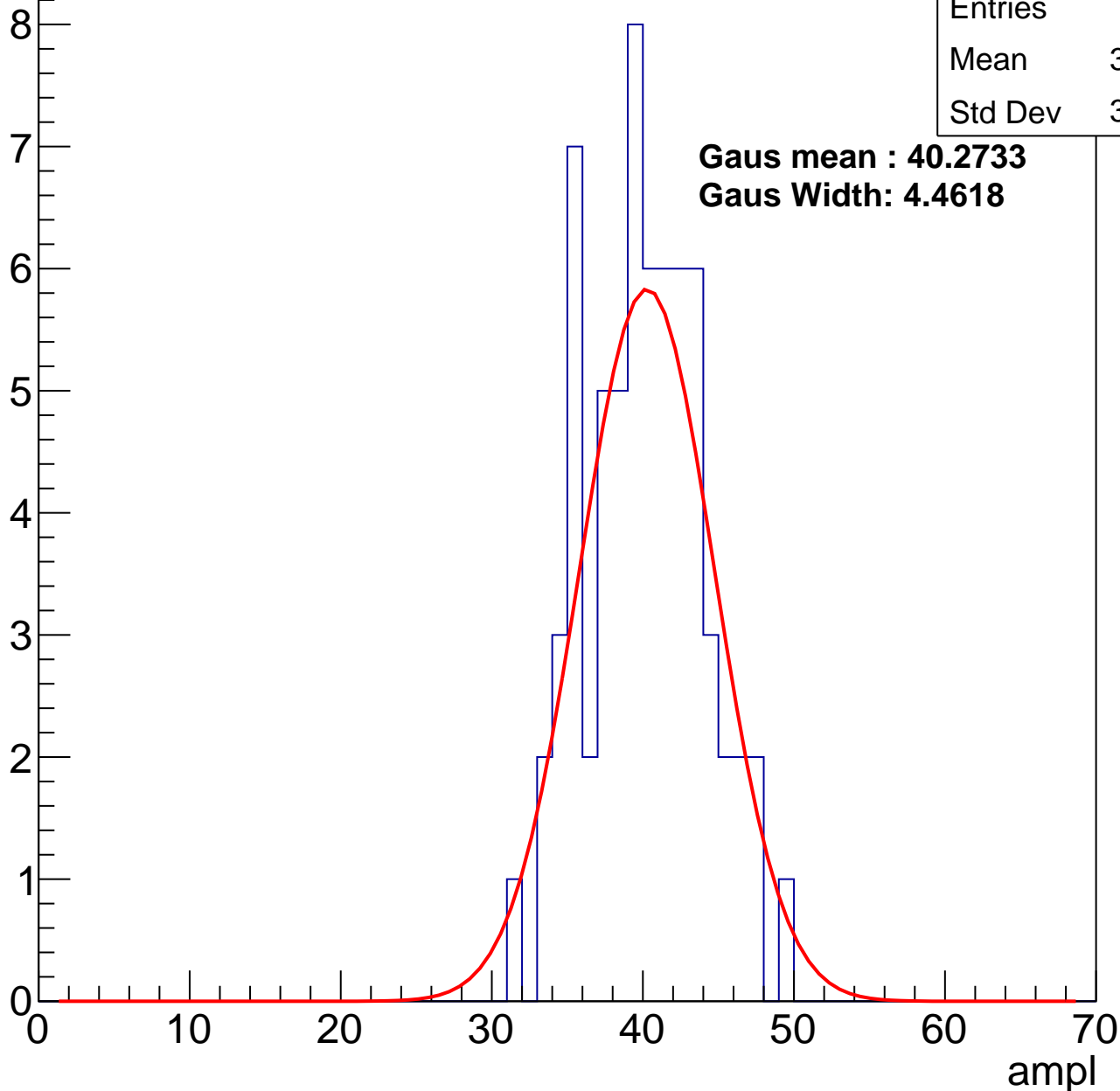
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	39.64
Std Dev	3.847

Gaus mean : 40.2733

Gaus Width: 4.4618

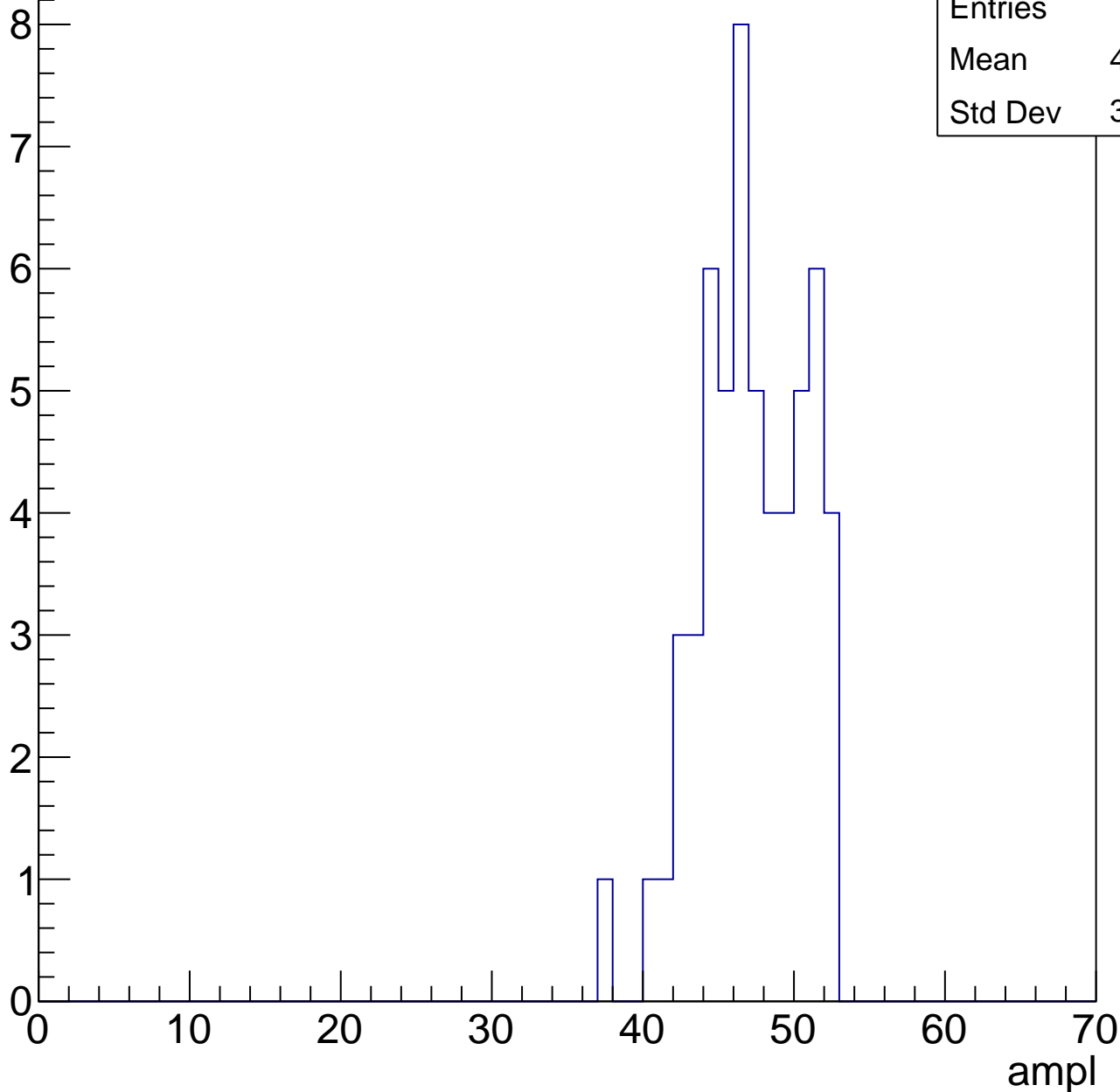


B1L101S, U4-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	46.73
Std Dev	3.399

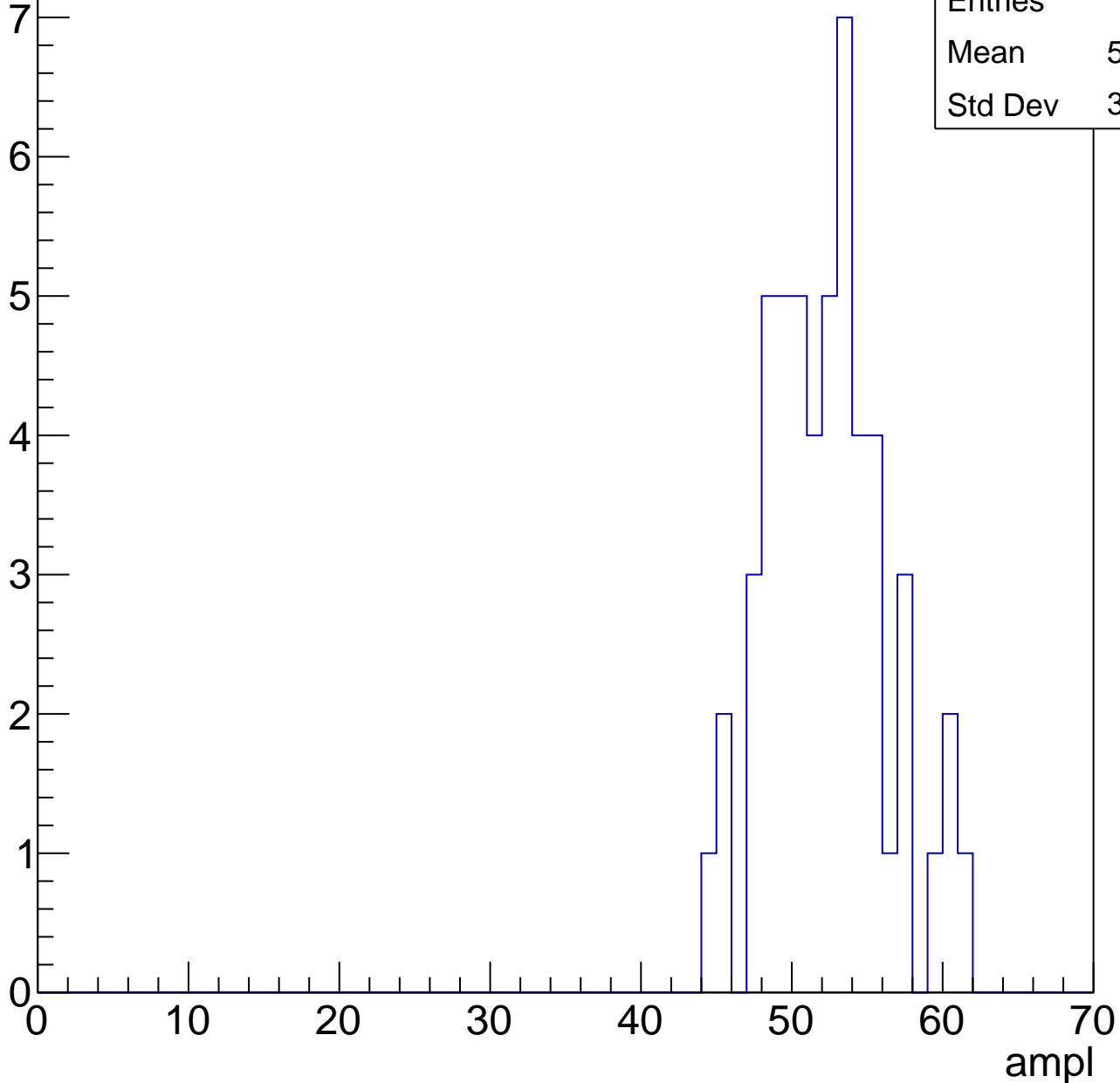


B1L101S, U4-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	51.85
Std Dev	3.868

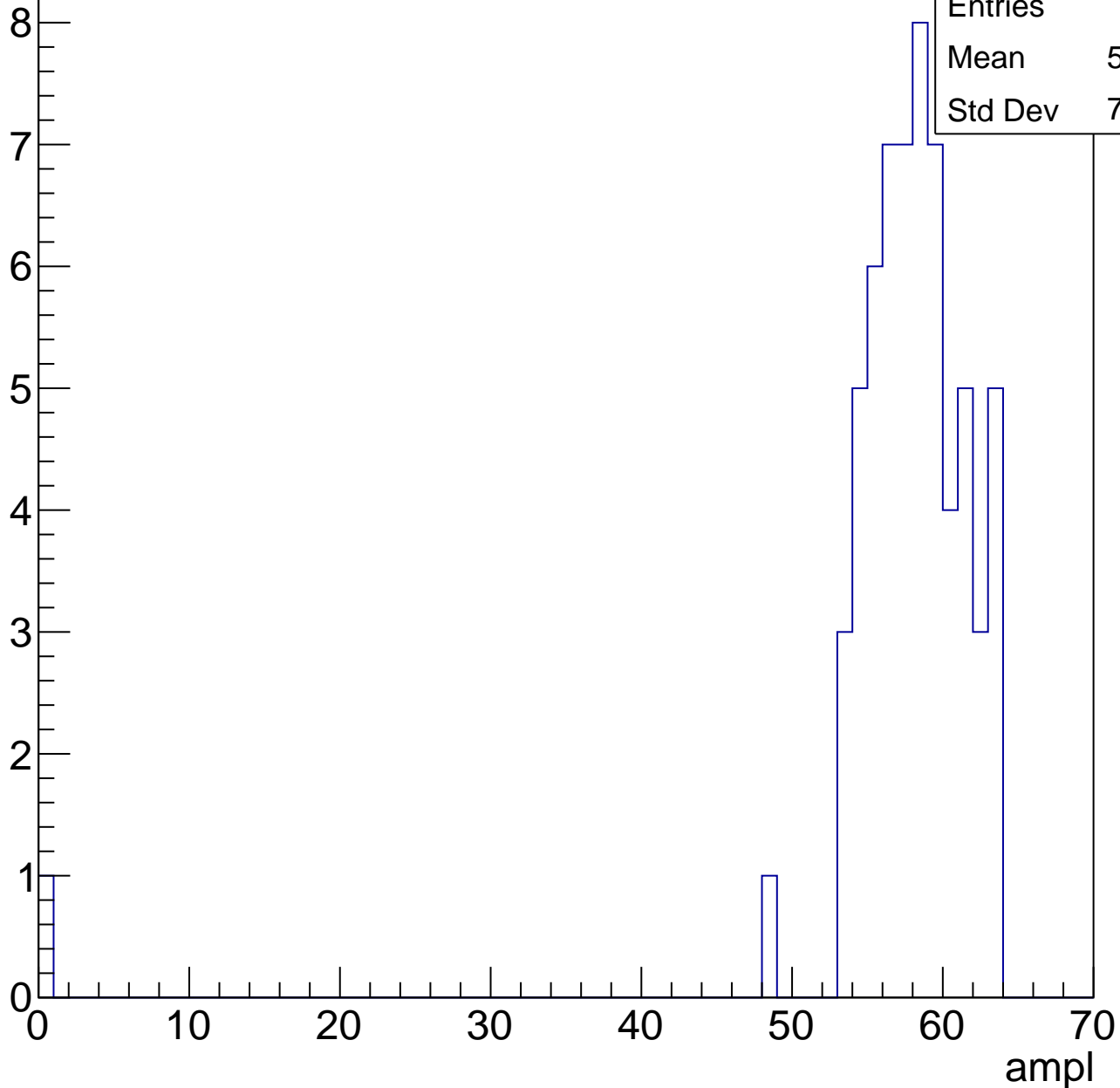


B1L101S, U4-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.79
Std Dev	7.889

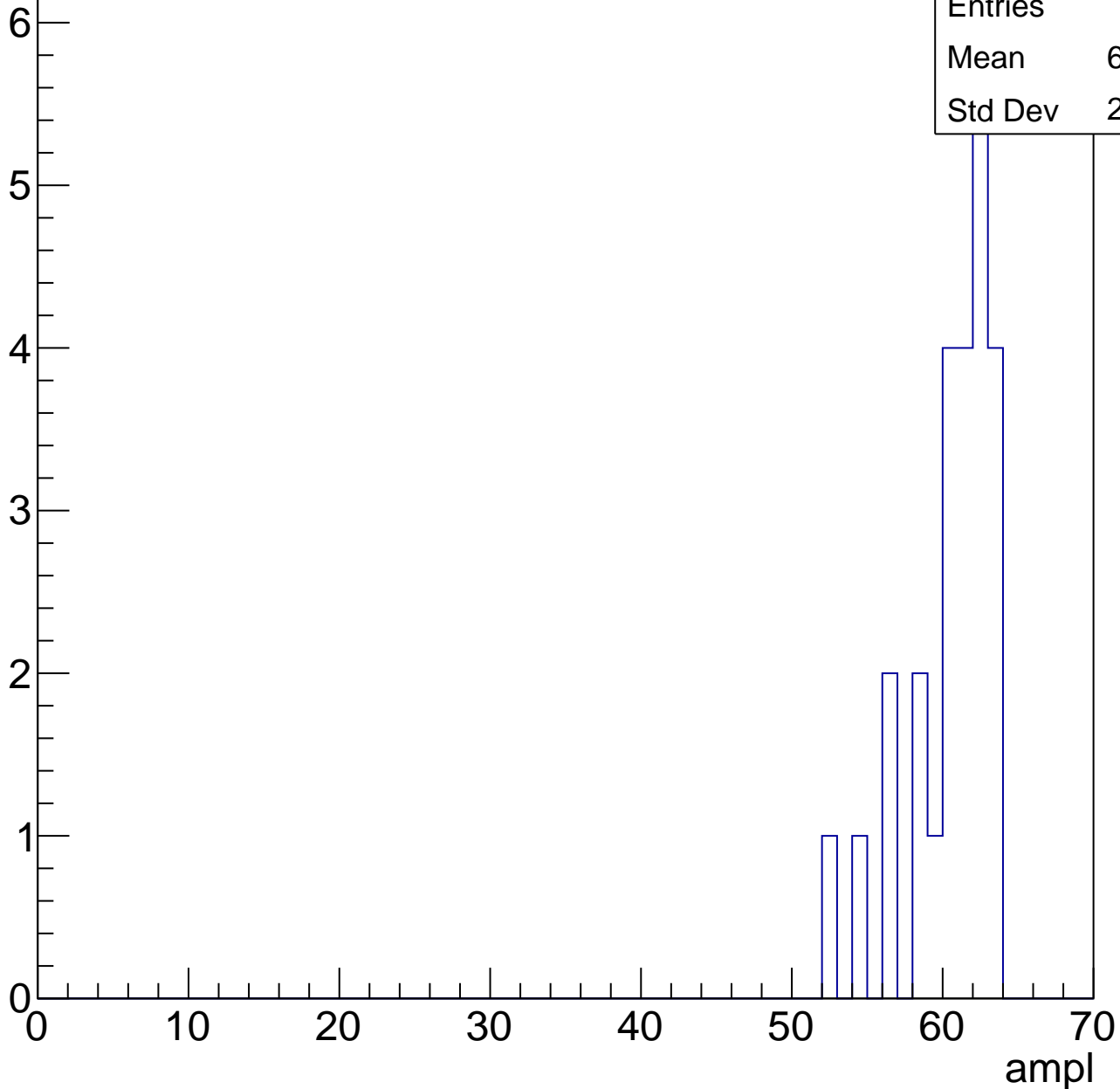


B1L101S, U4-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

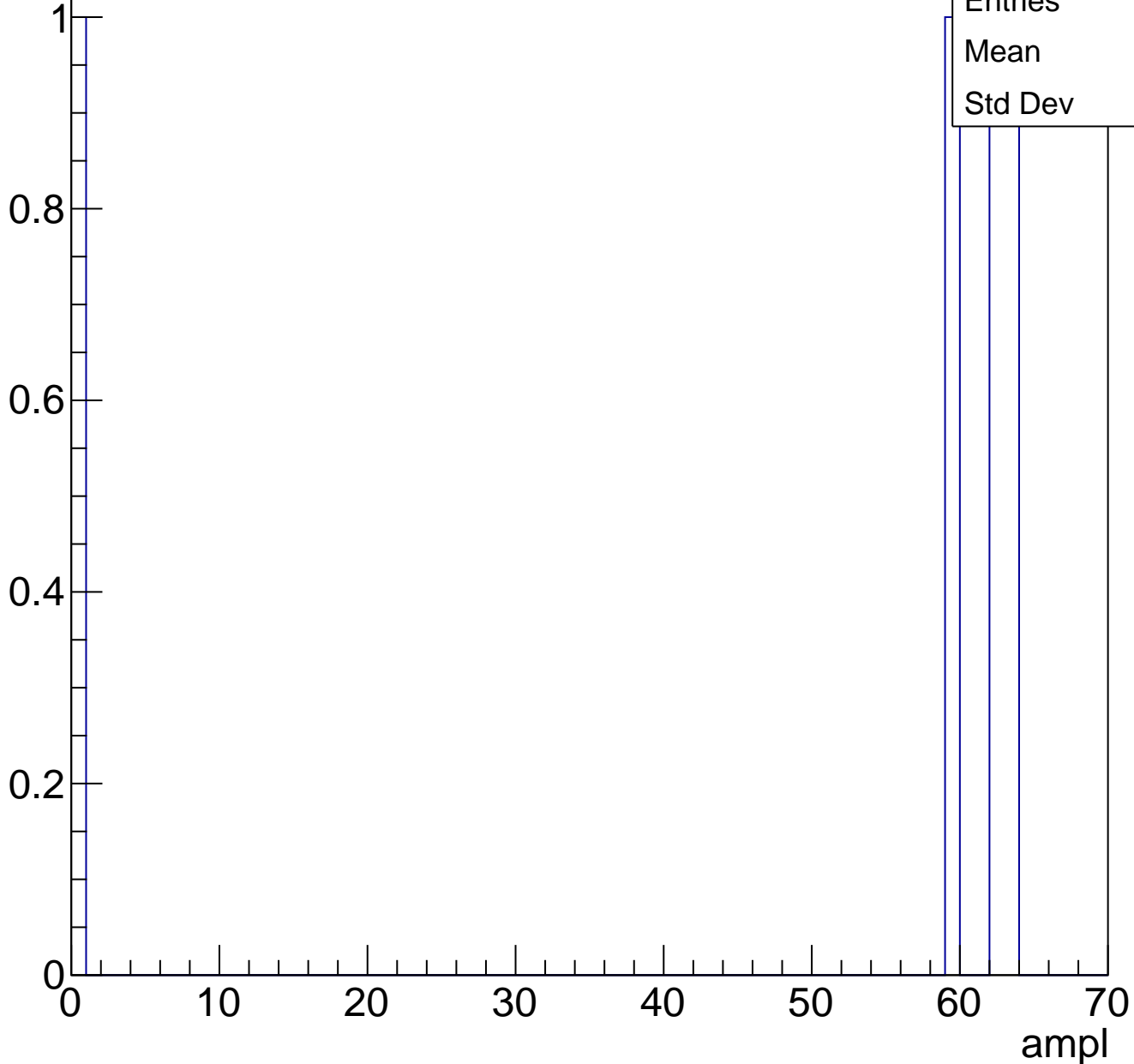
Entries	25
Mean	60.04
Std Dev	2.863



B1L101S, U4-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch127, adc0

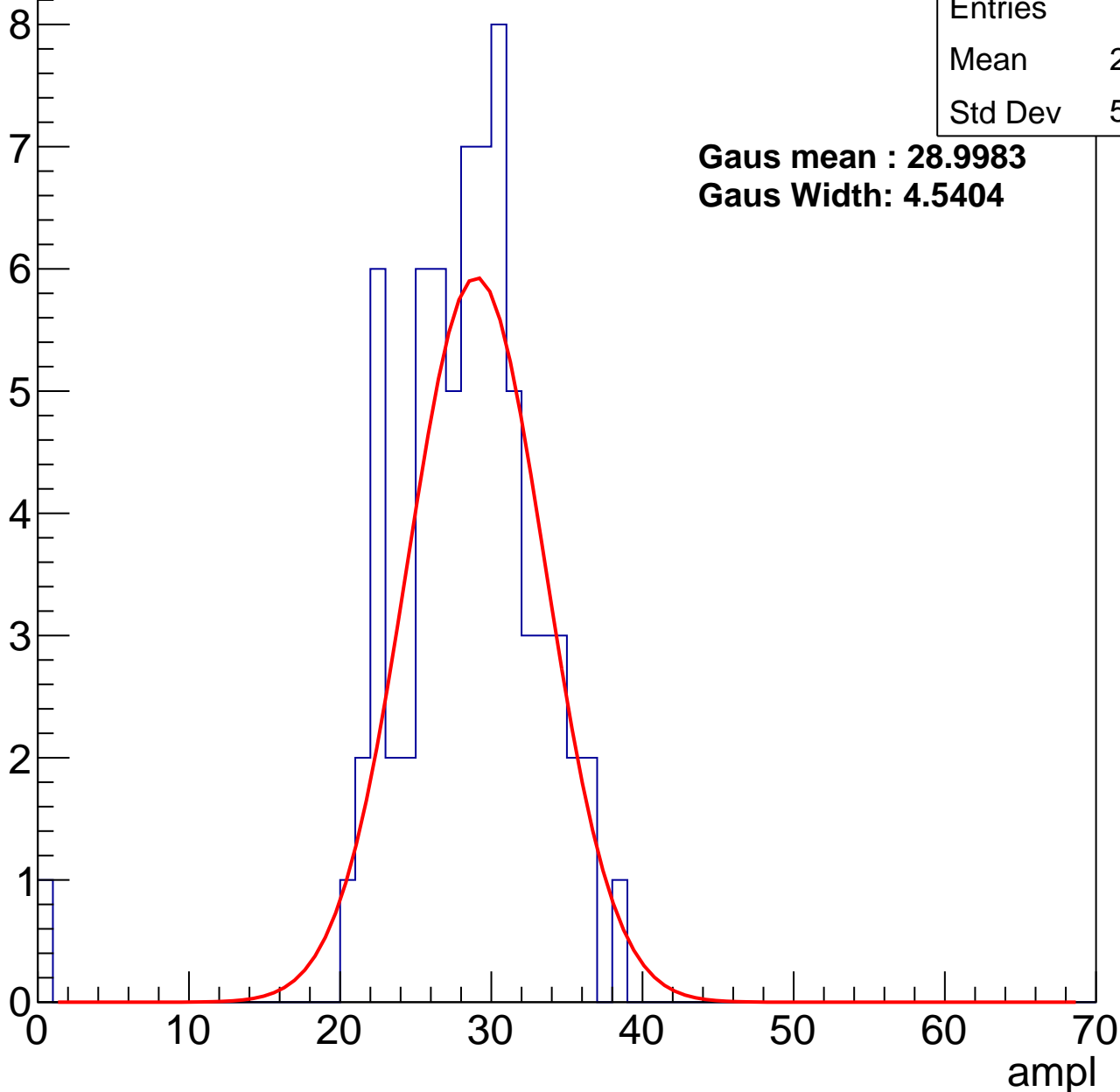
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.78
Std Dev	5.223

Gaus mean : 28.9983

Gaus Width: 4.5404



B1L101S, U4-ch127, adc1

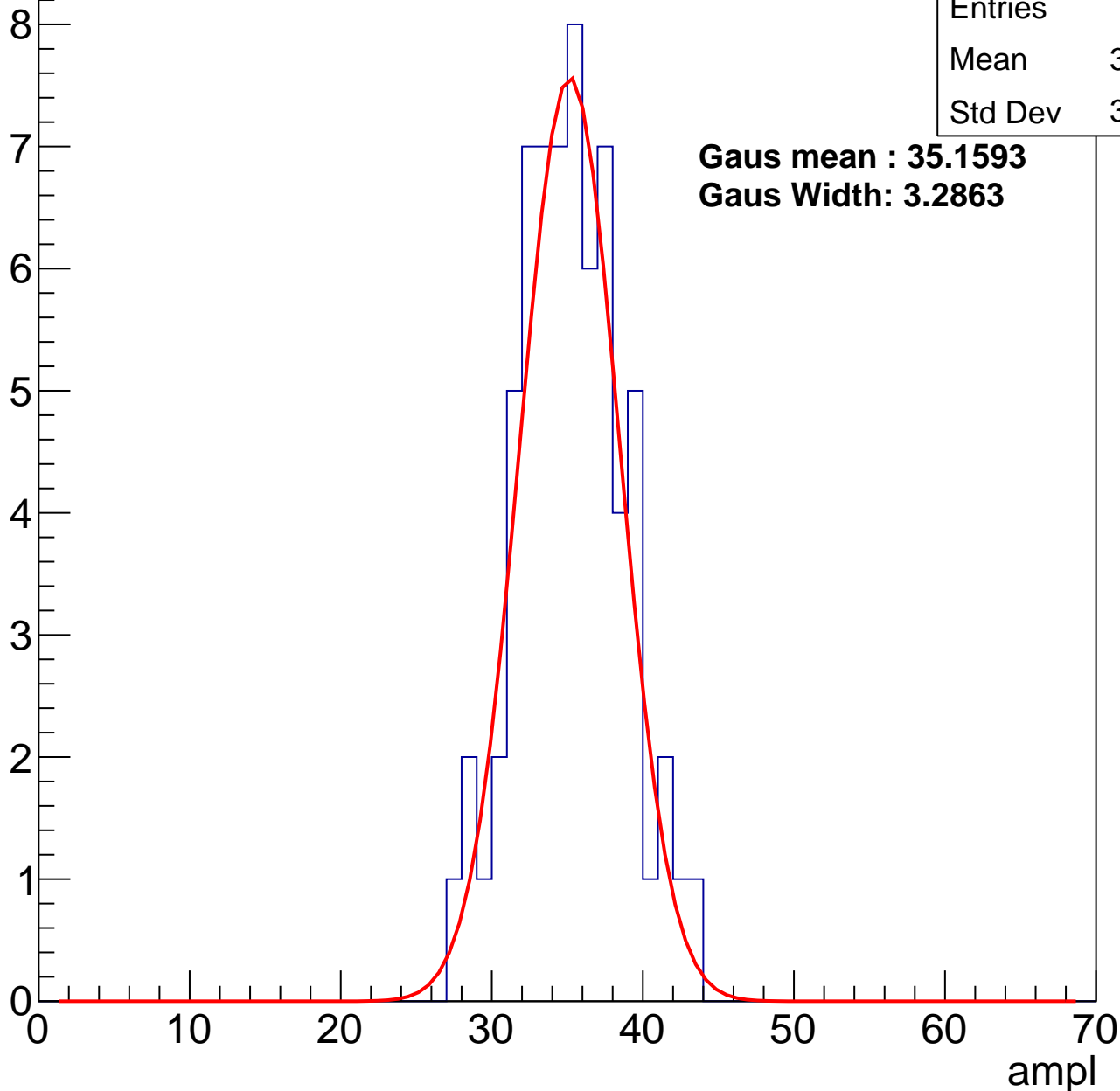
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.76
Std Dev	3.417

Gaus mean : 35.1593

Gaus Width: 3.2863



B1L101S, U4-ch127, adc2

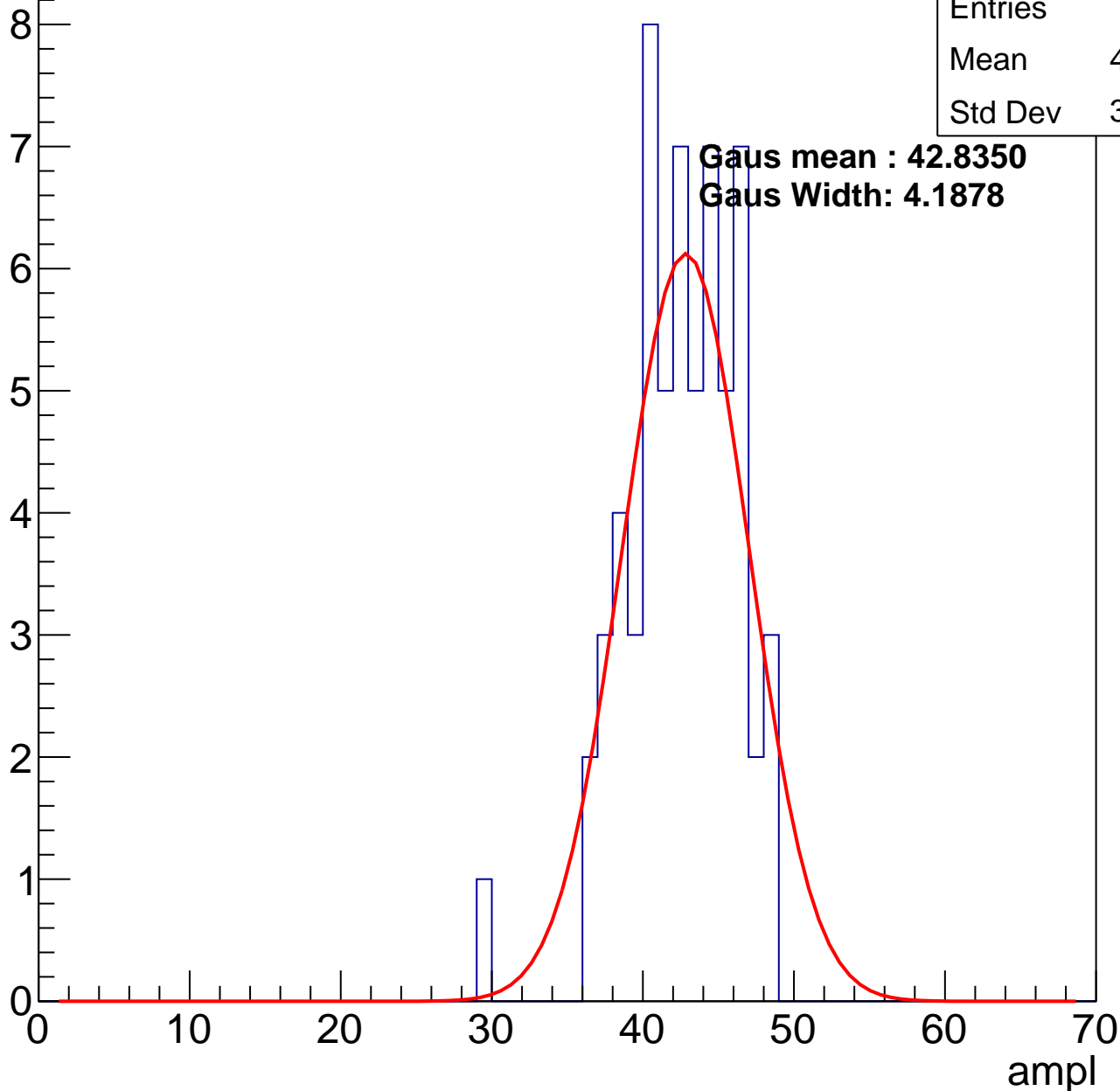
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.06
Std Dev	3.574

Gaus mean : 42.8350

Gaus Width: 4.1878

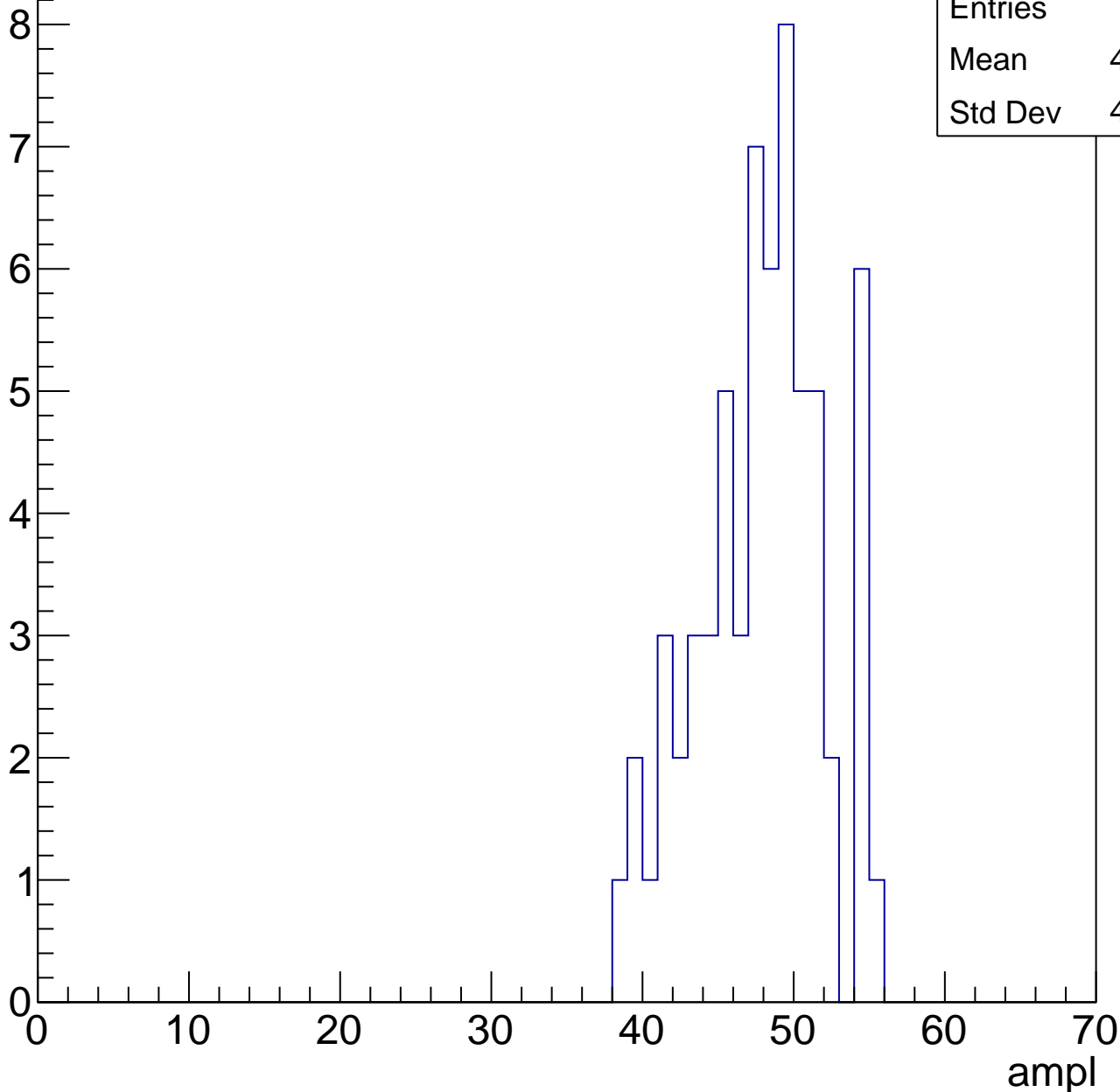


B1L101S, U4-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	47.37
Std Dev	4.134

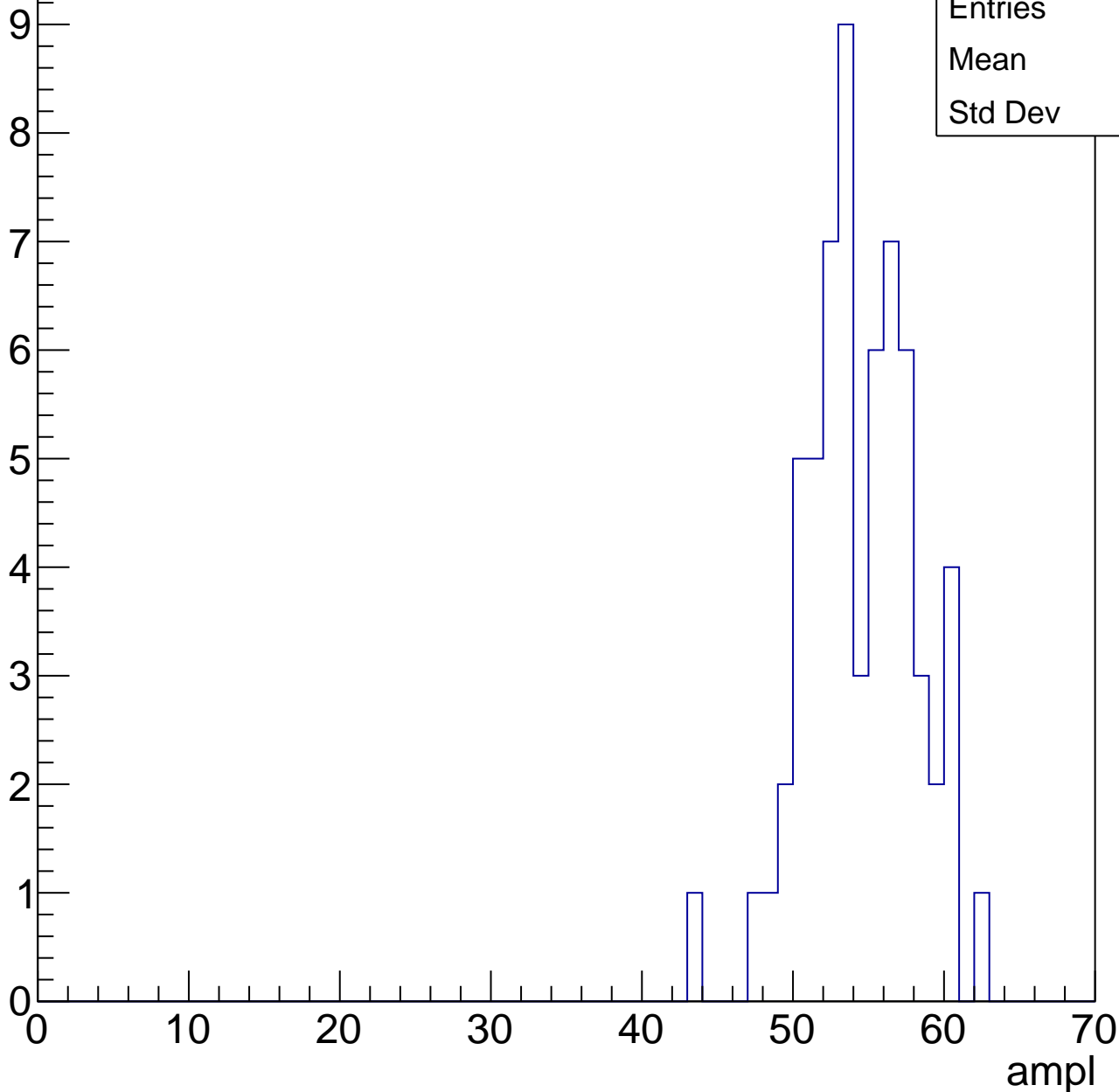


B1L101S, U4-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

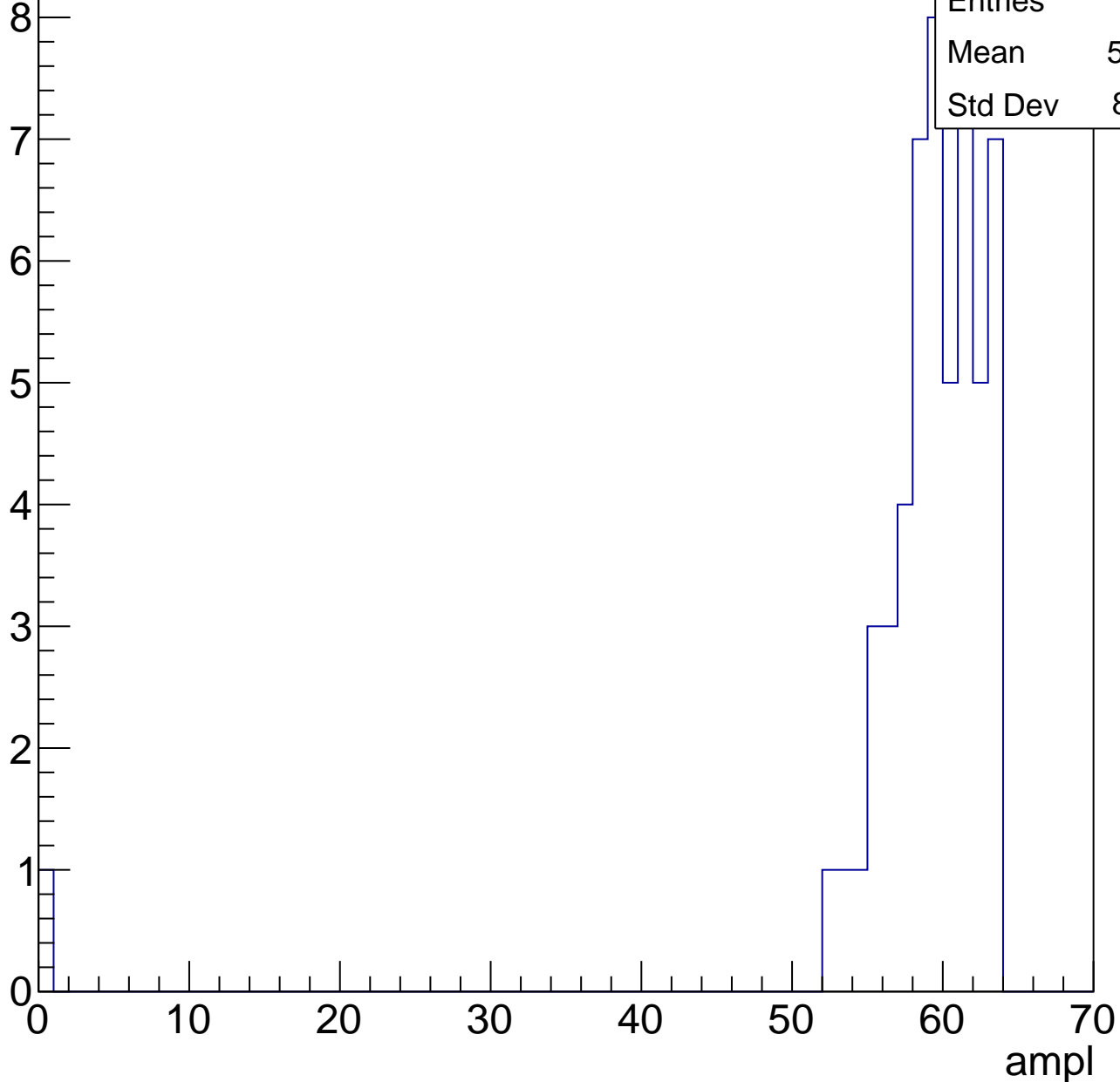
Entries	63
Mean	54
Std Dev	3.59



B1L101S, U4-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

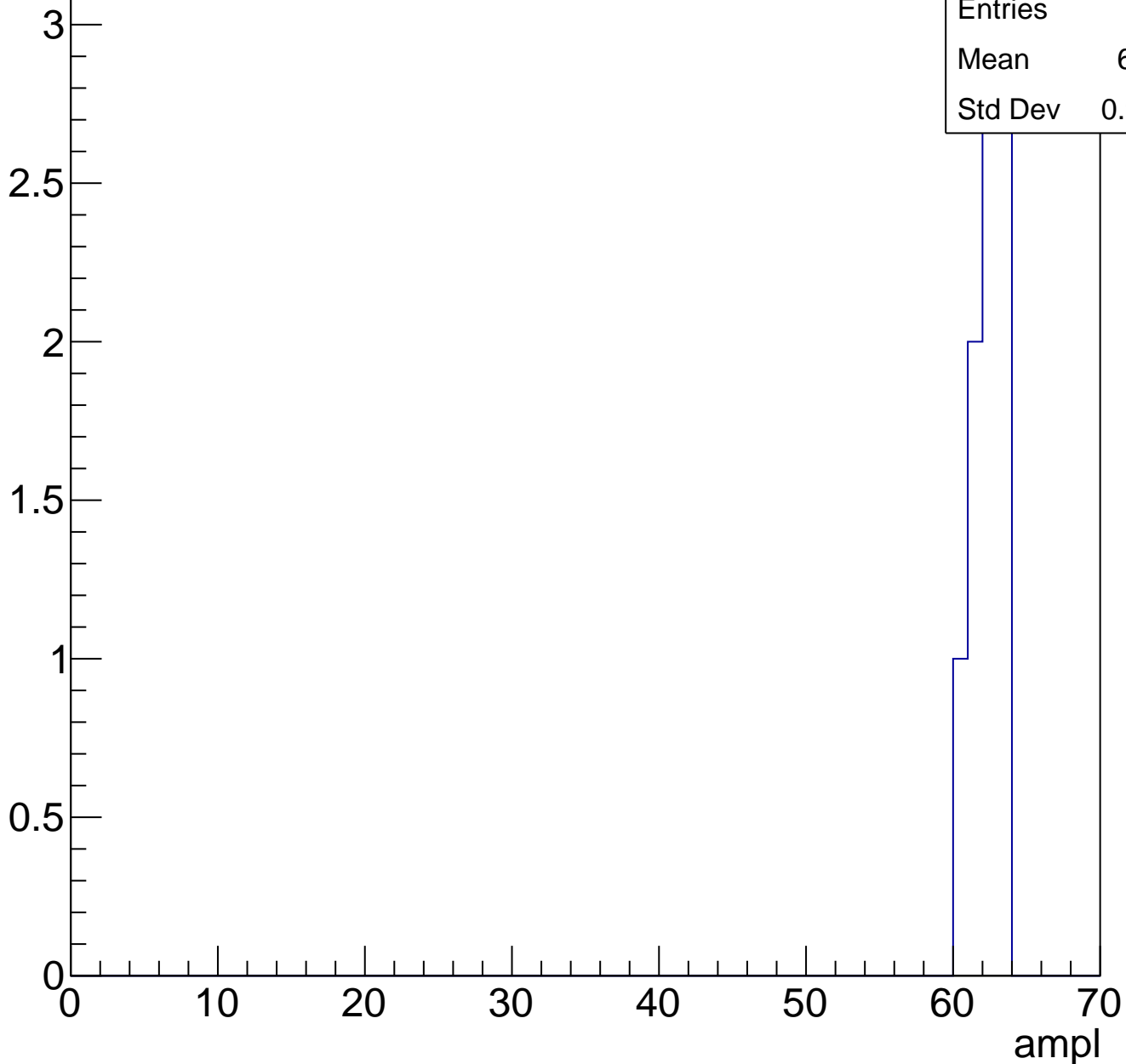
Entry



B1L101S, U4-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

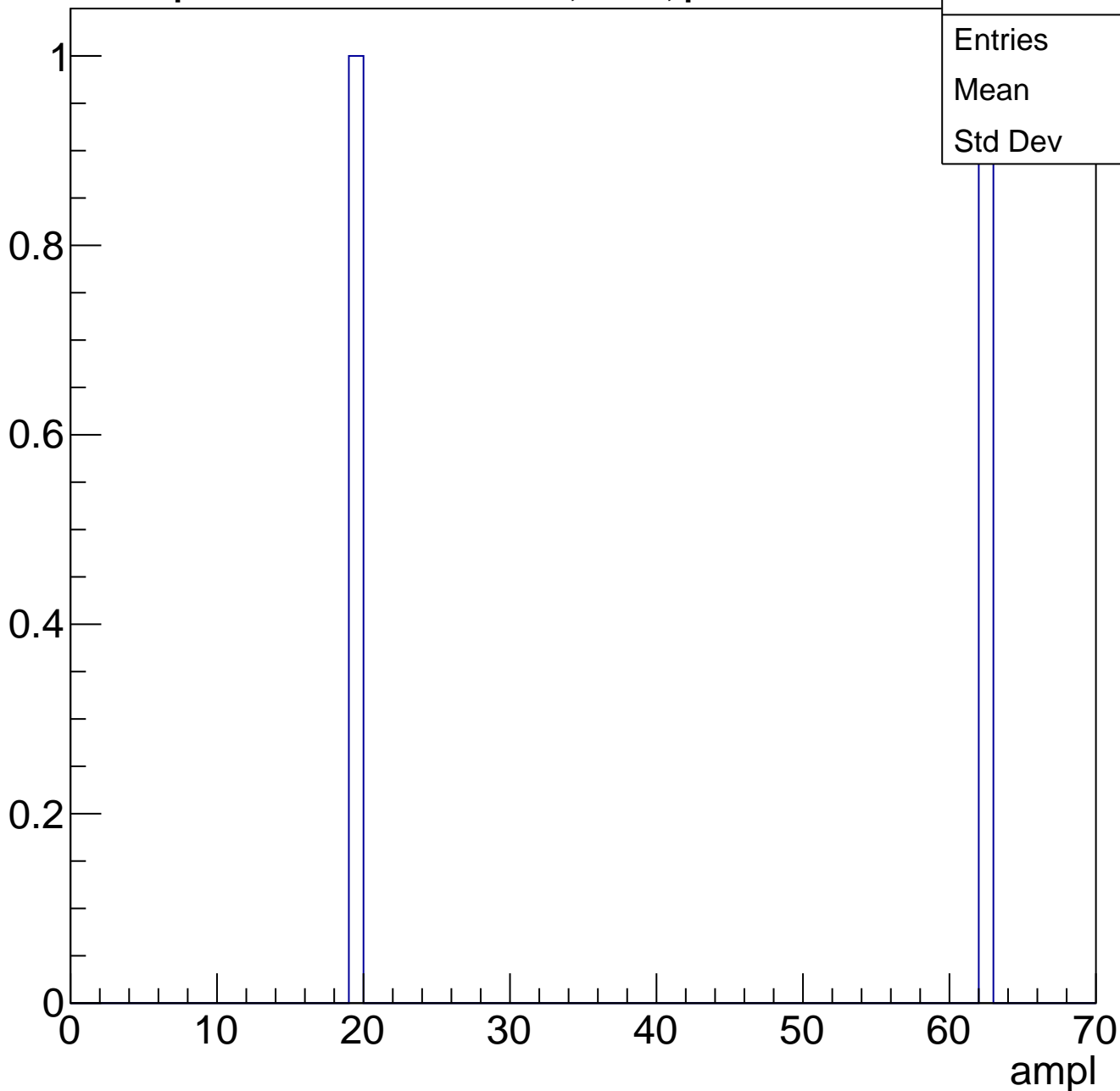


Entries	9
Mean	61.89
Std Dev	0.9938

B1L101S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

