



# B0L002S, U1-ch0, adc0

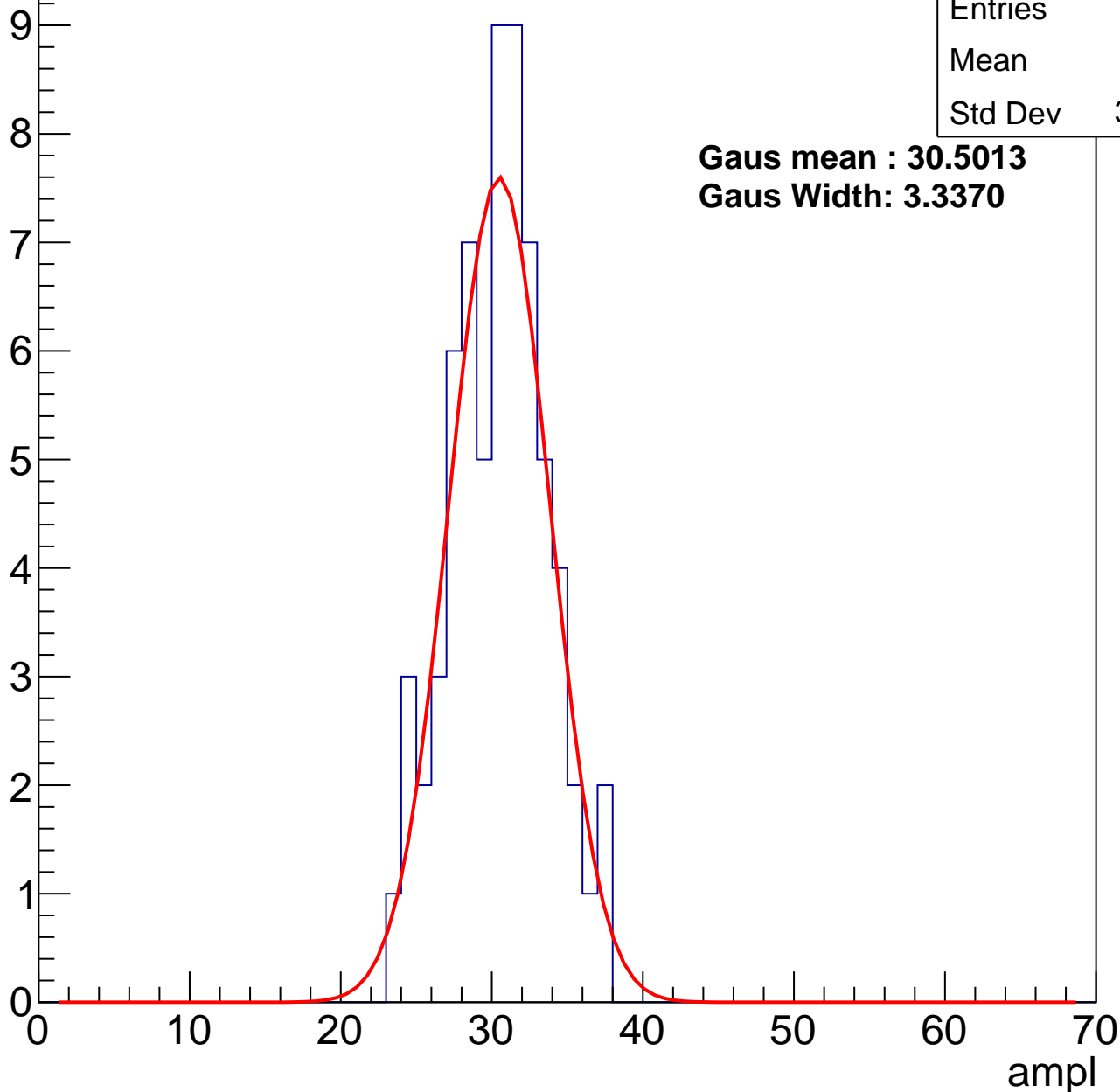
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 30    |
| Std Dev | 3.191 |

**Gaus mean : 30.5013**

**Gaus Width: 3.3370**

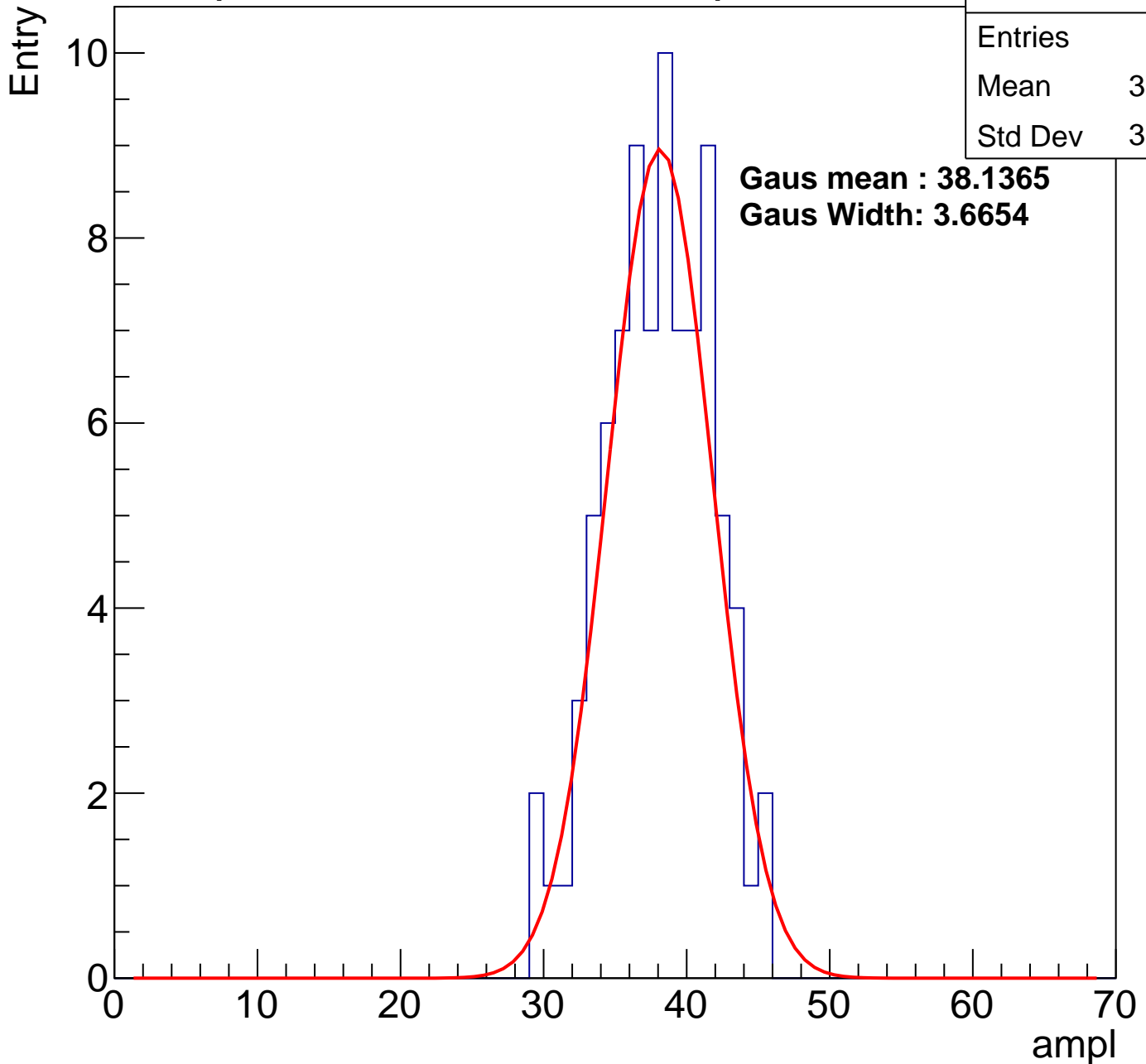


# B0L002S, U1-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 37.56 |
| Std Dev | 3.614 |

**Gaus mean : 38.1365**  
**Gaus Width: 3.6654**



# B0L002S, U1-ch0, adc2

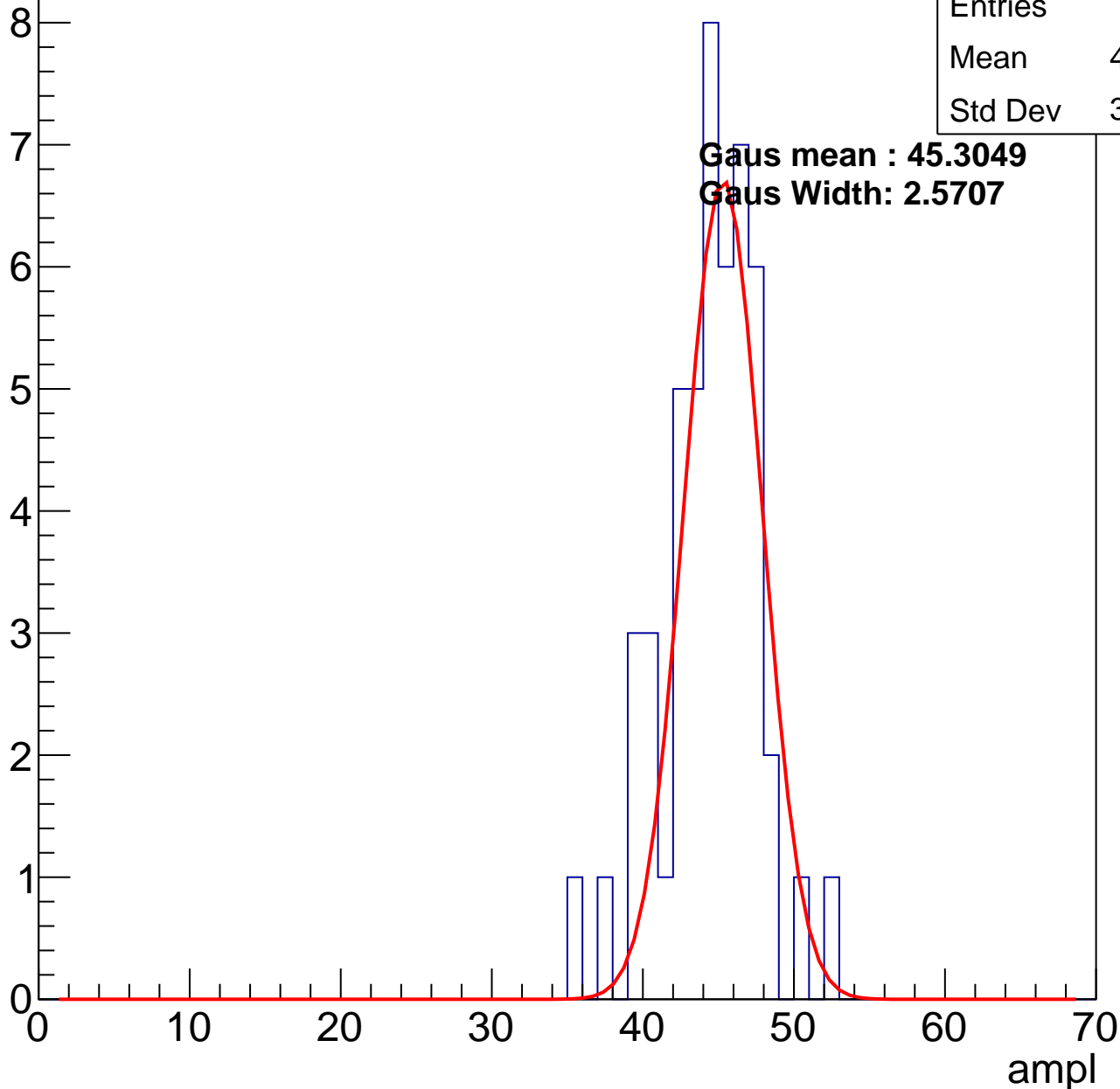
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 43.98 |
| Std Dev | 3.184 |

**Gaus mean : 45.3049**

**Gaus Width: 2.5707**



# B0L002S, U1-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

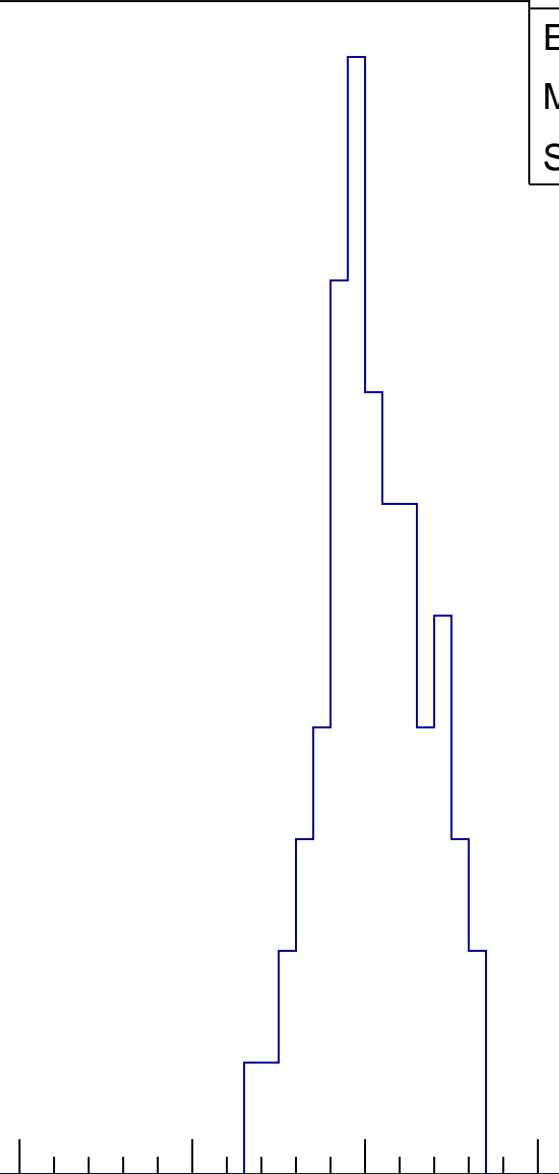
|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 50.06 |
| Std Dev | 2.989 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 56.03 |
| Std Dev | 3.059 |

Entry

10

8

6

4

2

0

0

10

20

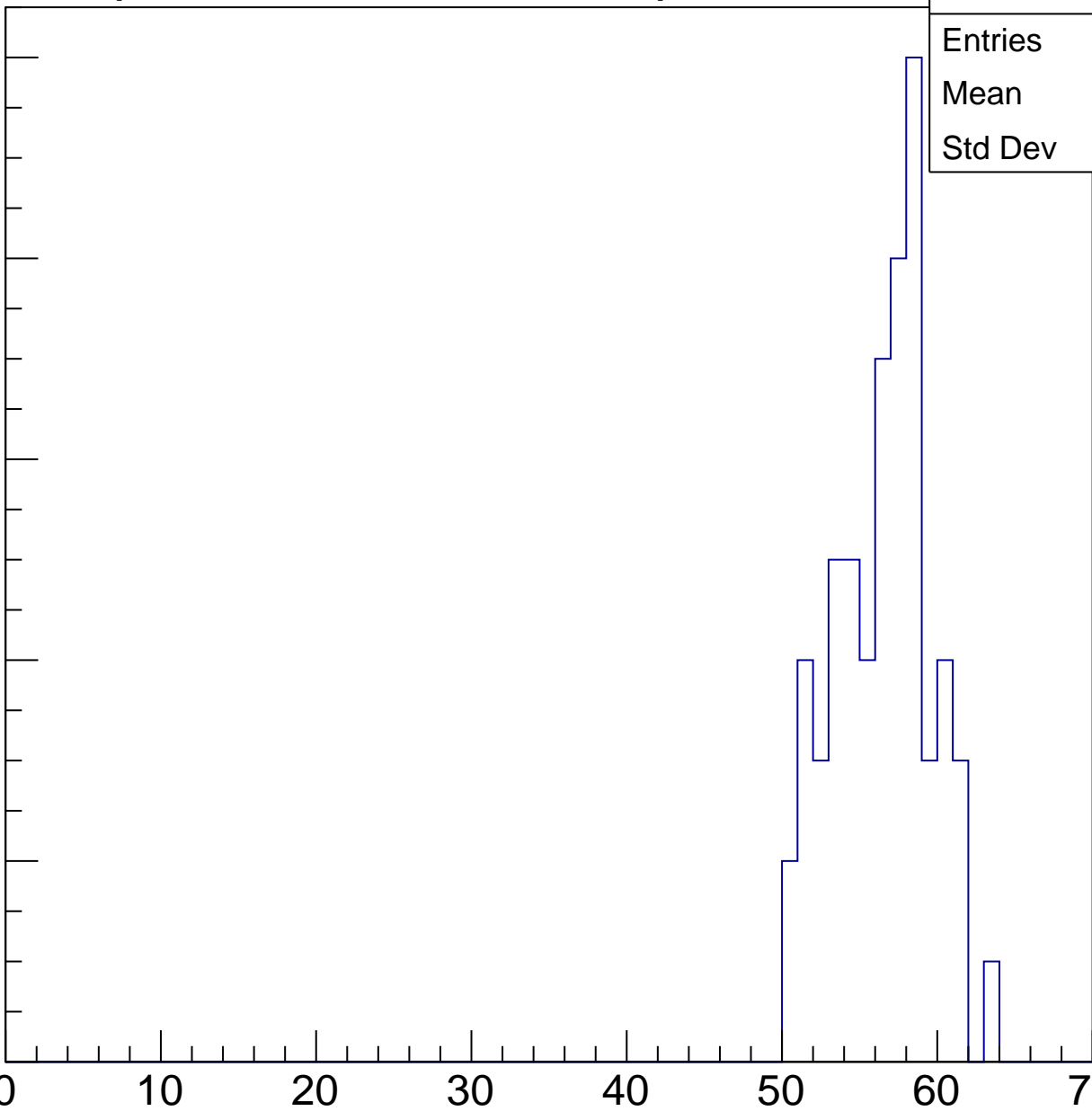
30

40

50

60

ampl

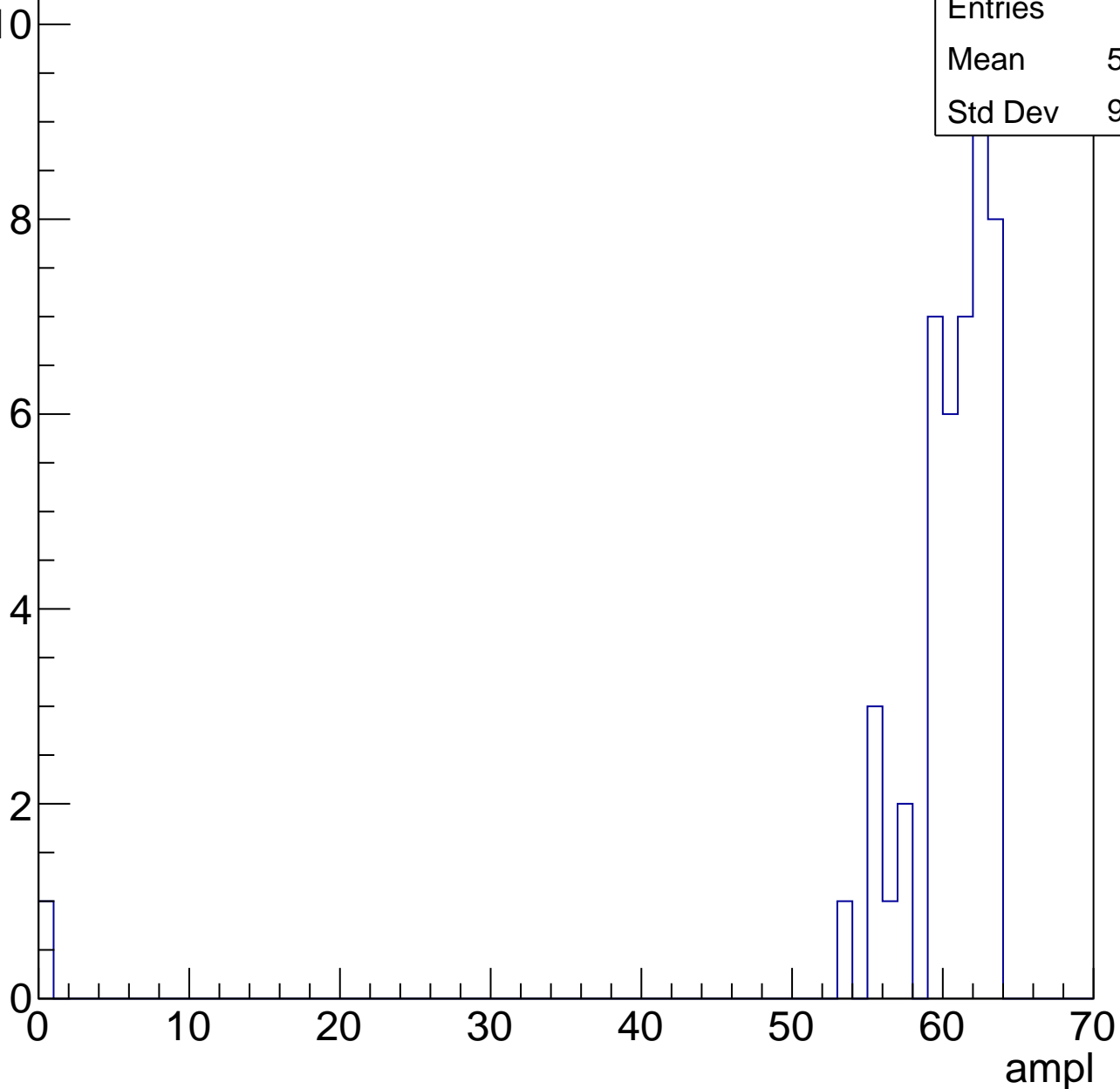


# B0L002S, U1-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 58.96 |
| Std Dev | 9.129 |



# B0L002S, U1-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 23 |
| Std Dev | 0  |

ampl

# B0L002S, U1-ch1, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 30.33 |
| Std Dev | 3.645 |

**Gaus mean : 30.8801**

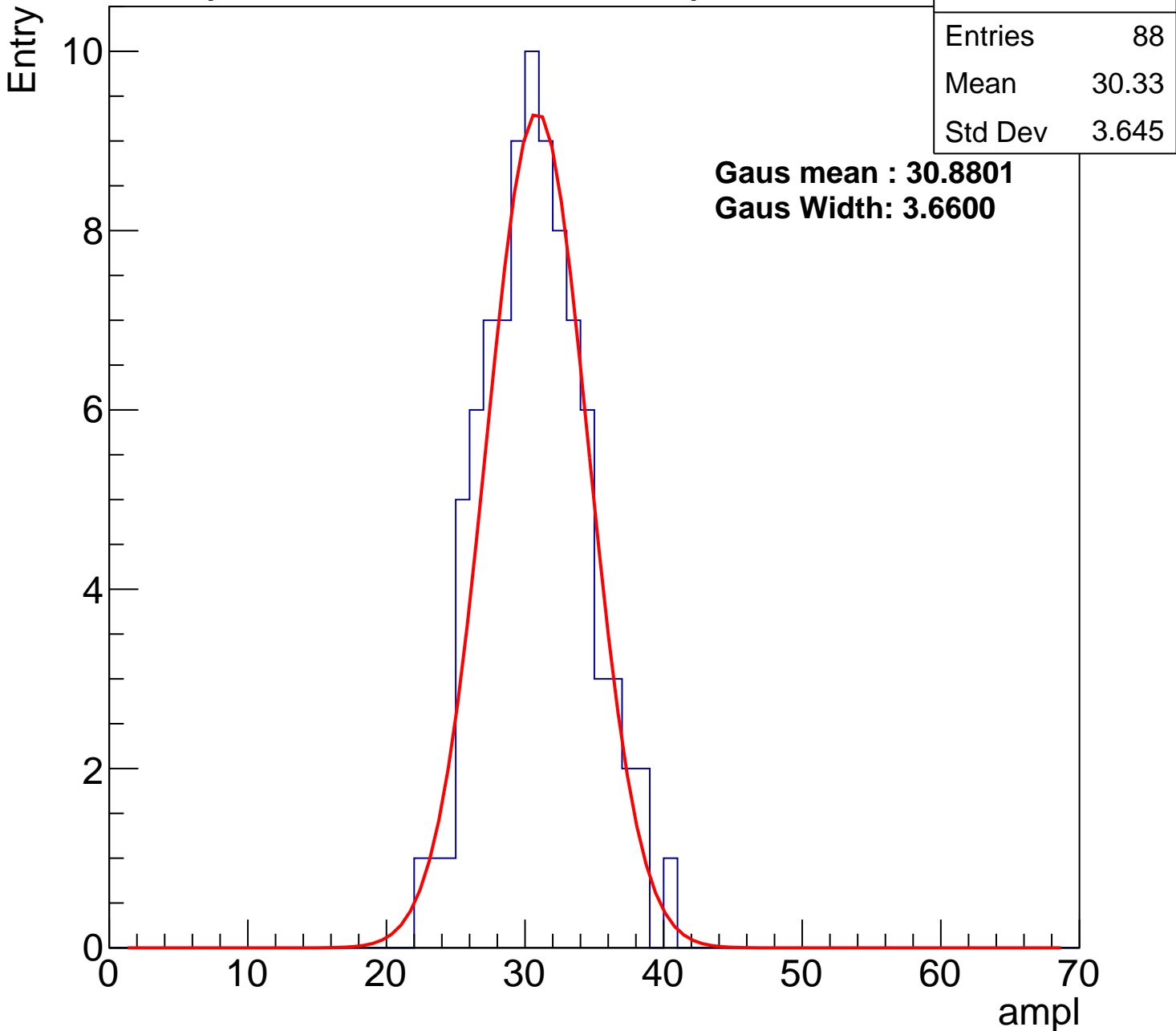
**Gaus Width: 3.6600**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch1, adc1

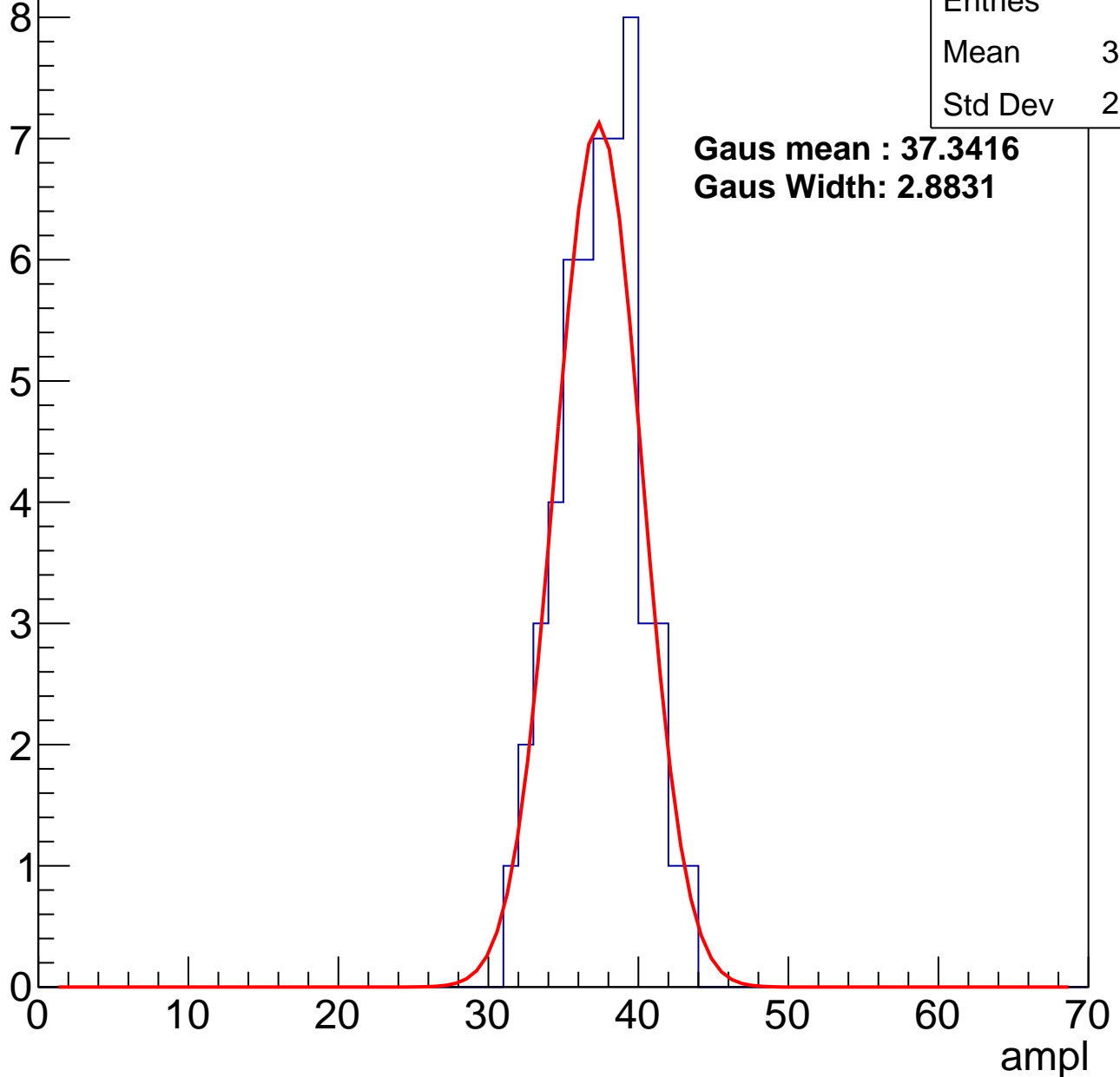
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 36.94 |
| Std Dev | 2.685 |

**Gaus mean : 37.3416**

**Gaus Width: 2.8831**



# B0L002S, U1-ch1, adc2

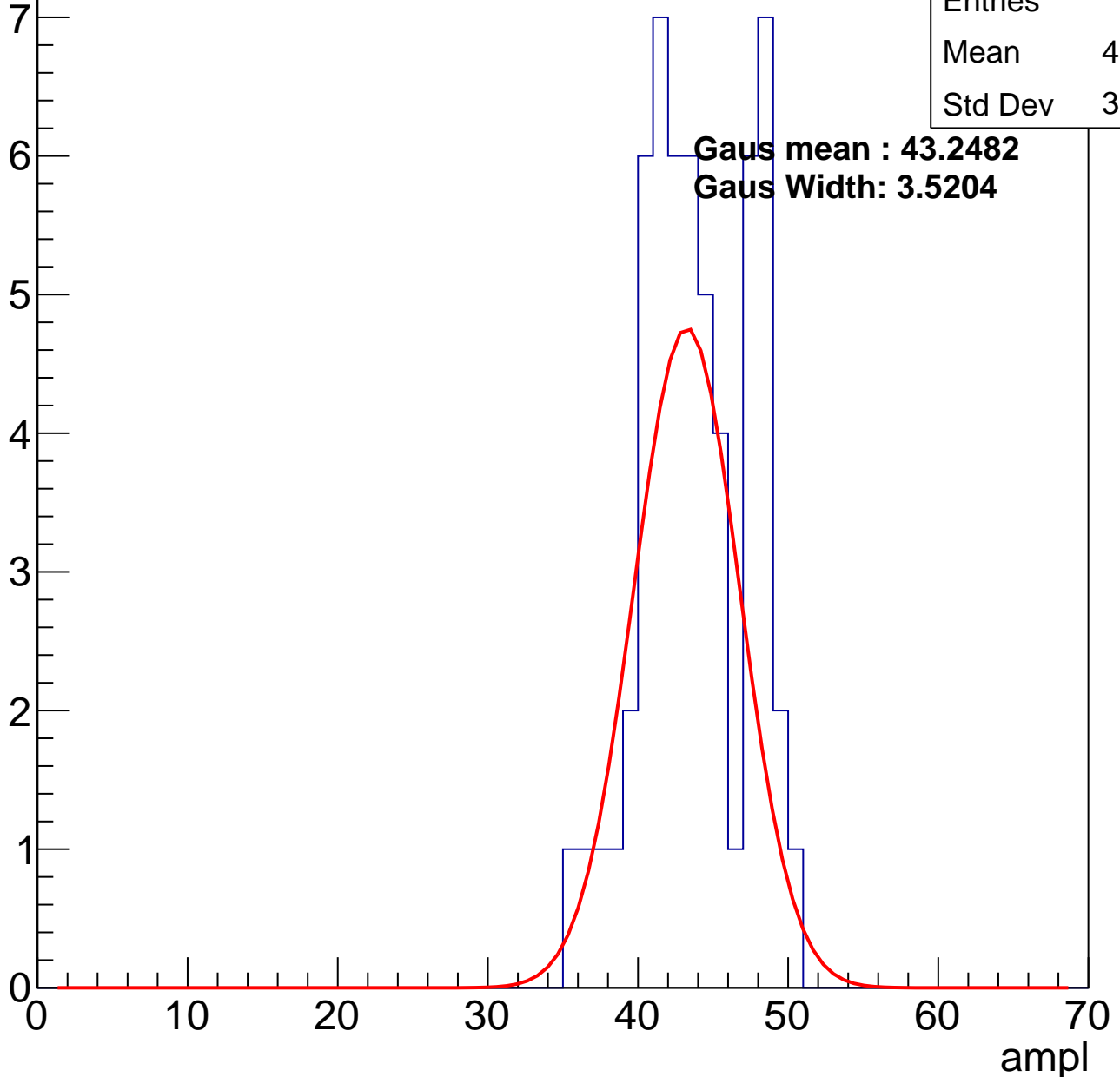
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 43.39 |
| Std Dev | 3.523 |

**Gaus mean : 43.2482**

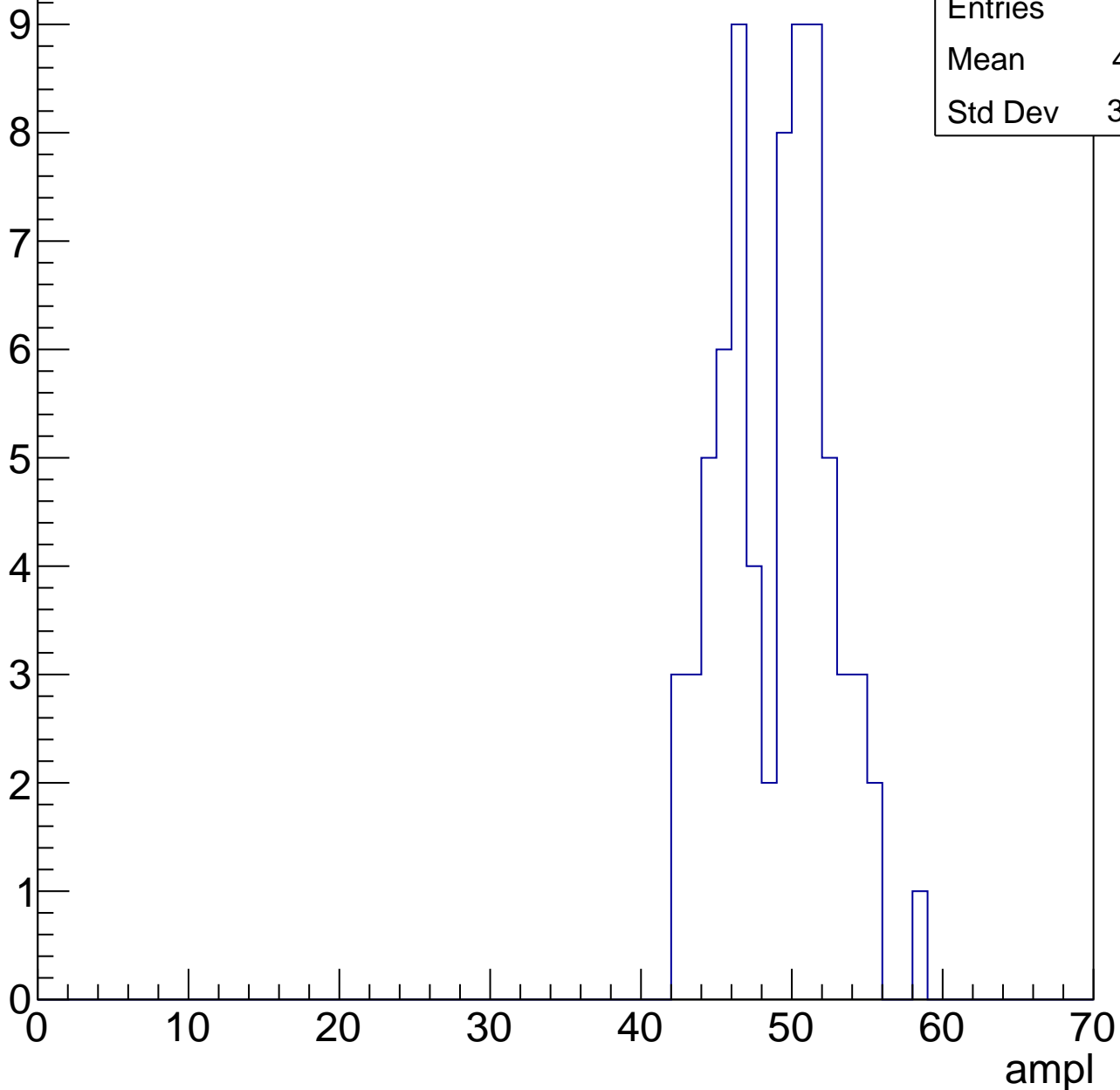
**Gaus Width: 3.5204**



# B0L002S, U1-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

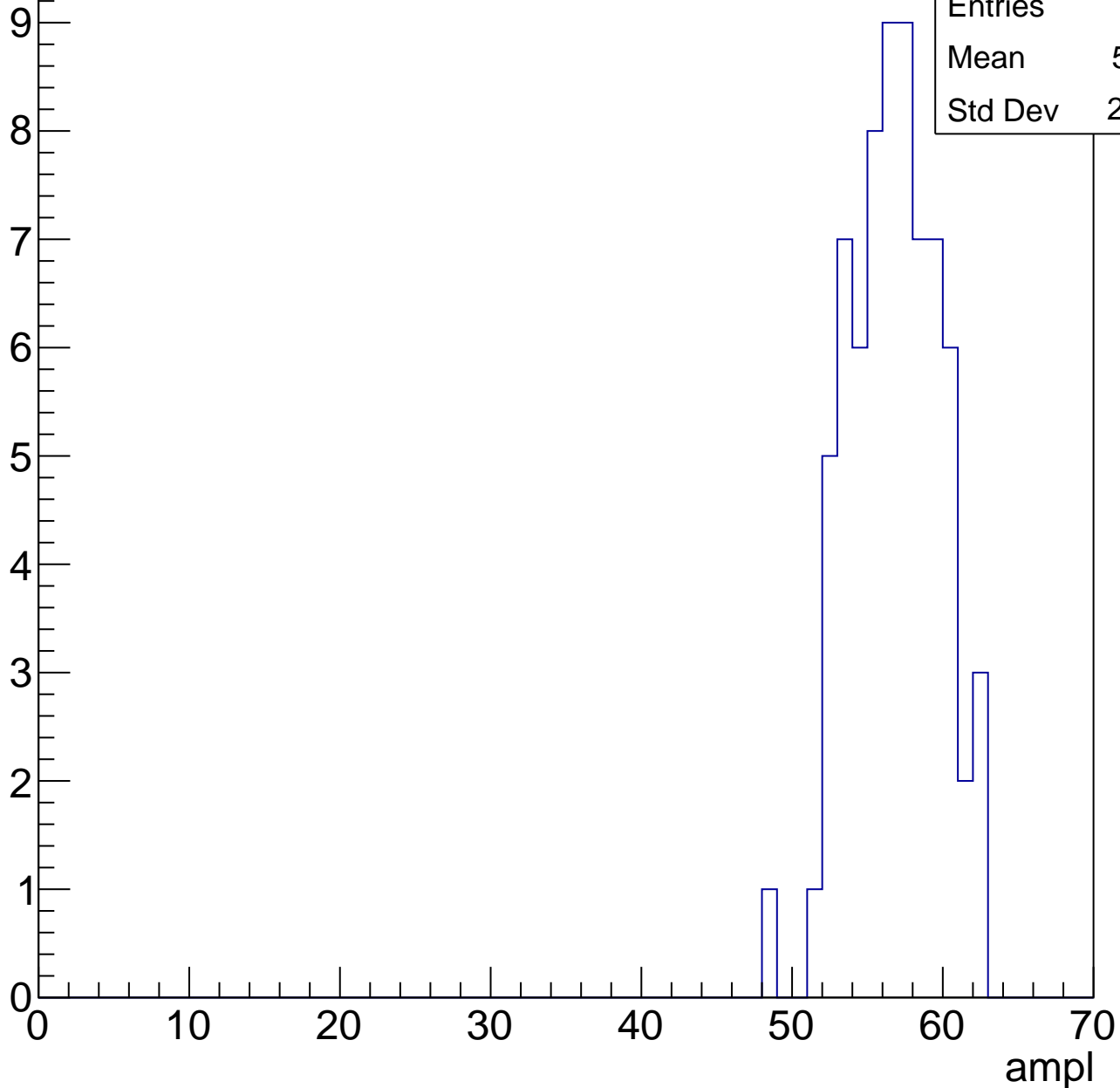
Entry



# B0L002S, U1-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



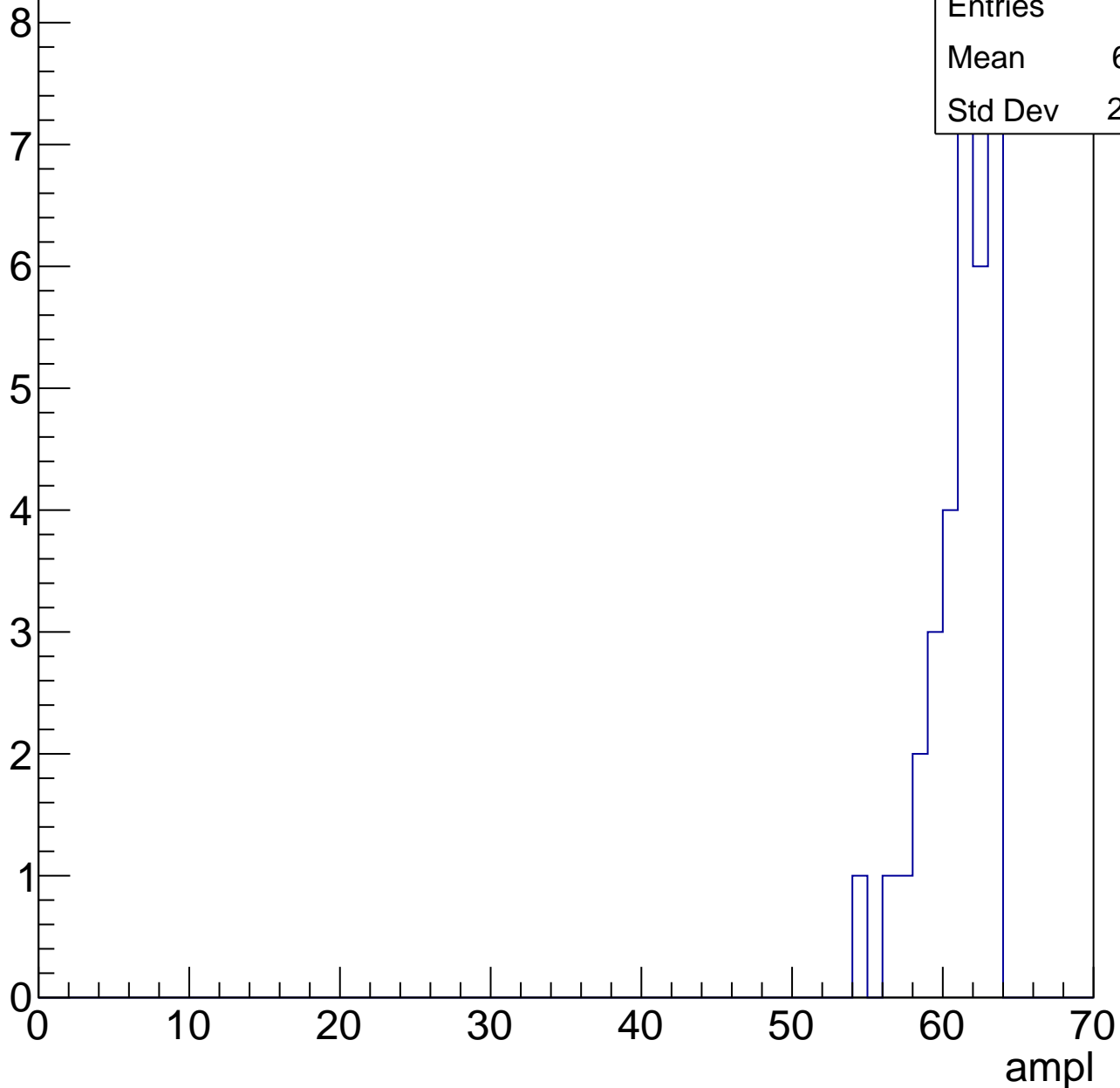
|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 56.31 |
| Std Dev | 2.934 |

# B0L002S, U1-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 60.71 |
| Std Dev | 2.163 |



# B0L002S, U1-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch2, adc0

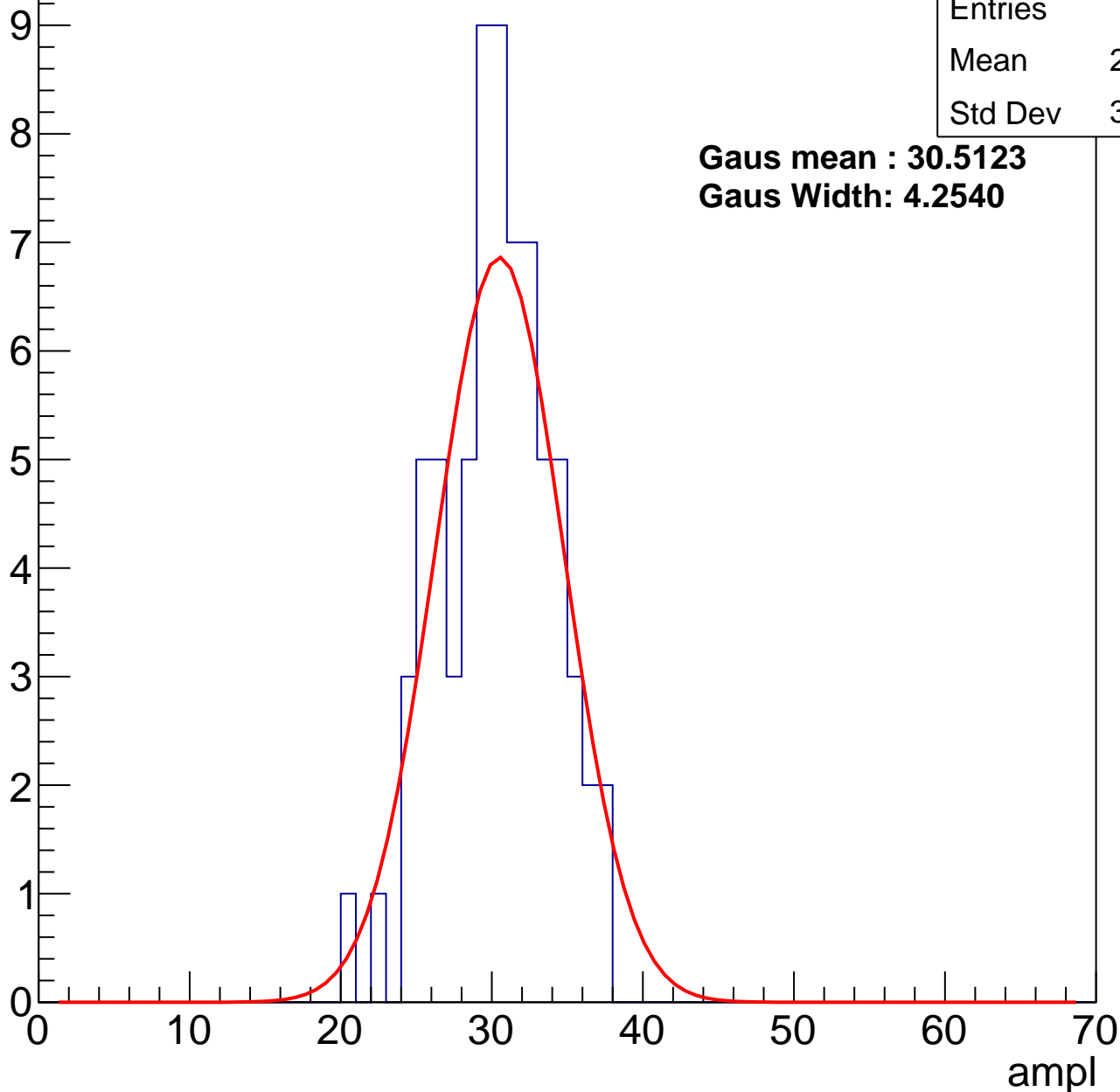
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 29.83 |
| Std Dev | 3.606 |

**Gaus mean : 30.5123**

**Gaus Width: 4.2540**

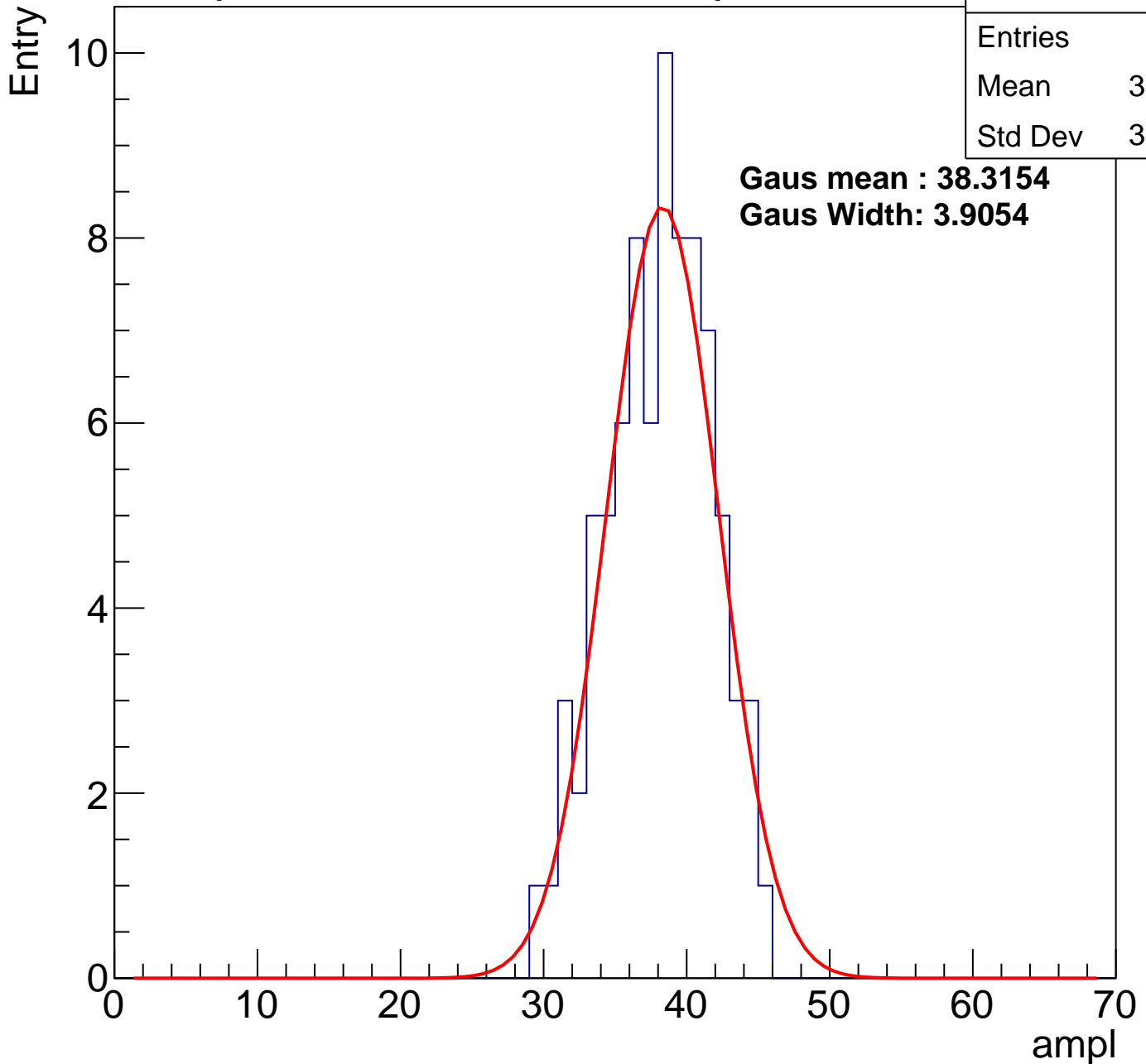


# B0L002S, U1-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 37.63 |
| Std Dev | 3.604 |

**Gaus mean : 38.3154**  
**Gaus Width: 3.9054**



# B0L002S, U1-ch2, adc2

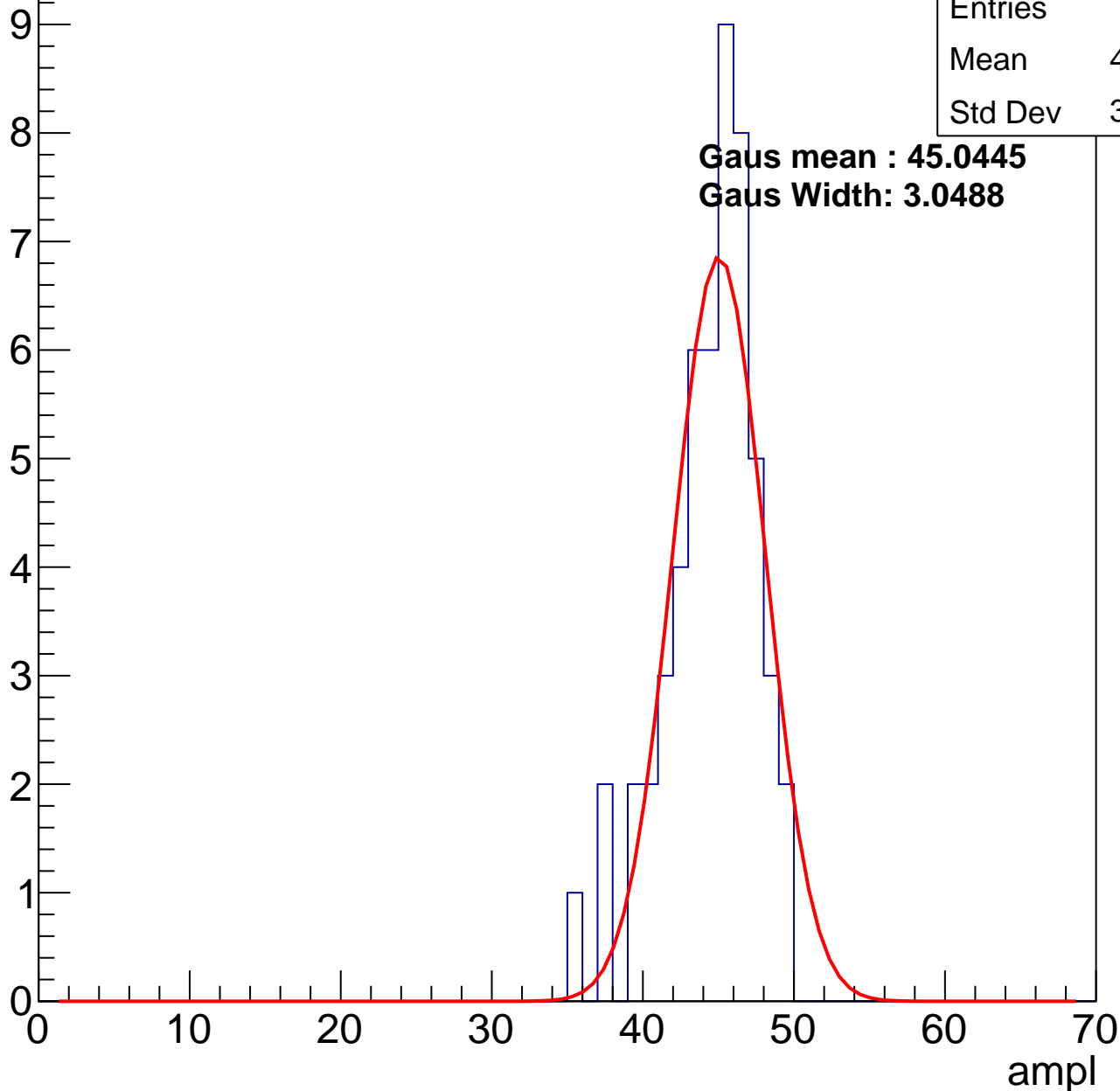
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 43.96 |
| Std Dev | 3.053 |

**Gaus mean : 45.0445**

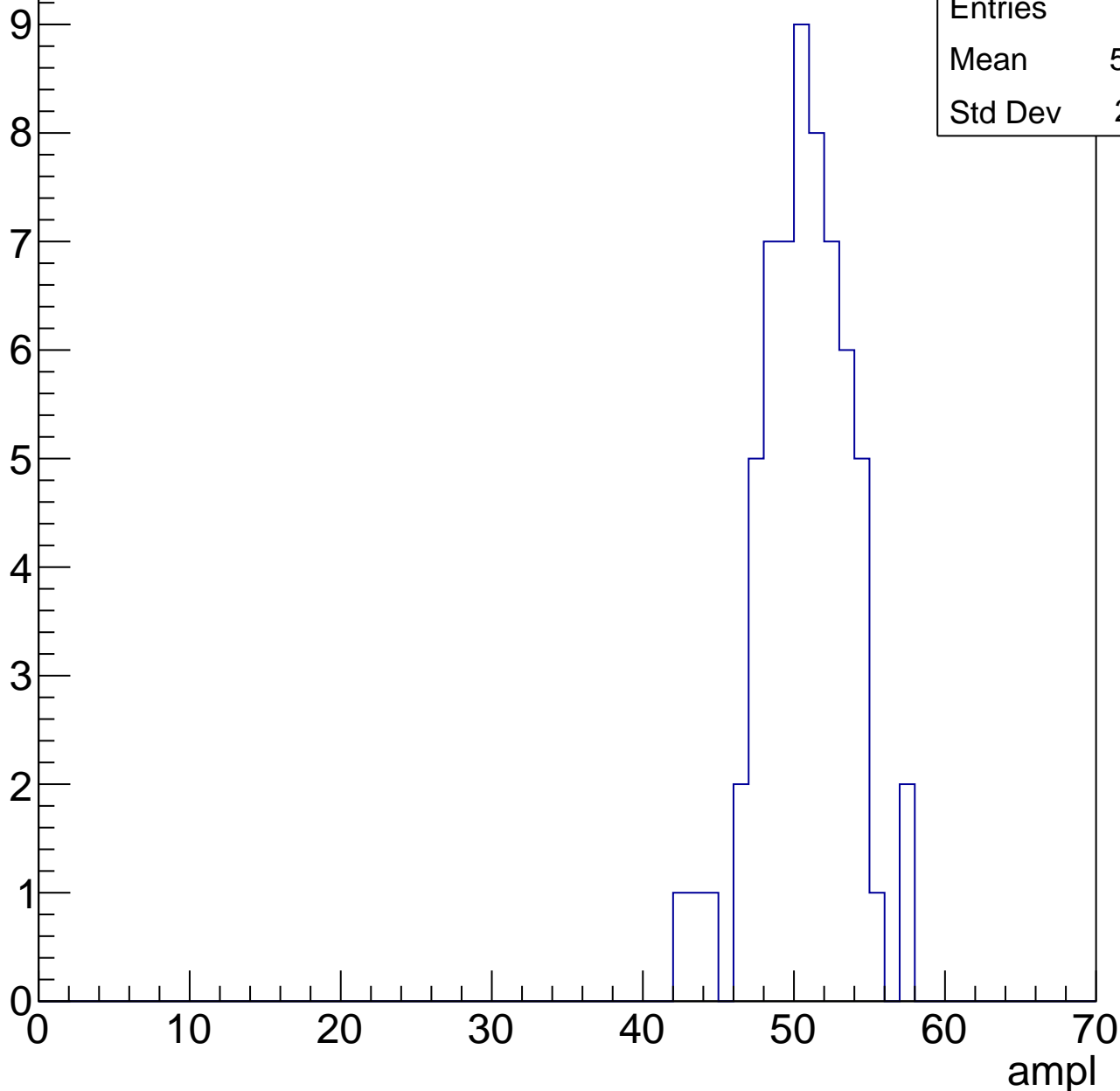
**Gaus Width: 3.0488**



# B0L002S, U1-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

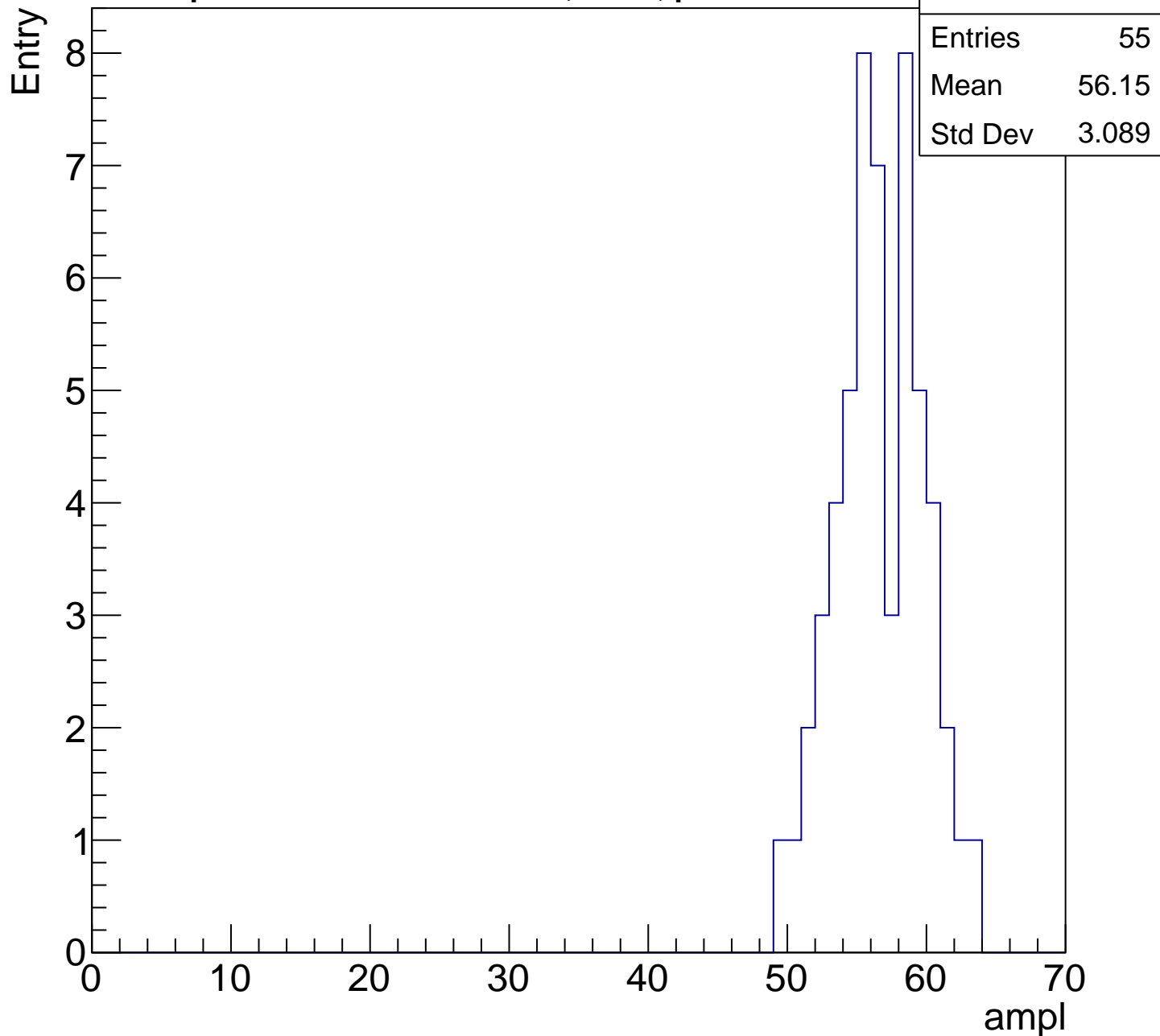
Entry



|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 50.23 |
| Std Dev | 2.981 |

# B0L002S, U1-ch2, adc4

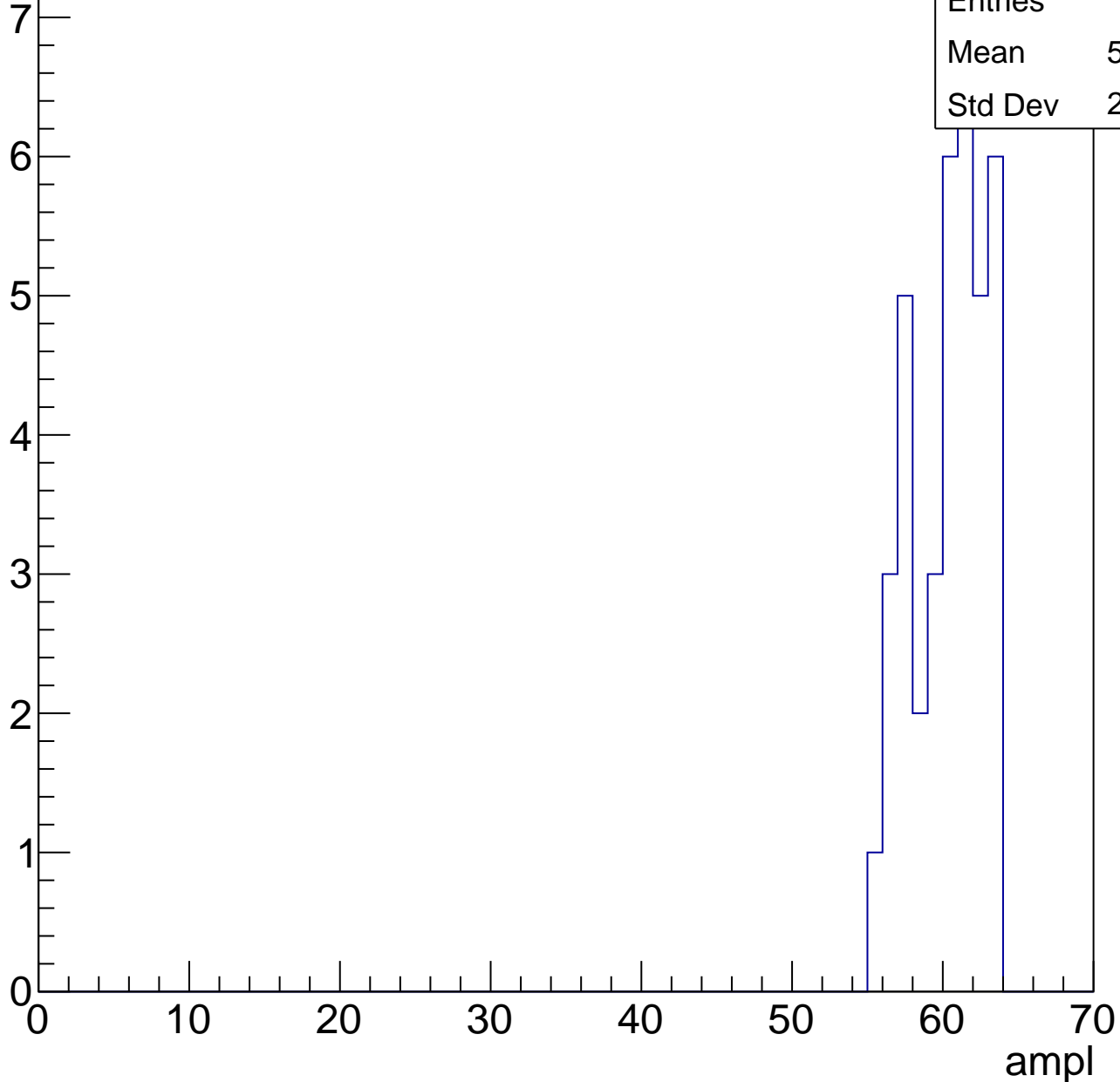
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

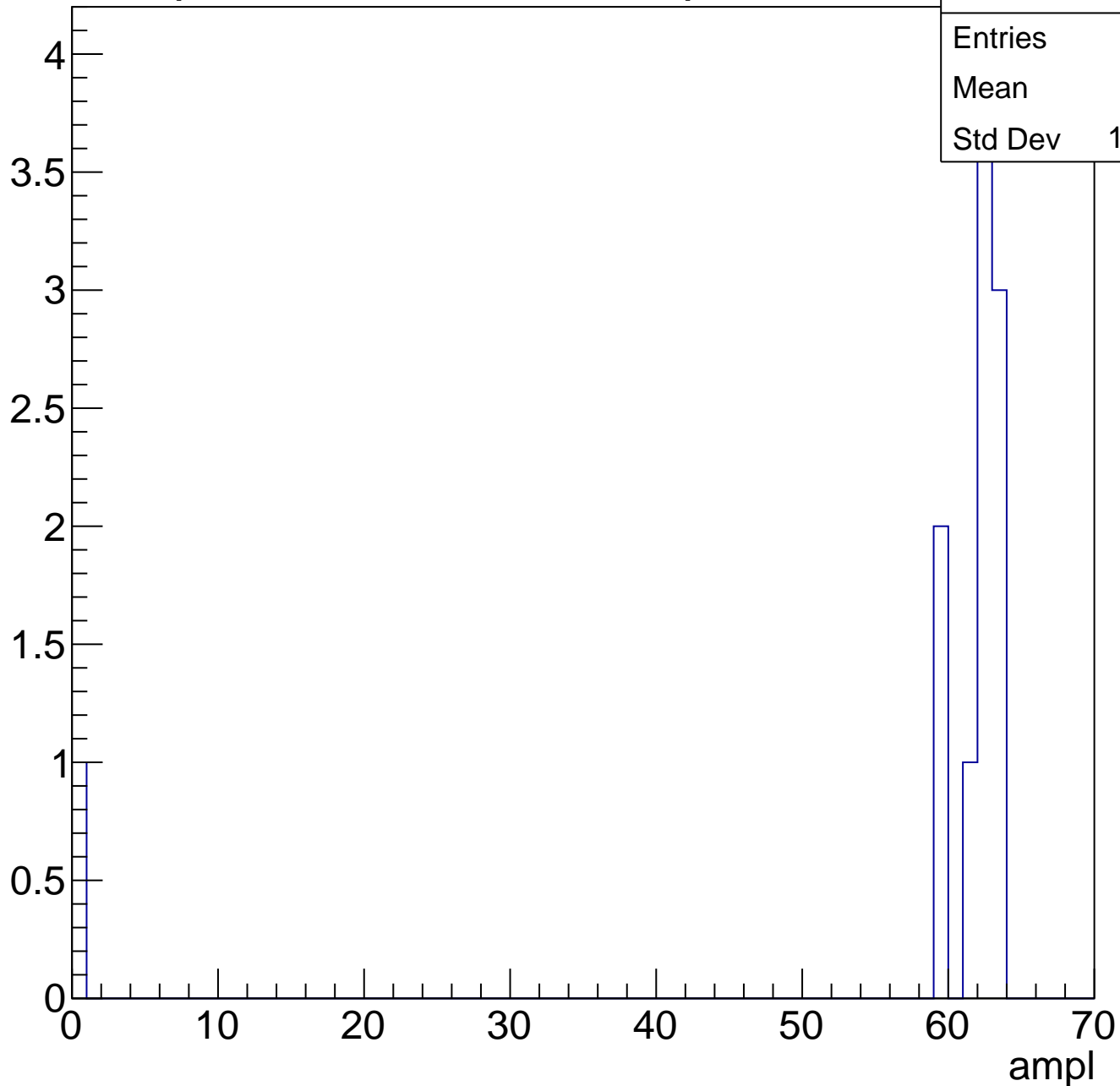
Entry



# B0L002S, U1-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch3, adc0

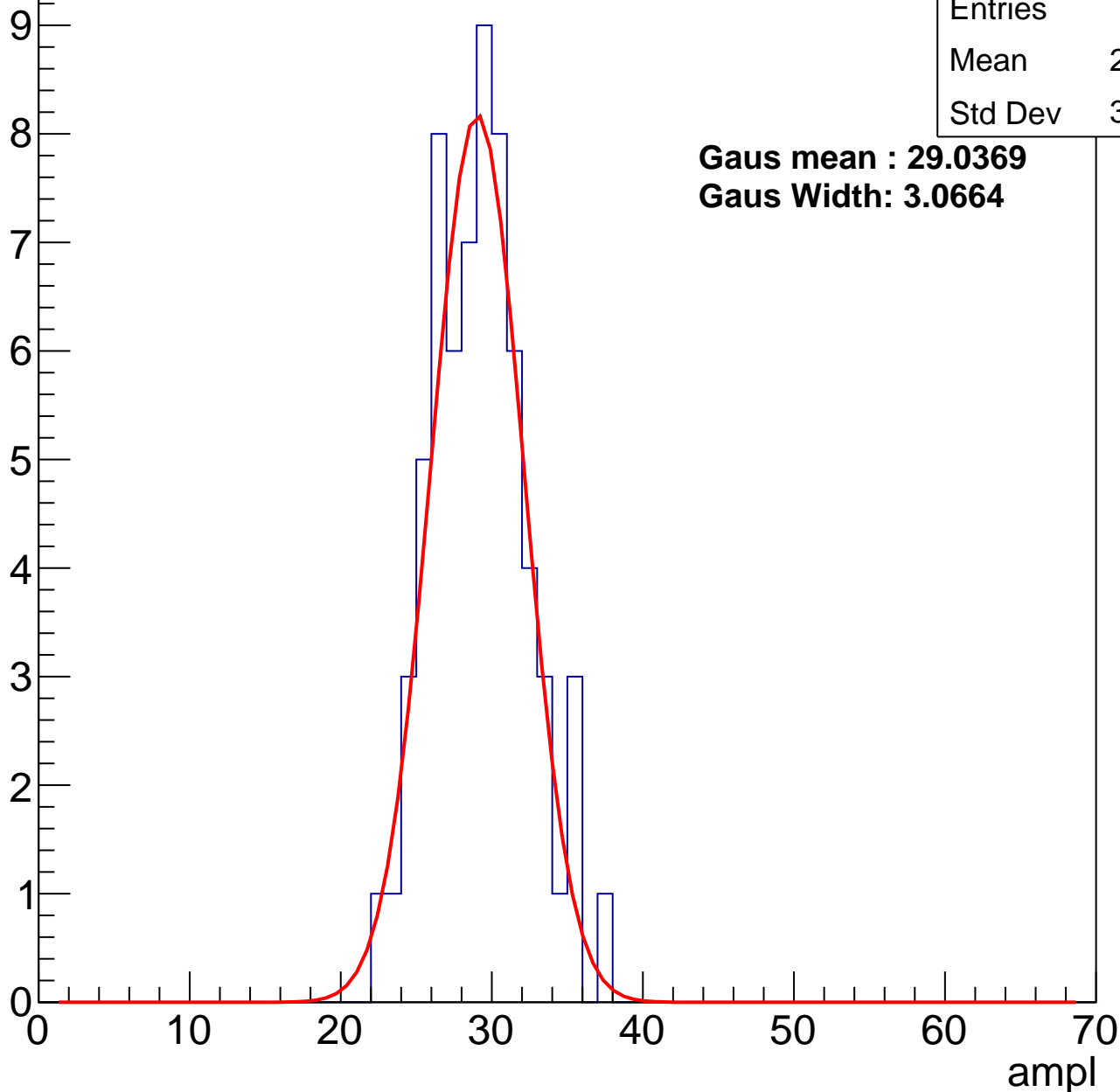
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 28.76 |
| Std Dev | 3.143 |

**Gaus mean : 29.0369**

**Gaus Width: 3.0664**



# B0L002S, U1-ch3, adc1

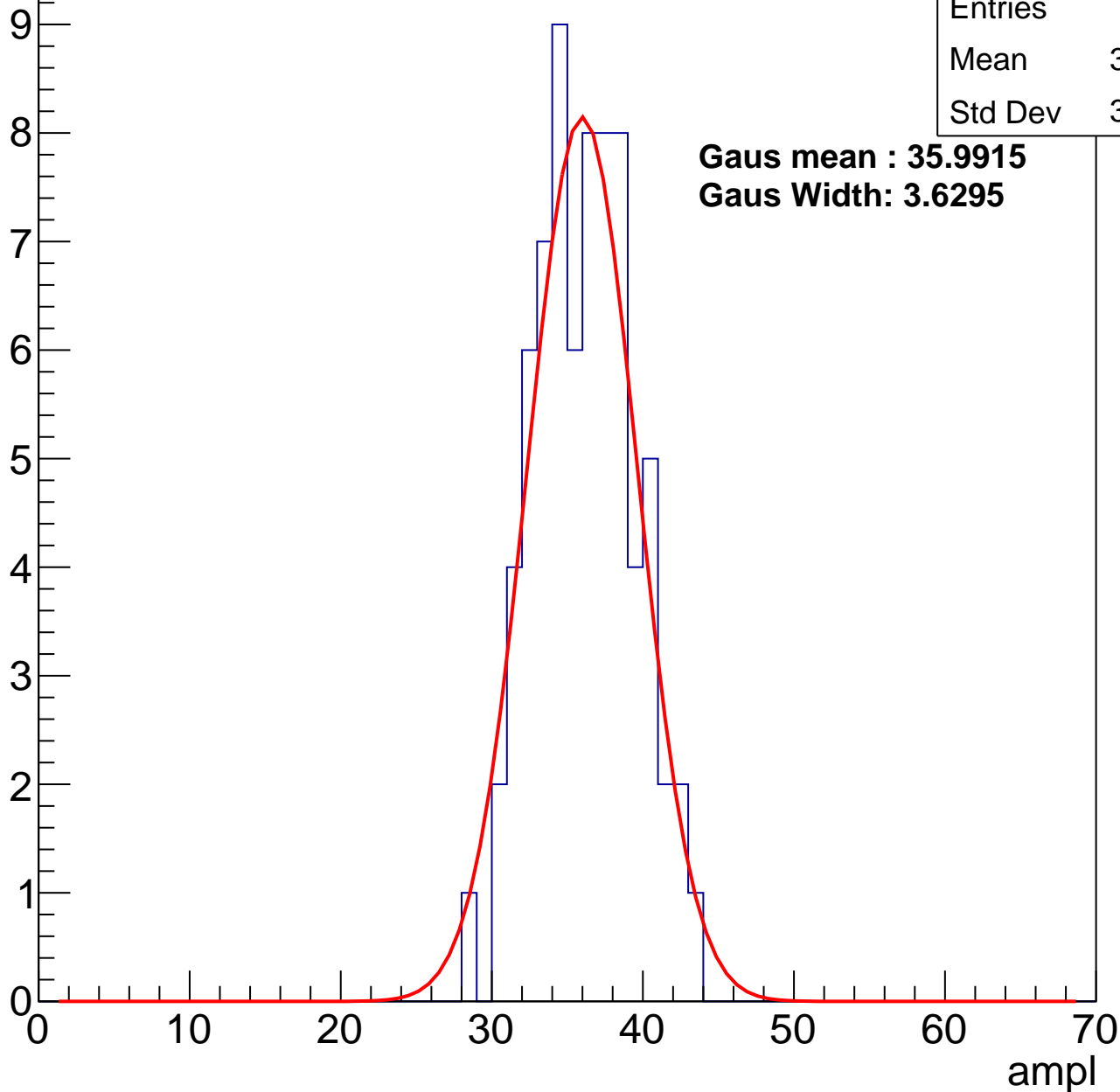
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 35.67 |
| Std Dev | 3.218 |

**Gaus mean : 35.9915**

**Gaus Width: 3.6295**



# B0L002S, U1-ch3, adc2

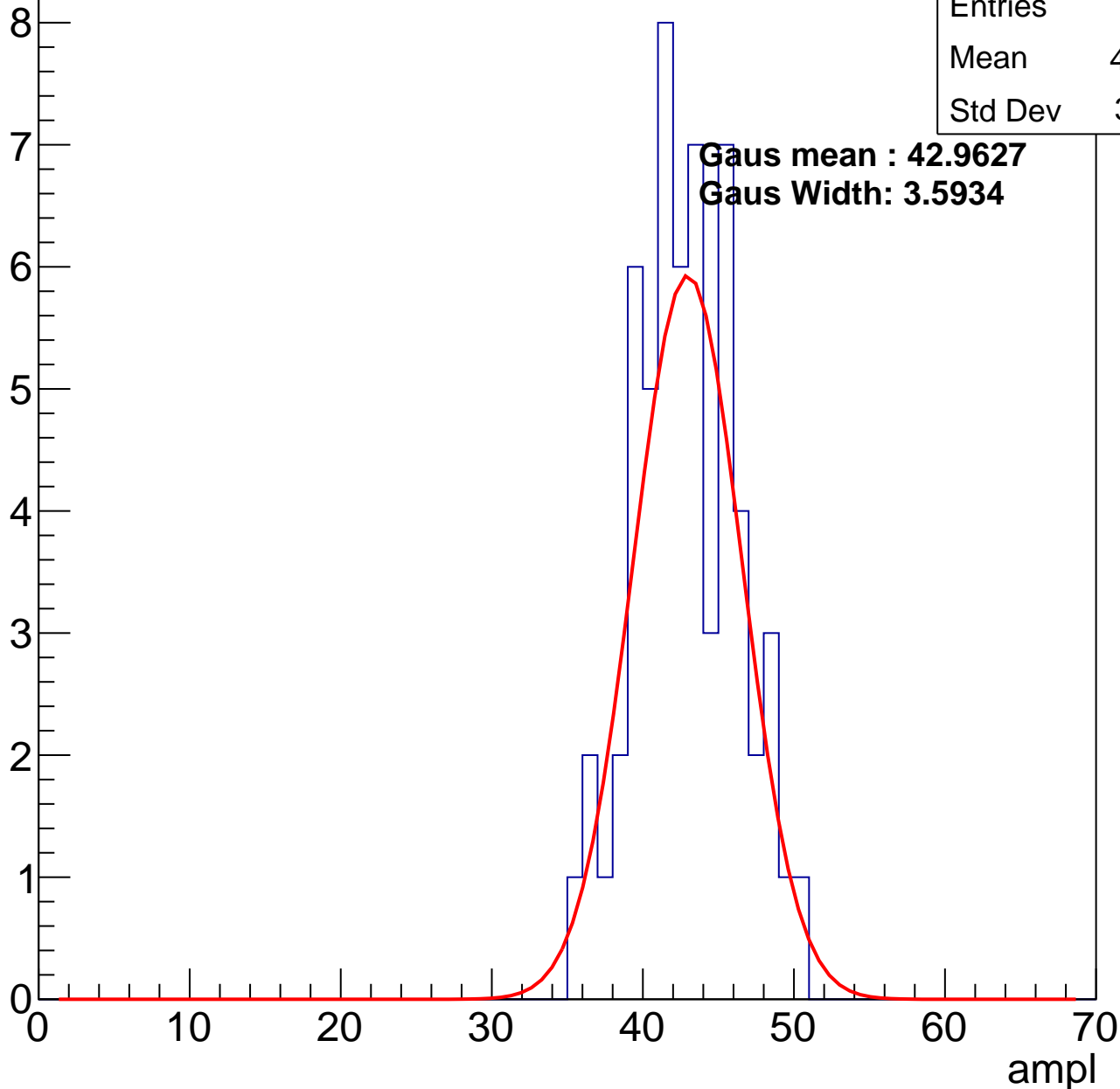
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 42.42 |
| Std Dev | 3.361 |

**Gaus mean : 42.9627**

**Gaus Width: 3.5934**

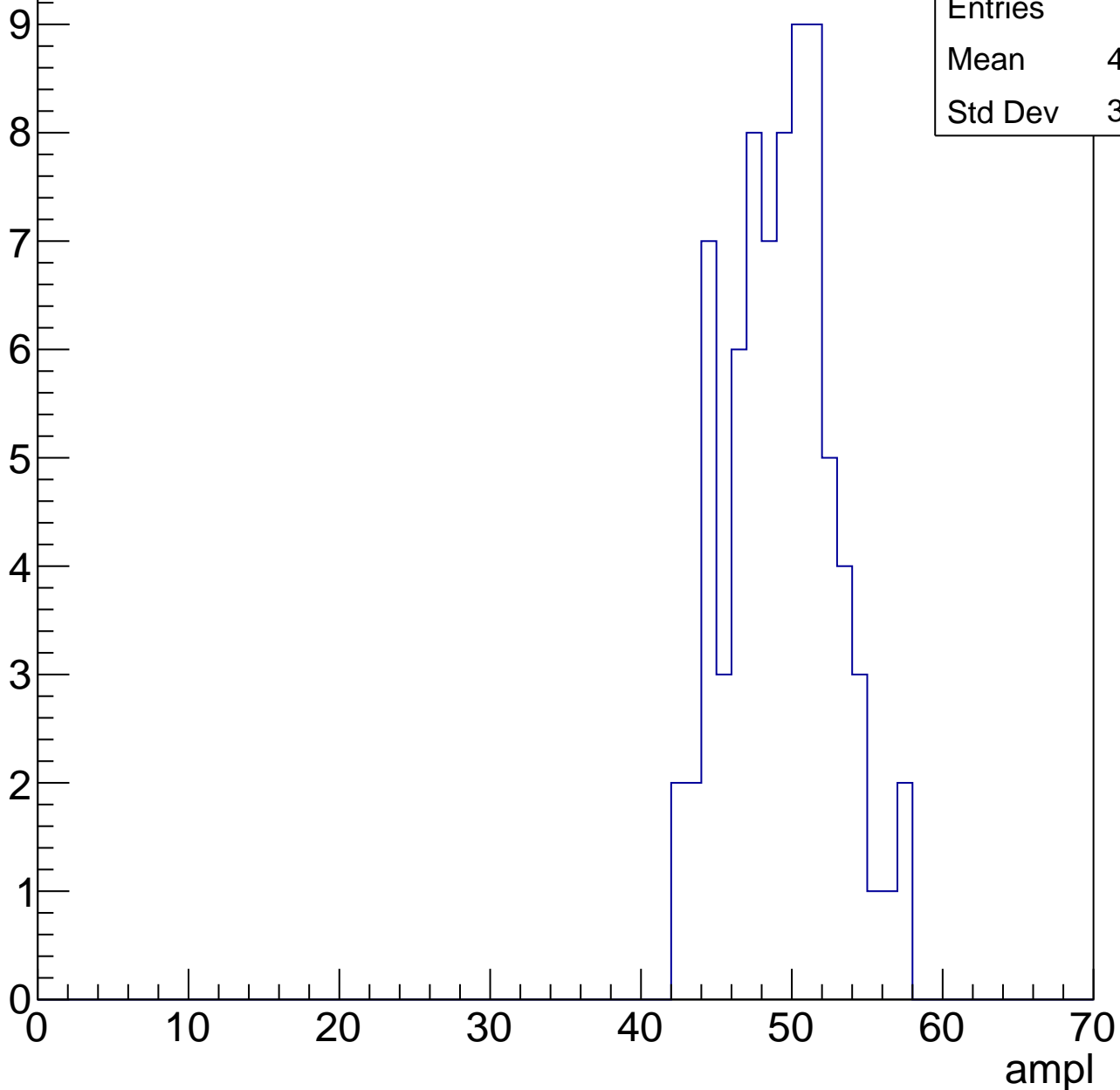


# B0L002S, U1-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

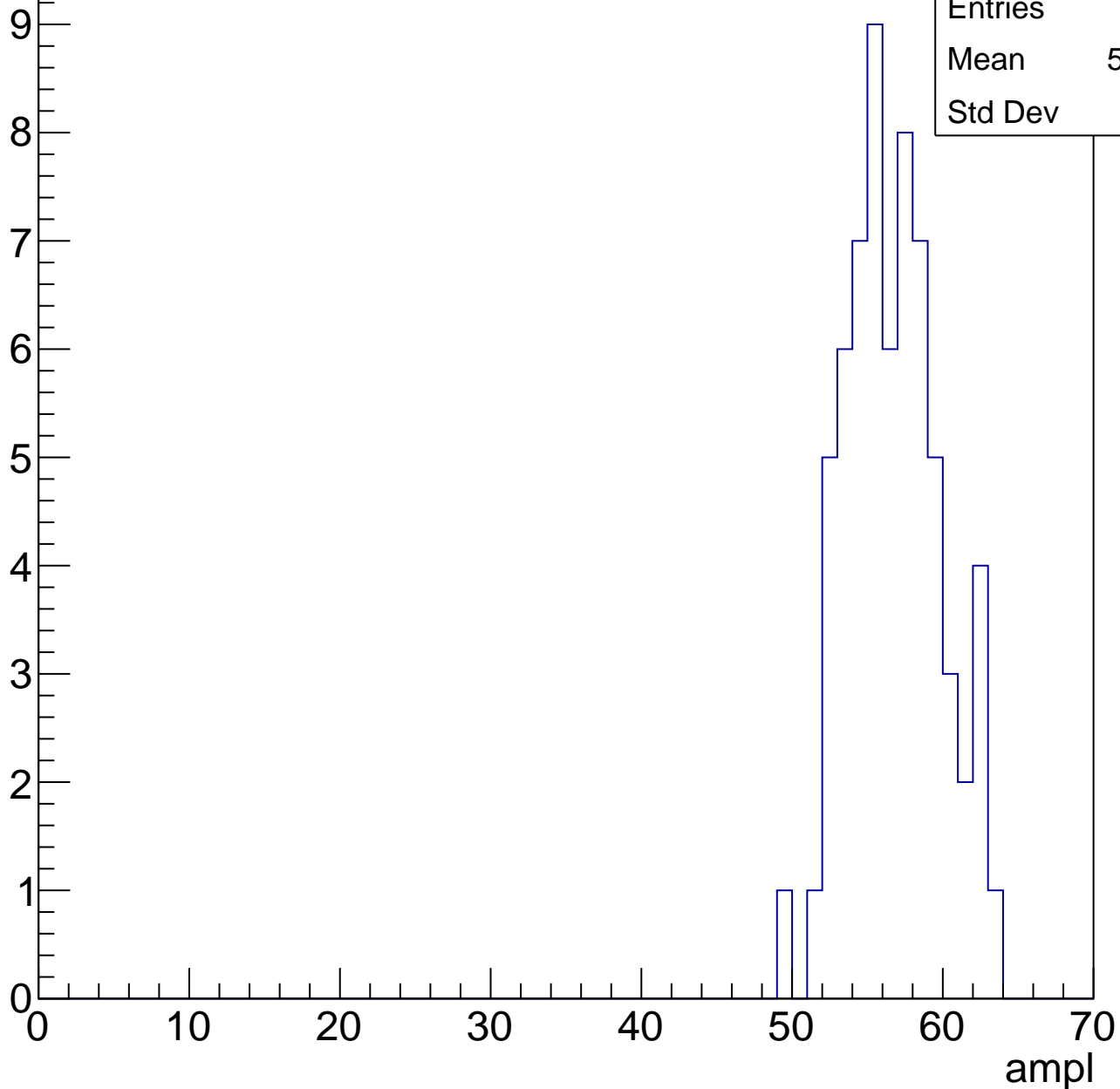
|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 48.84 |
| Std Dev | 3.468 |



# B0L002S, U1-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 56.26 |
| Std Dev | 3.07  |

# B0L002S, U1-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 35

Mean 58.51

Std Dev 10.22

ampl

0

10

20

30

40

50

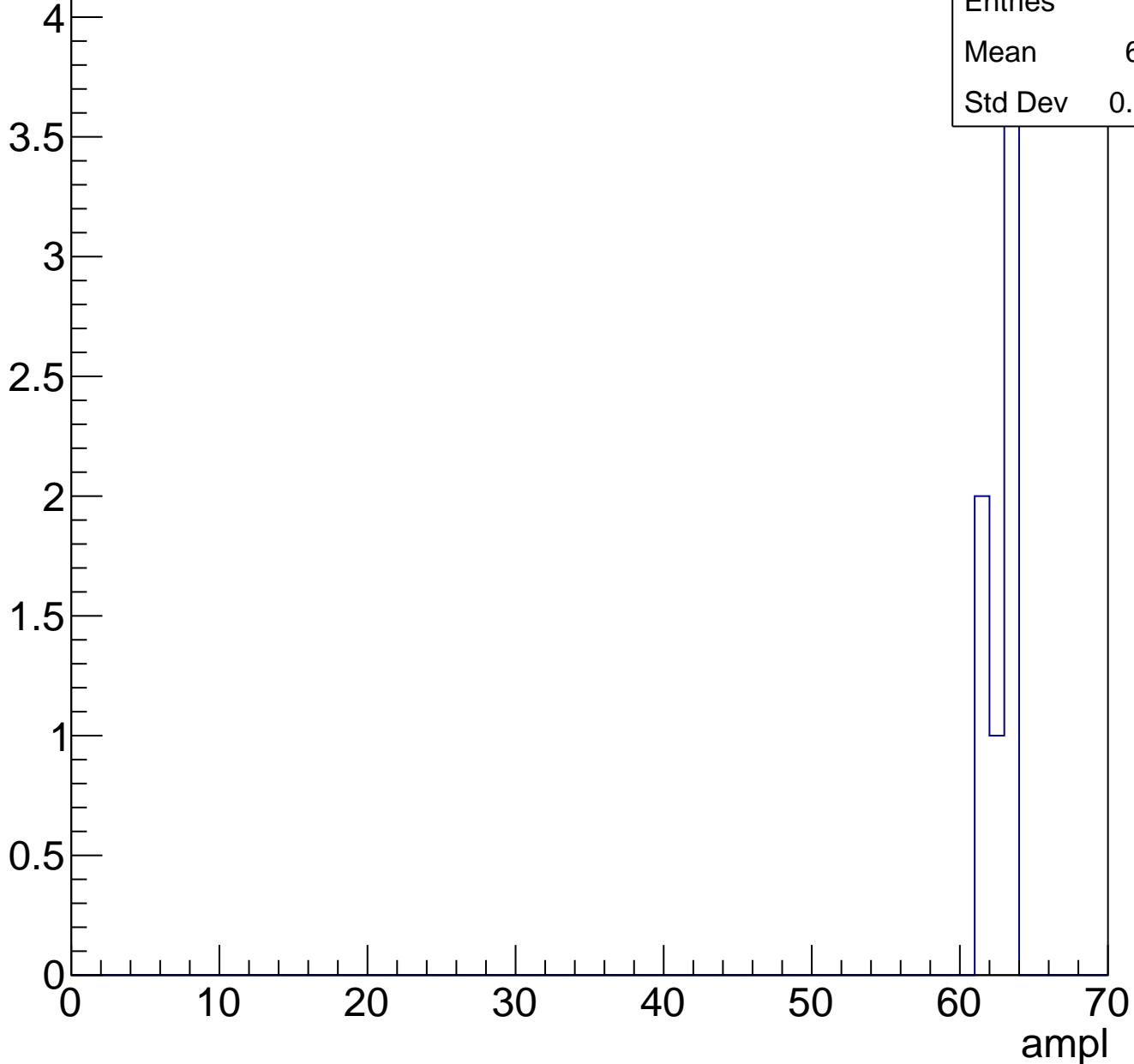
60

70

# B0L002S, U1-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 20 |
| Std Dev | 0  |

ampl

# B0L002S, U1-ch4, adc0

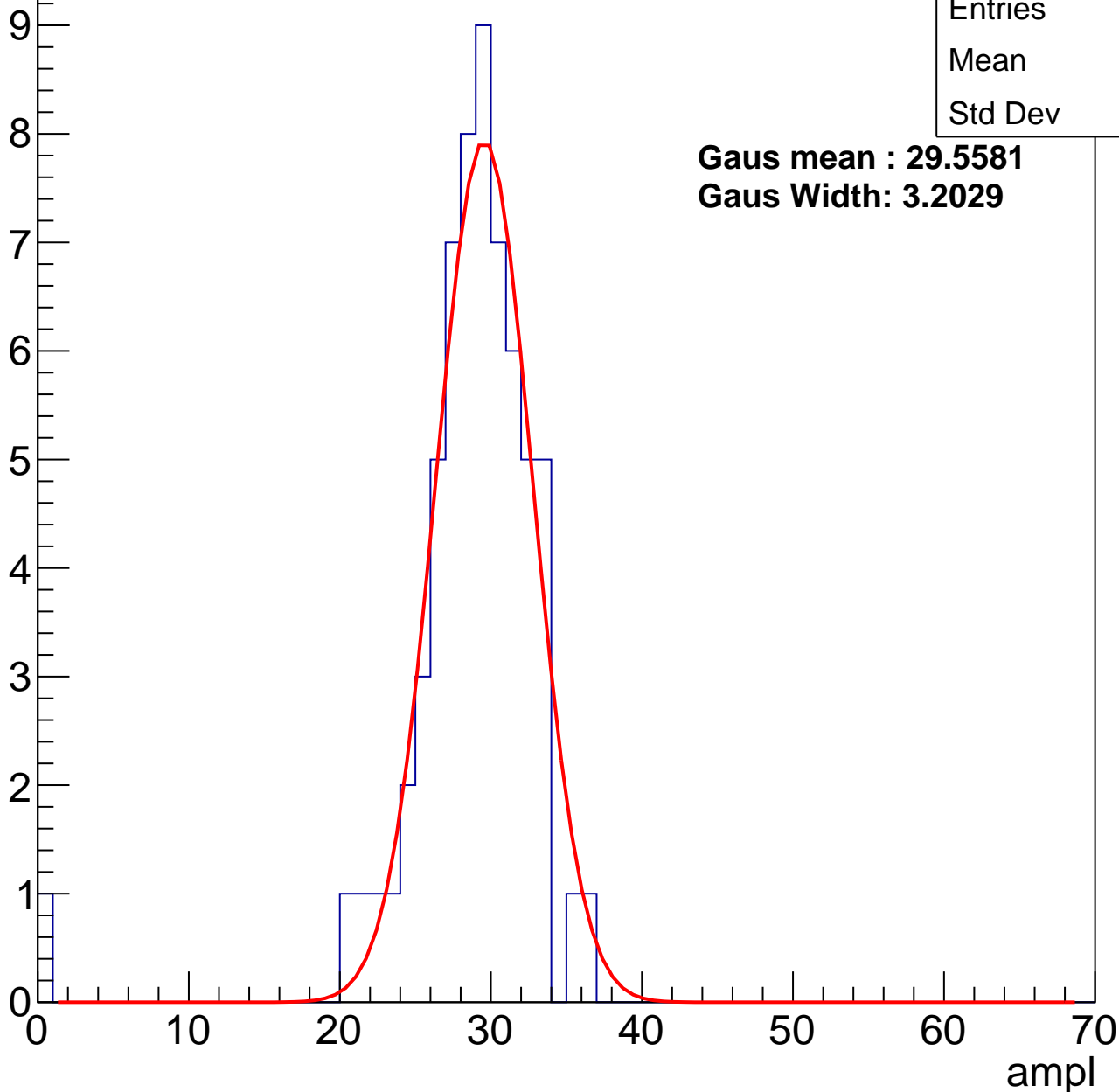
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |      |
|---------|------|
| Entries | 64   |
| Mean    | 28.2 |
| Std Dev | 4.76 |

**Gaus mean : 29.5581**

**Gaus Width: 3.2029**



# B0L002S, U1-ch4, adc1

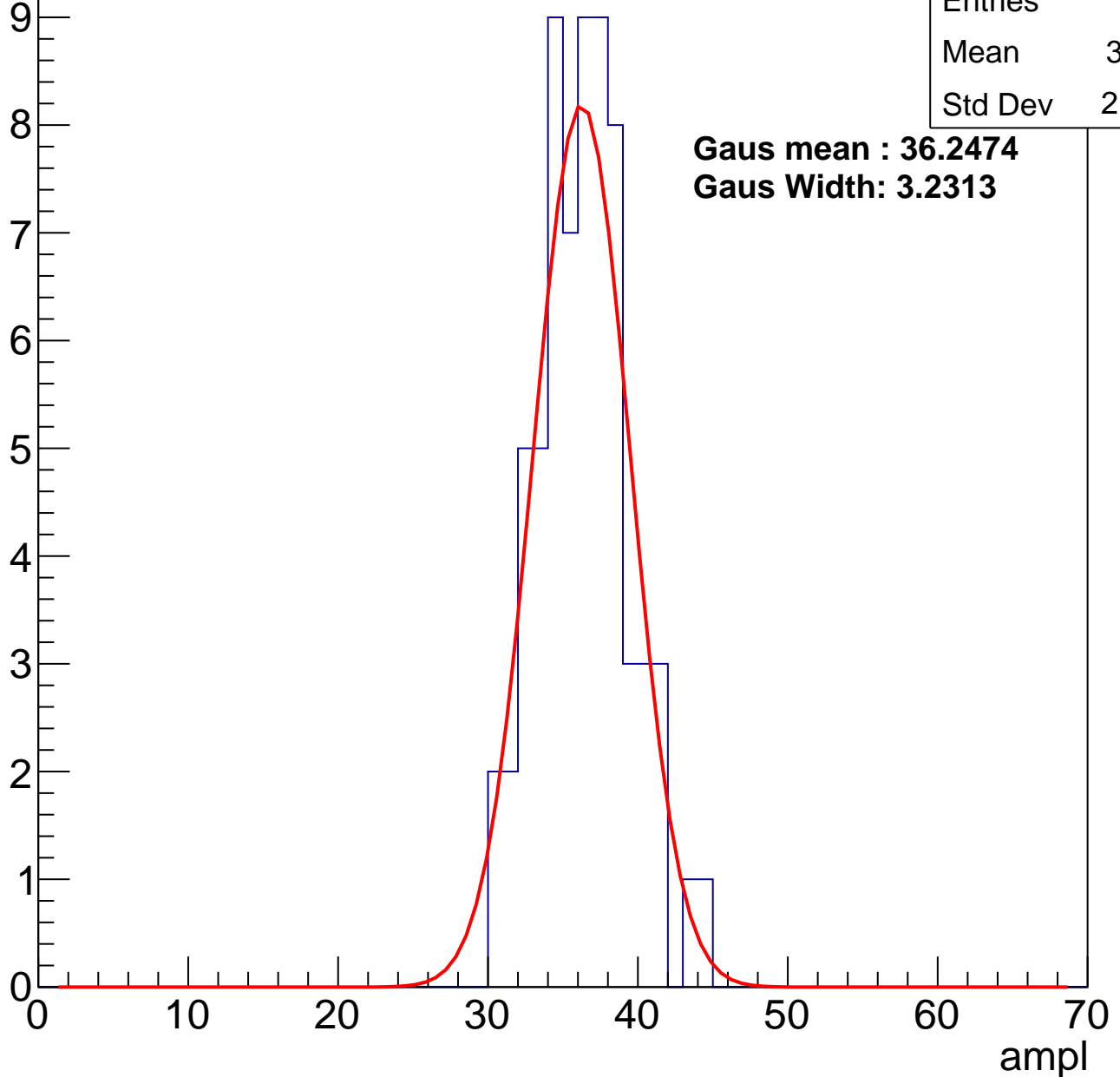
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 35.91 |
| Std Dev | 2.976 |

**Gaus mean : 36.2474**

**Gaus Width: 3.2313**



# B0L002S, U1-ch4, adc2

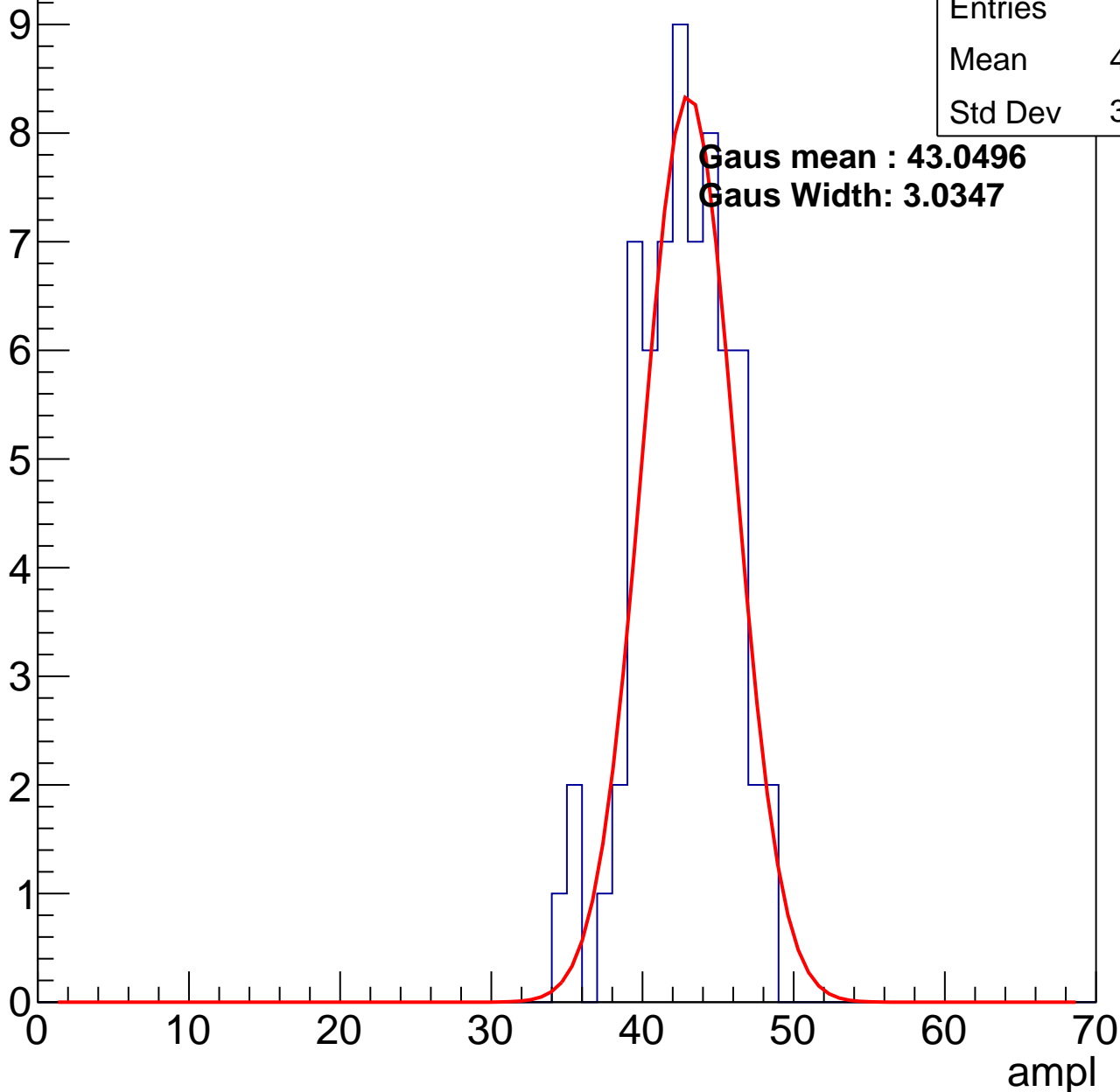
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 42.18 |
| Std Dev | 3.069 |

**Gaus mean : 43.0496**

**Gaus Width: 3.0347**

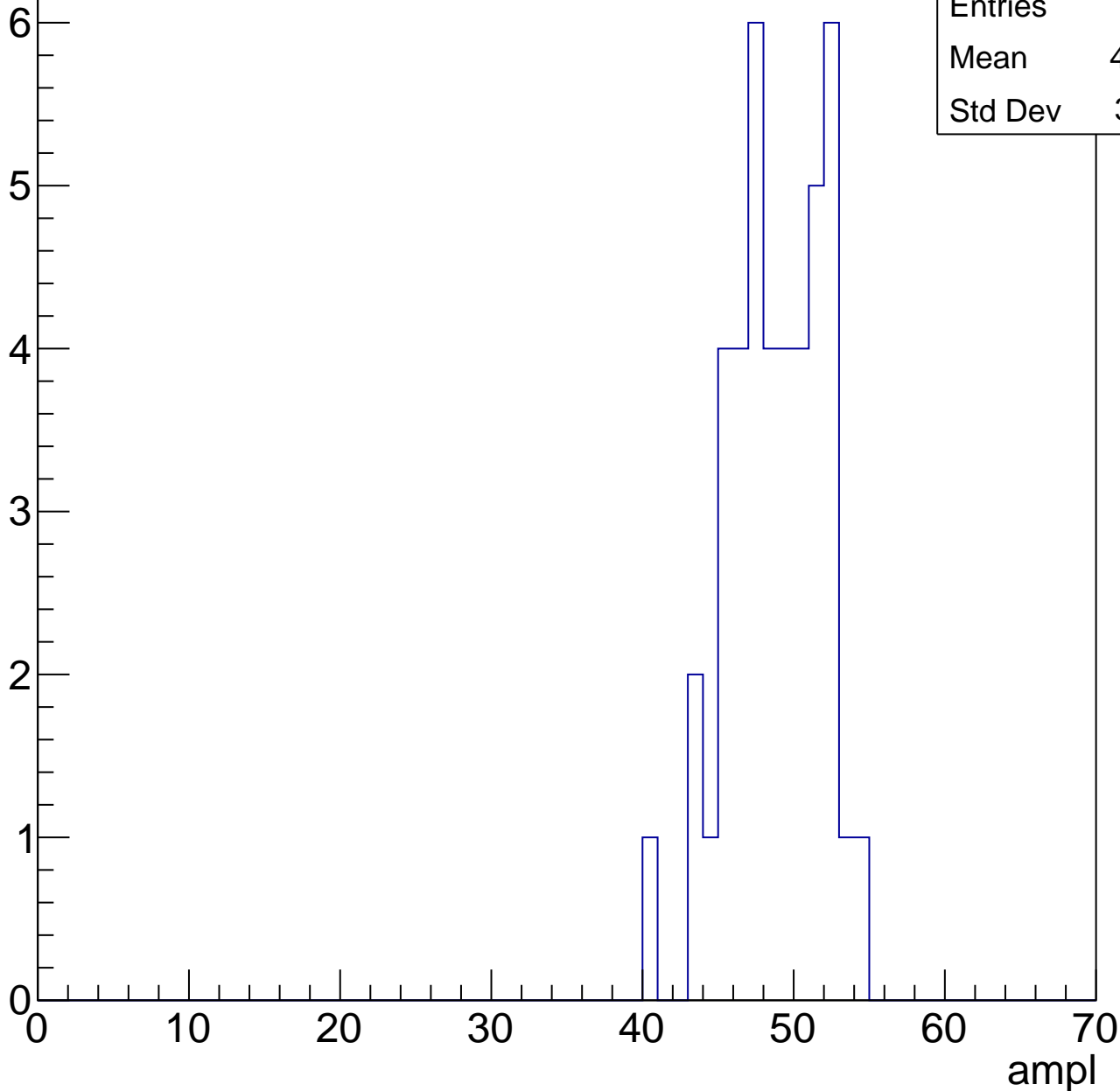


# B0L002S, U1-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

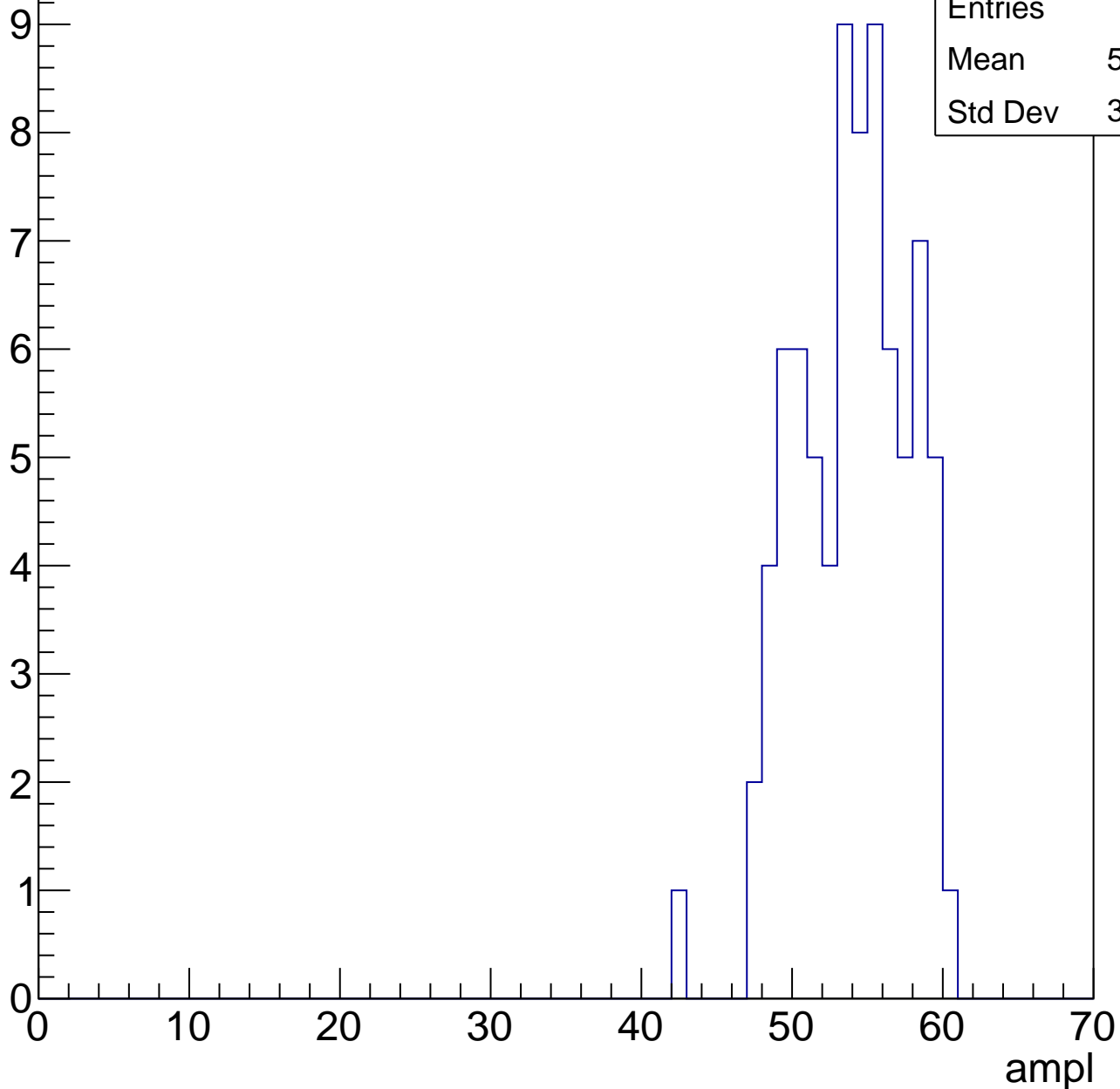
|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 48.33 |
| Std Dev | 3.071 |



# B0L002S, U1-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch4, adc5

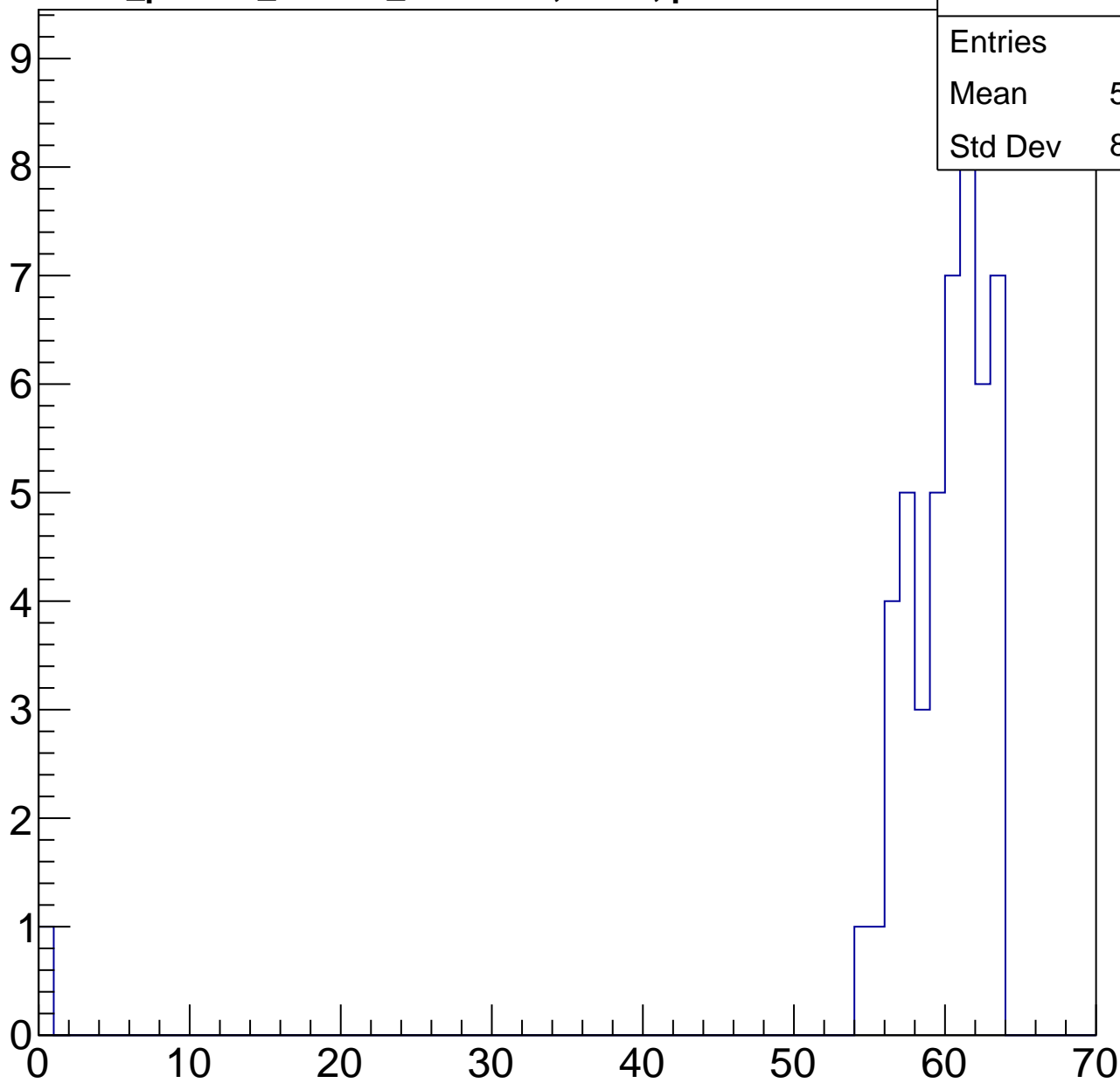
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 58.55 |
| Std Dev | 8.783 |

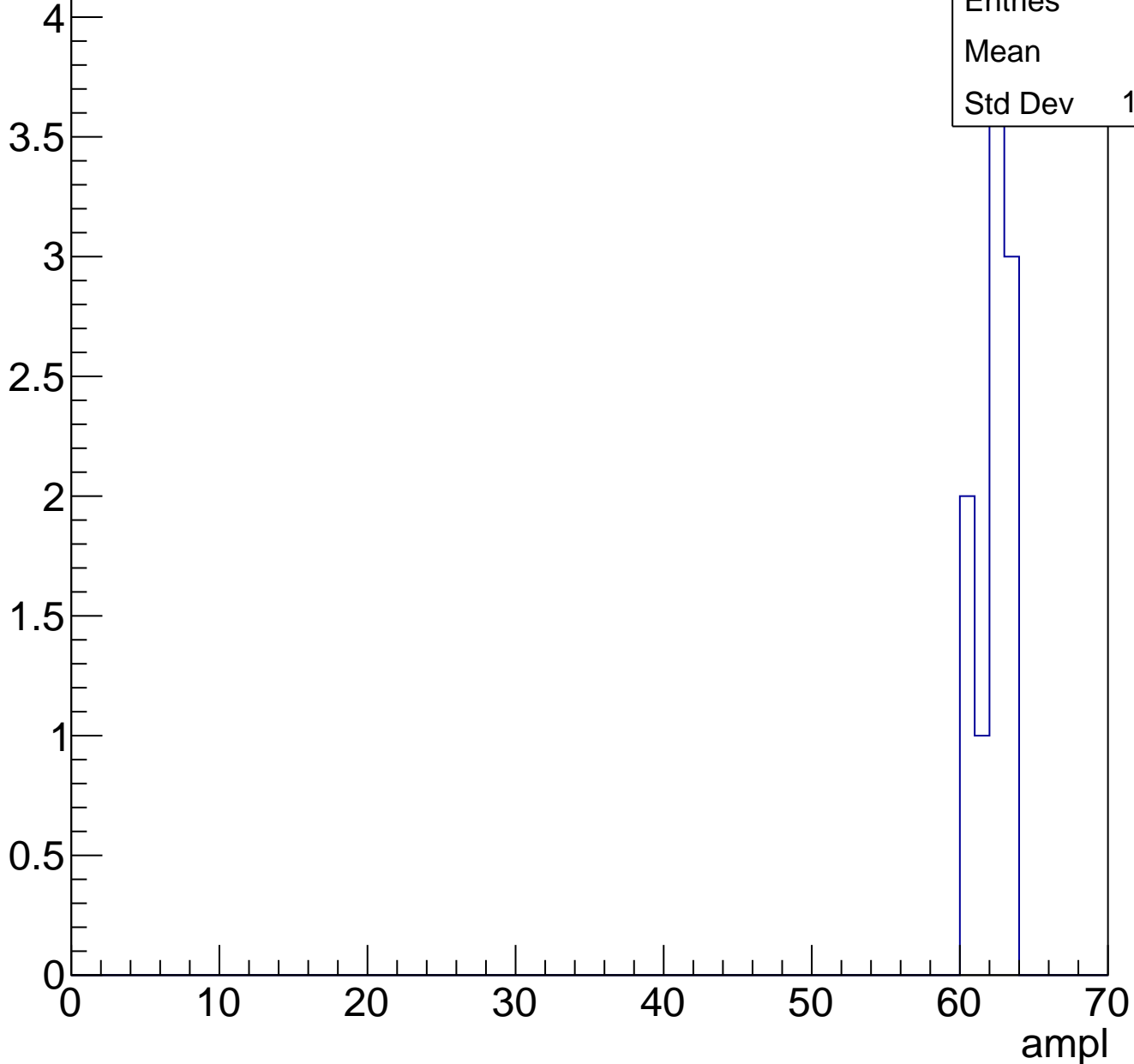
ampl



# B0L002S, U1-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

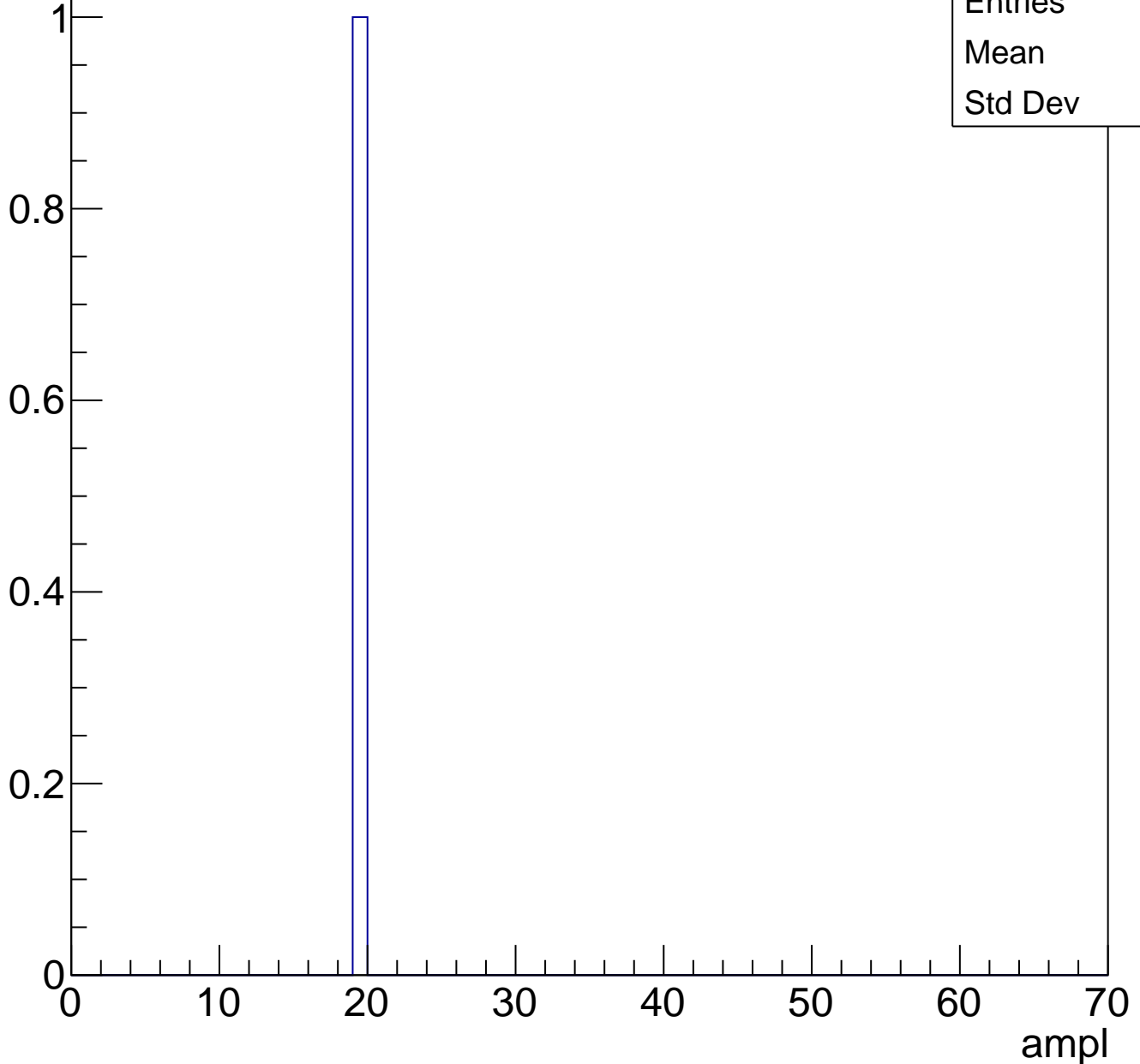




# B0L002S, U1-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 19 |
| Std Dev | 0  |

# B0L002S, U1-ch5, adc0

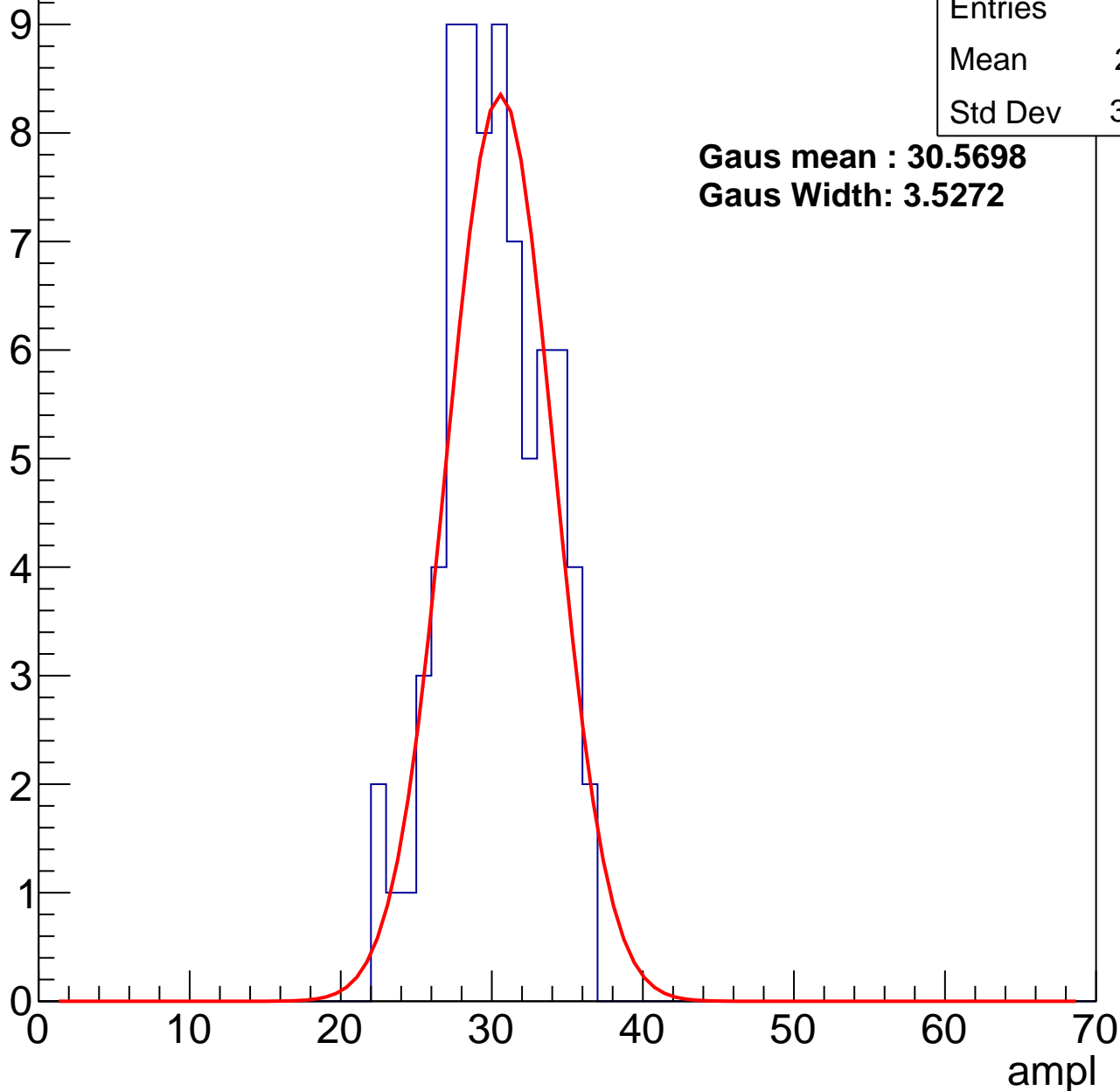
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 29.71 |
| Std Dev | 3.284 |

**Gaus mean : 30.5698**

**Gaus Width: 3.5272**



# B0L002S, U1-ch5, adc1

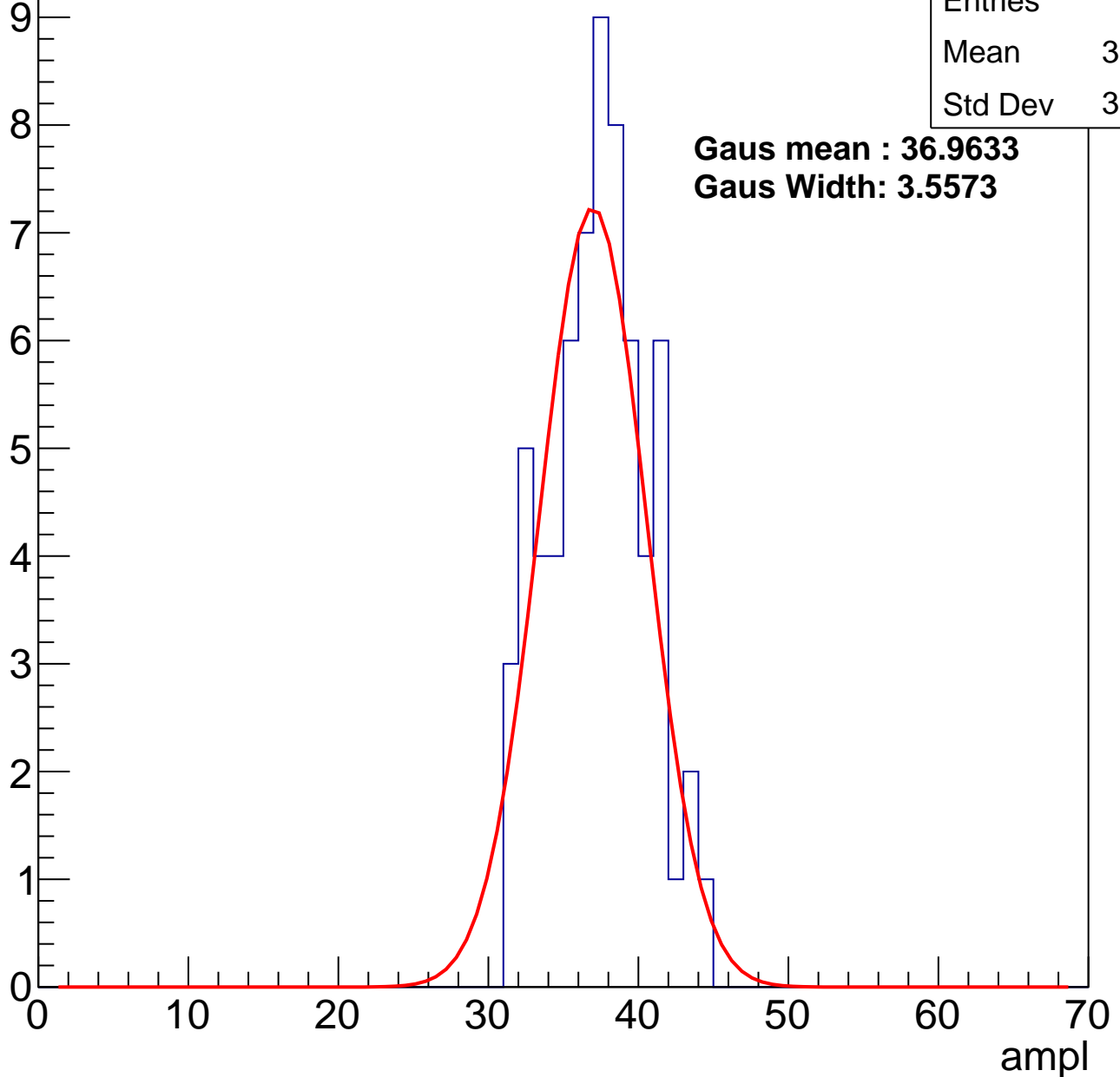
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 36.85 |
| Std Dev | 3.192 |

**Gaus mean : 36.9633**

**Gaus Width: 3.5573**



# B0L002S, U1-ch5, adc2

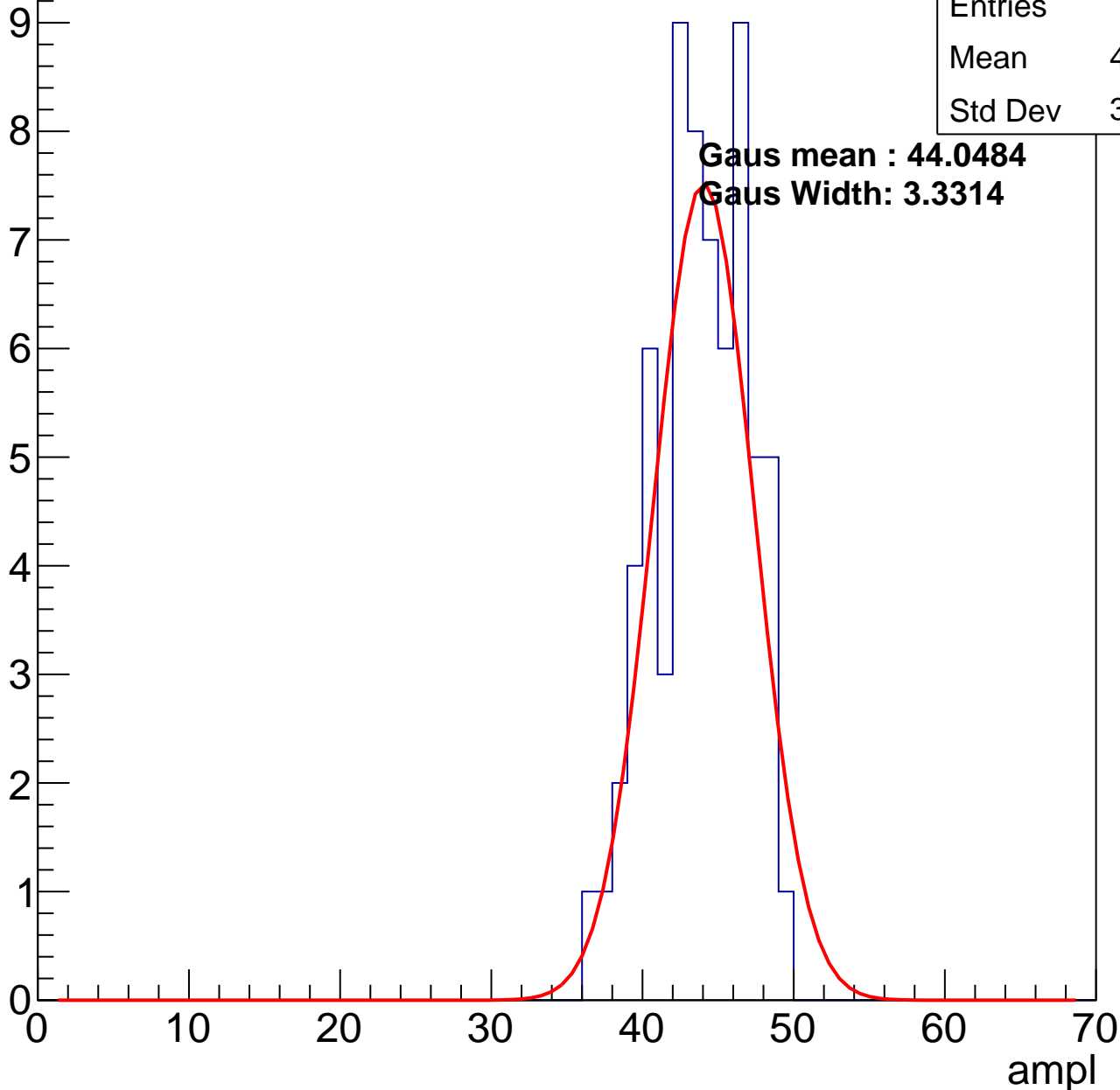
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 43.37 |
| Std Dev | 3.036 |

**Gaus mean : 44.0484**

**Gaus Width: 3.3314**

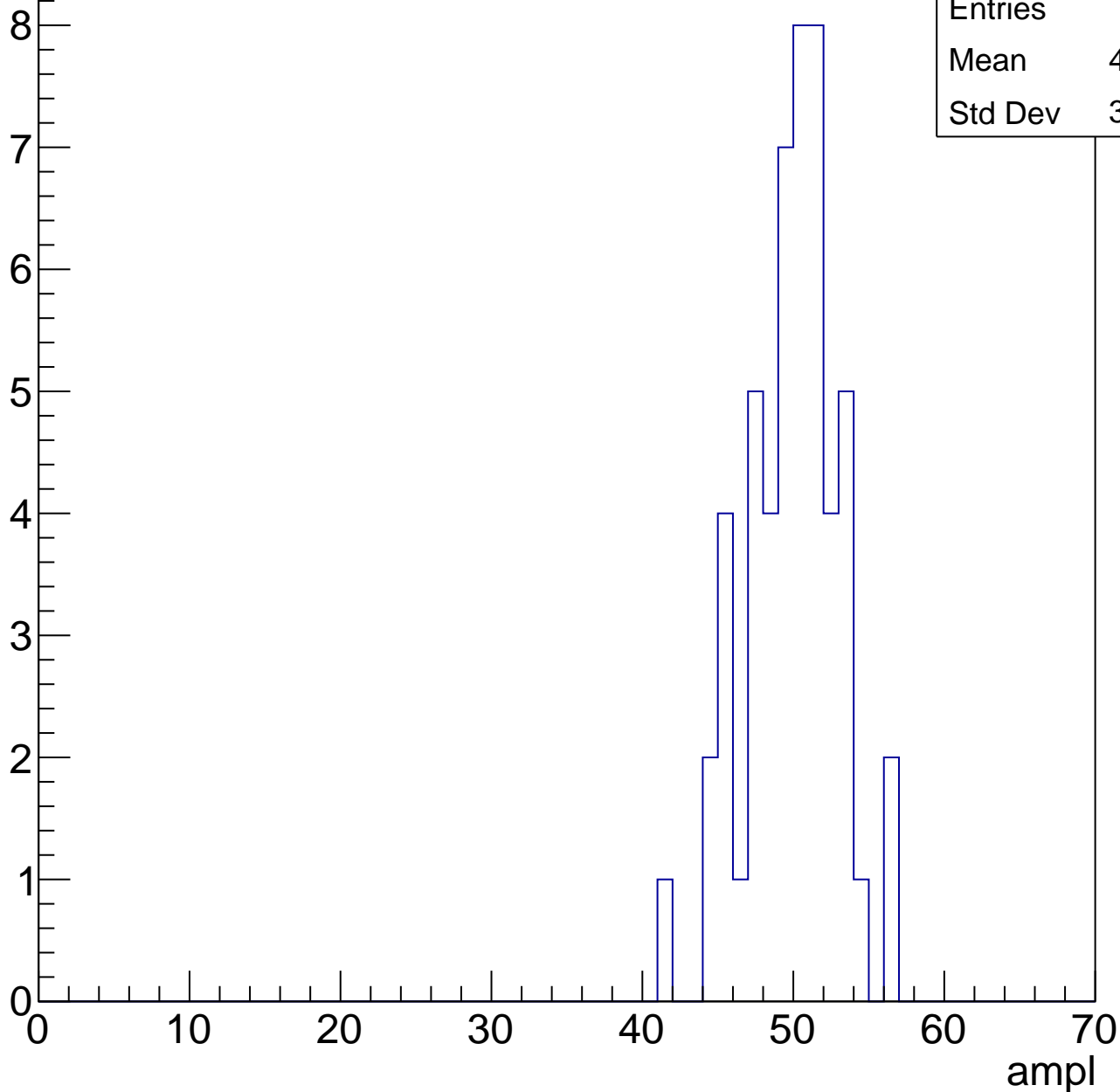


# B0L002S, U1-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

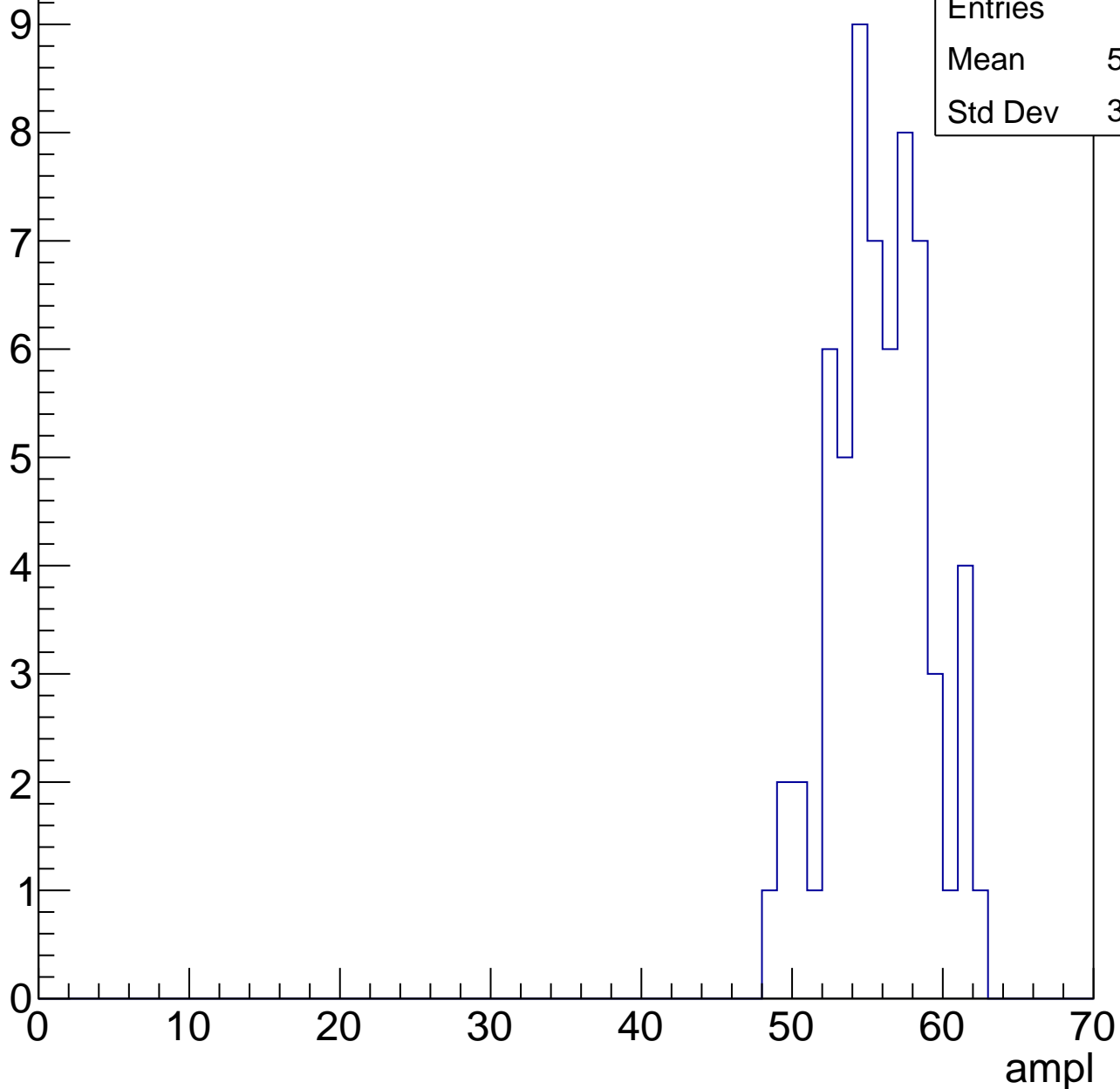
|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 49.46 |
| Std Dev | 3.035 |



# B0L002S, U1-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 55.33 |
| Std Dev | 3.162 |

# B0L002S, U1-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

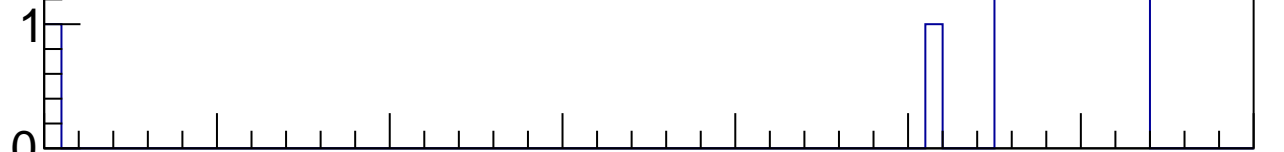
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 58.56 |
| Std Dev | 8.93  |

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch6, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 30.2  |
| Std Dev | 3.398 |

**Gaus mean : 31.2229**

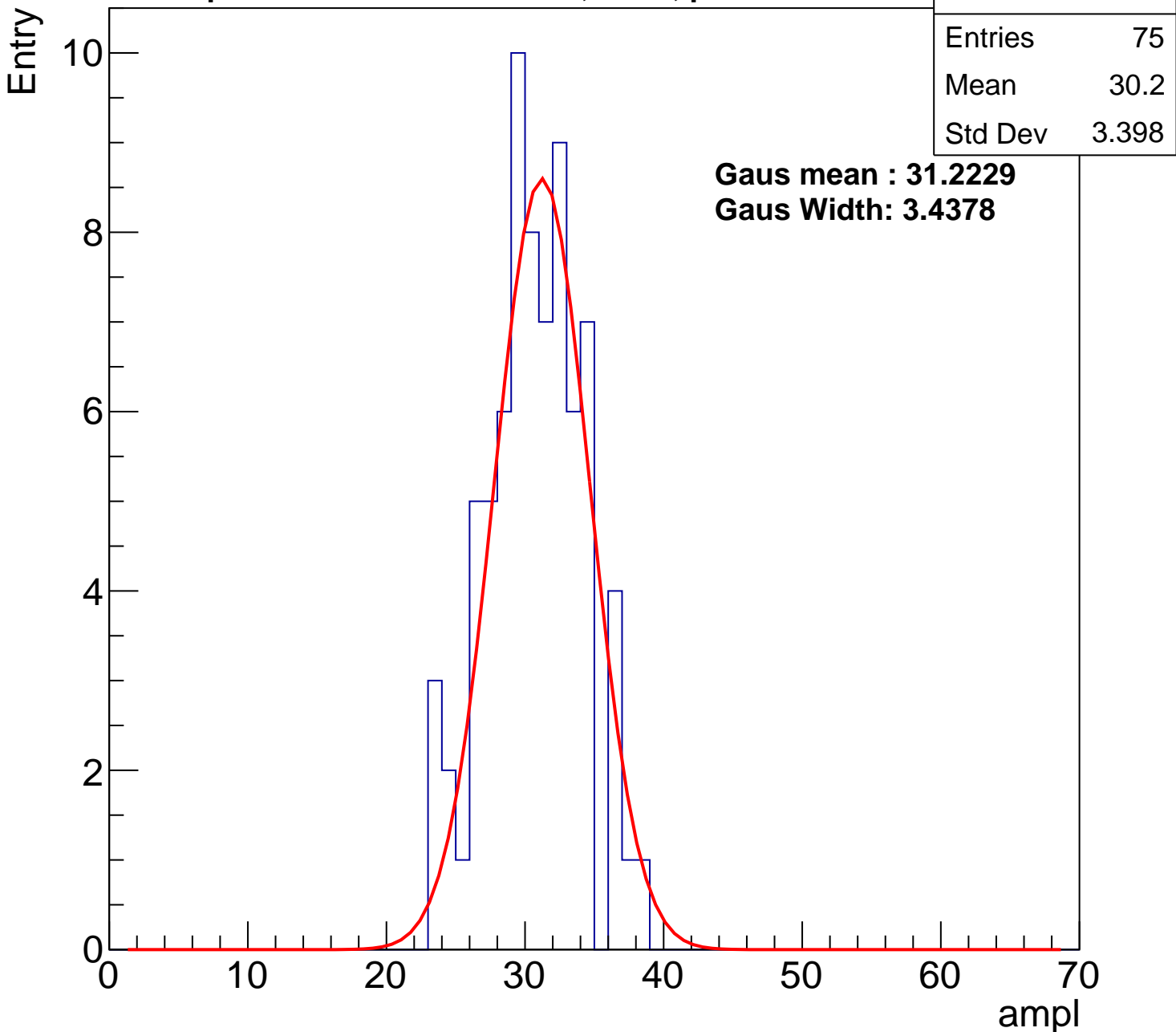
**Gaus Width: 3.4378**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch6, adc1

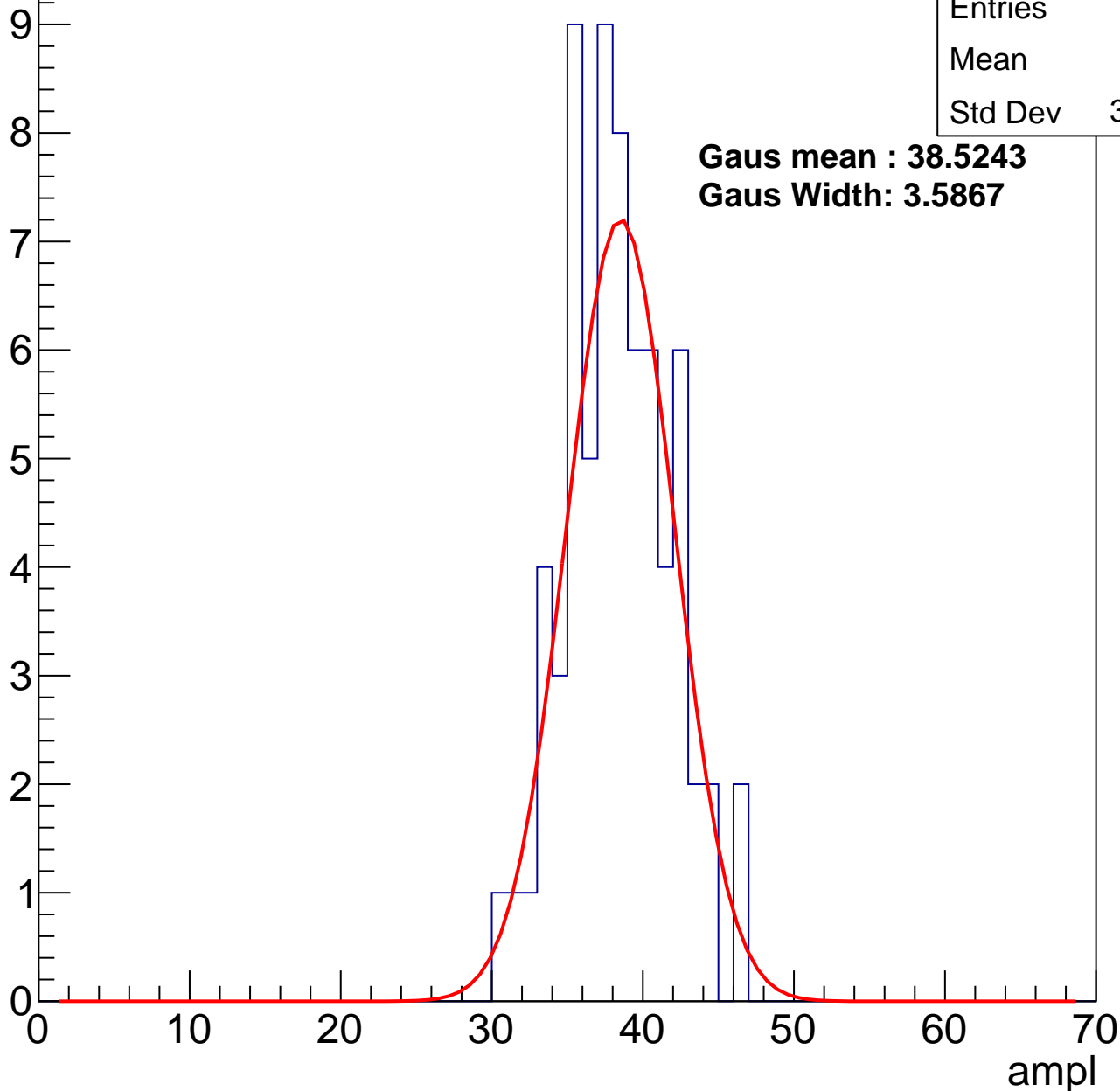
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 37.9  |
| Std Dev | 3.444 |

**Gaus mean : 38.5243**

**Gaus Width: 3.5867**

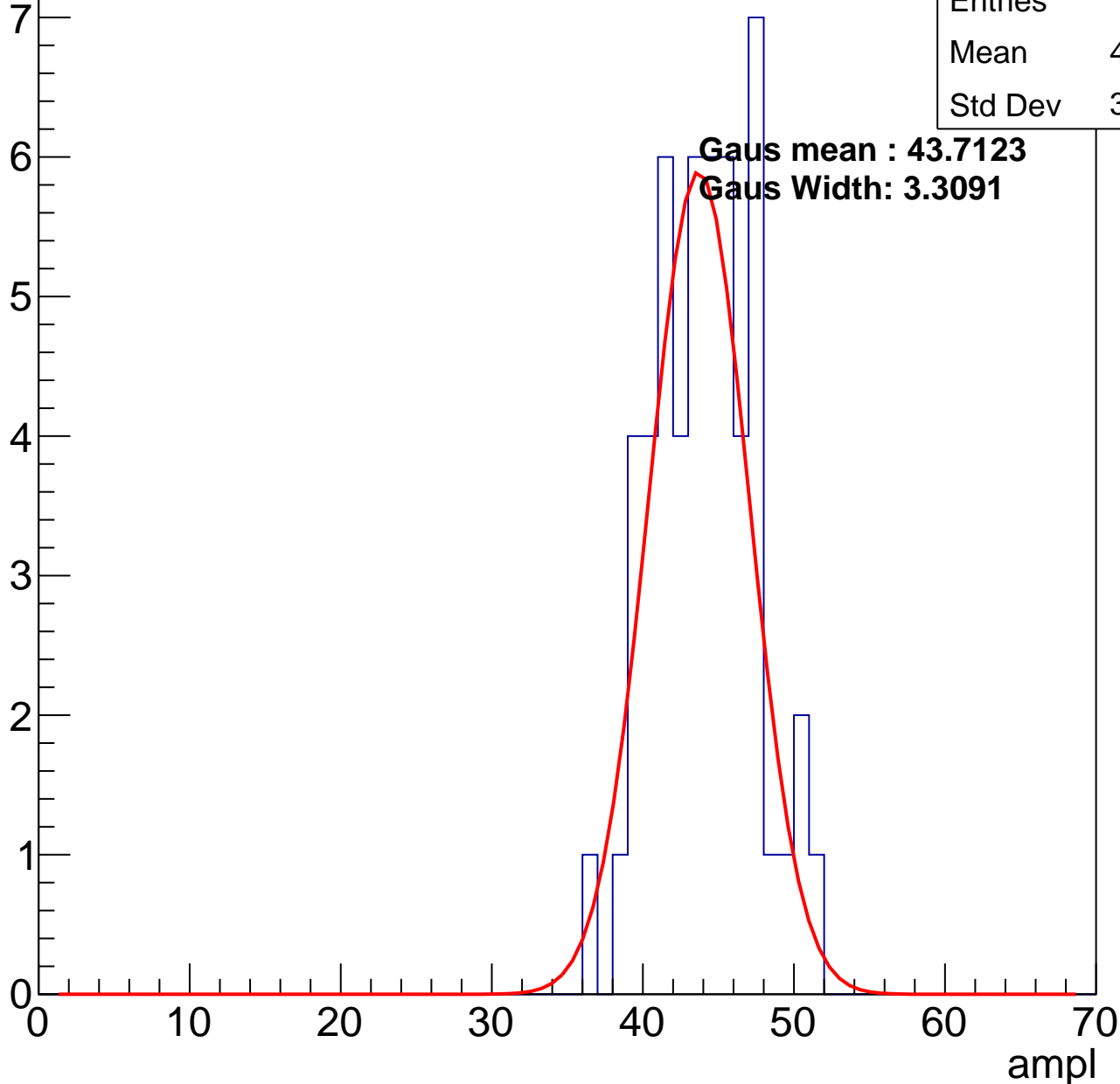


# B0L002S, U1-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 43.65 |
| Std Dev | 3.278 |



# B0L002S, U1-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 50.23 |
| Std Dev | 3.404 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

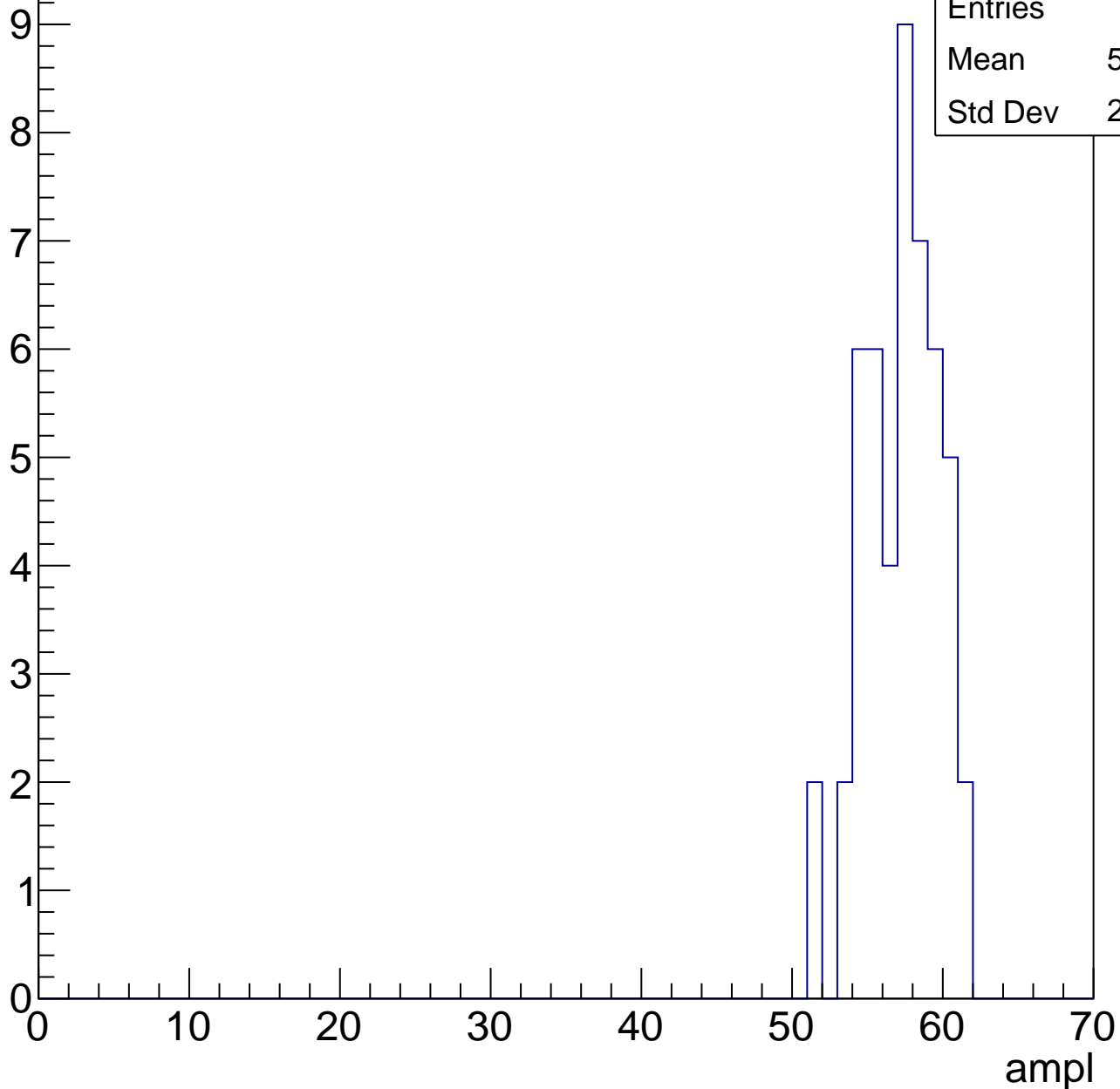
8

10

# B0L002S, U1-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 60.63 |
| Std Dev | 2.127 |

Entry

10

8

6

4

2

0

0

10

20

30

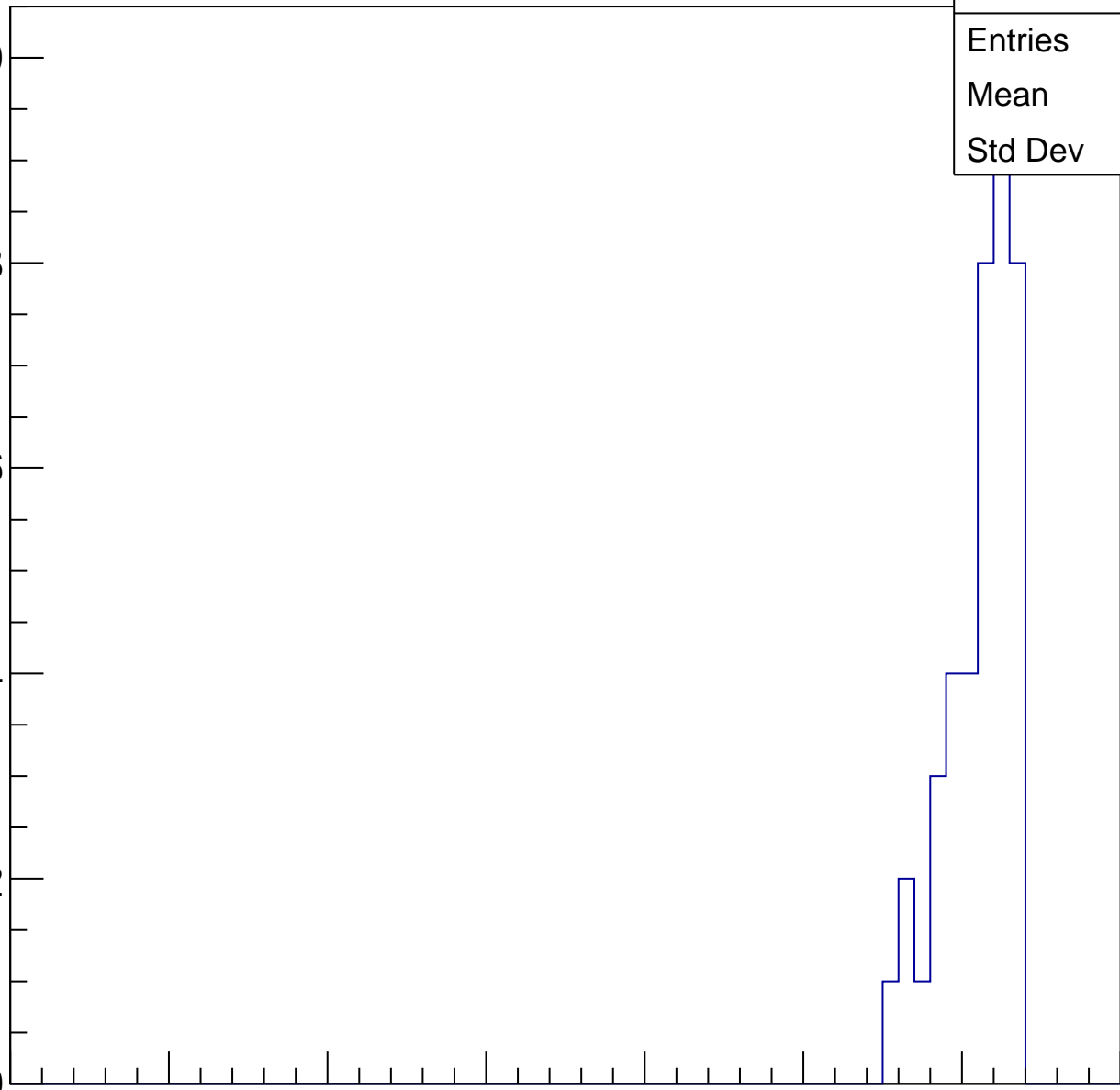
40

50

60

70

ampl



# B0L002S, U1-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch7, adc0

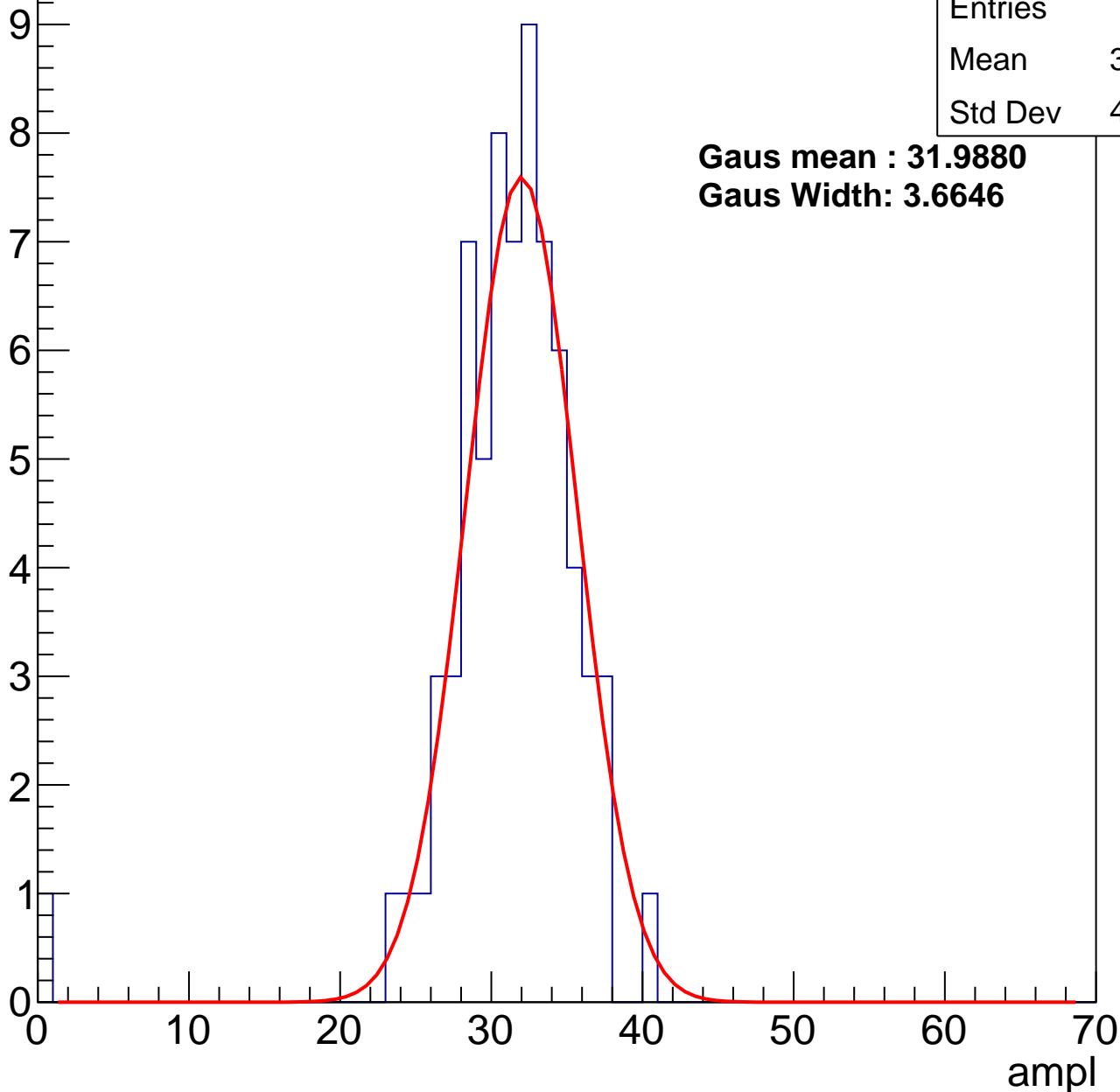
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 30.73 |
| Std Dev | 4.983 |

**Gaus mean : 31.9880**

**Gaus Width: 3.6646**



# B0L002S, U1-ch7, adc1

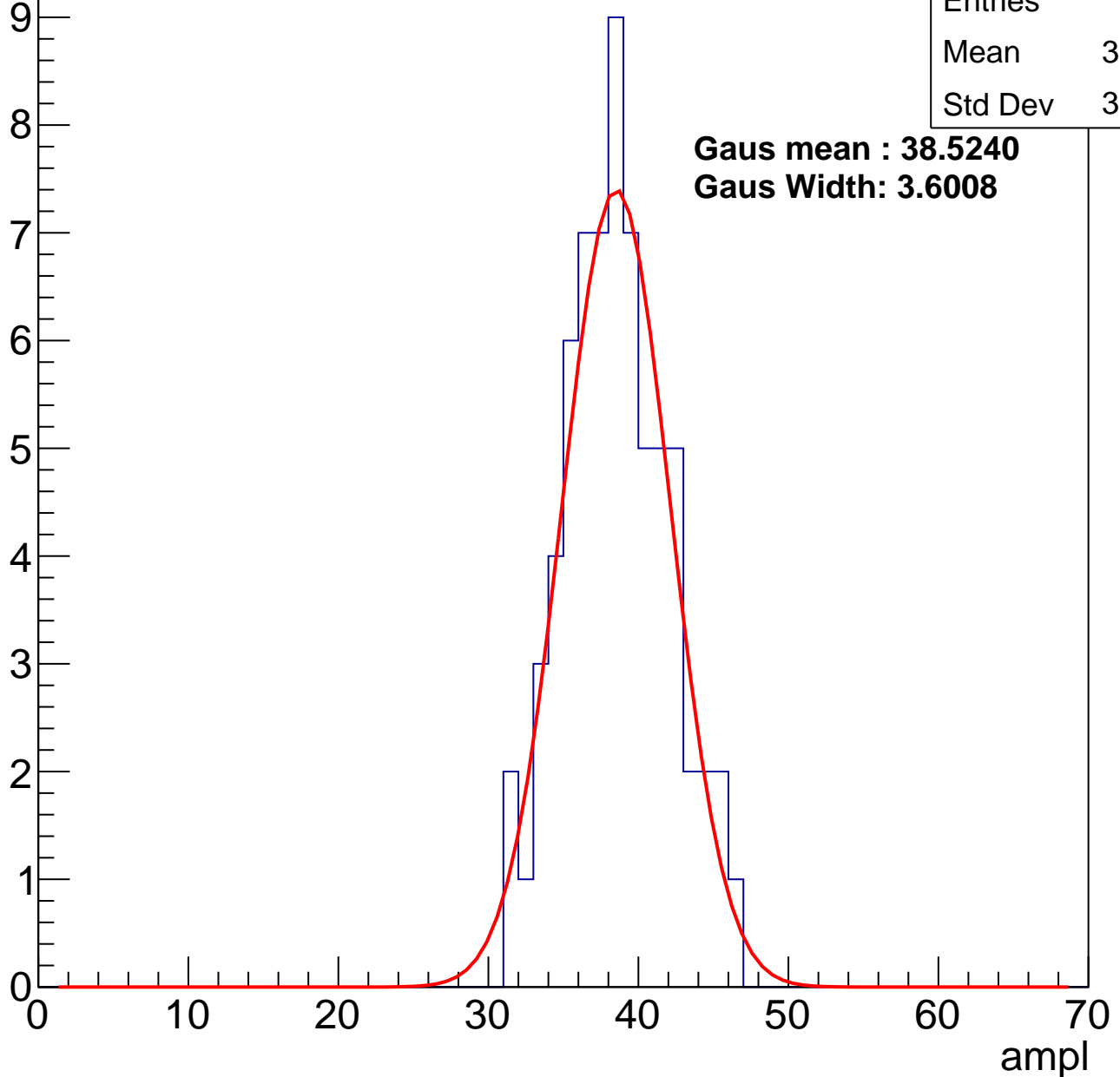
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 38.09 |
| Std Dev | 3.425 |

**Gaus mean : 38.5240**

**Gaus Width: 3.6008**



# B0L002S, U1-ch7, adc2

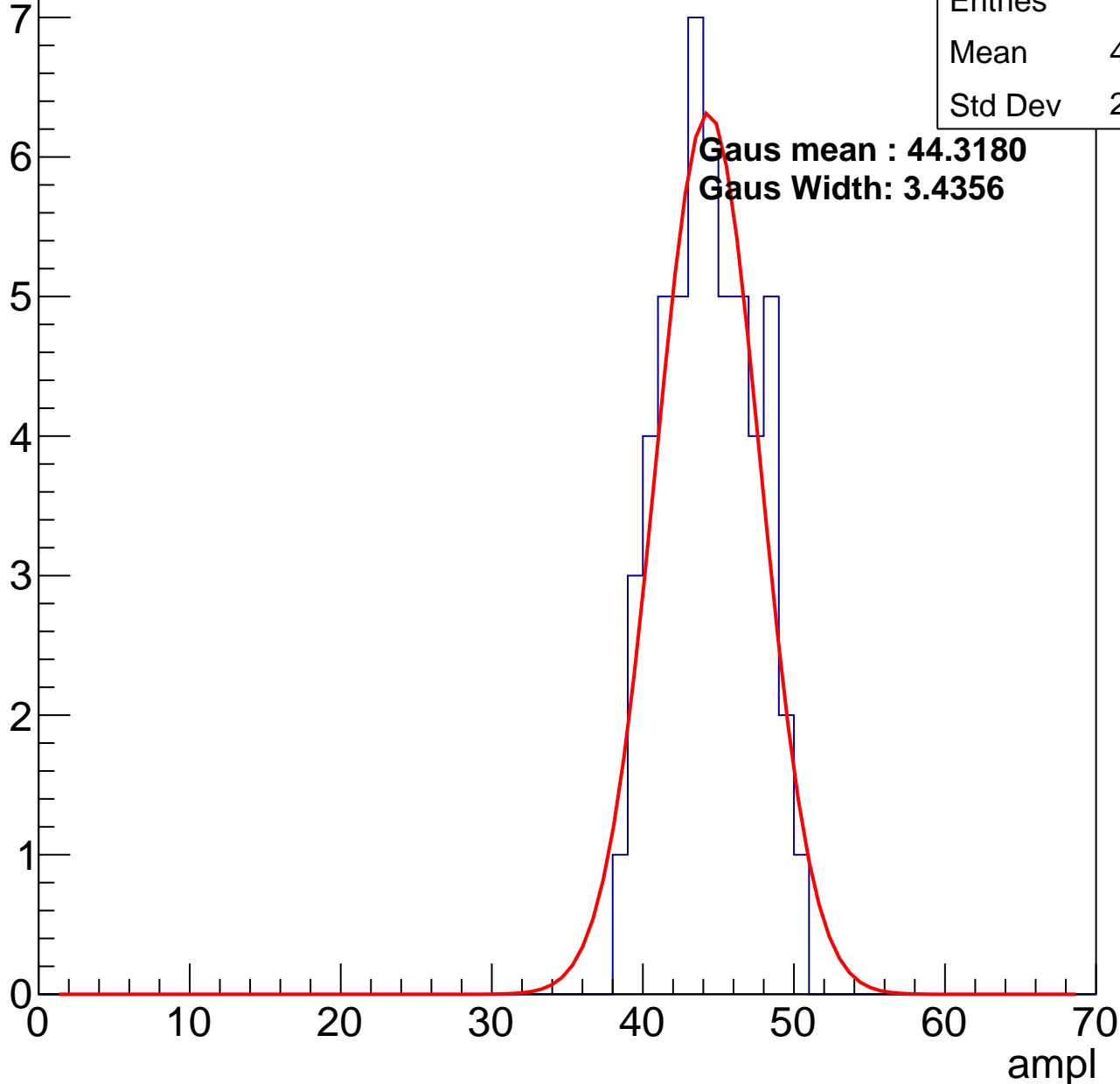
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 43.89 |
| Std Dev | 2.988 |

**Gaus mean : 44.3180**

**Gaus Width: 3.4356**

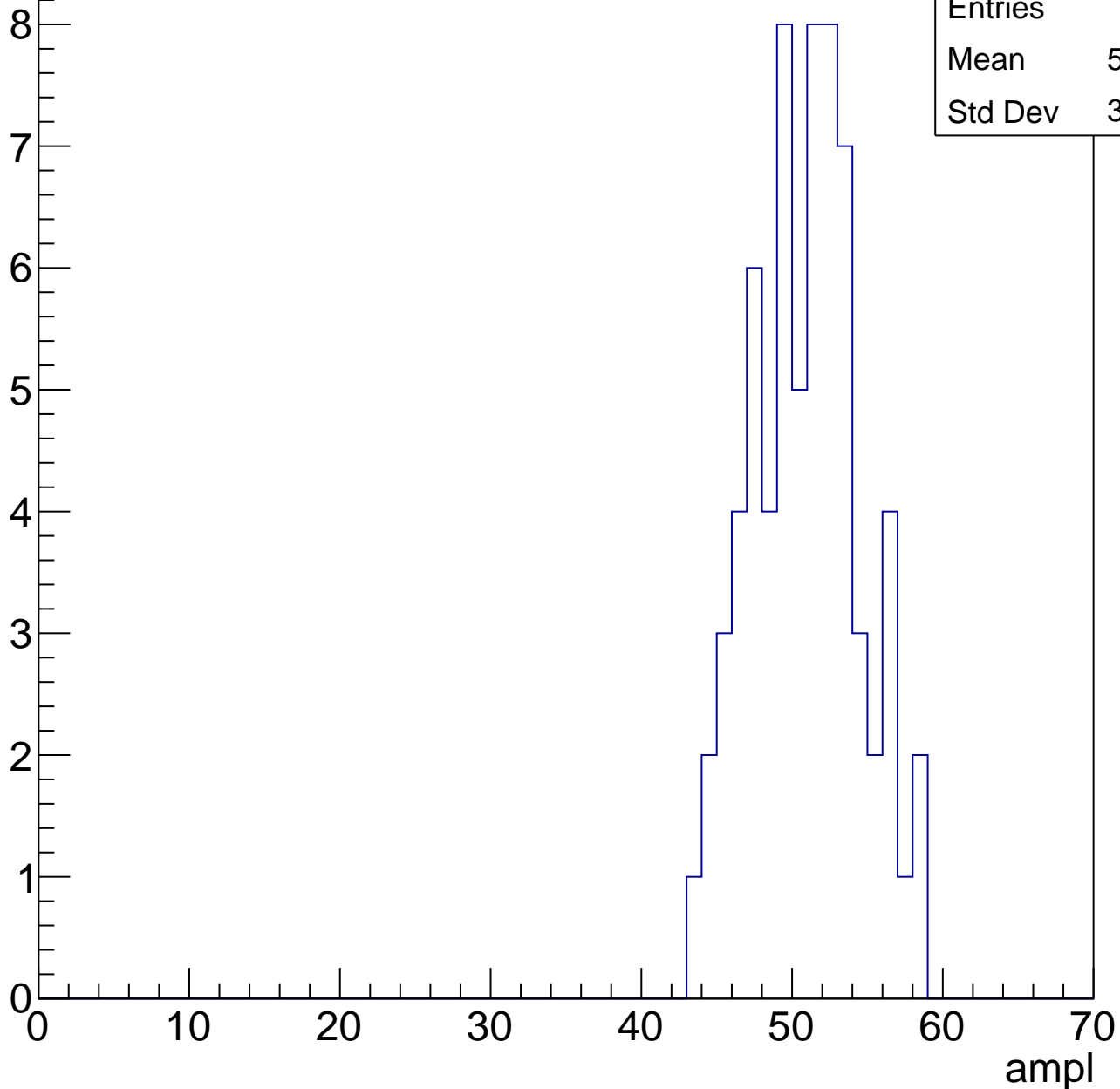


# B0L002S, U1-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

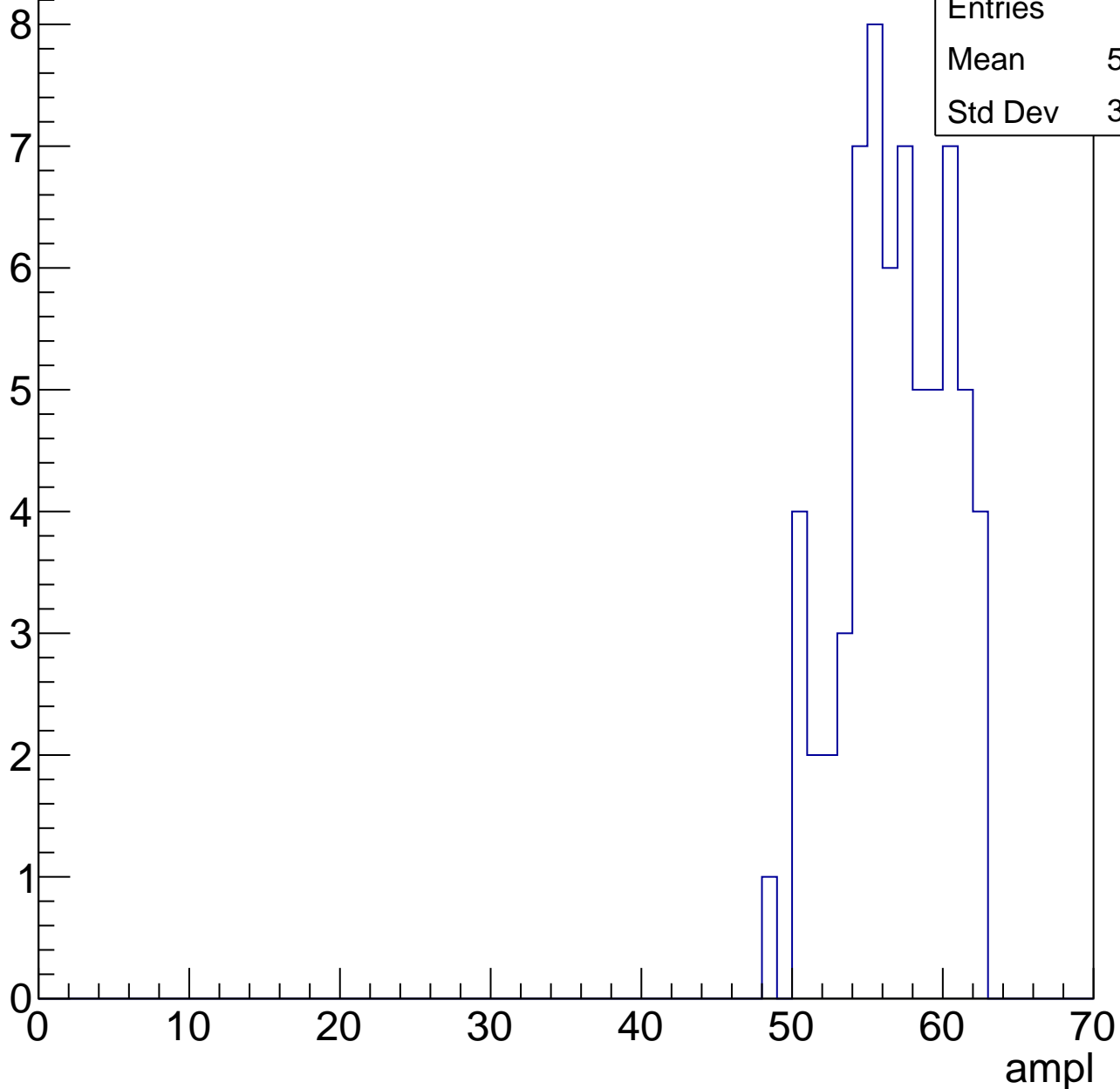
|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 50.44 |
| Std Dev | 3.529 |



# B0L002S, U1-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

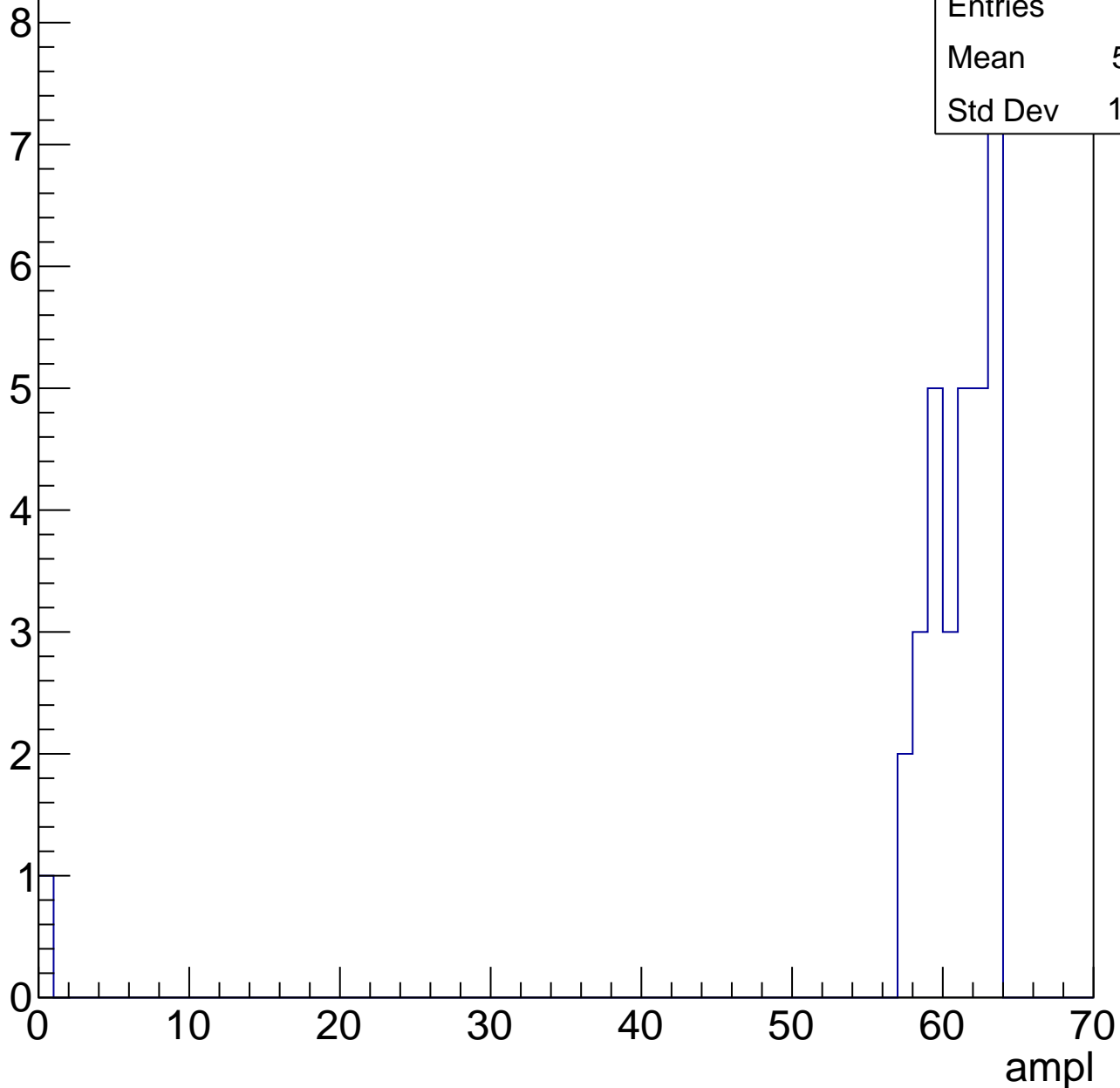


|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 56.42 |
| Std Dev | 3.469 |

# B0L002S, U1-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

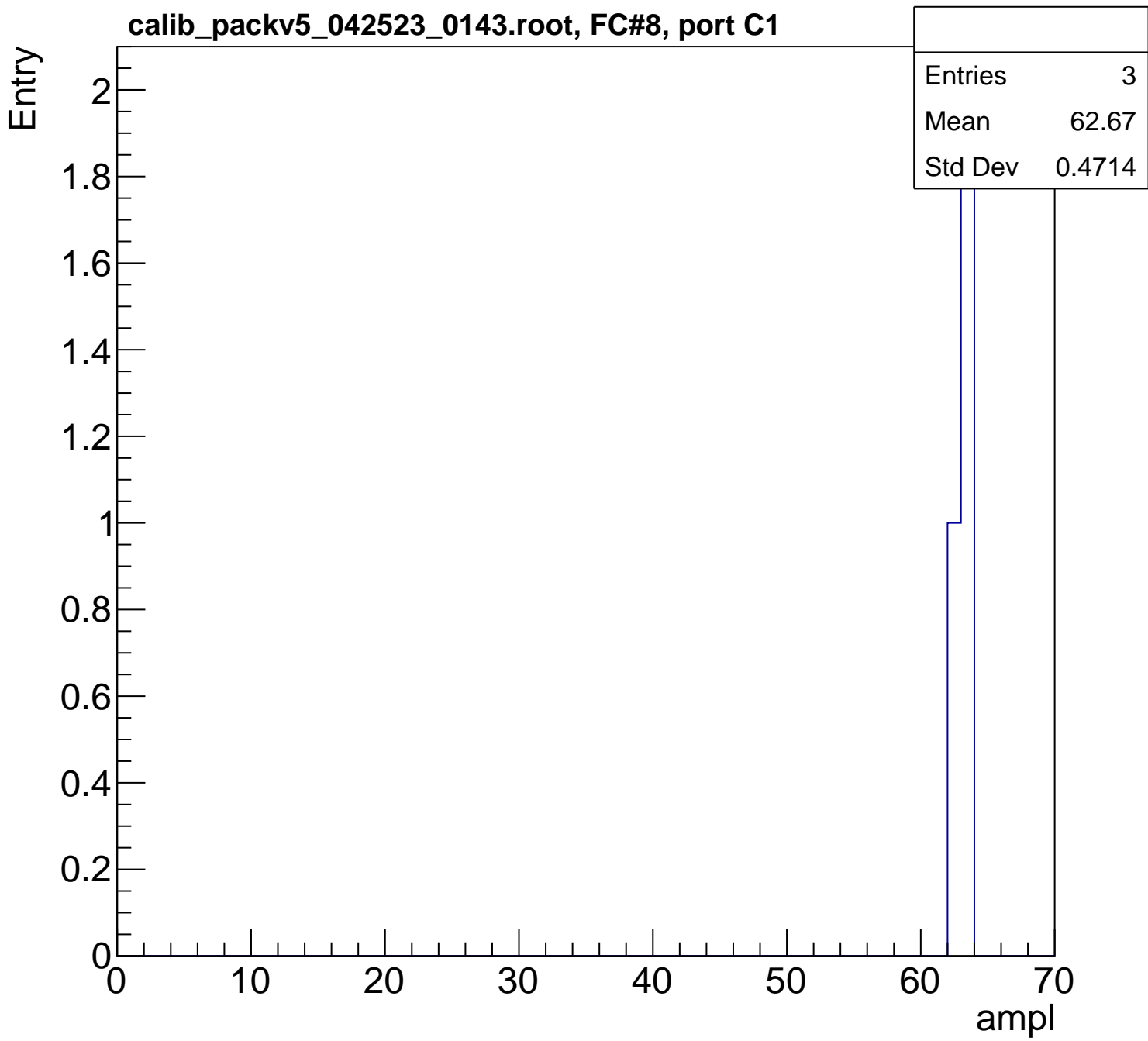
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 3      |
| Mean    | 62.67  |
| Std Dev | 0.4714 |

0 10 20 30 40 50 60 70

ampl





# B0L002S, U1-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch8, adc0

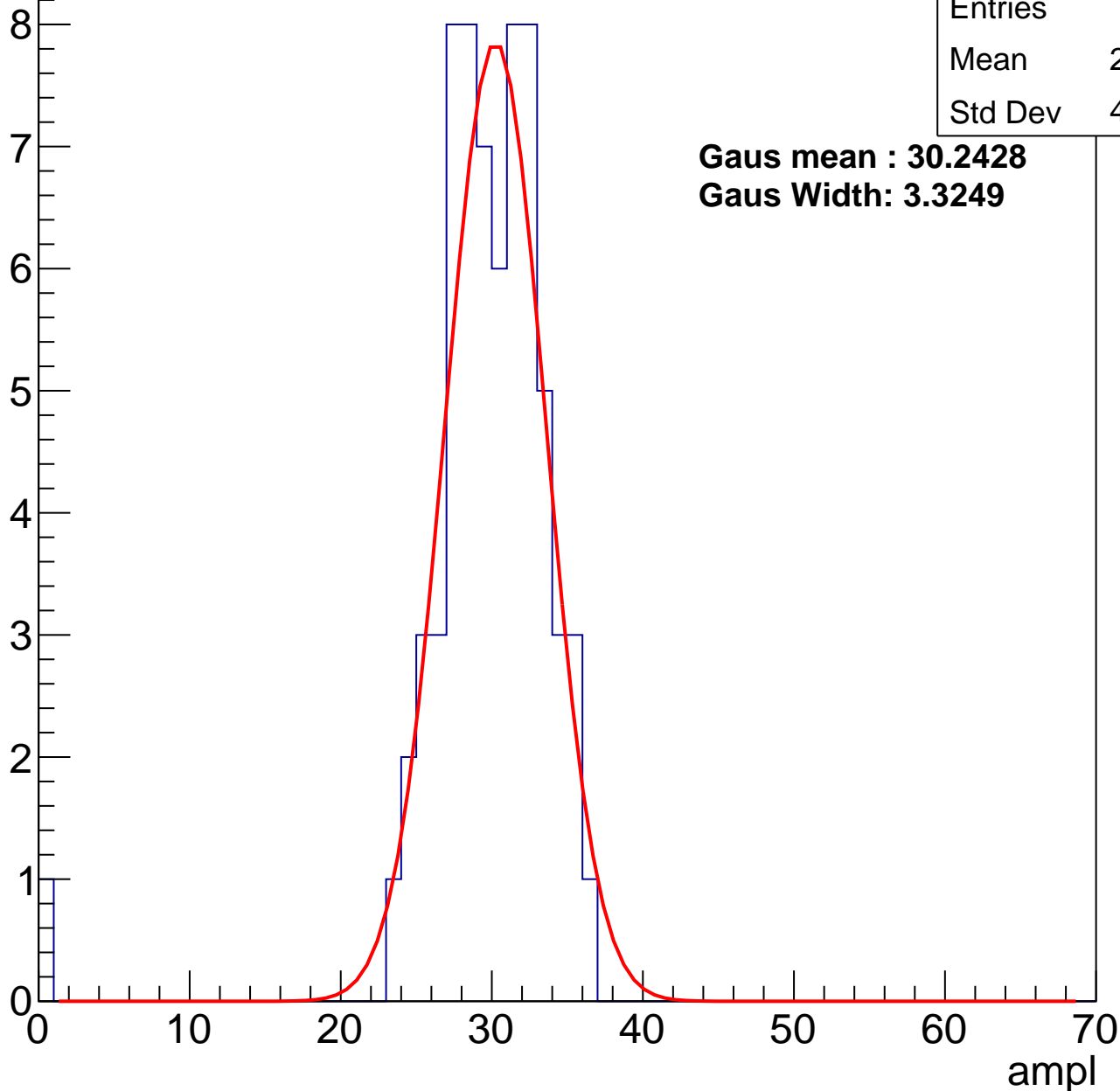
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 29.24 |
| Std Dev | 4.668 |

**Gaus mean : 30.2428**

**Gaus Width: 3.3249**



# B0L002S, U1-ch8, adc1

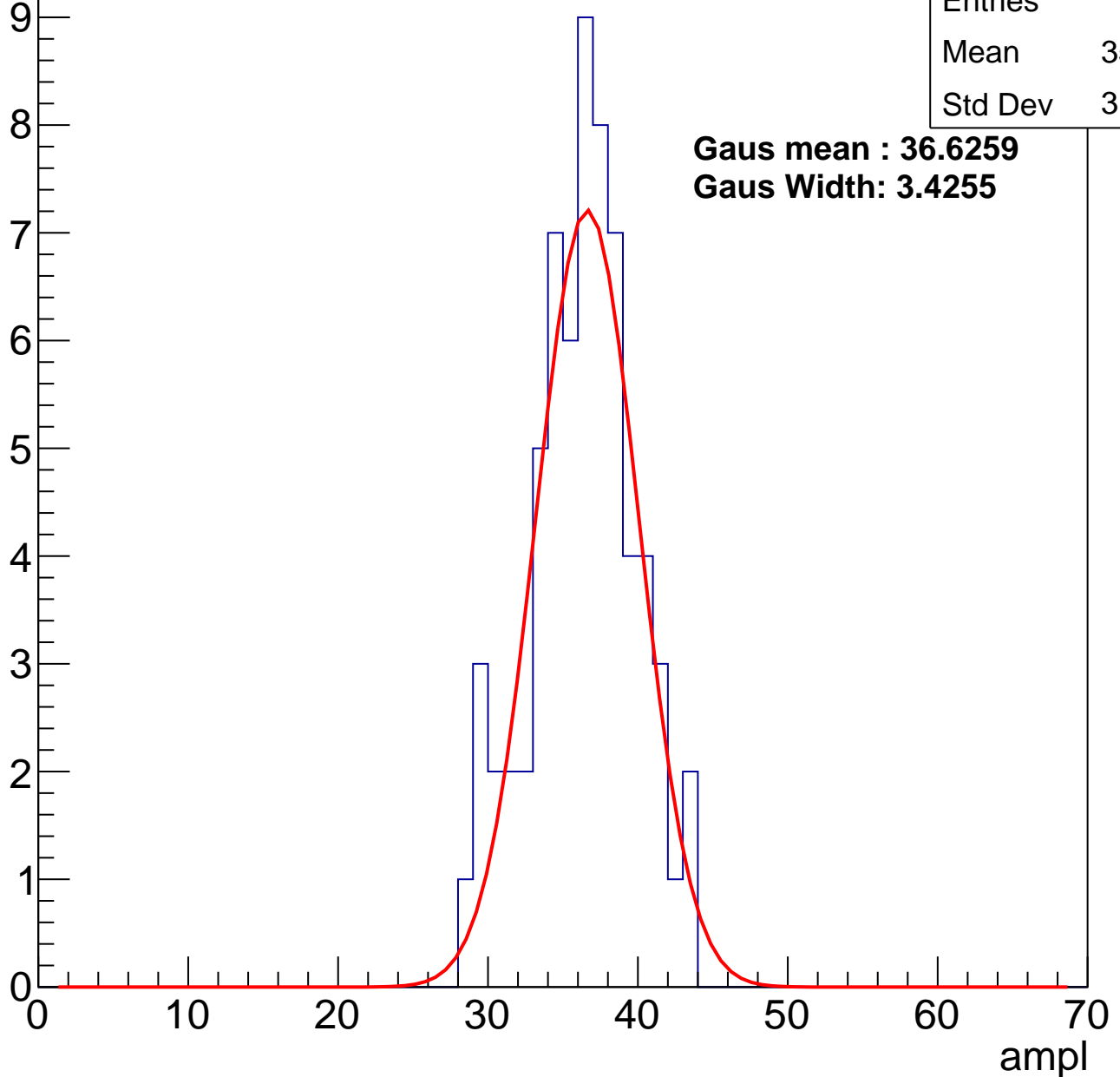
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 35.86 |
| Std Dev | 3.455 |

**Gaus mean : 36.6259**

**Gaus Width: 3.4255**



# B0L002S, U1-ch8, adc2

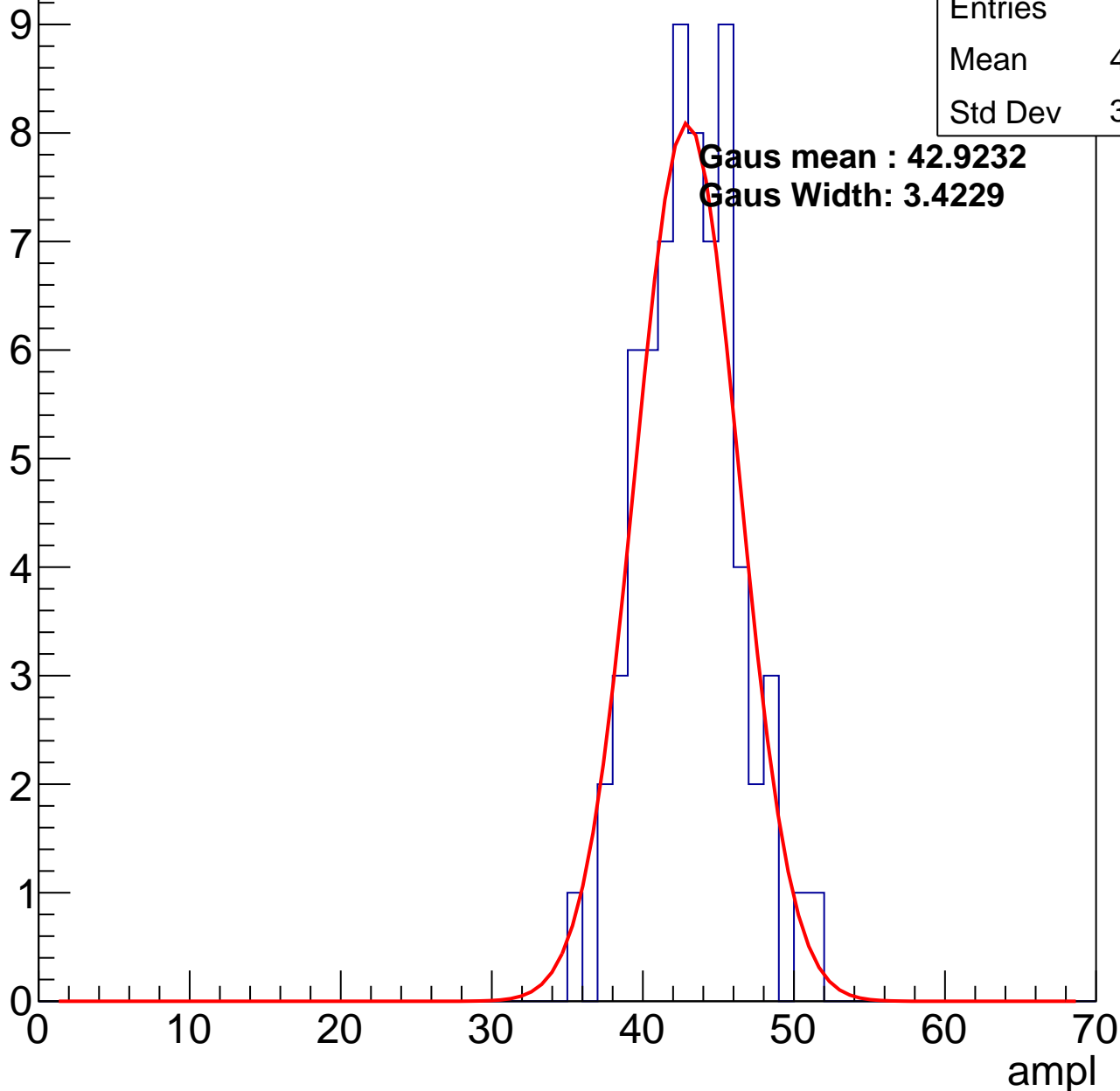
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 42.64 |
| Std Dev | 3.158 |

**Gaus mean : 42.9232**

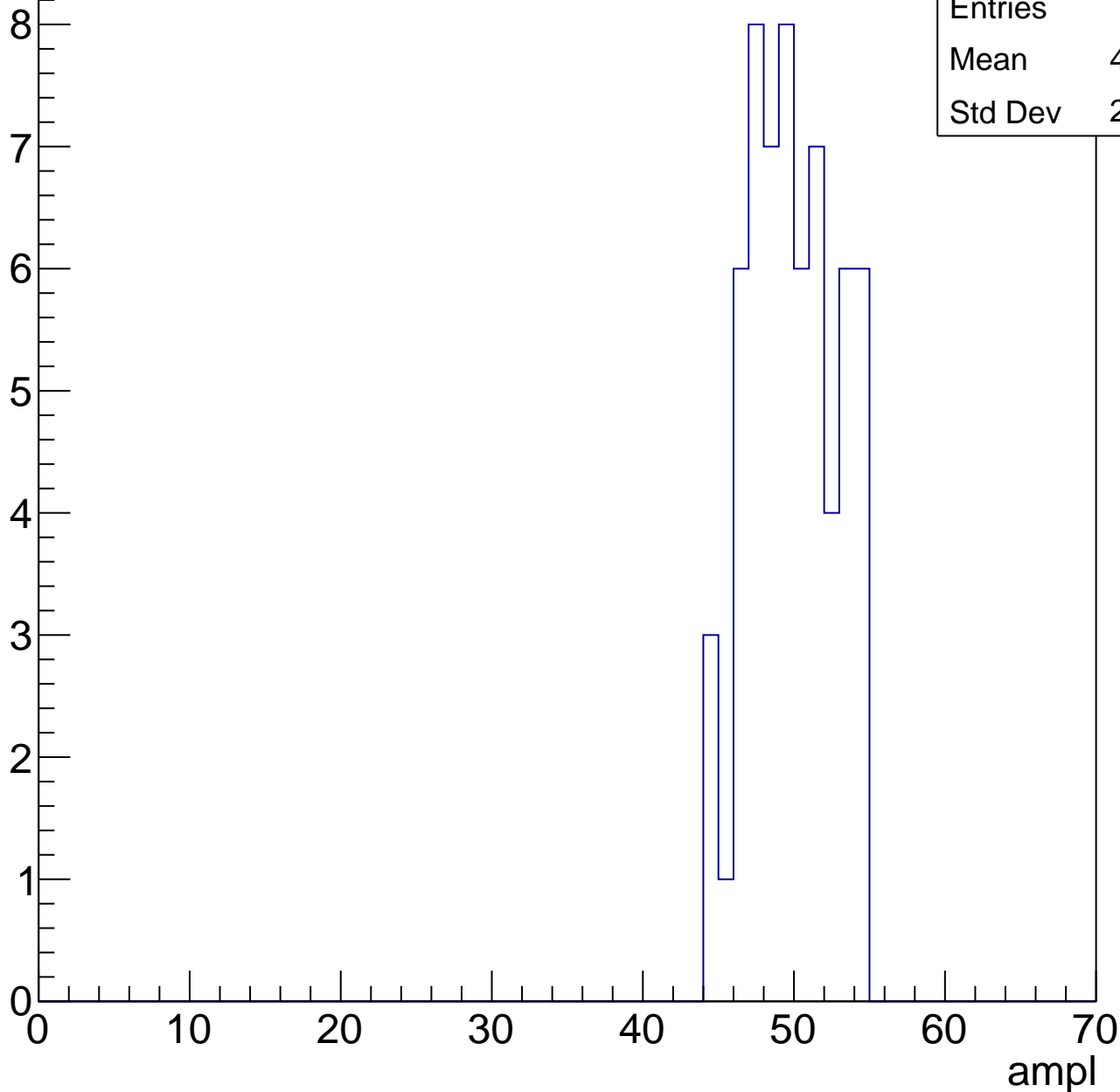
**Gaus Width: 3.4229**



# B0L002S, U1-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



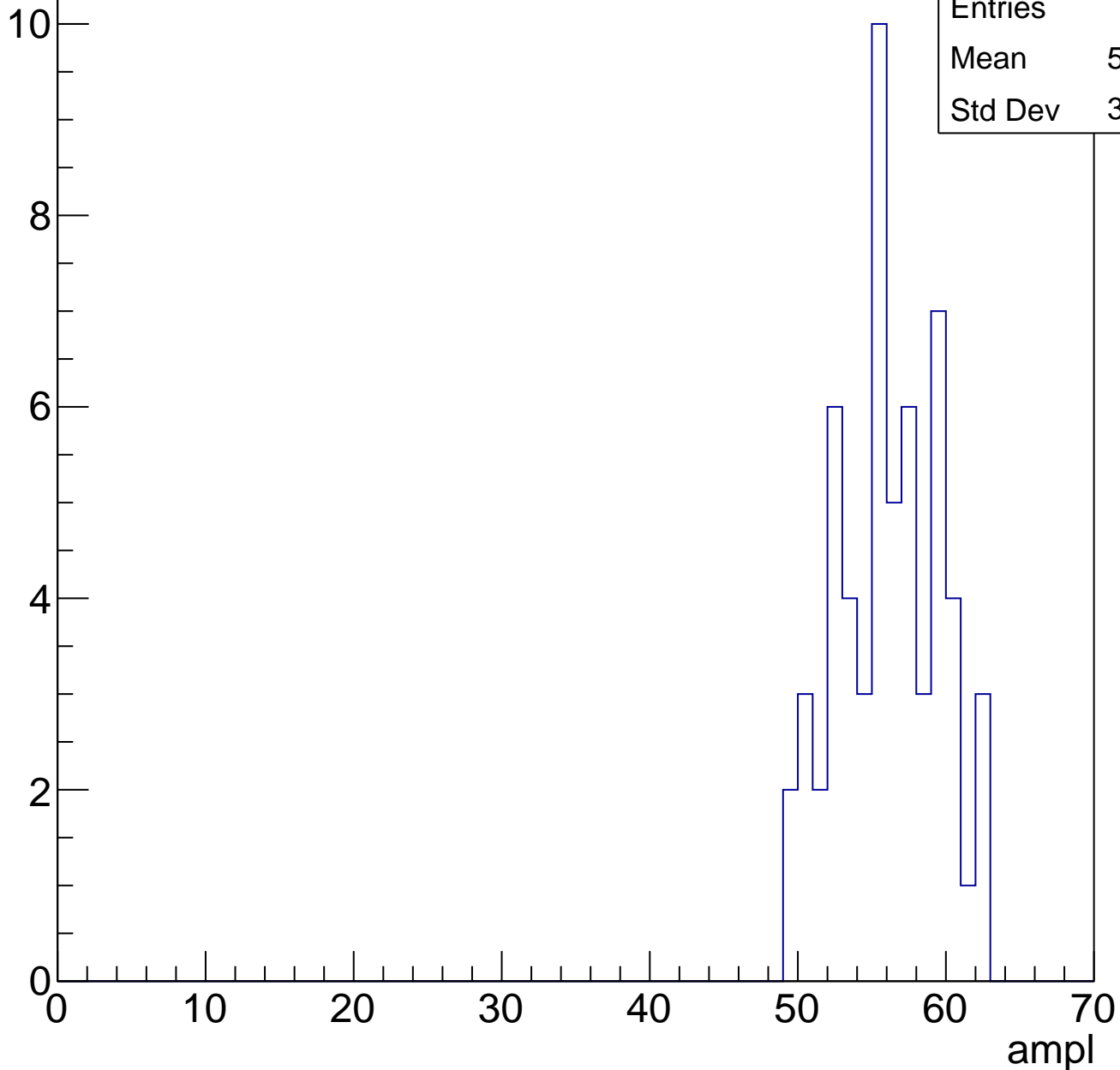
|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 49.42 |
| Std Dev | 2.809 |

# B0L002S, U1-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 55.63 |
| Std Dev | 3.394 |

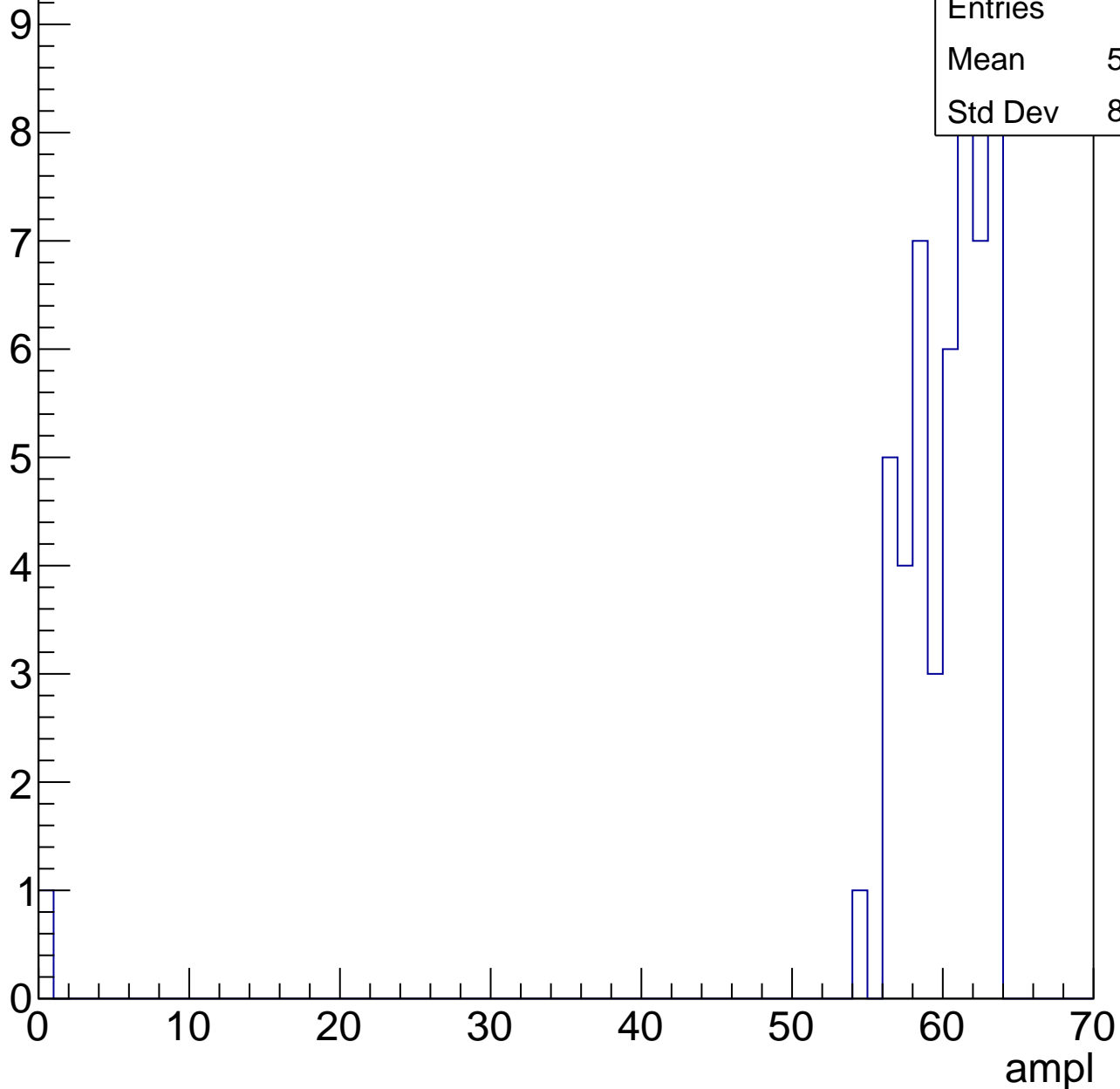
Entry



# B0L002S, U1-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 63 |
| Std Dev | 0  |

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch9, adc0

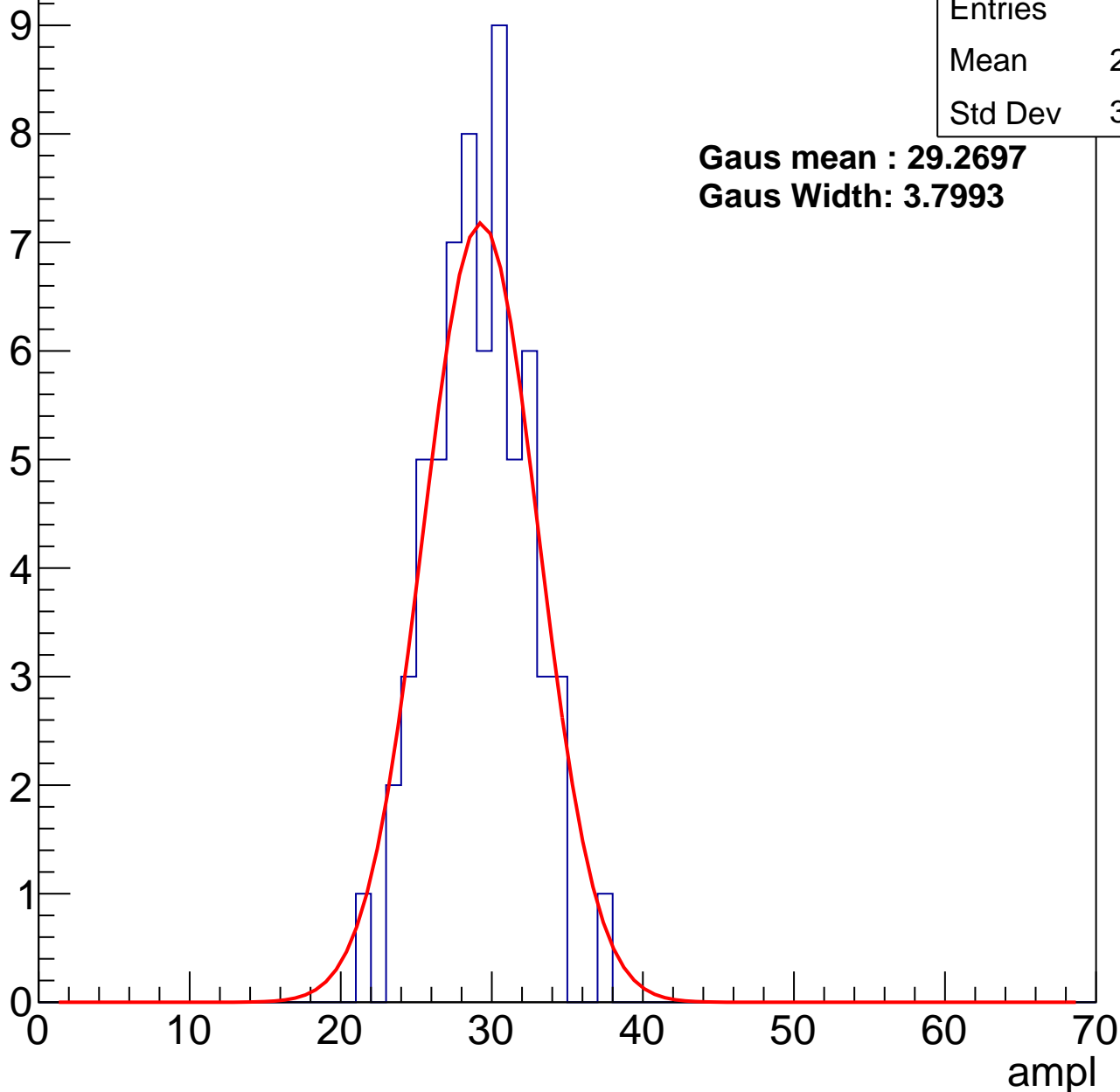
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 28.69 |
| Std Dev | 3.152 |

**Gaus mean : 29.2697**

**Gaus Width: 3.7993**



# B0L002S, U1-ch9, adc1

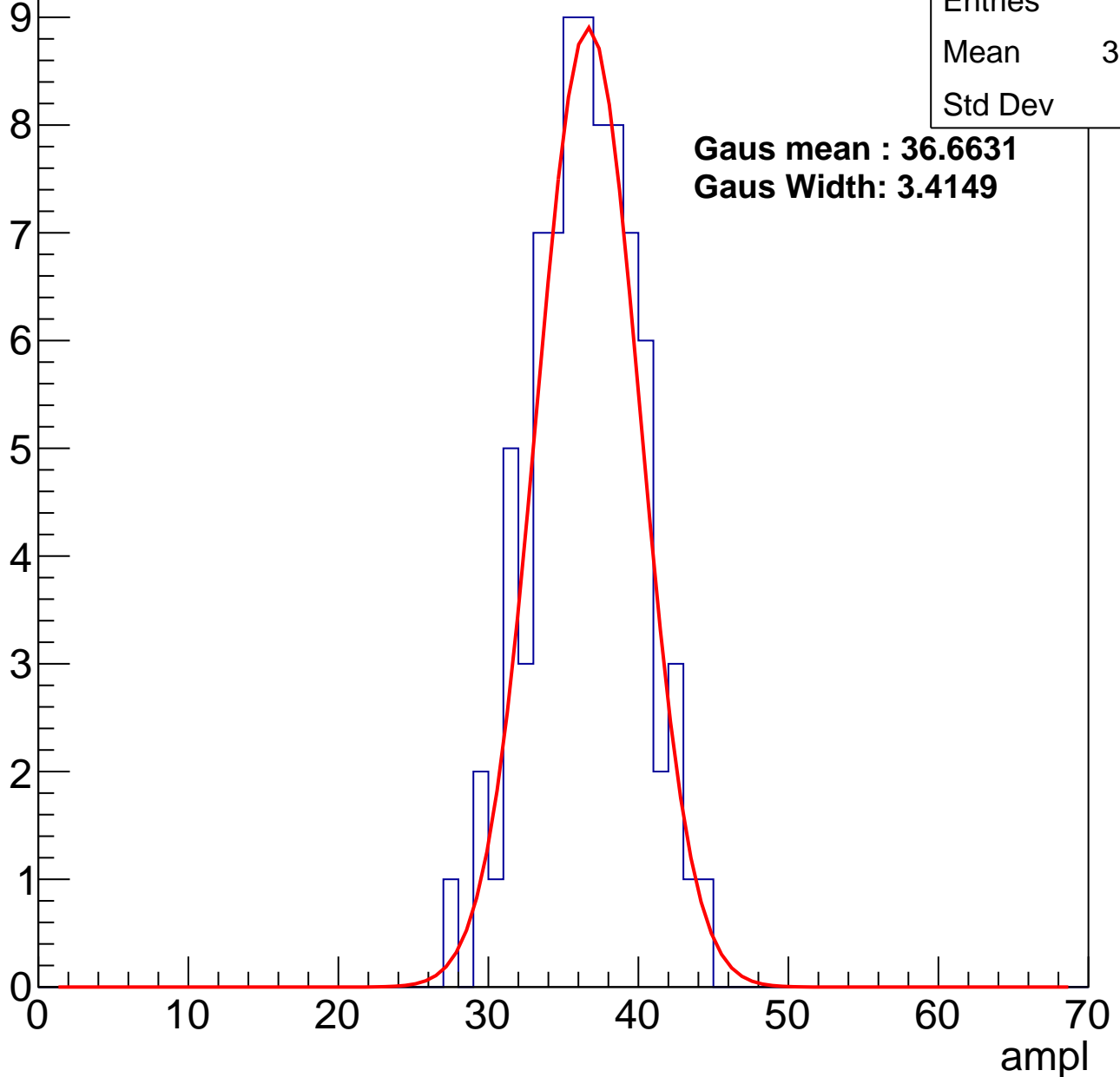
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 36.02 |
| Std Dev | 3.46  |

**Gaus mean : 36.6631**

**Gaus Width: 3.4149**



# B0L002S, U1-ch9, adc2

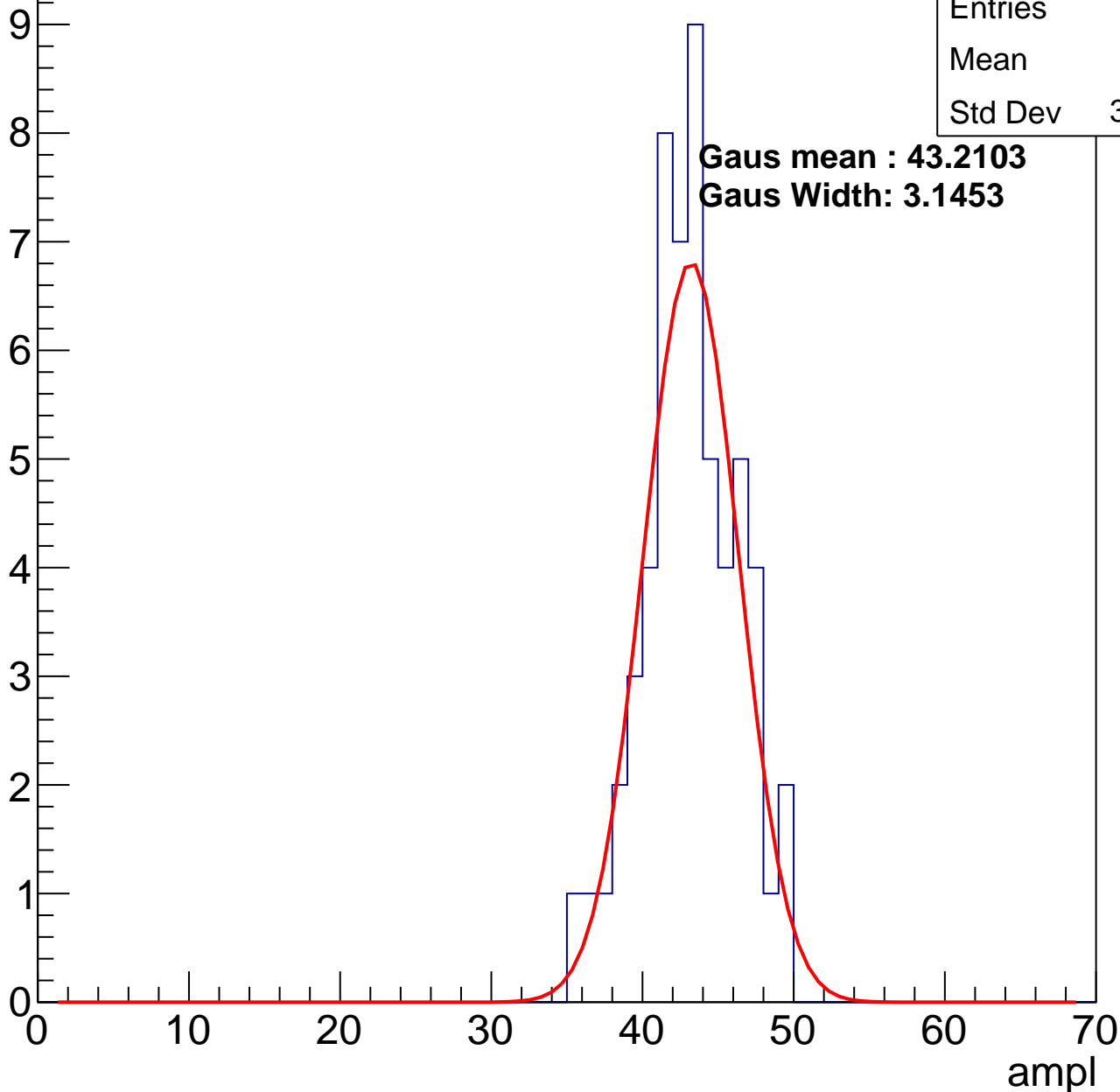
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 42.7  |
| Std Dev | 3.089 |

**Gaus mean : 43.2103**

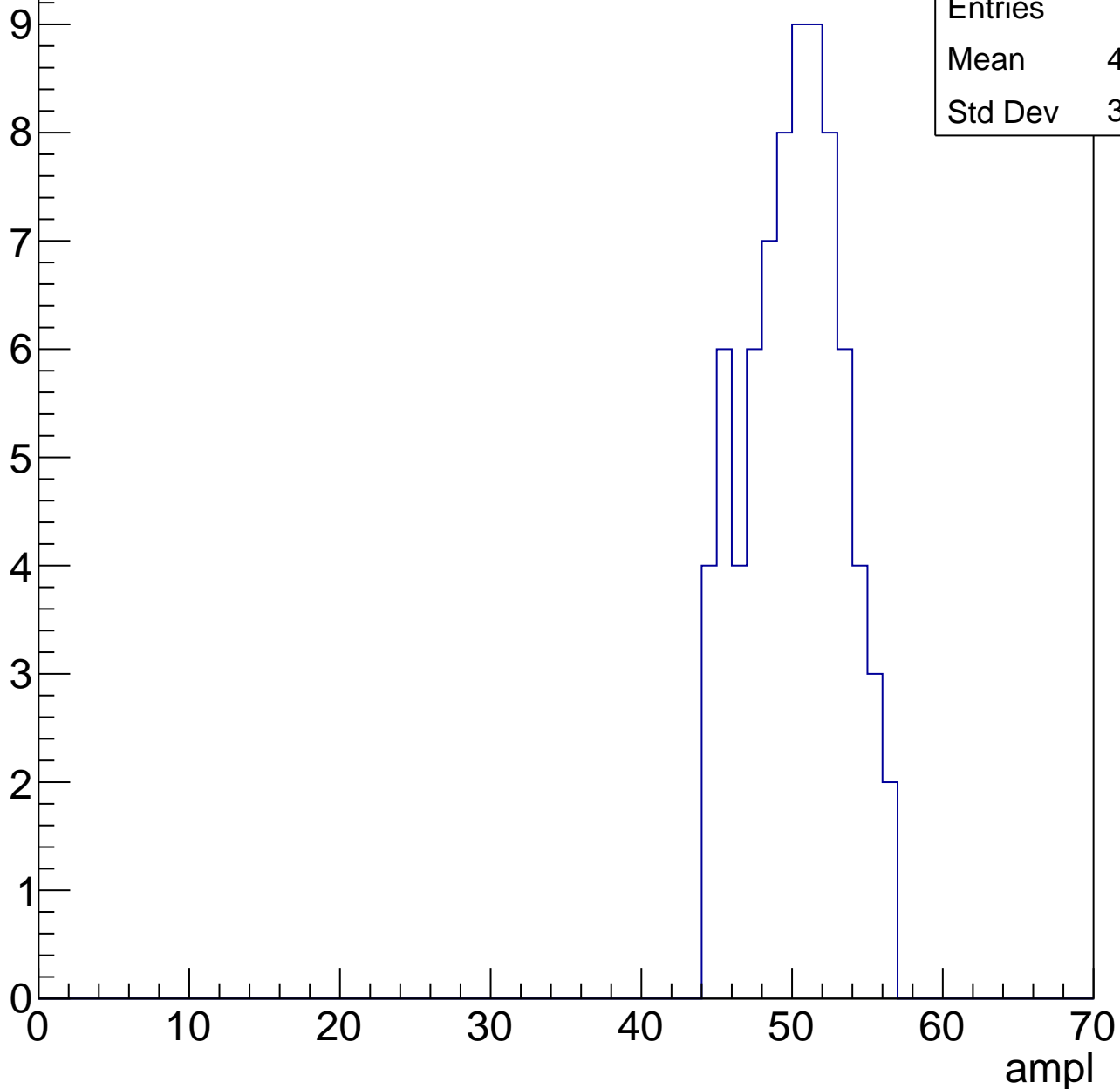
**Gaus Width: 3.1453**



# B0L002S, U1-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

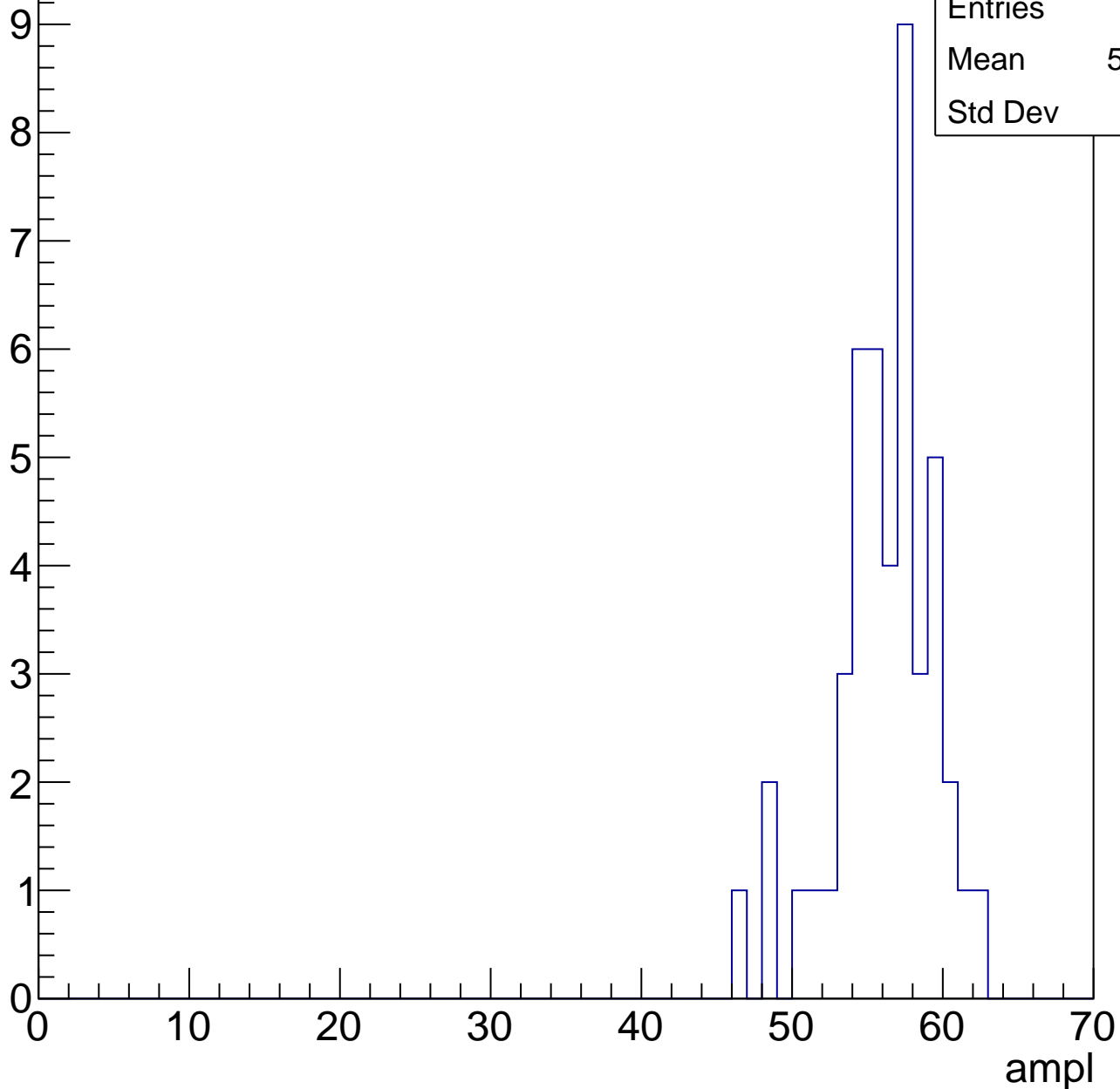


# B0L002S, U1-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

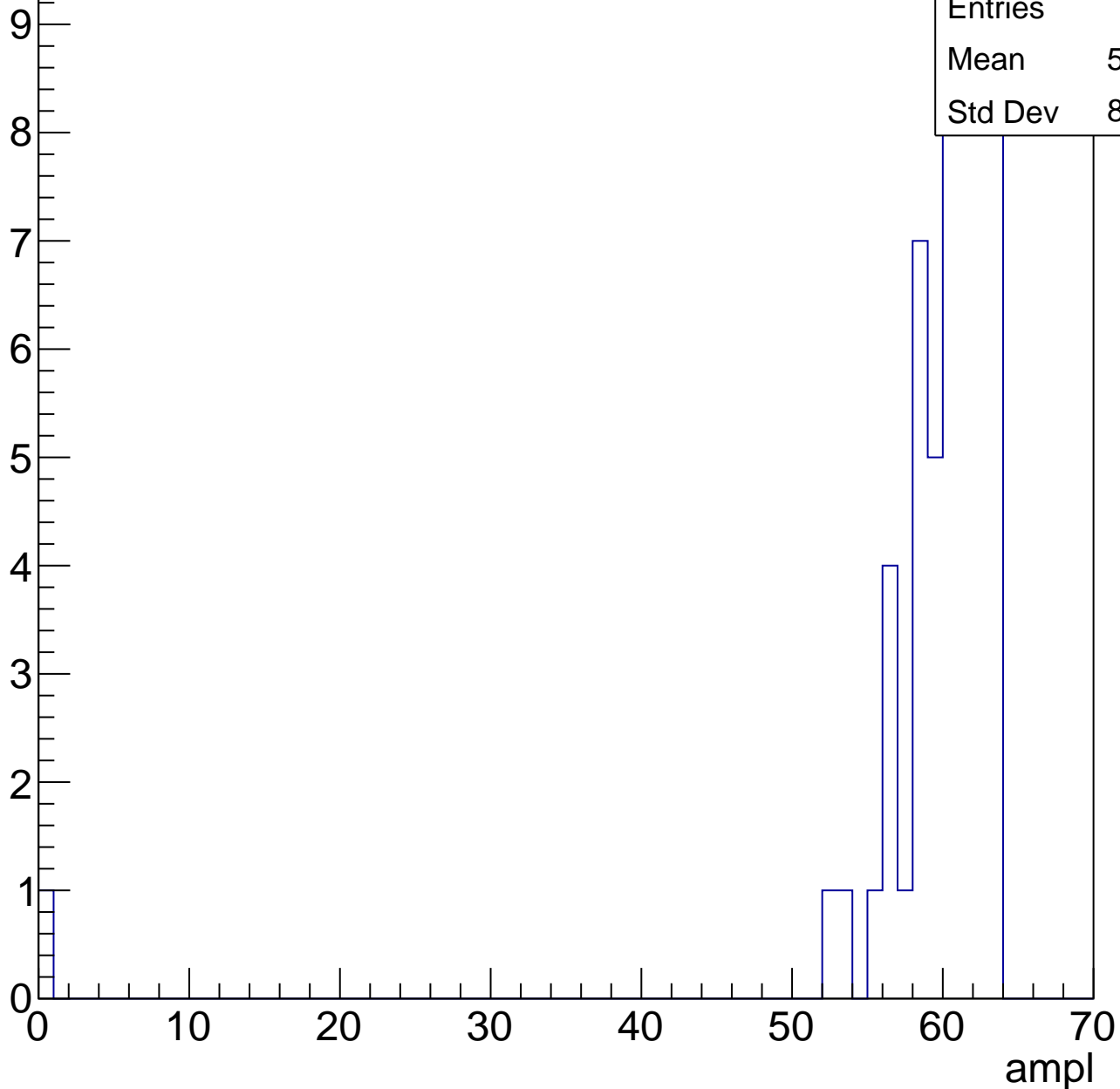
|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 55.59 |
| Std Dev | 3.34  |



# B0L002S, U1-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch10, adc0

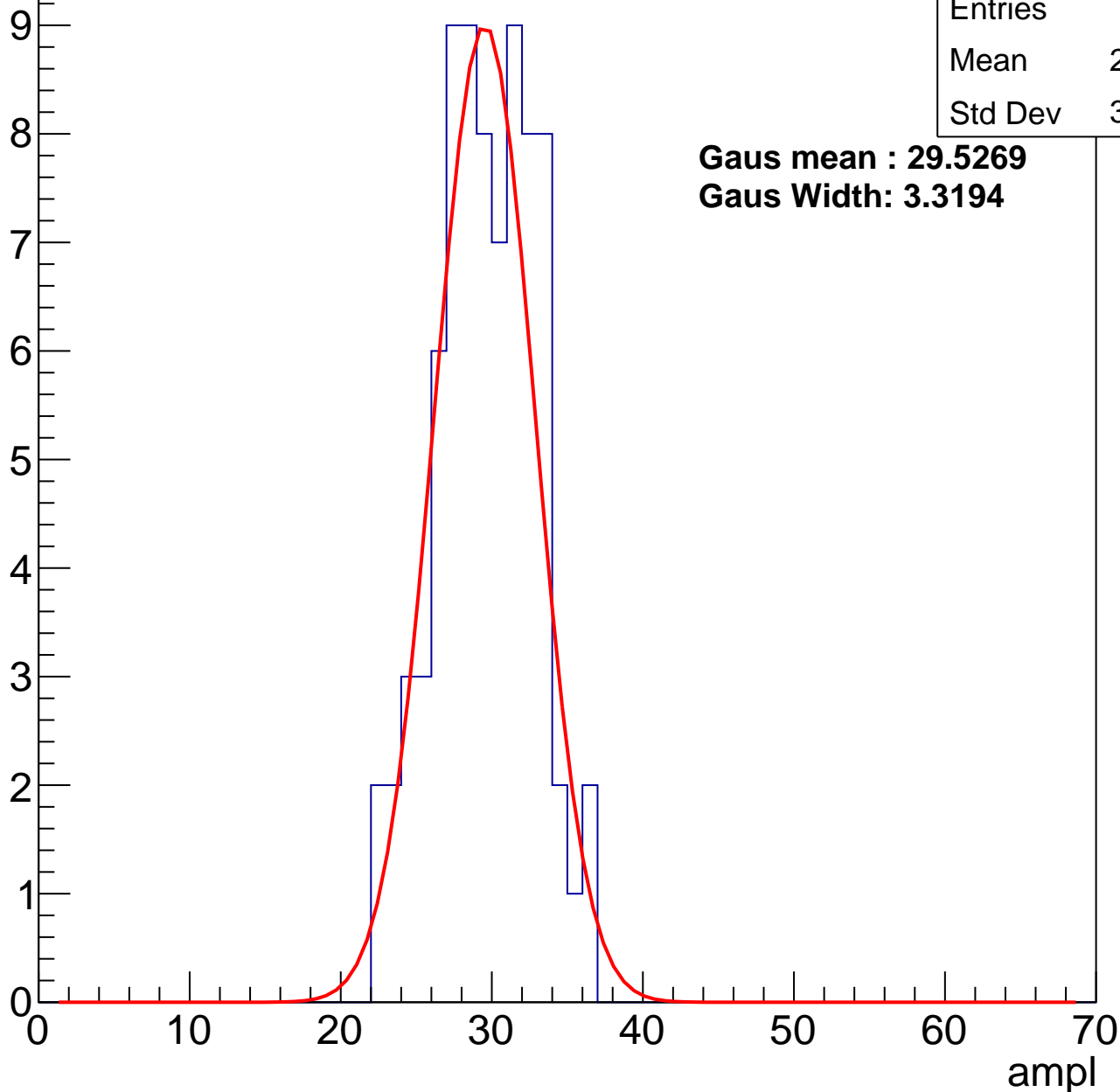
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 29.16 |
| Std Dev | 3.216 |

**Gaus mean : 29.5269**

**Gaus Width: 3.3194**



# B0L002S, U1-ch10, adc1

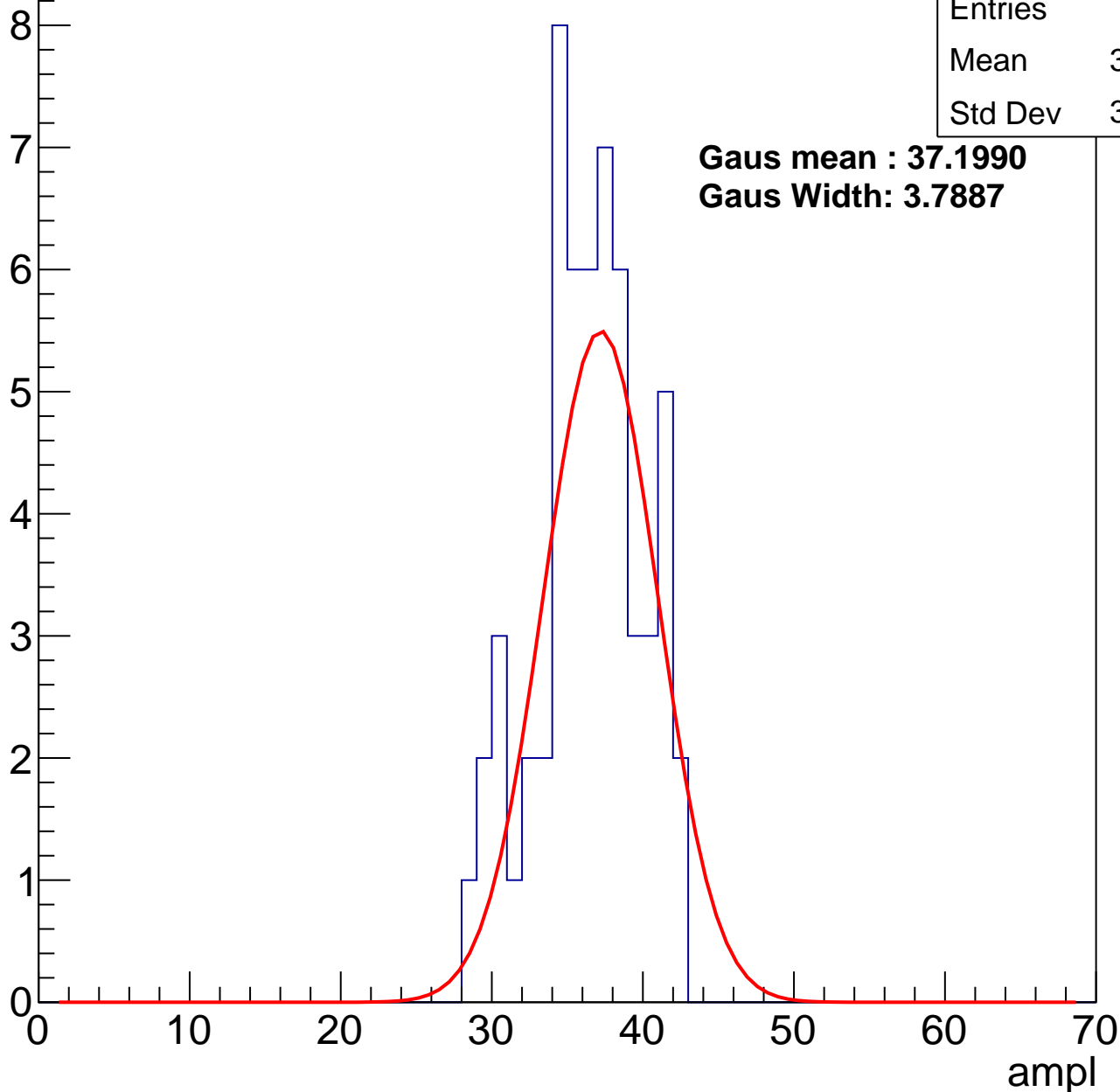
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 35.93 |
| Std Dev | 3.468 |

**Gaus mean : 37.1990**

**Gaus Width: 3.7887**



# B0L002S, U1-ch10, adc2

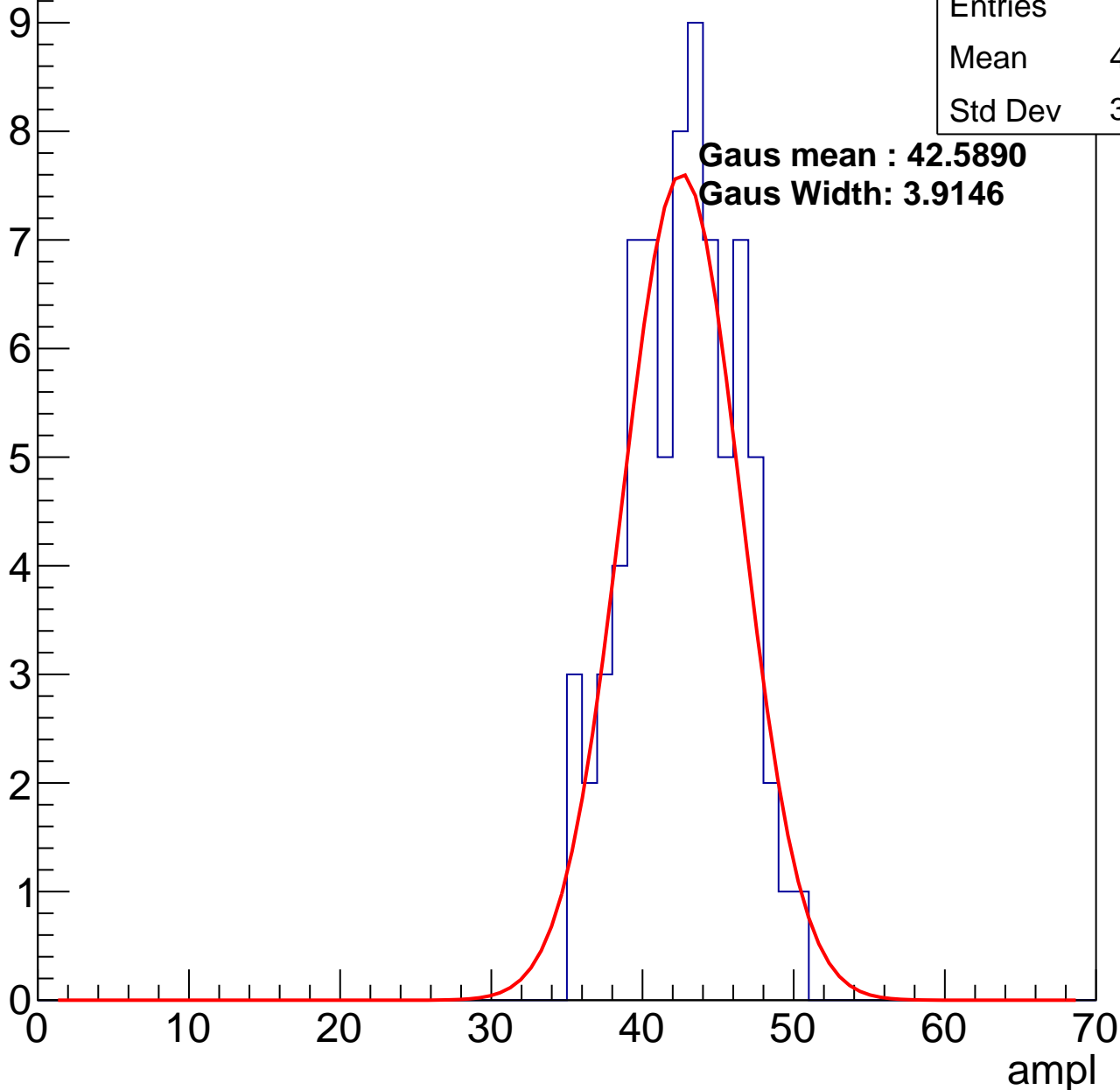
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 42.18 |
| Std Dev | 3.546 |

**Gaus mean : 42.5890**

**Gaus Width: 3.9146**

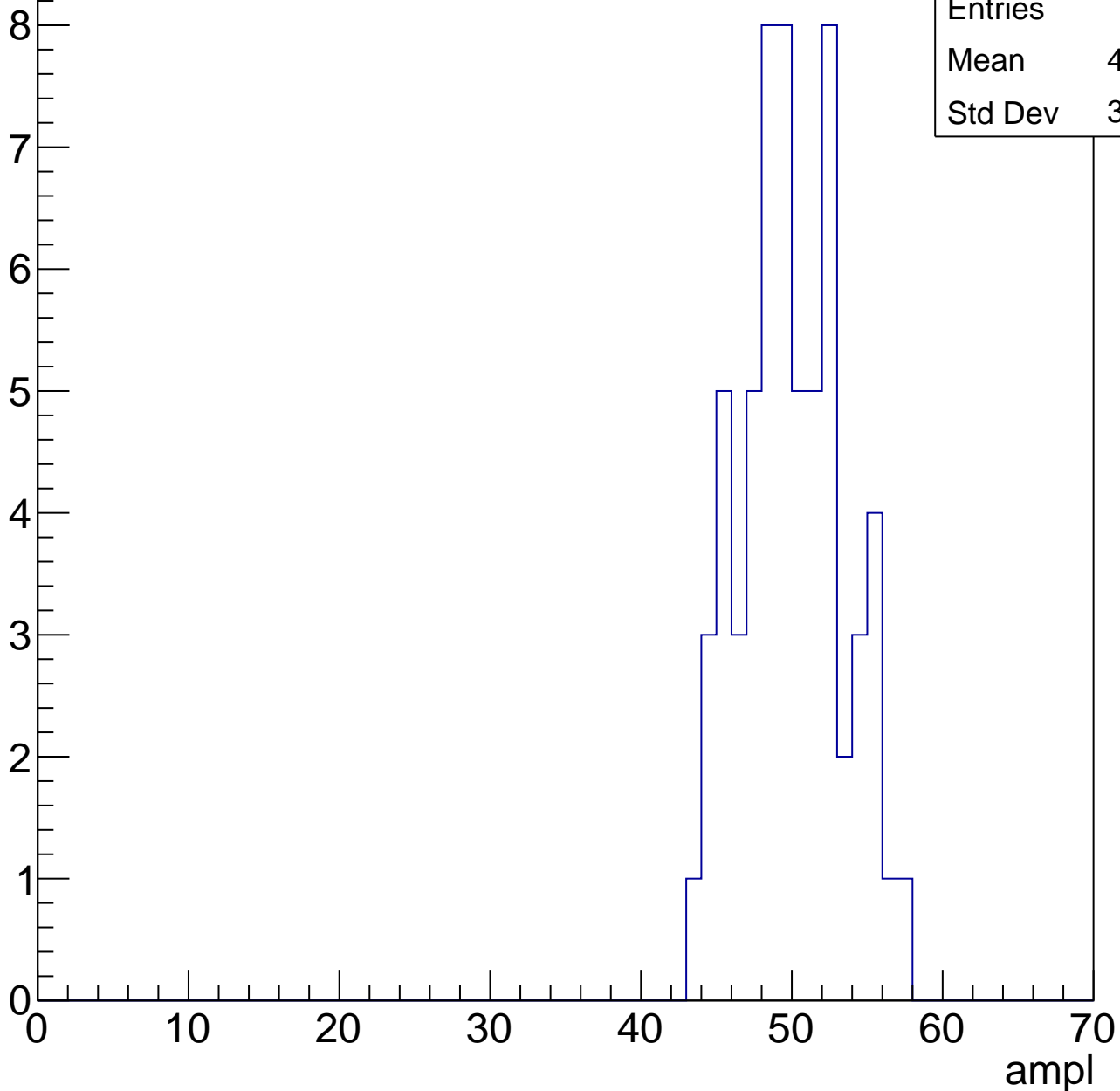


# B0L002S, U1-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 49.53 |
| Std Dev | 3.335 |

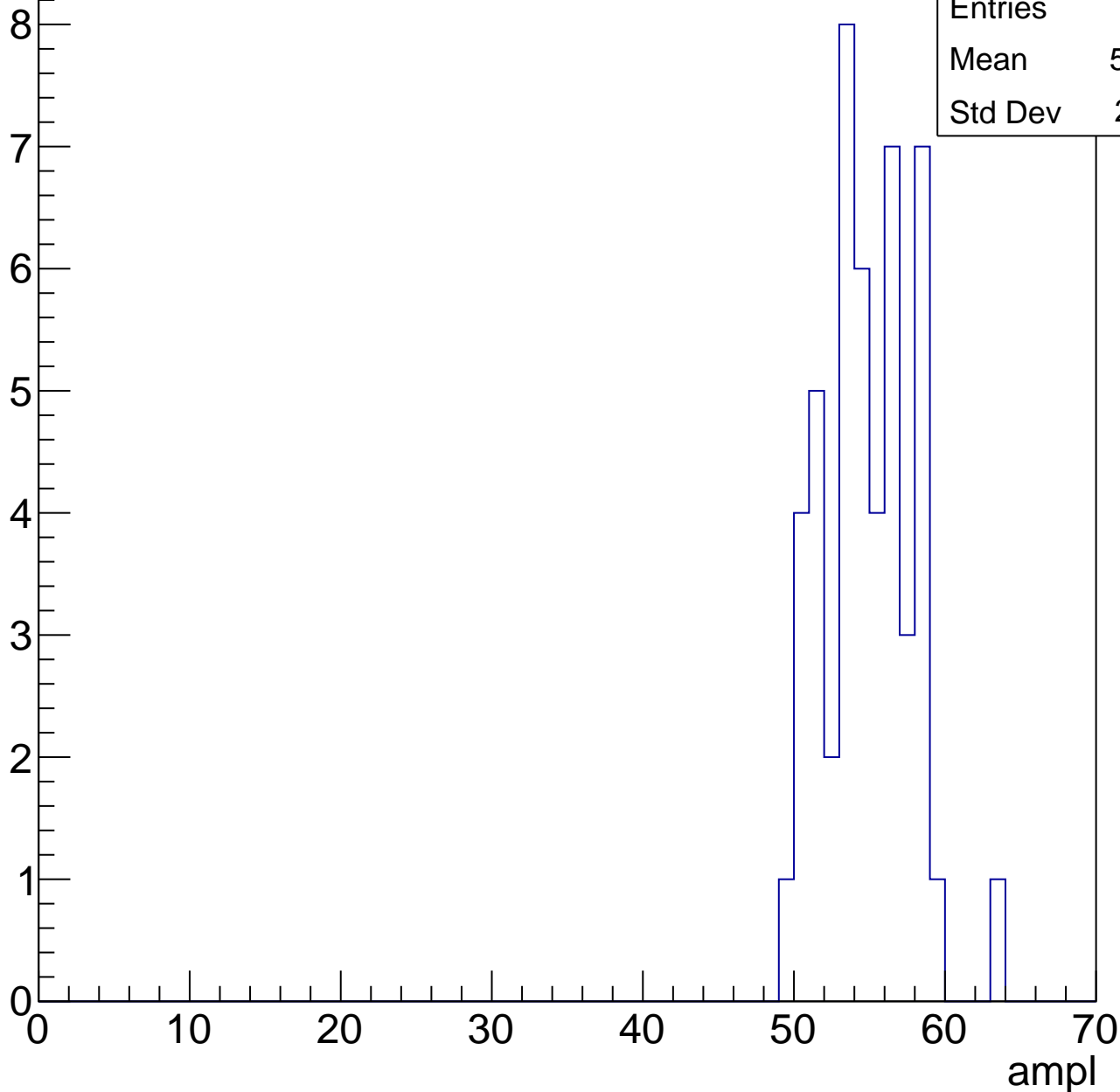


# B0L002S, U1-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 54.43 |
| Std Dev | 2.921 |

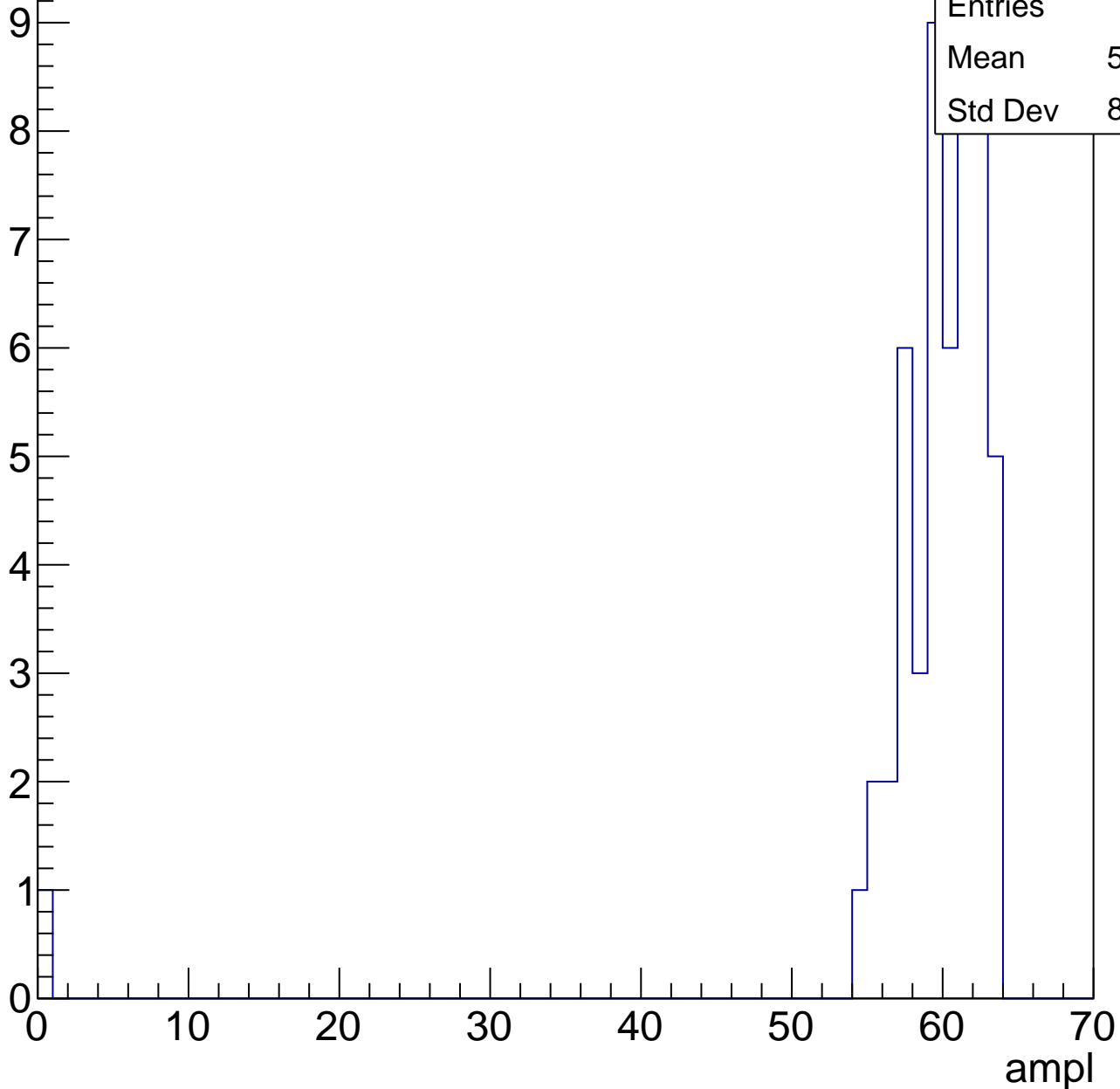


# B0L002S, U1-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

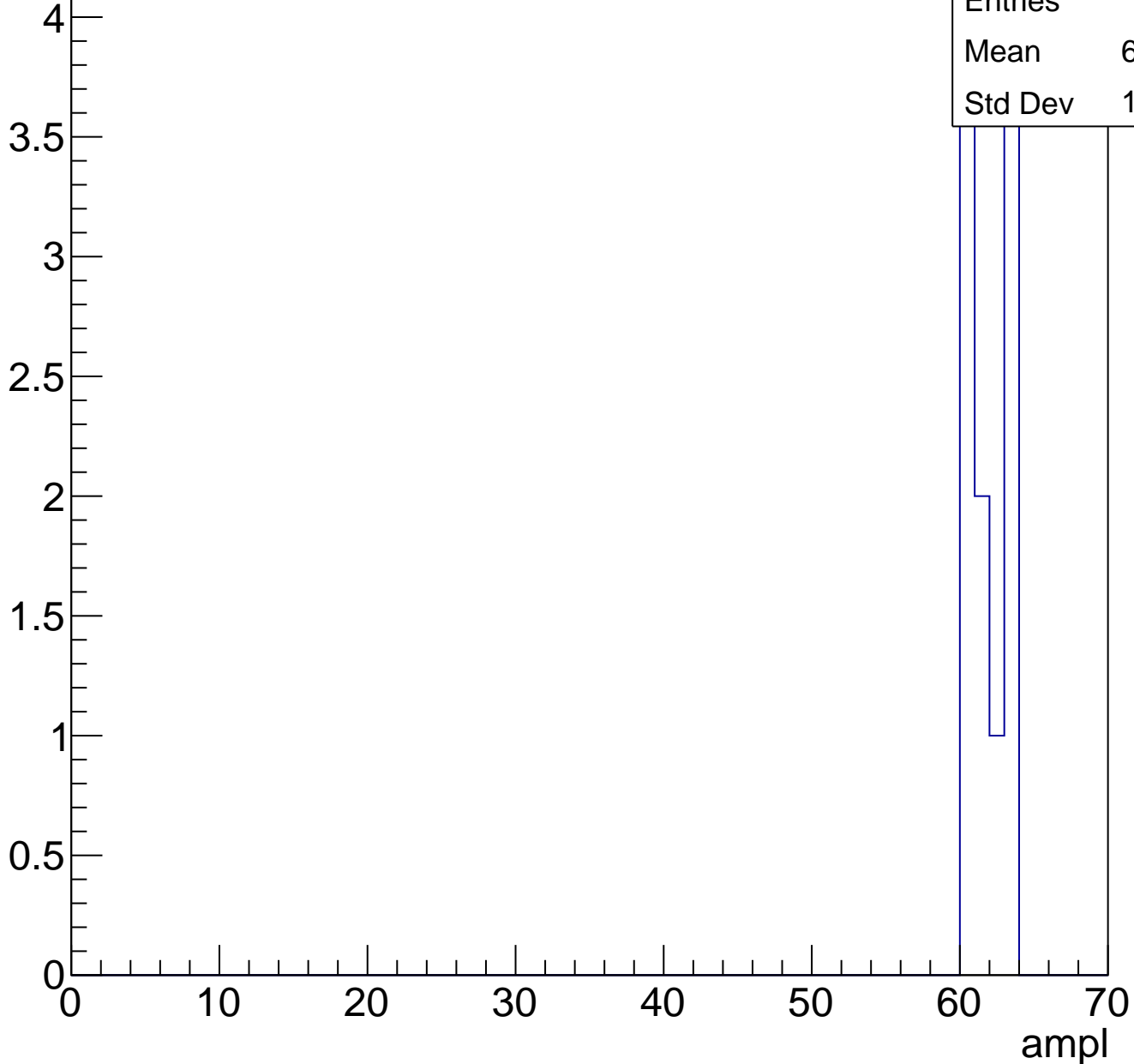
|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 58.54 |
| Std Dev | 8.516 |



# B0L002S, U1-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch11, adc0

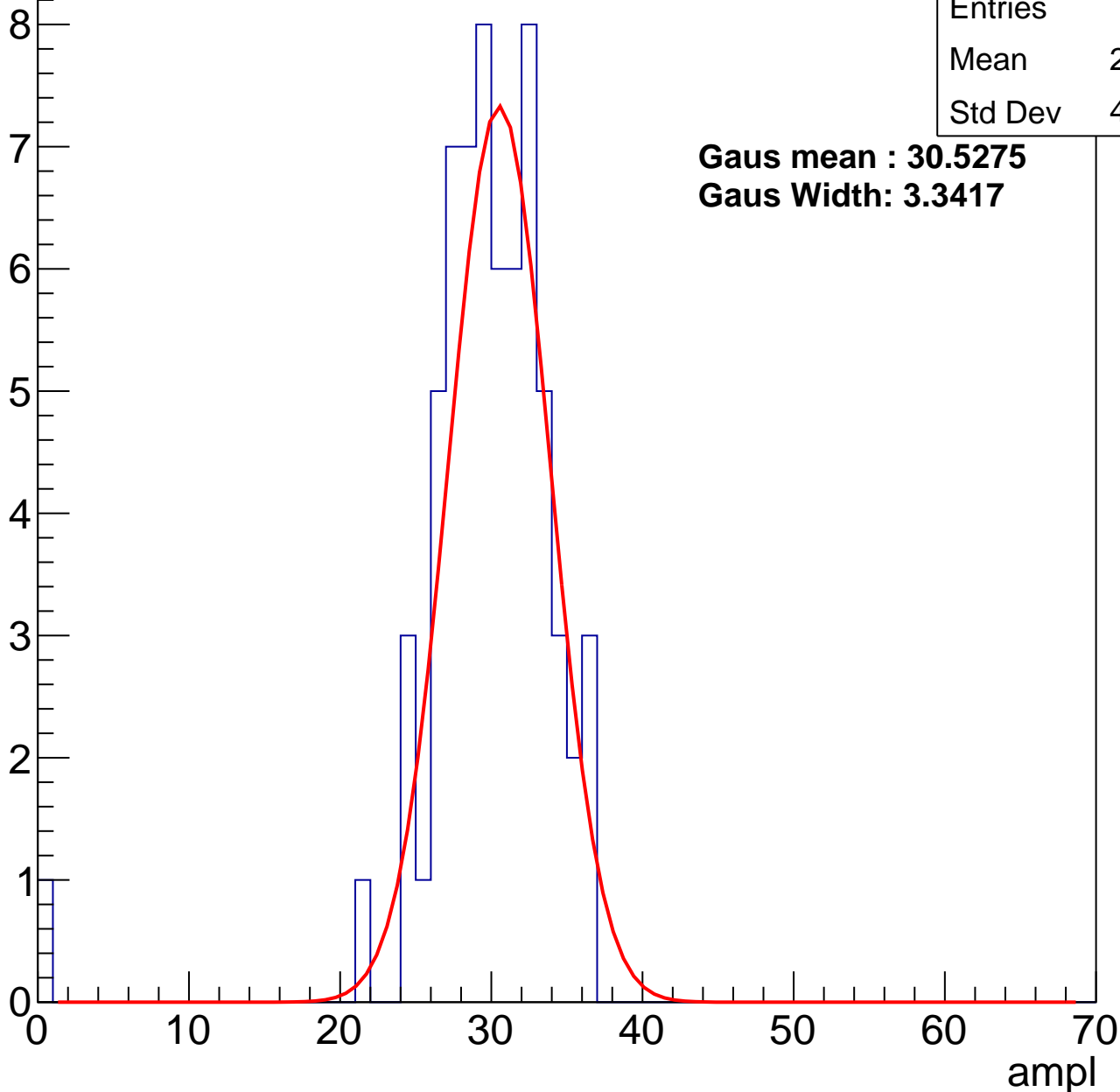
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 29.27 |
| Std Dev | 4.842 |

**Gaus mean : 30.5275**

**Gaus Width: 3.3417**



# B0L002S, U1-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 35.98 |
| Std Dev | 2.913 |

**Gaus mean : 36.6402**

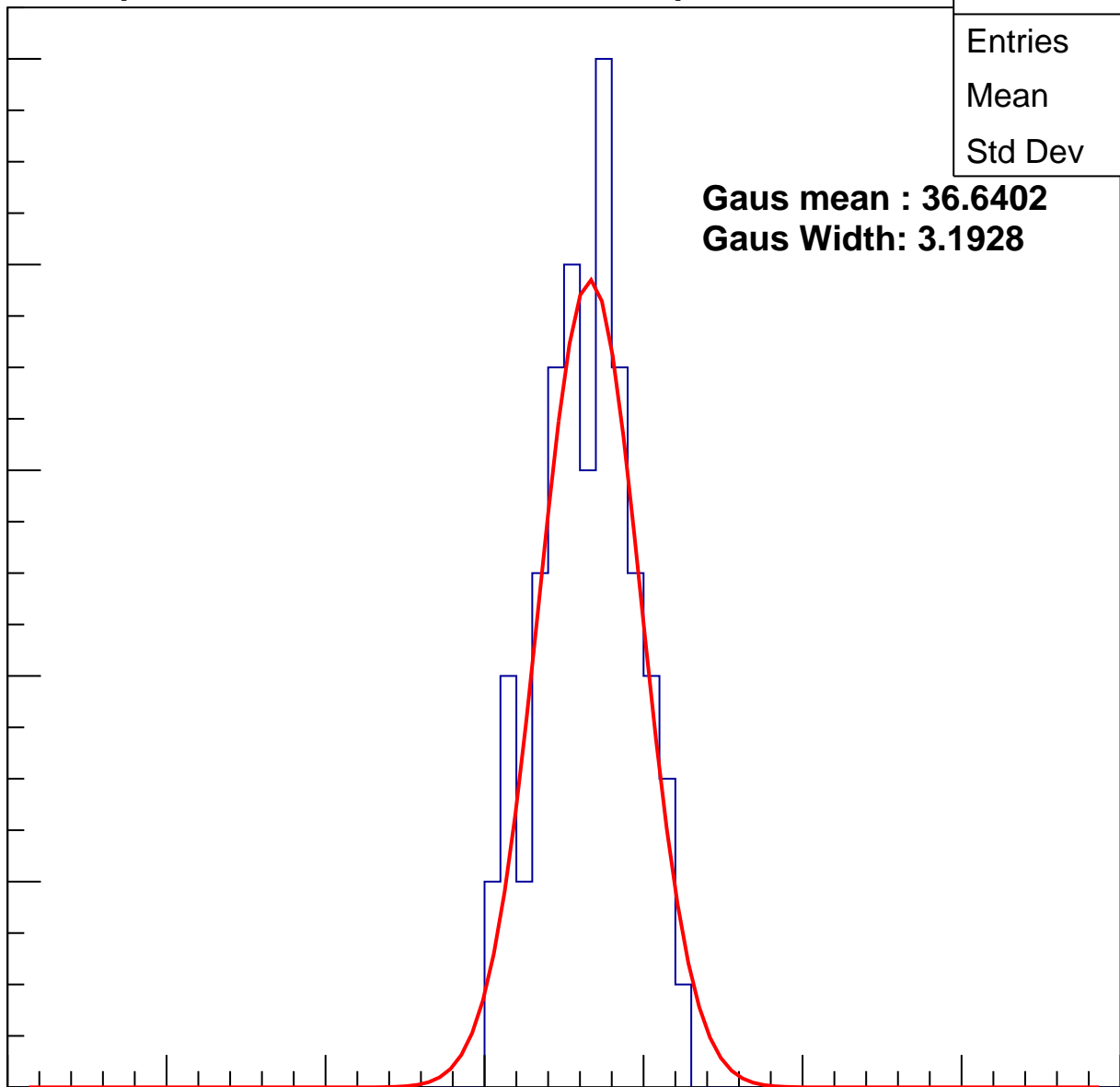
**Gaus Width: 3.1928**

Entry

10  
8  
6  
4  
2  
0

ampl

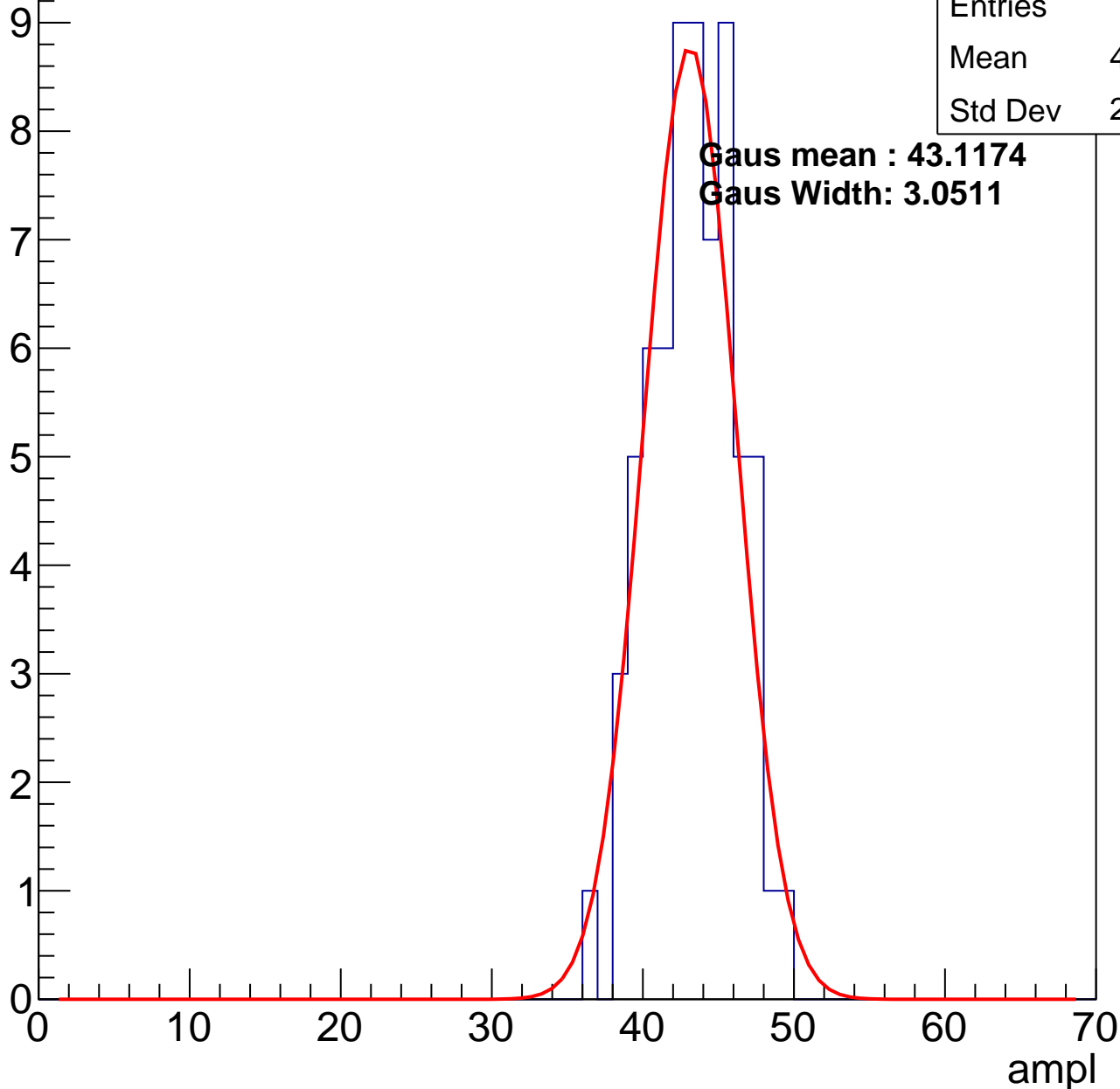
0 10 20 30 40 50 60 70



# B0L002S, U1-ch11, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

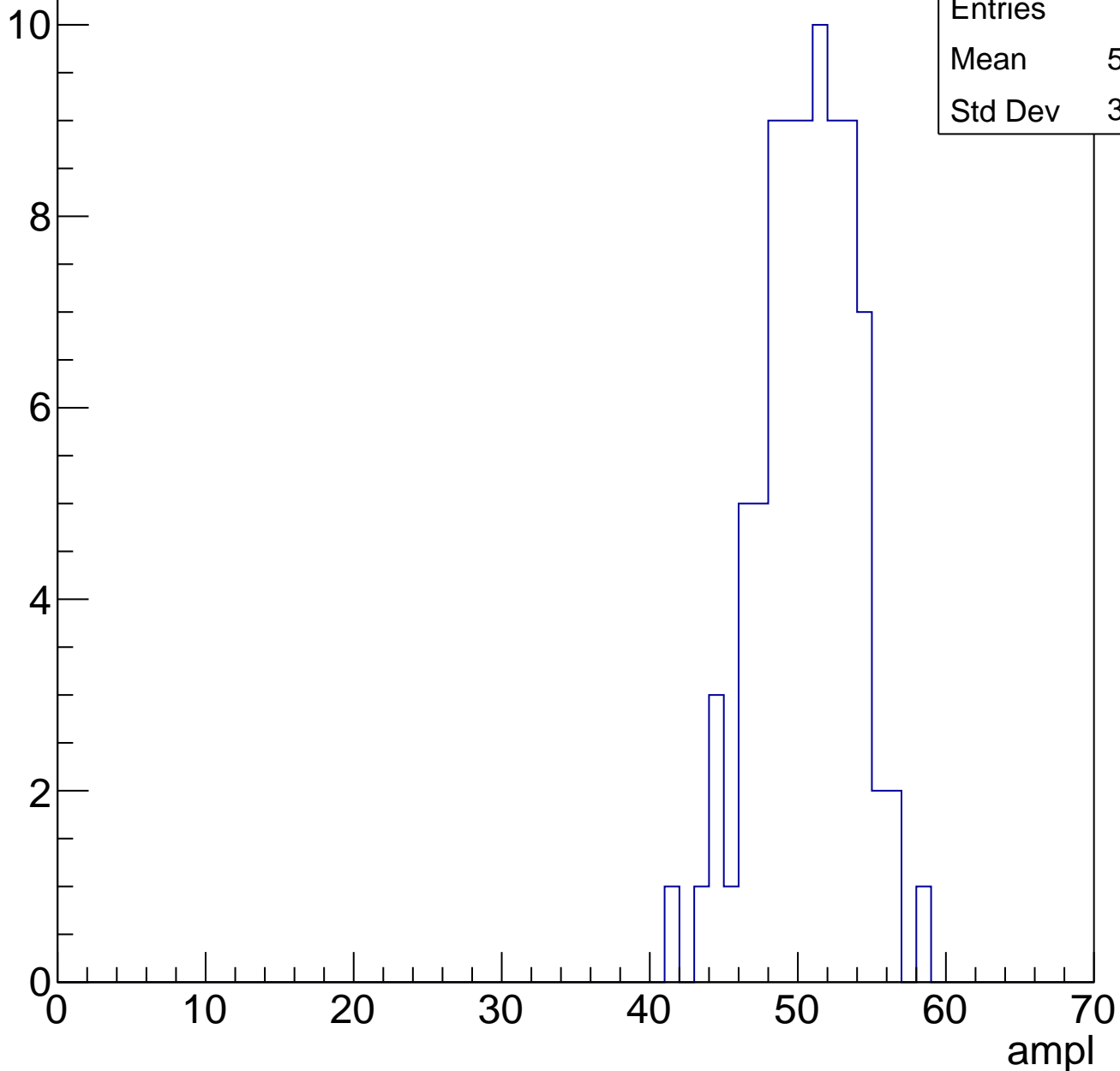


# B0L002S, U1-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 50.14 |
| Std Dev | 3.212 |

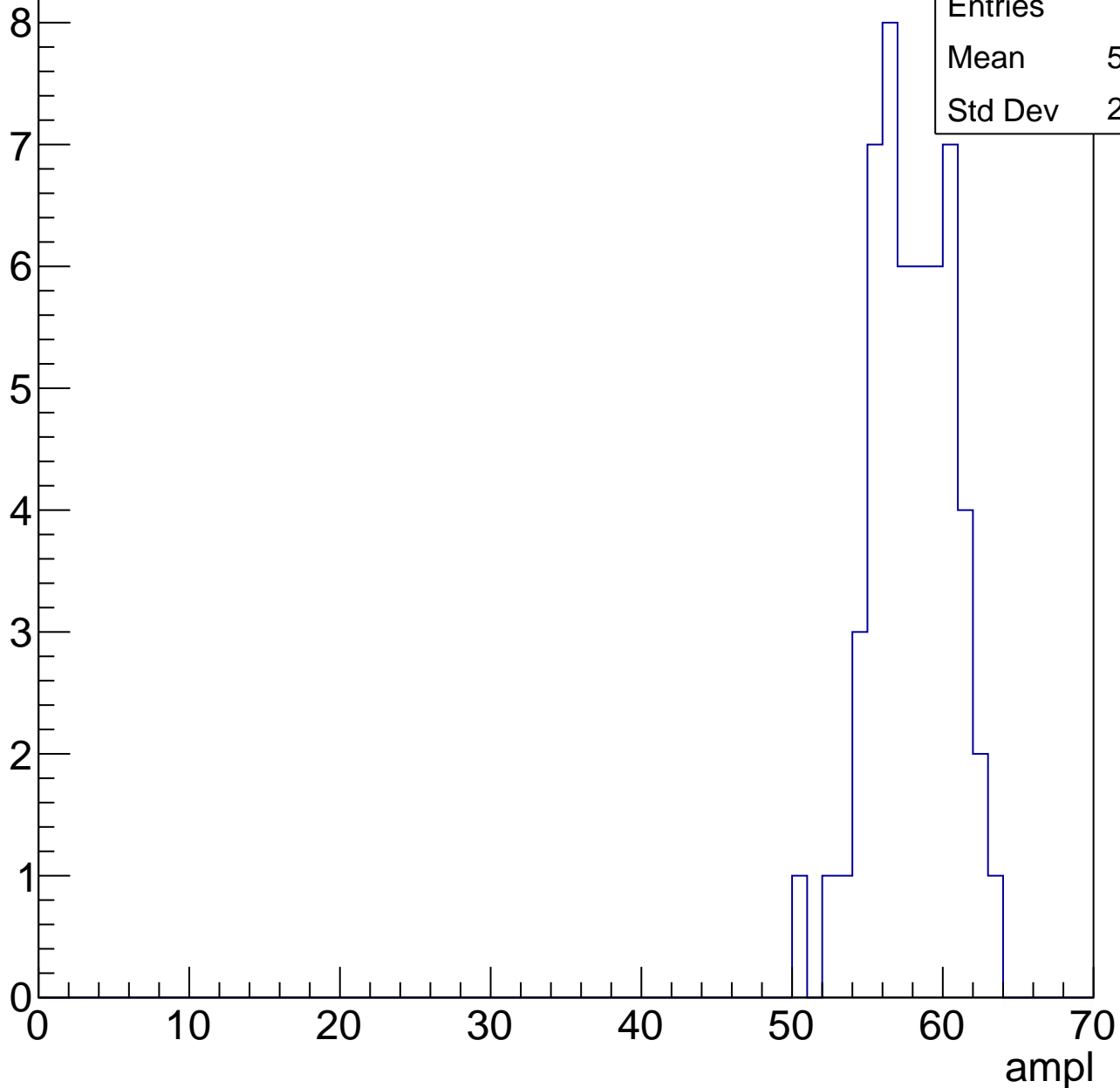
Entry



# B0L002S, U1-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

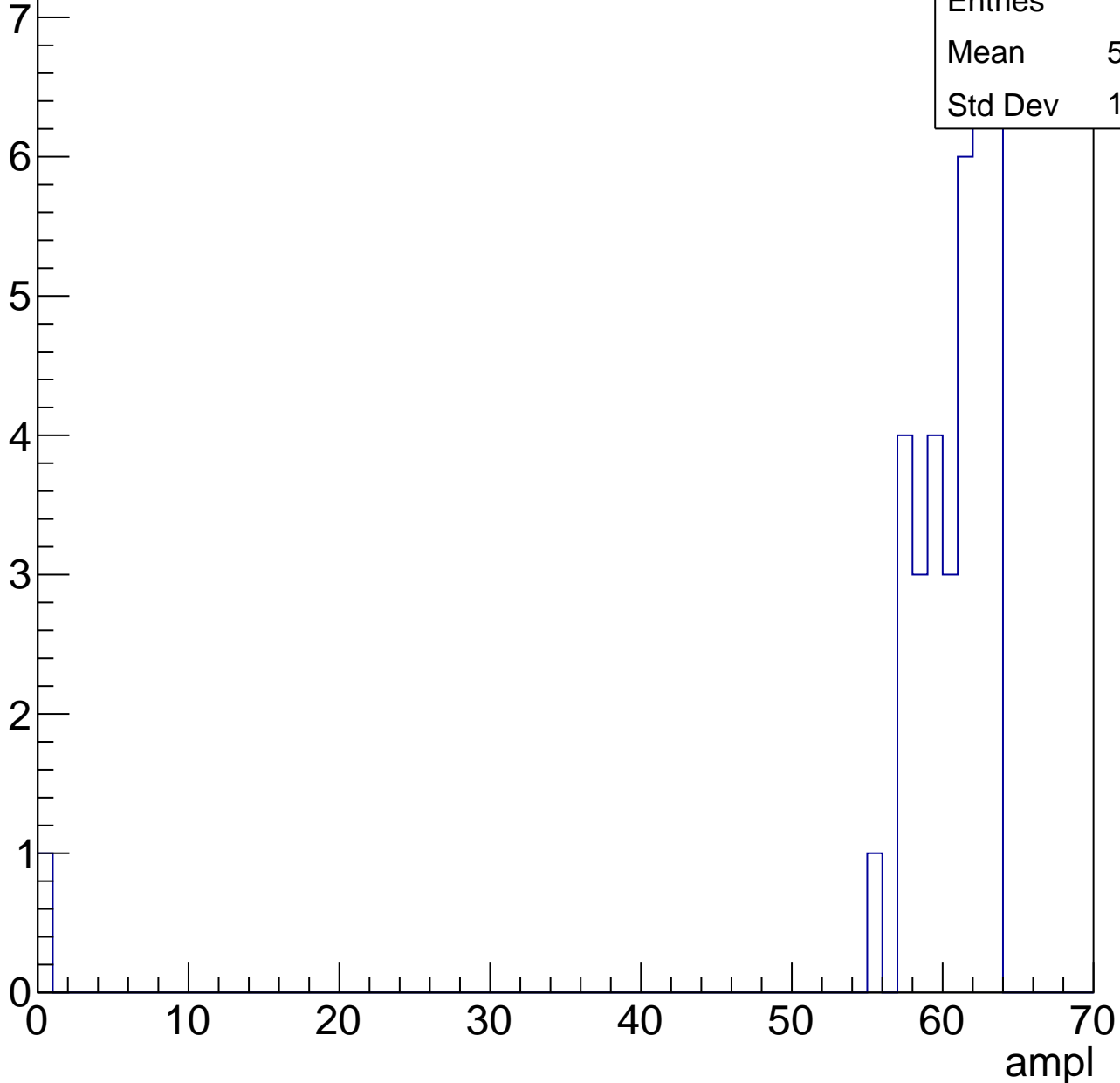


# B0L002S, U1-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 58.72 |
| Std Dev | 10.16 |



# B0L002S, U1-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

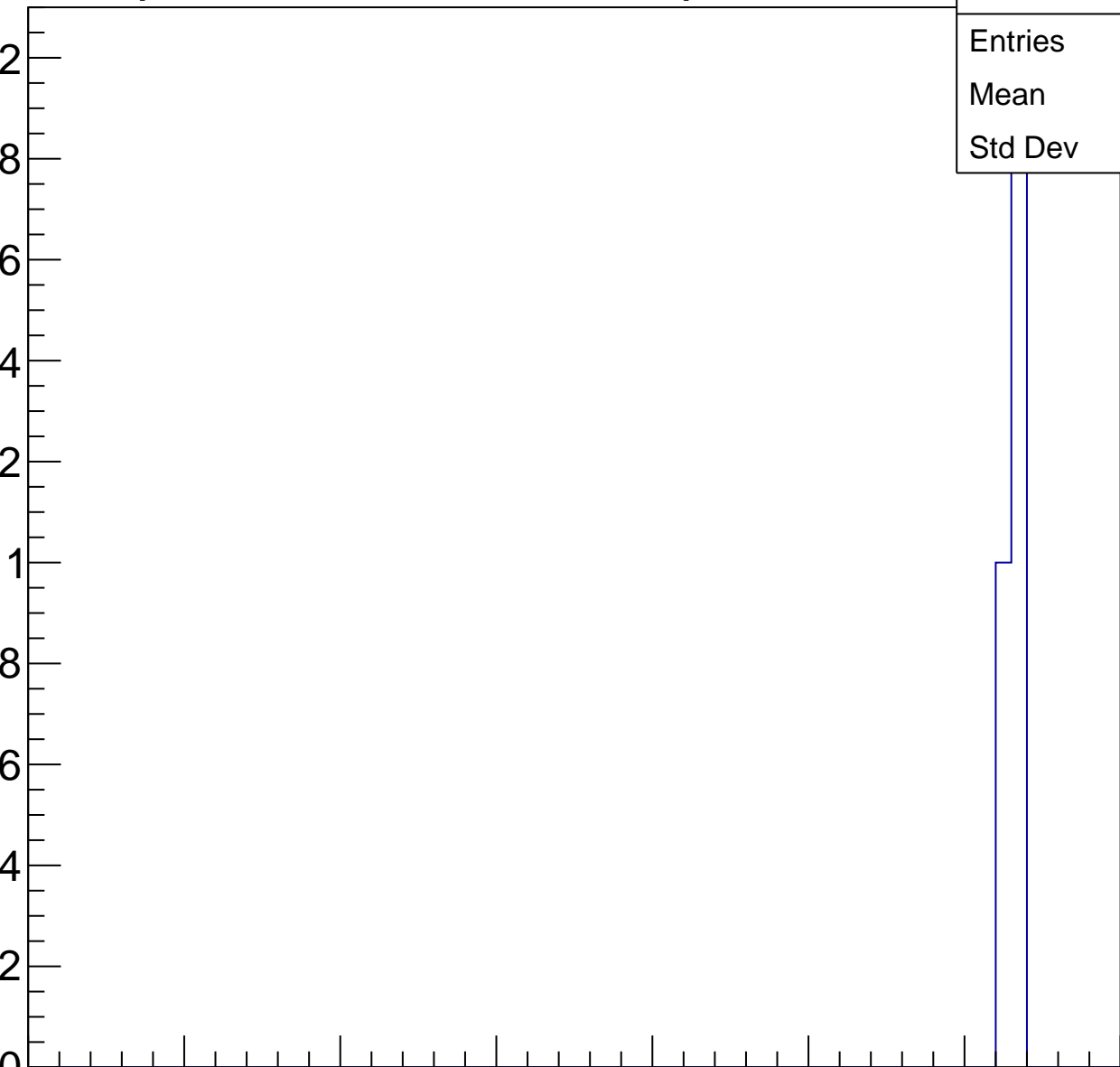
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 3      |
| Mean    | 62.67  |
| Std Dev | 0.4714 |

0 10 20 30 40 50 60 70

ampl





# B0L002S, U1-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch12, adc0

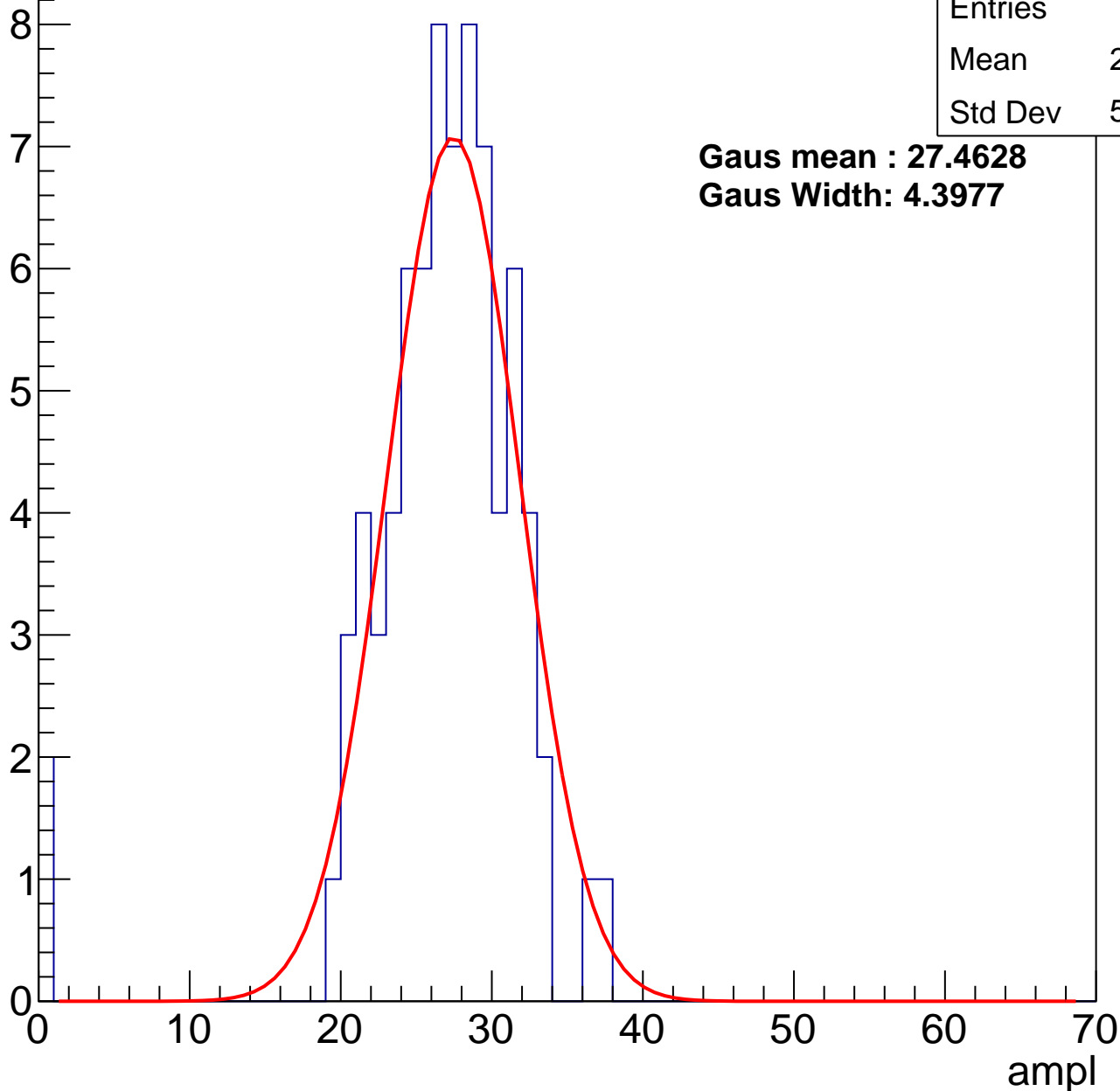
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 26.13 |
| Std Dev | 5.699 |

**Gaus mean : 27.4628**

**Gaus Width: 4.3977**



# B0L002S, U1-ch12, adc1

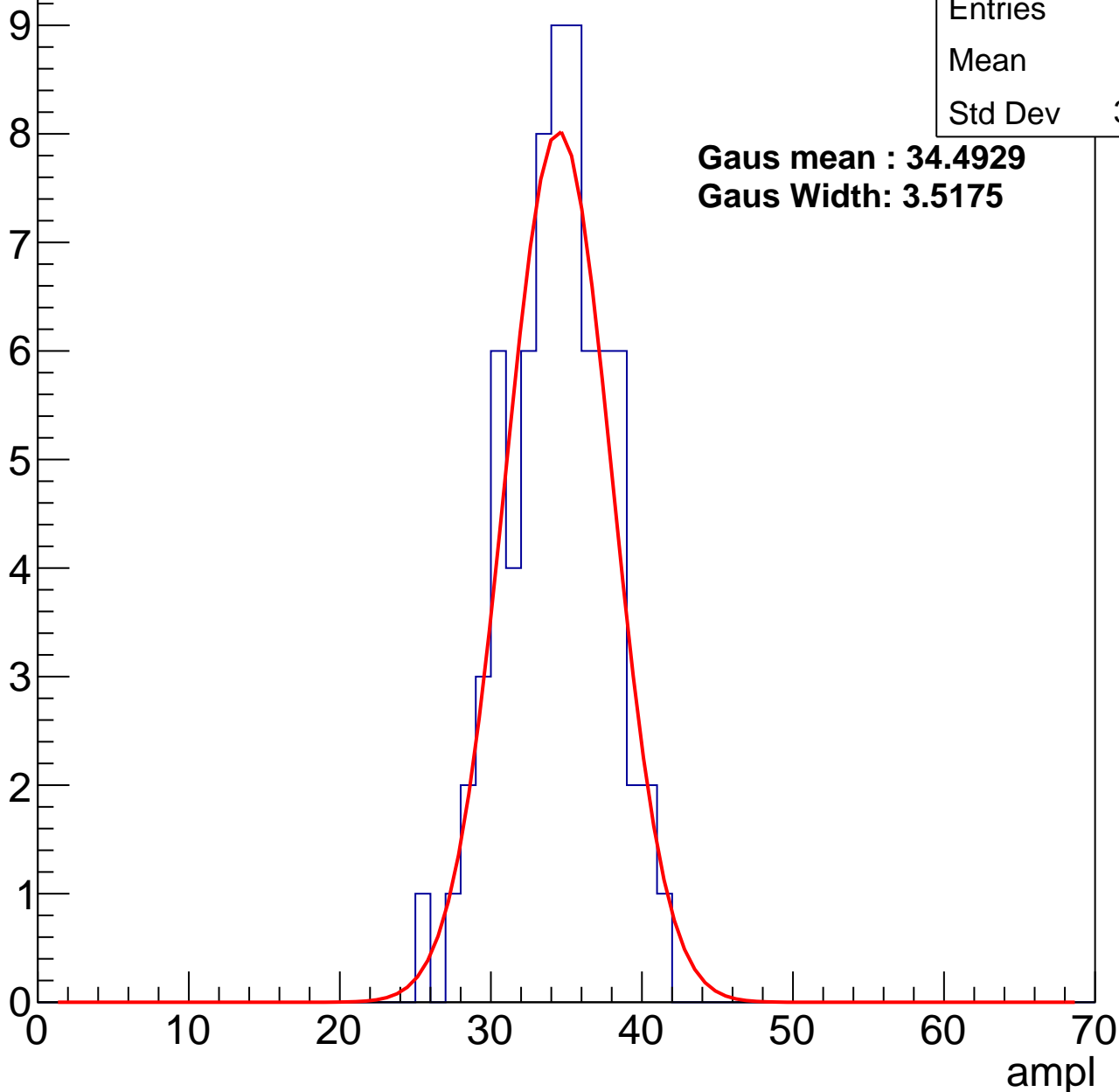
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 33.9  |
| Std Dev | 3.321 |

**Gaus mean : 34.4929**

**Gaus Width: 3.5175**



# B0L002S, U1-ch12, adc2

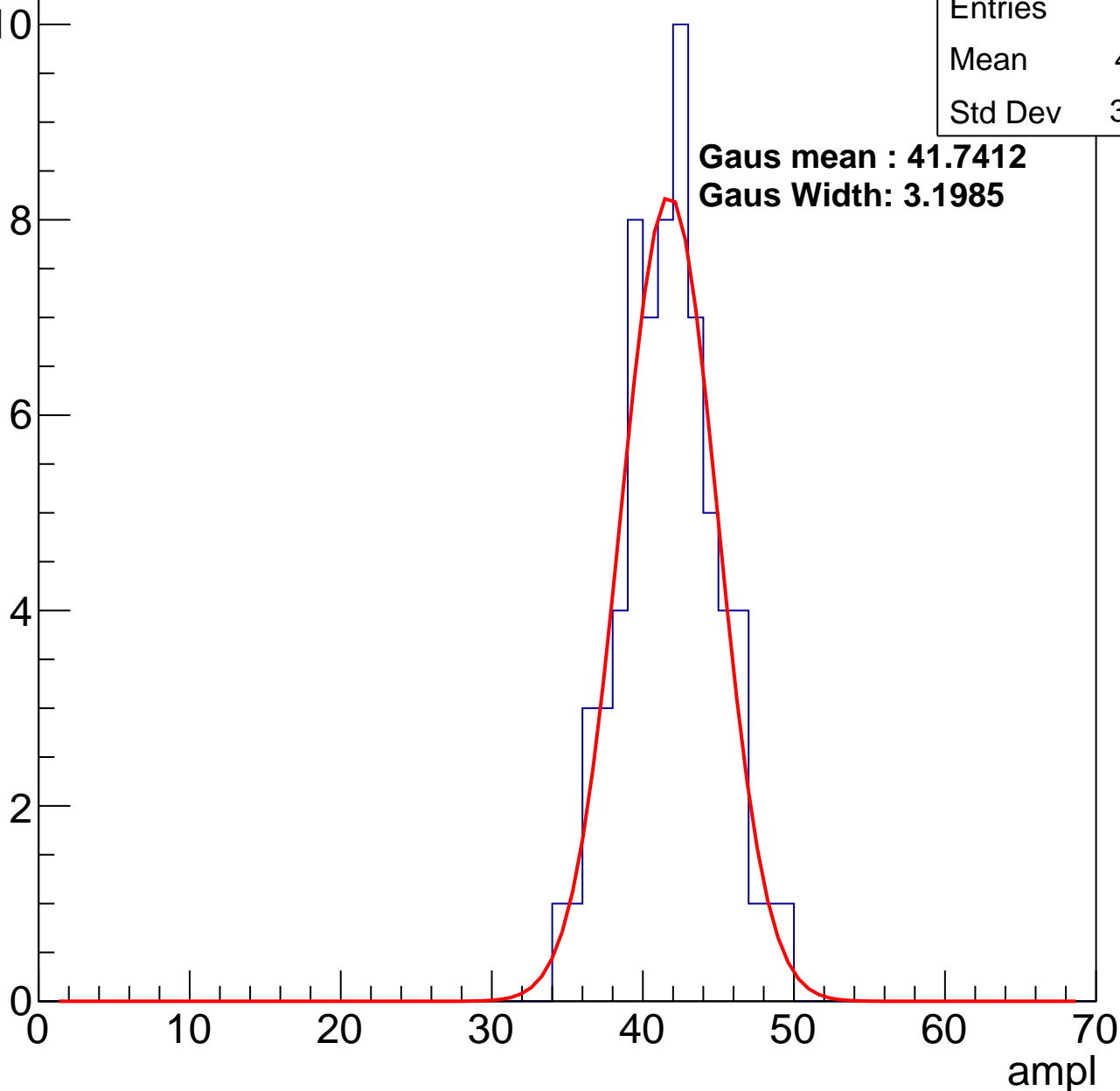
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 41.31 |
| Std Dev | 3.145 |

**Gaus mean : 41.7412**

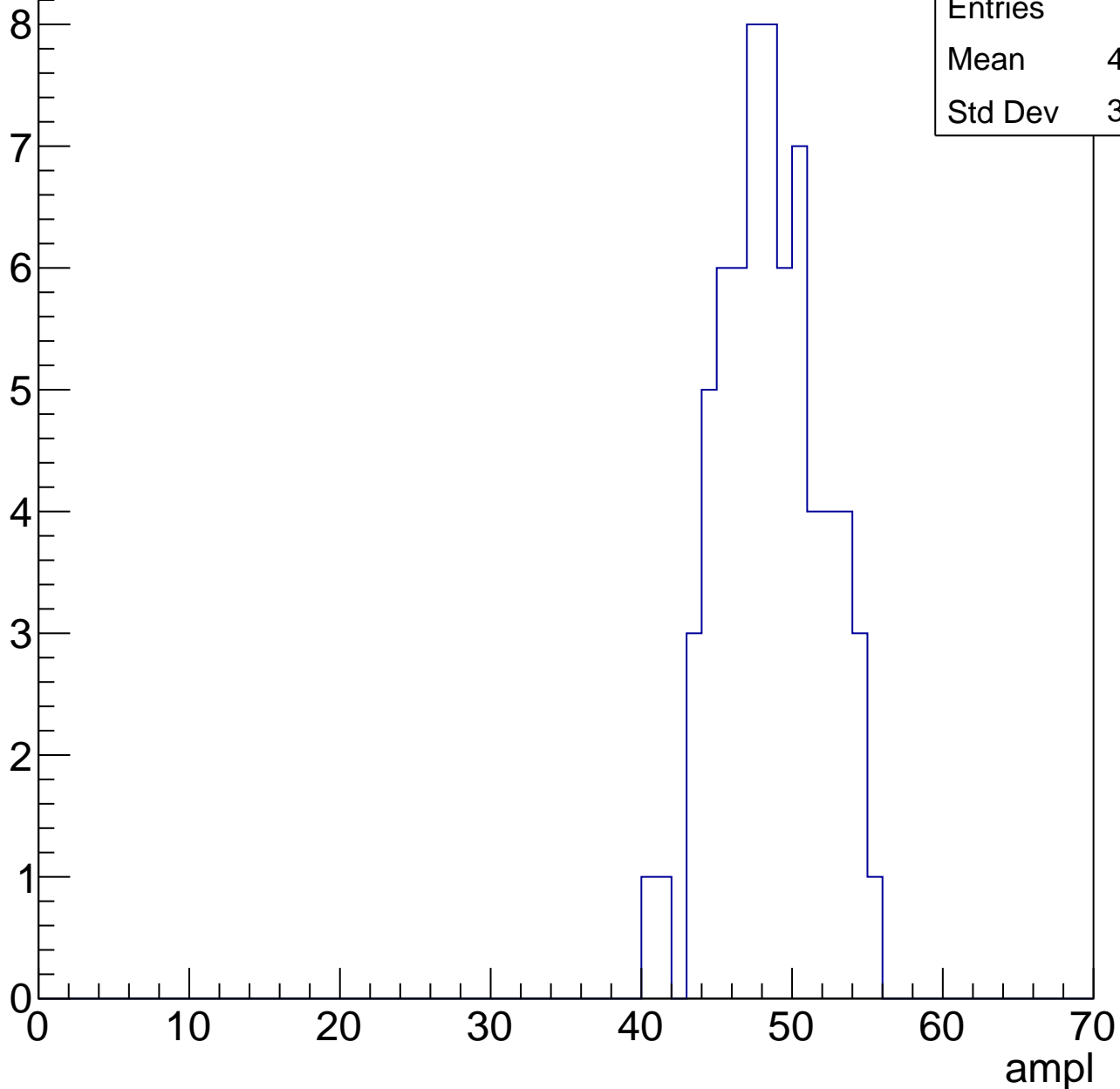
**Gaus Width: 3.1985**



# B0L002S, U1-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



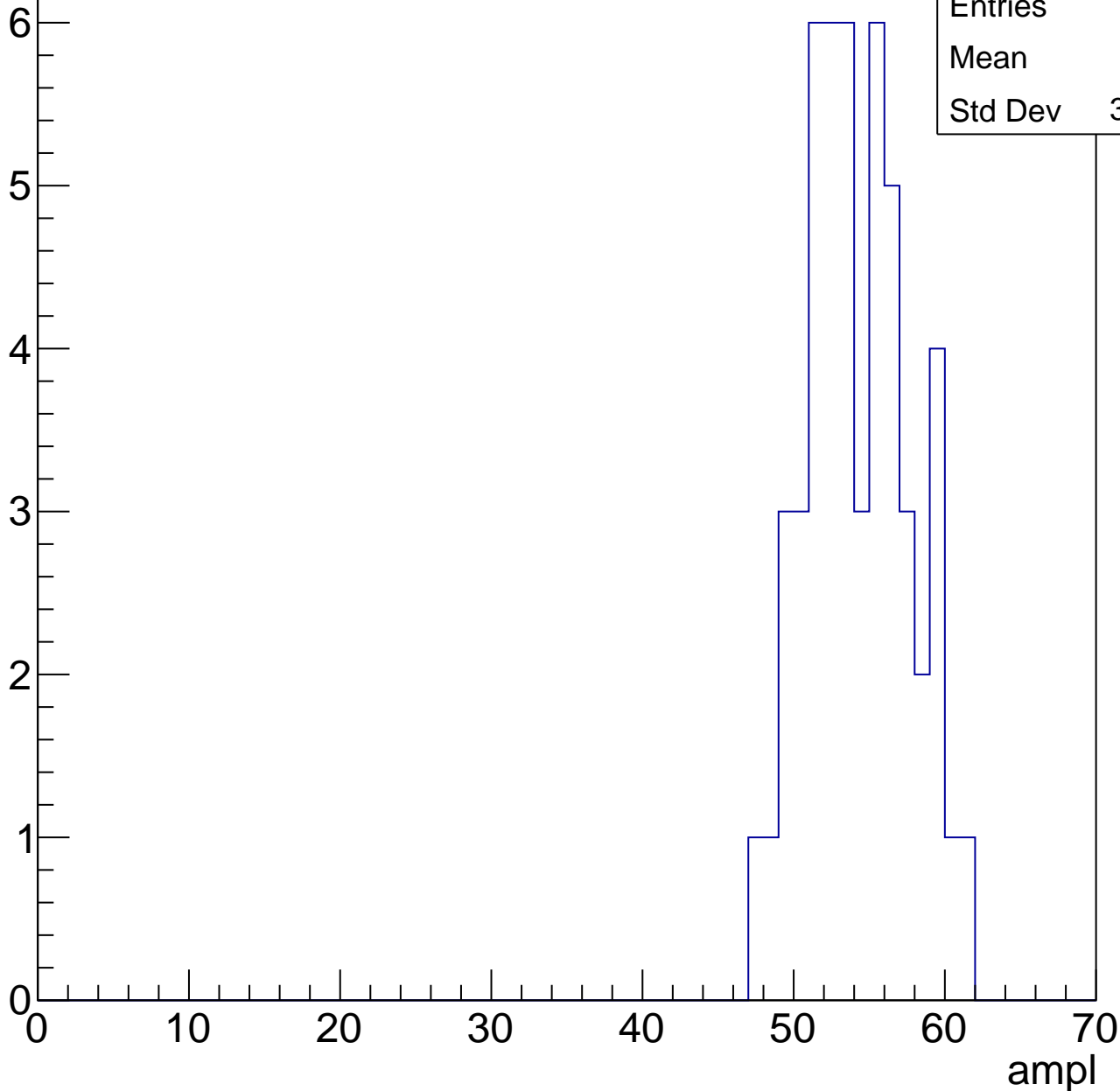
|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 48.07 |
| Std Dev | 3.334 |

# B0L002S, U1-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 53.8  |
| Std Dev | 3.314 |

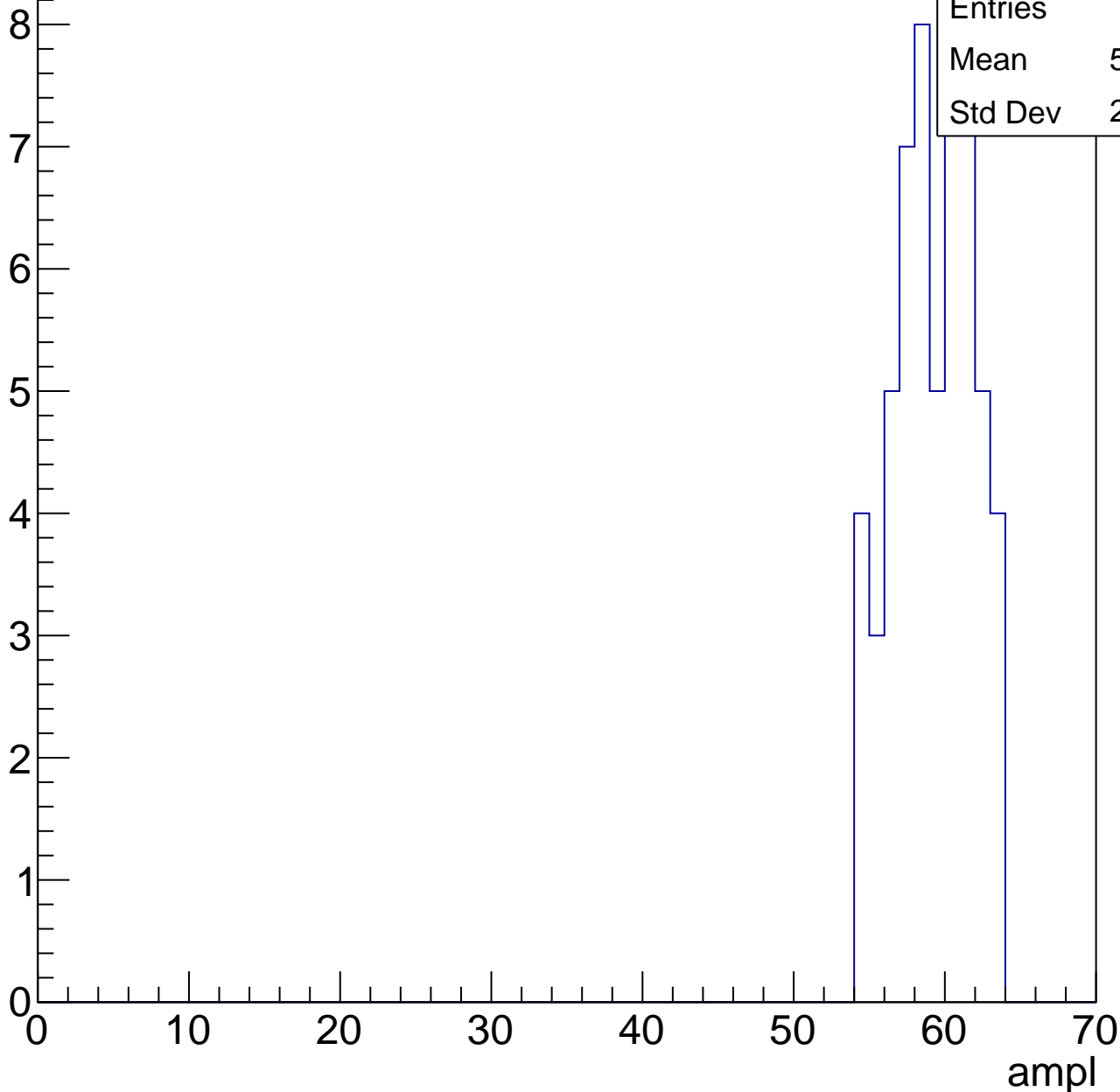


# B0L002S, U1-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 58.75 |
| Std Dev | 2.563 |

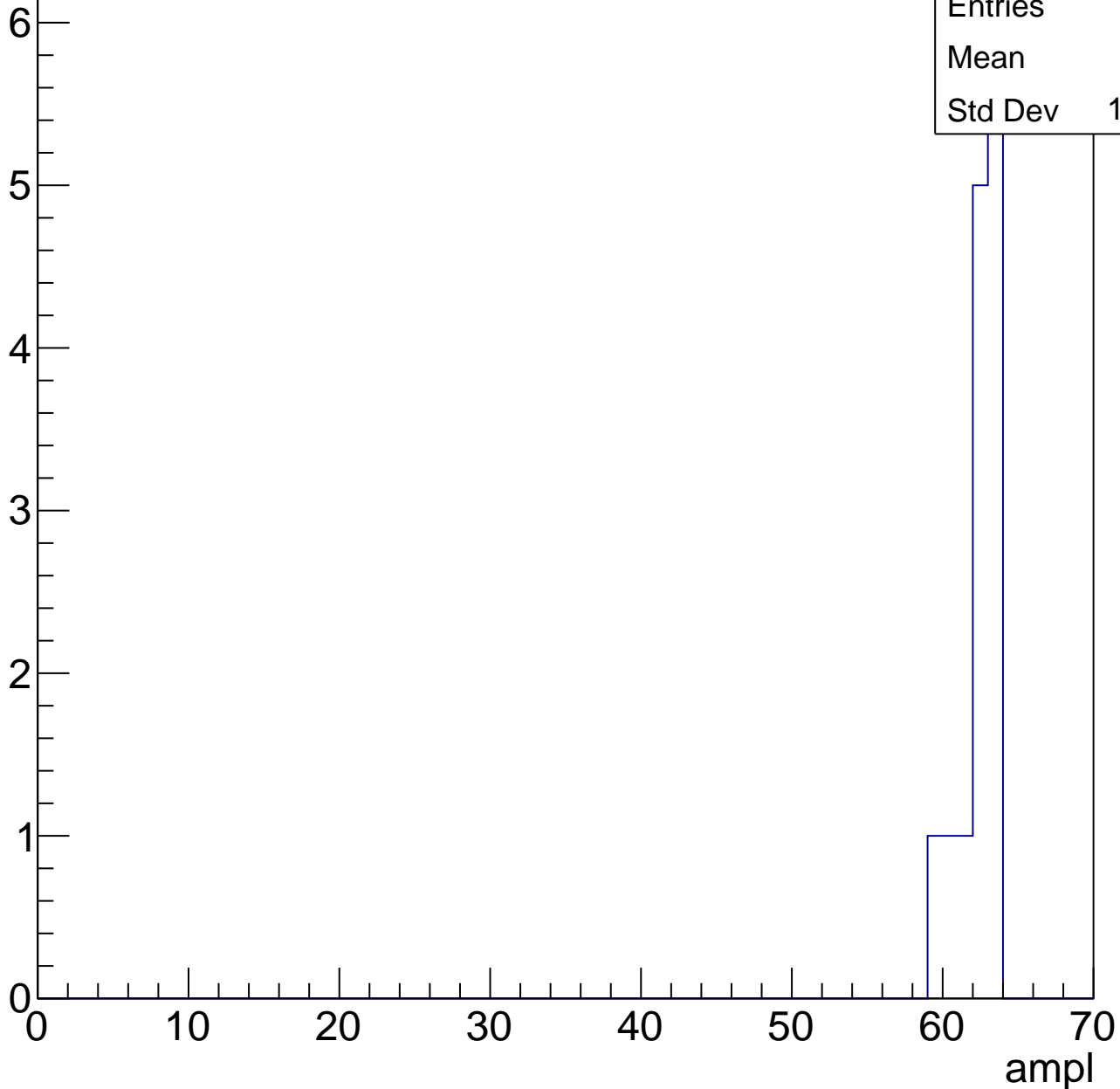


# B0L002S, U1-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 14    |
| Mean    | 62    |
| Std Dev | 1.195 |





# B0L002S, U1-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 3 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch13, adc0

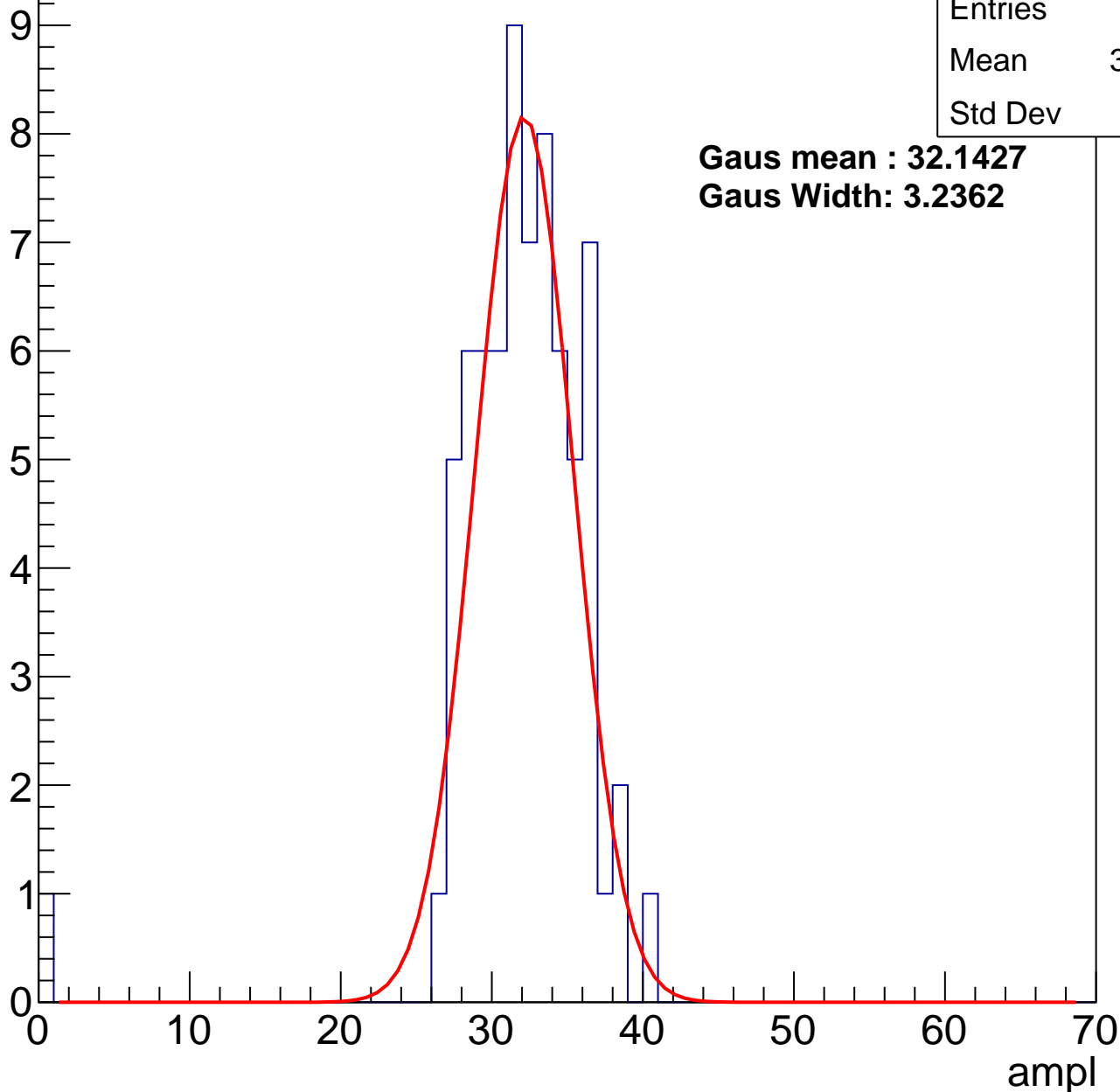
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 31.46 |
| Std Dev | 4.89  |

**Gaus mean : 32.1427**

**Gaus Width: 3.2362**



# B0L002S, U1-ch13, adc1

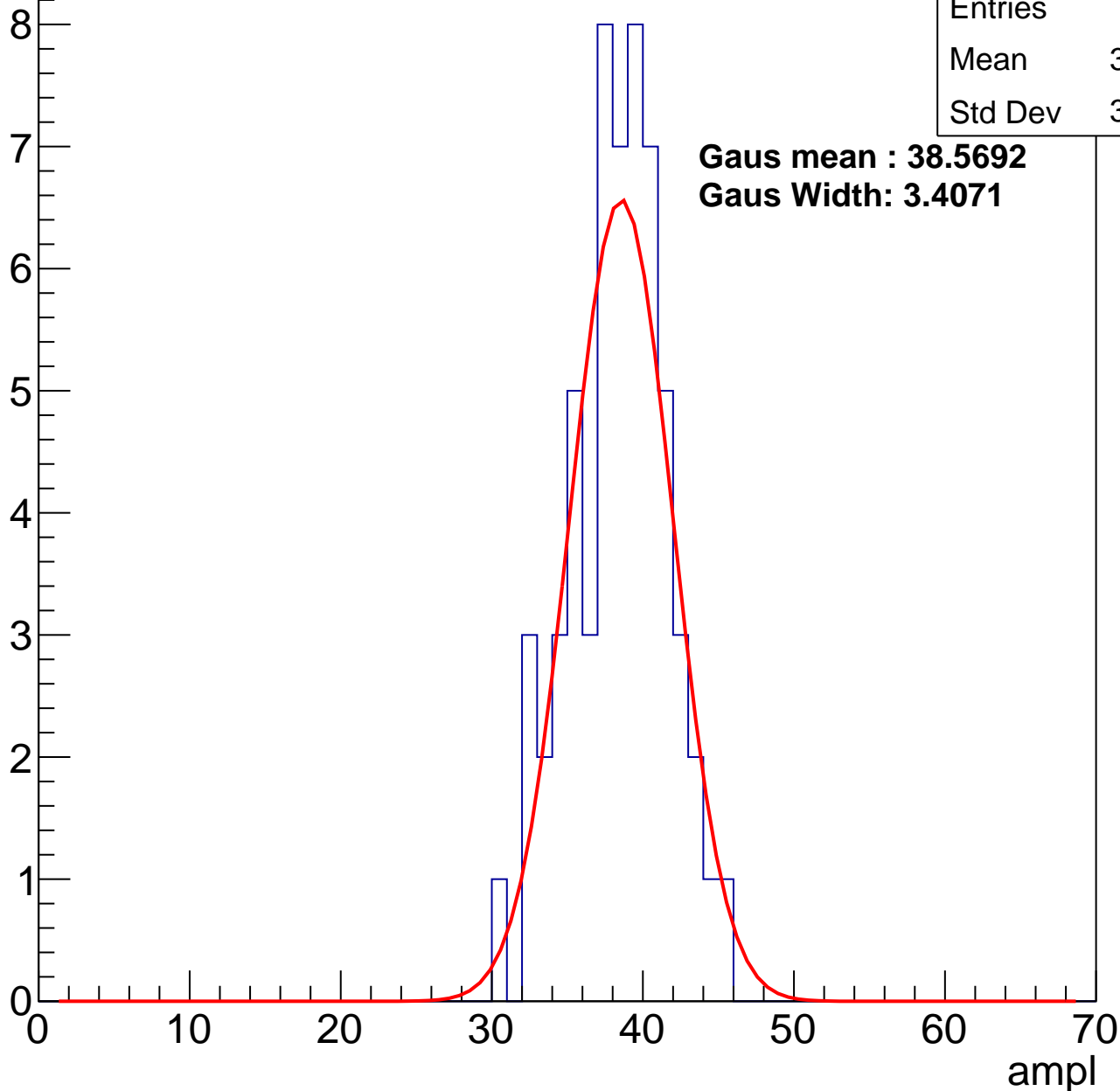
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 37.92 |
| Std Dev | 3.185 |

**Gaus mean : 38.5692**

**Gaus Width: 3.4071**



# B0L002S, U1-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 44.41 |
| Std Dev | 3.474 |

**Gaus mean : 44.6534**

**Gaus Width: 2.9150**

10

8

6

4

2

0

0

10

20

30

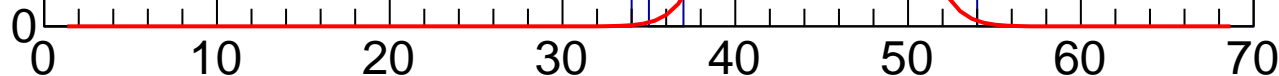
40

50

60

70

ampl

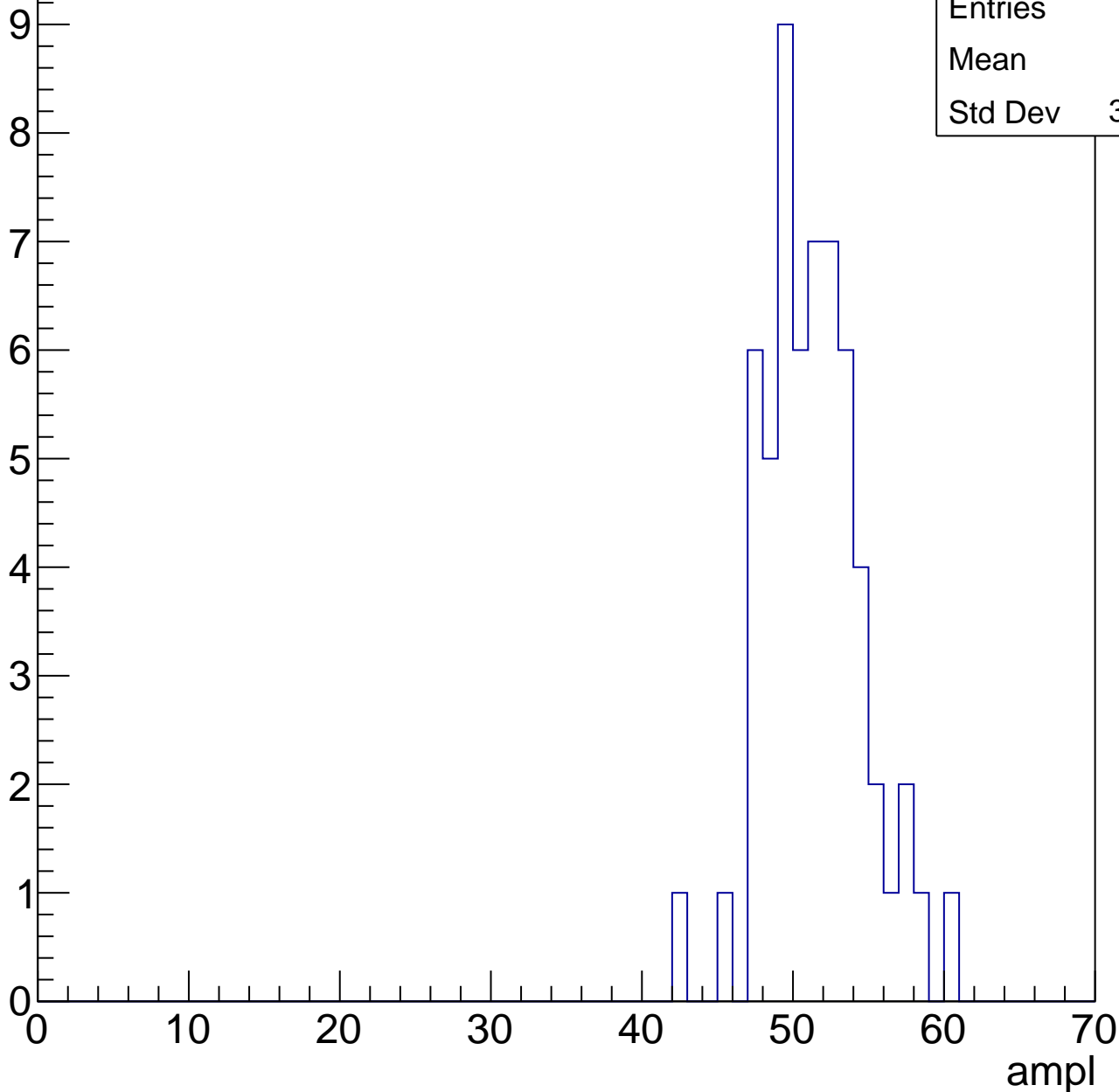


# B0L002S, U1-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

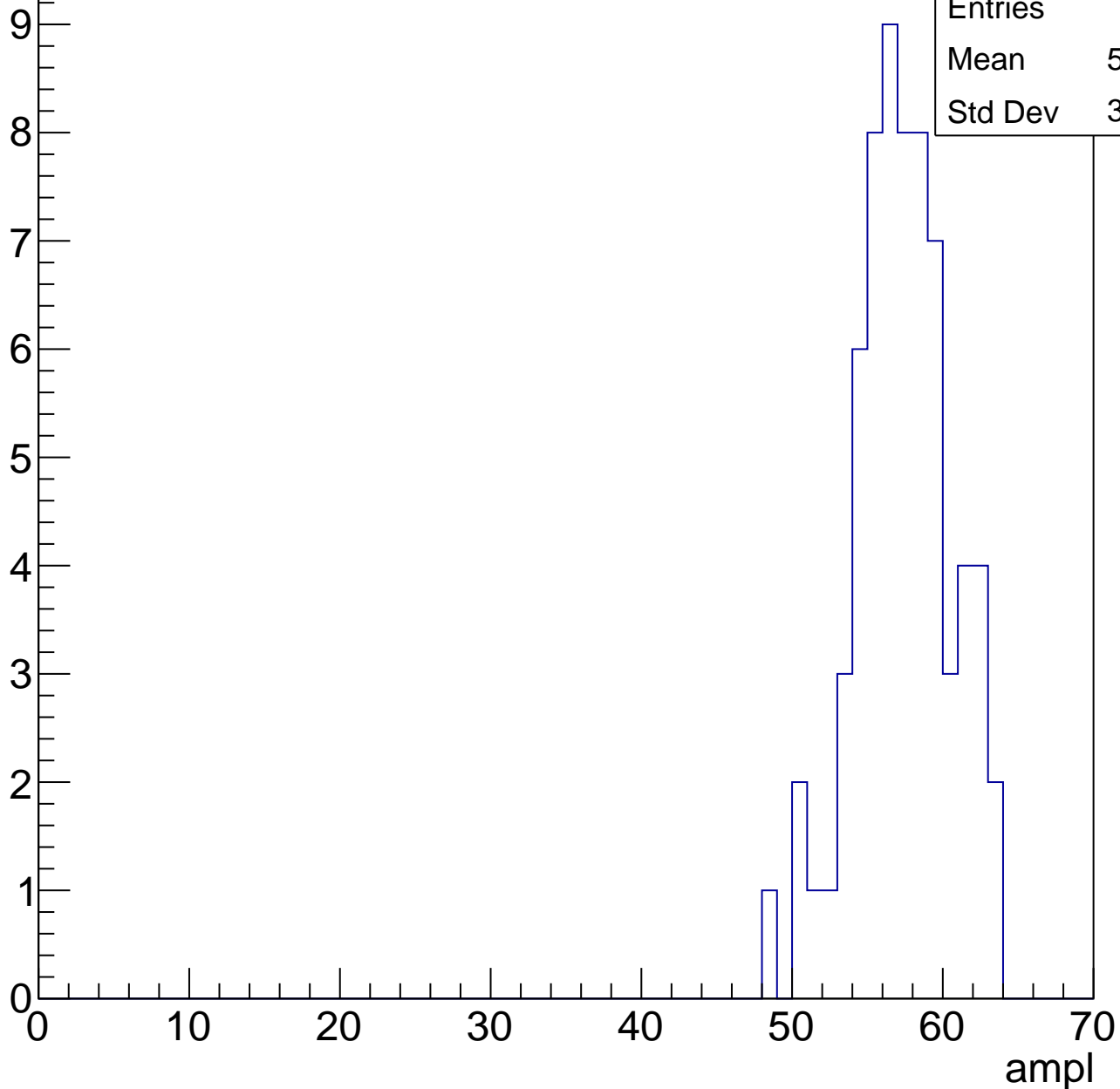
|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 50.9  |
| Std Dev | 3.256 |



# B0L002S, U1-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

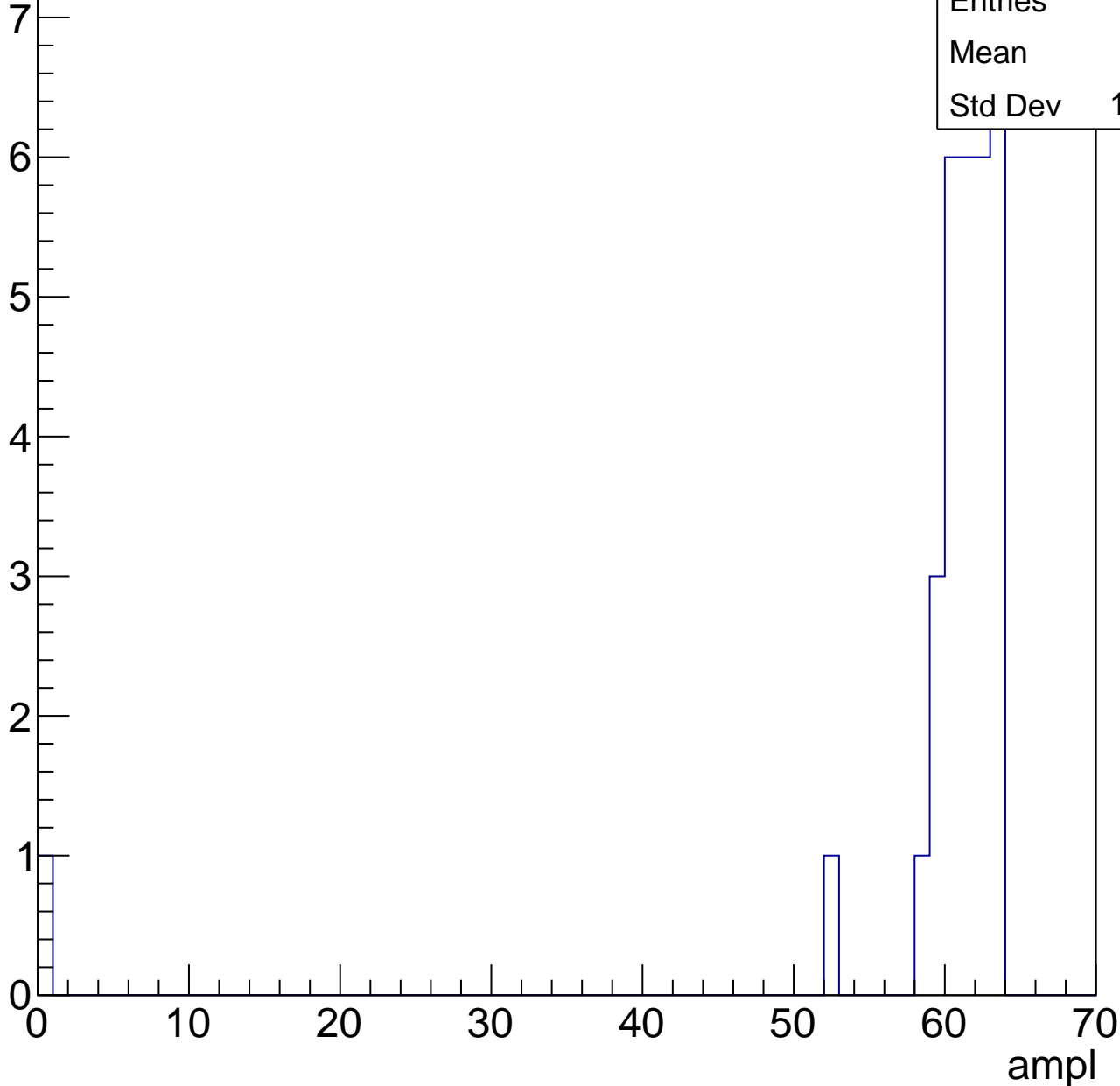


# B0L002S, U1-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 31    |
| Mean    | 58.9  |
| Std Dev | 10.96 |



# B0L002S, U1-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch14, adc0

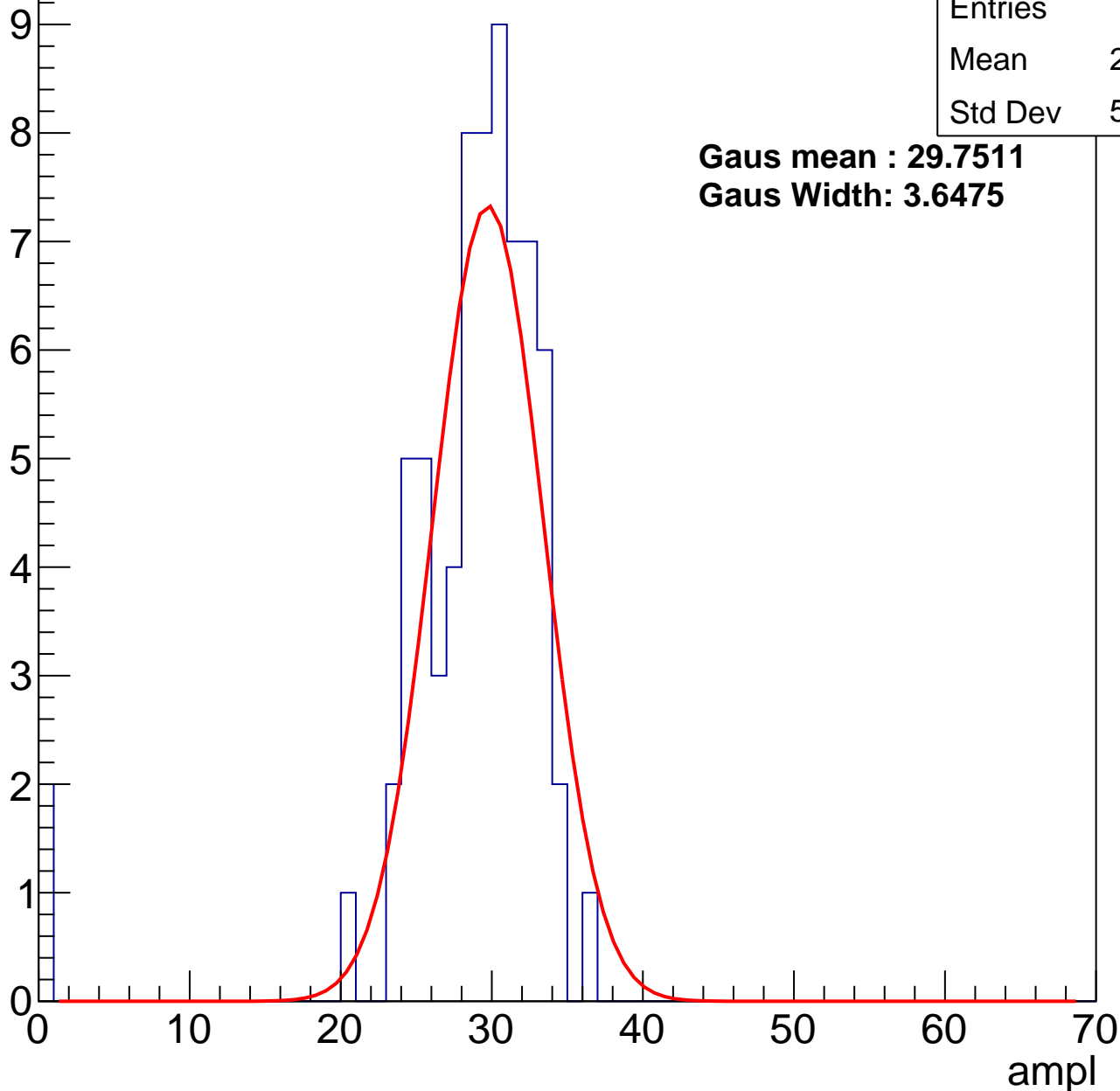
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 28.09 |
| Std Dev | 5.766 |

**Gaus mean : 29.7511**

**Gaus Width: 3.6475**



# B0L002S, U1-ch14, adc1

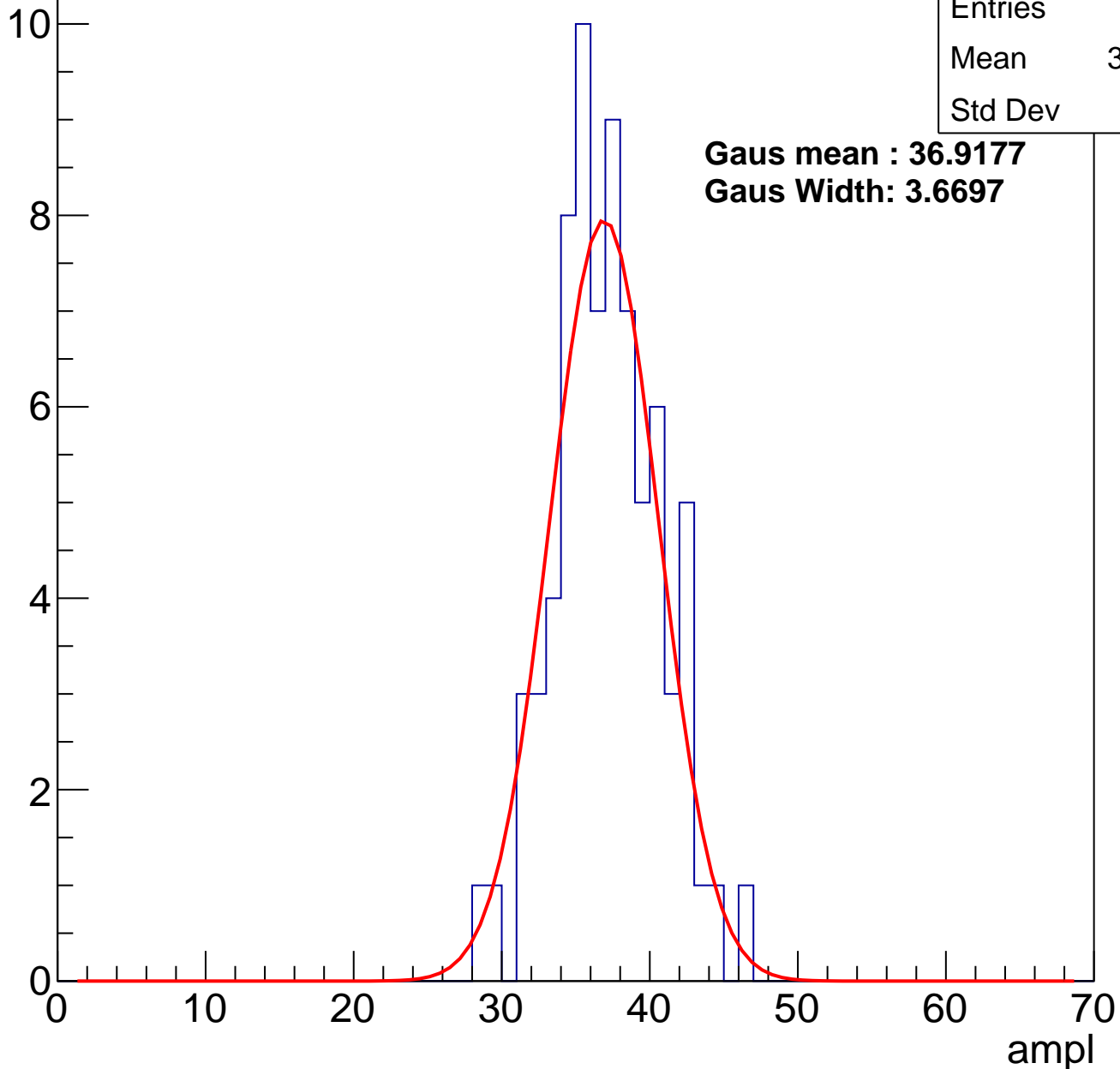
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 36.69 |
| Std Dev | 3.51  |

**Gaus mean : 36.9177**

**Gaus Width: 3.6697**

Entry



# B0L002S, U1-ch14, adc2

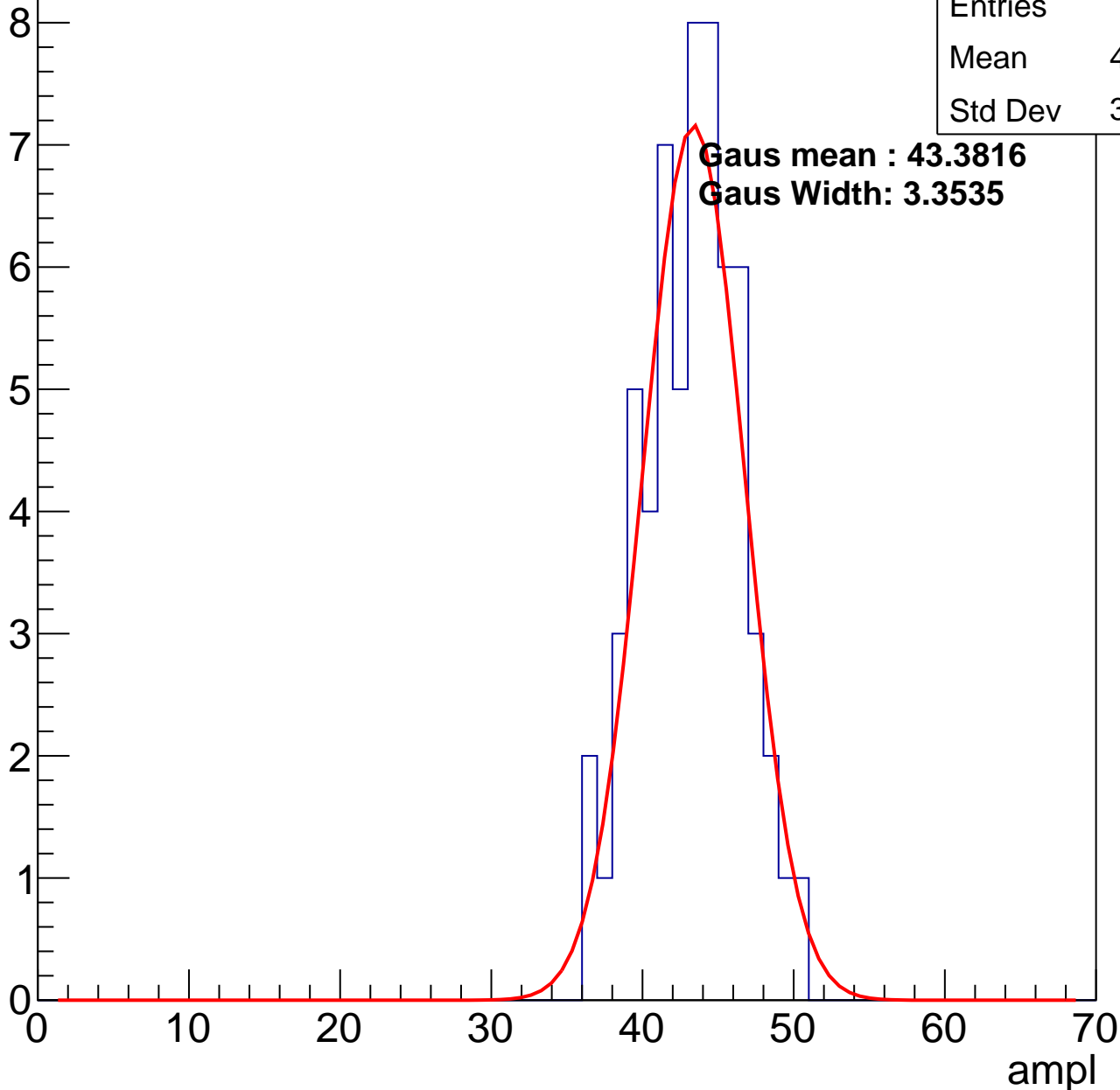
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 42.79 |
| Std Dev | 3.173 |

**Gaus mean : 43.3816**

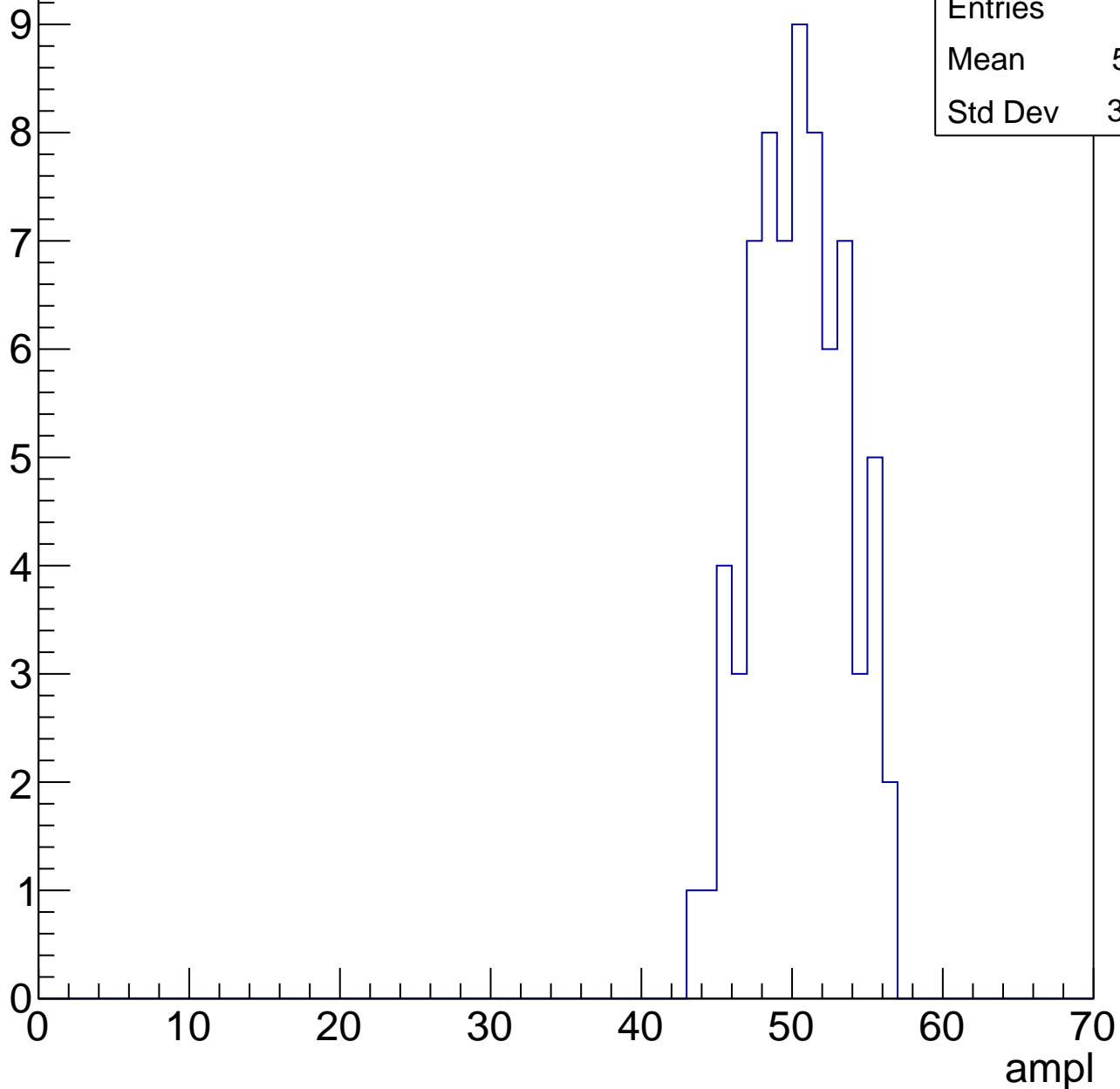
**Gaus Width: 3.3535**



# B0L002S, U1-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

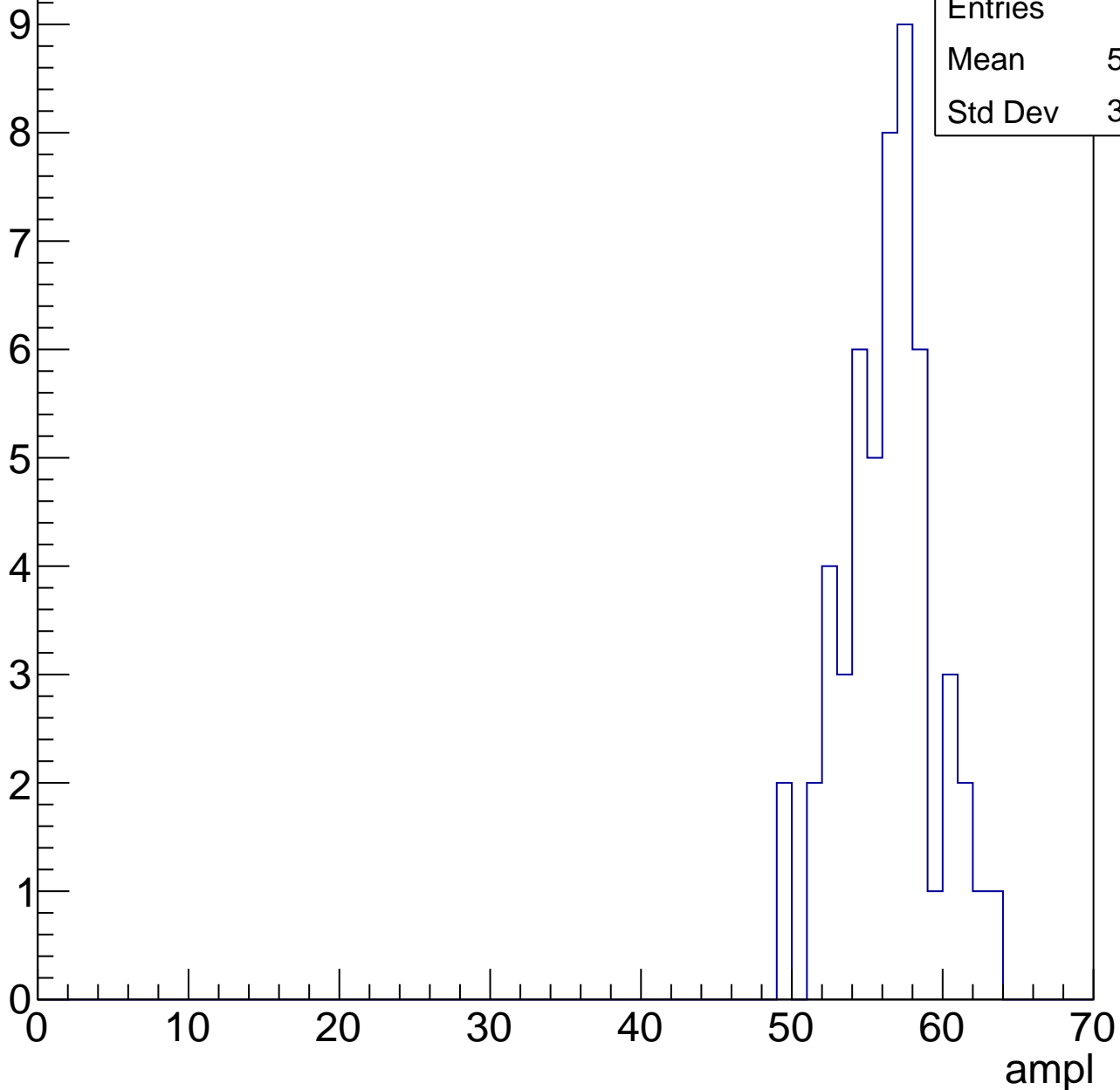


# B0L002S, U1-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 55.87 |
| Std Dev | 3.047 |

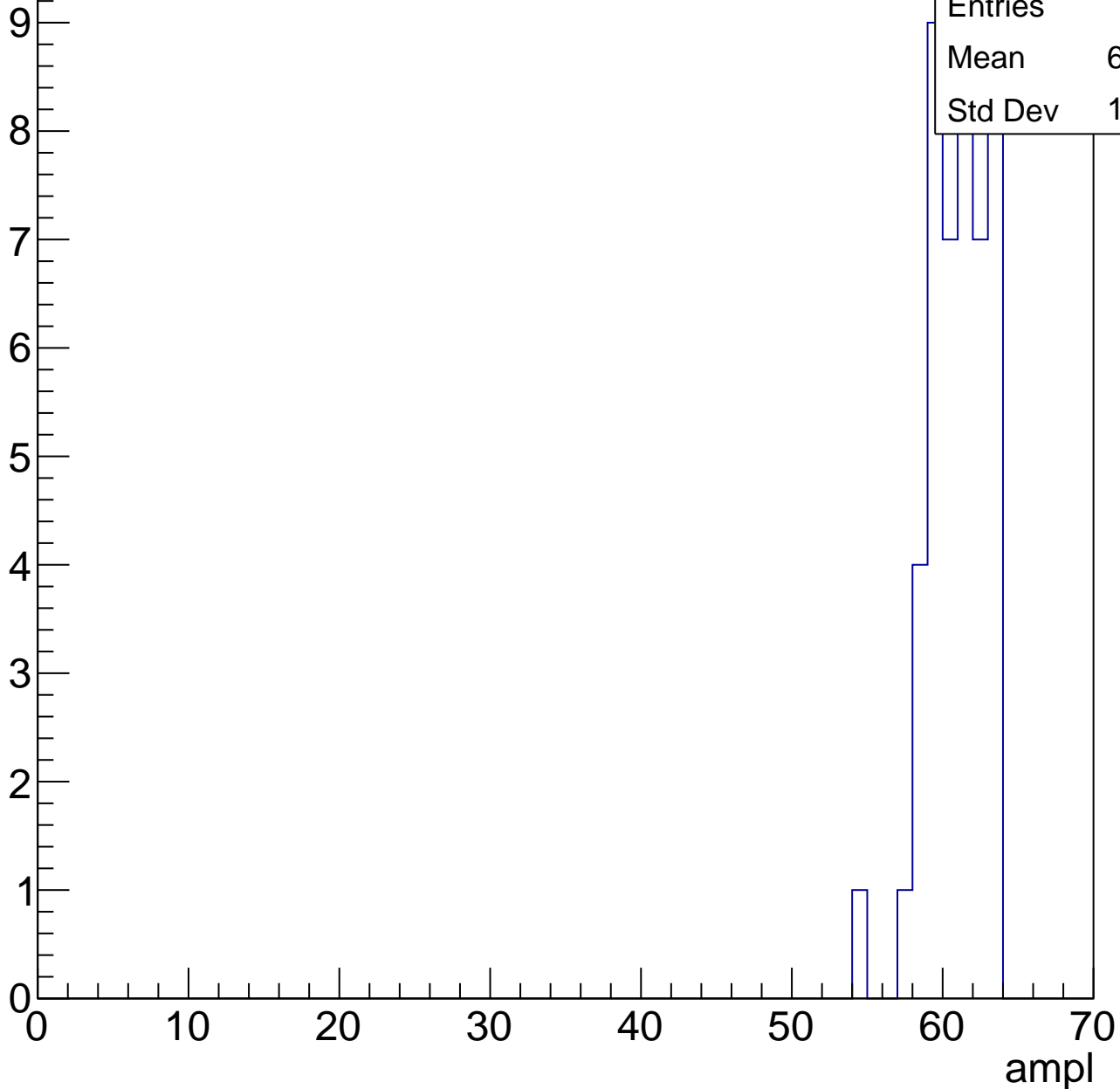


# B0L002S, U1-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 60.44 |
| Std Dev | 1.939 |



# B0L002S, U1-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 46.75 |
| Std Dev | 26.99 |



# B0L002S, U1-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch15, adc0

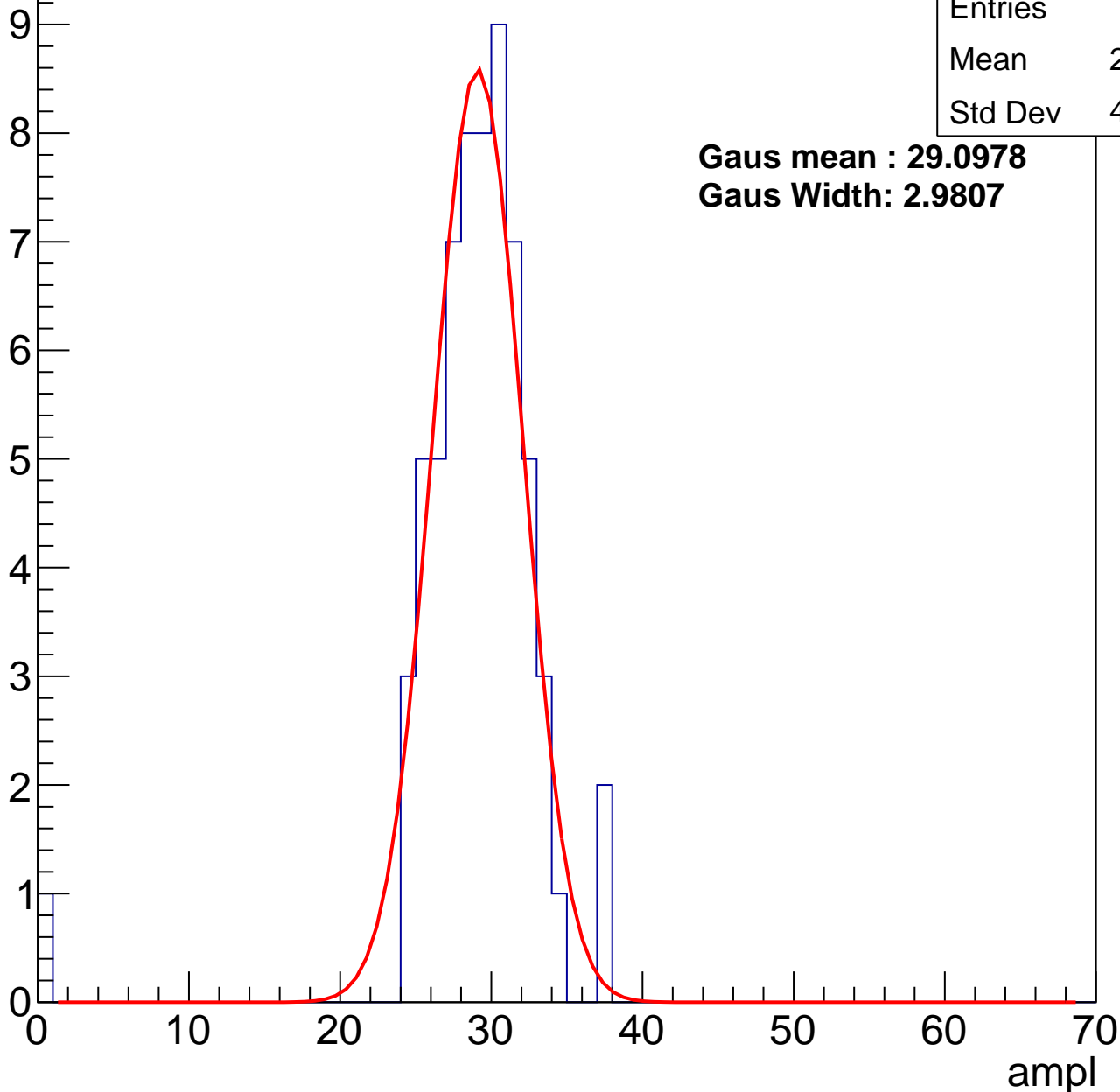
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 28.53 |
| Std Dev | 4.586 |

**Gaus mean : 29.0978**

**Gaus Width: 2.9807**



# B0L002S, U1-ch15, adc1

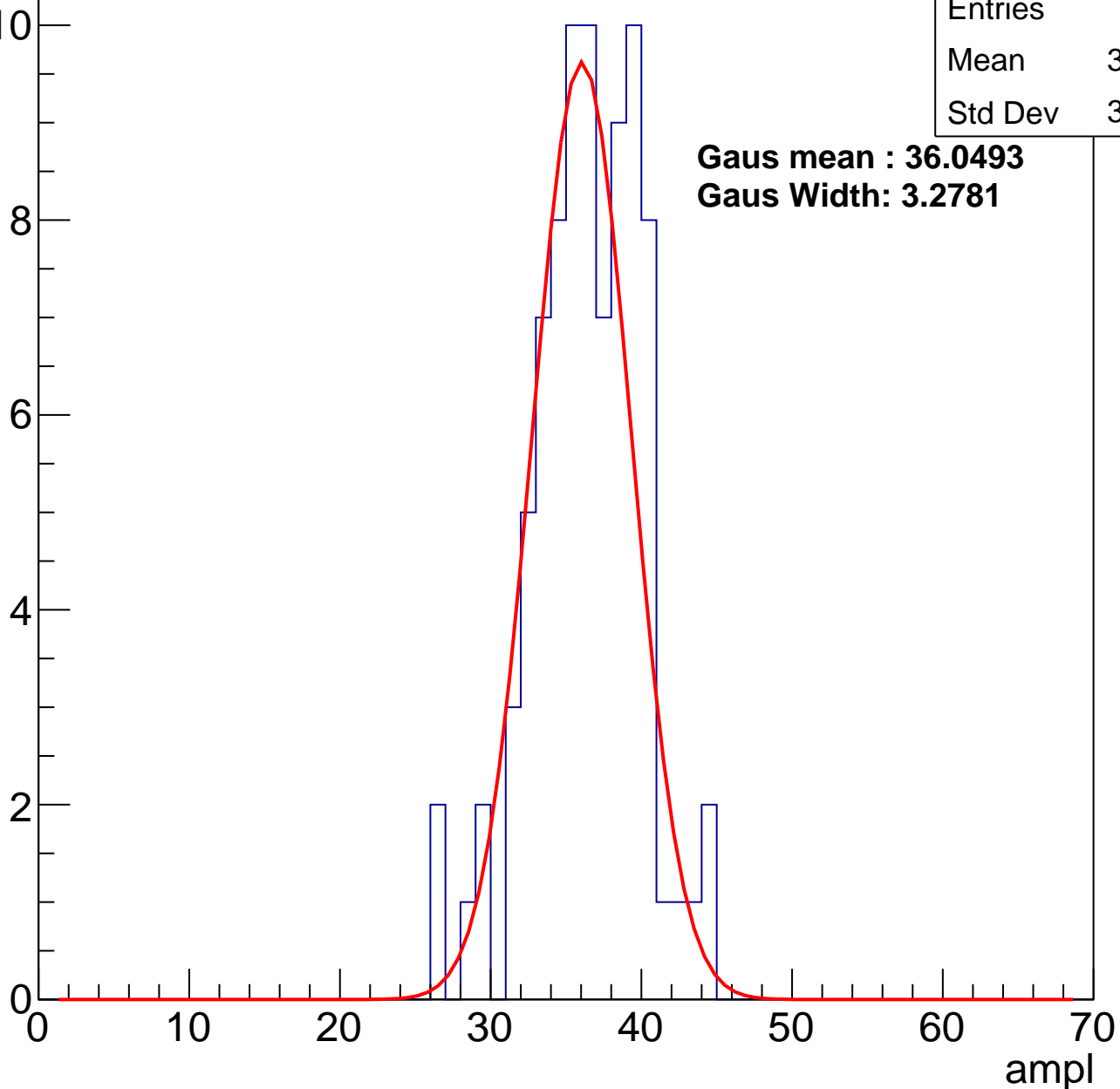
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 87    |
| Mean    | 35.97 |
| Std Dev | 3.589 |

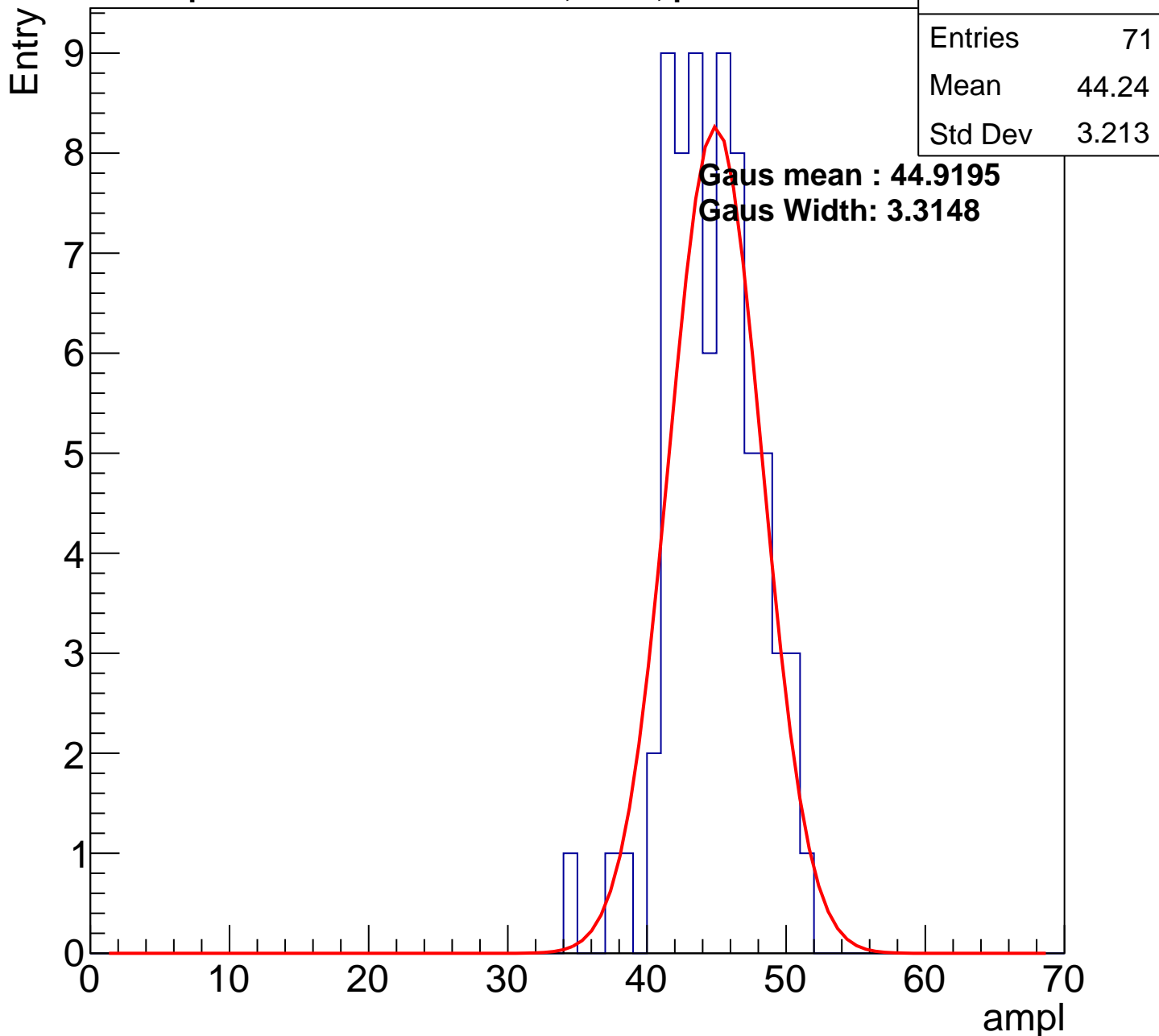
**Gaus mean : 36.0493**

**Gaus Width: 3.2781**



# B0L002S, U1-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

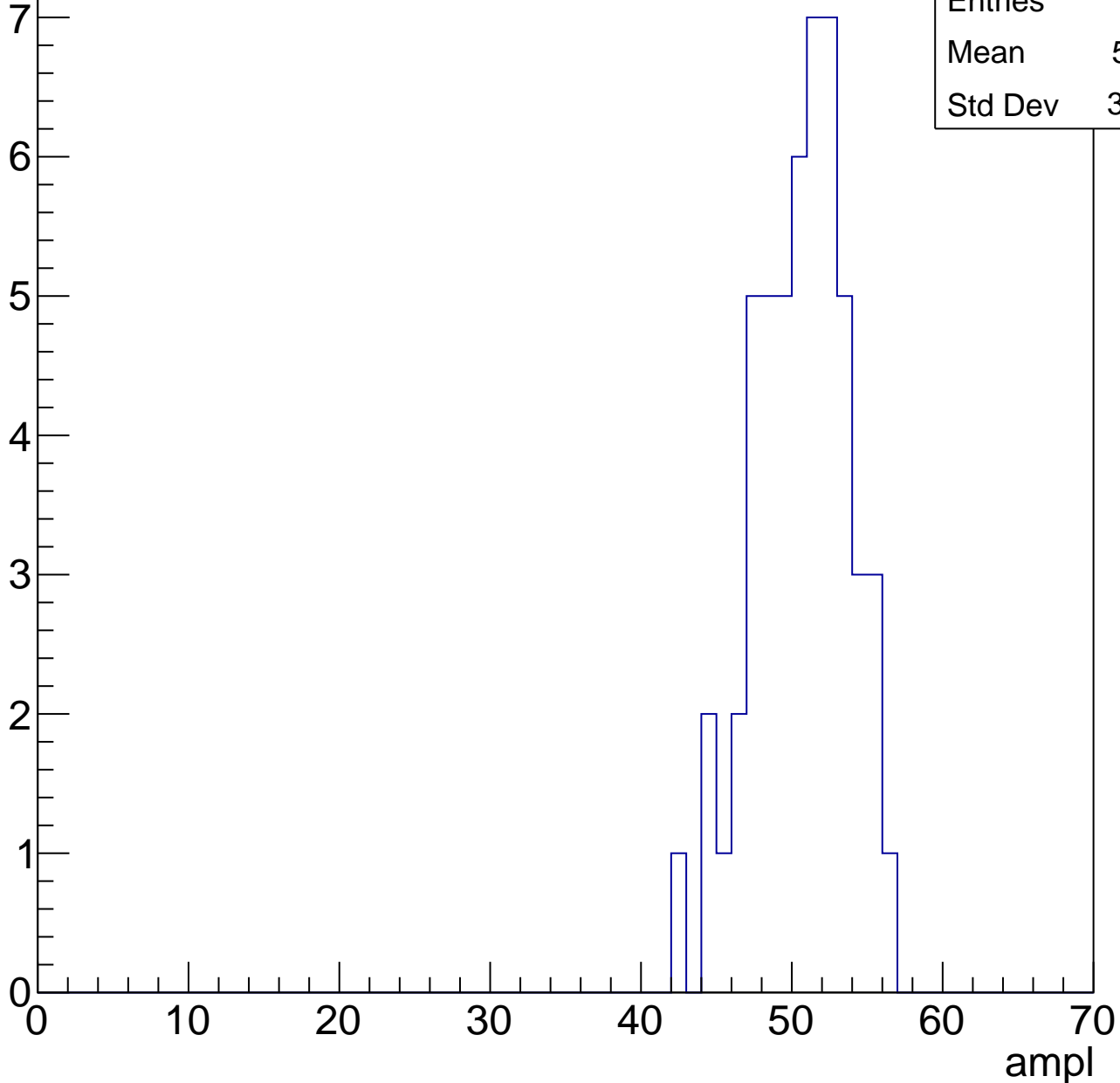


# B0L002S, U1-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 50.11 |
| Std Dev | 3.076 |

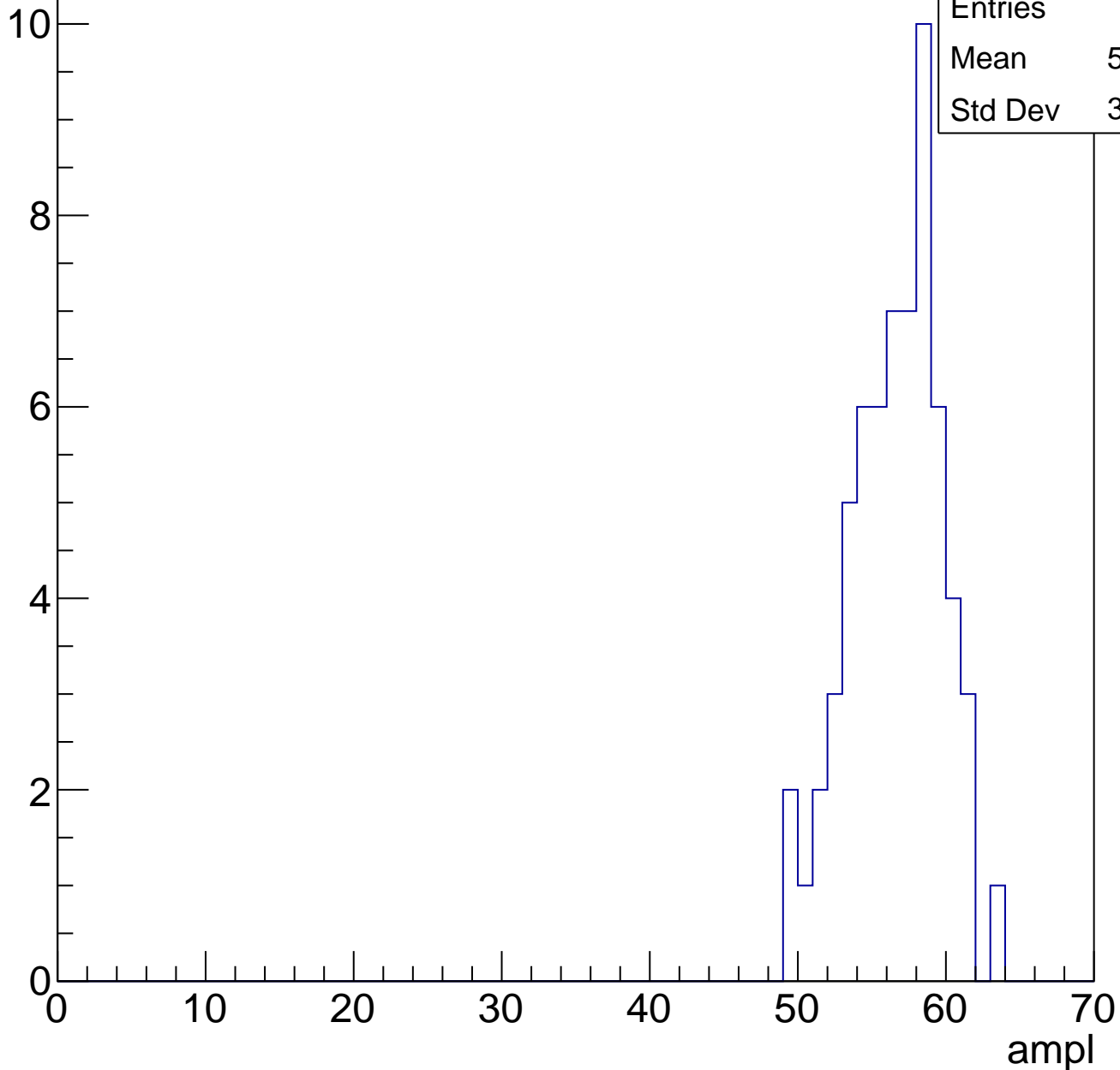


# B0L002S, U1-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 56.13 |
| Std Dev | 3.073 |

Entry

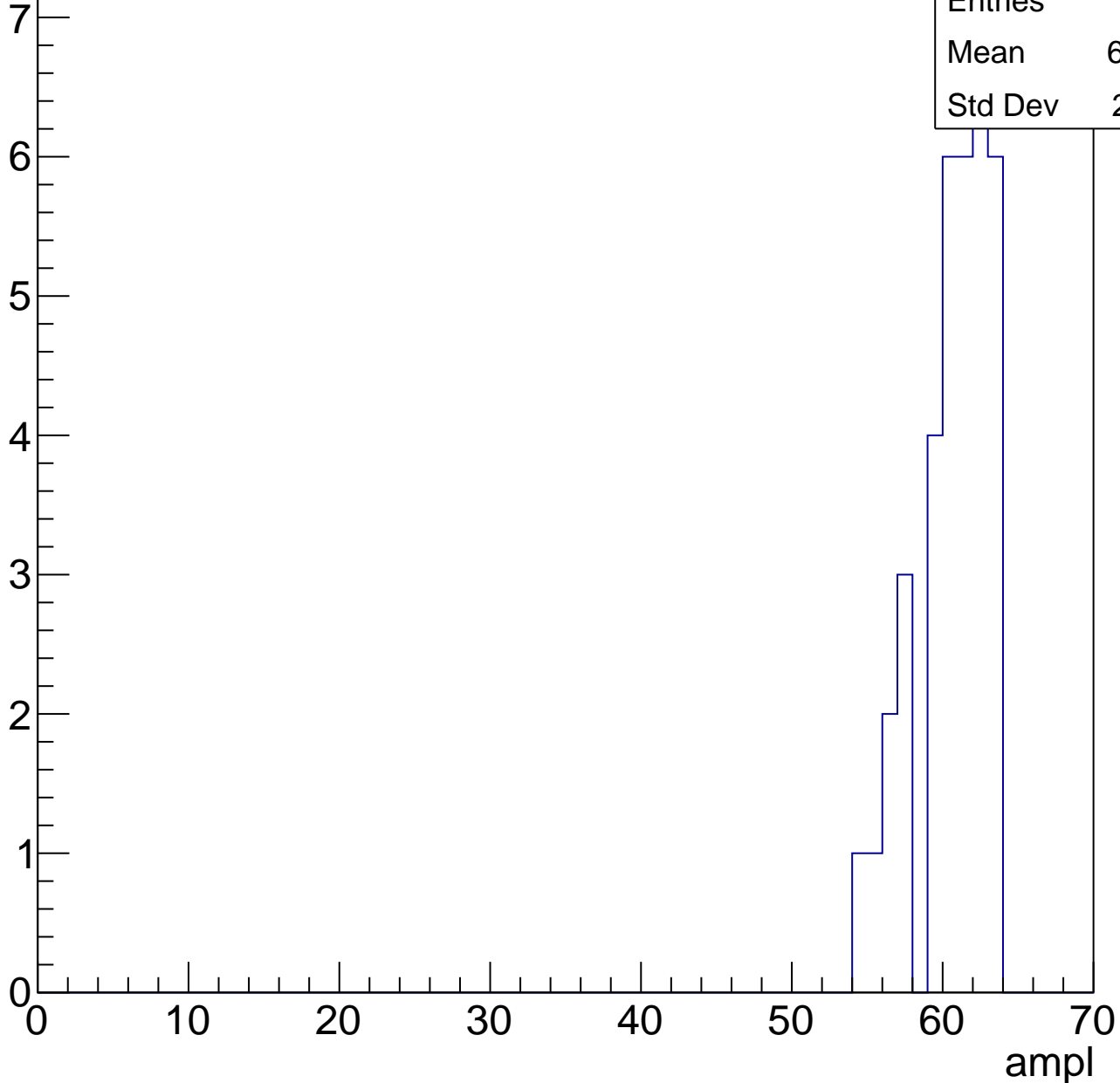


# B0L002S, U1-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

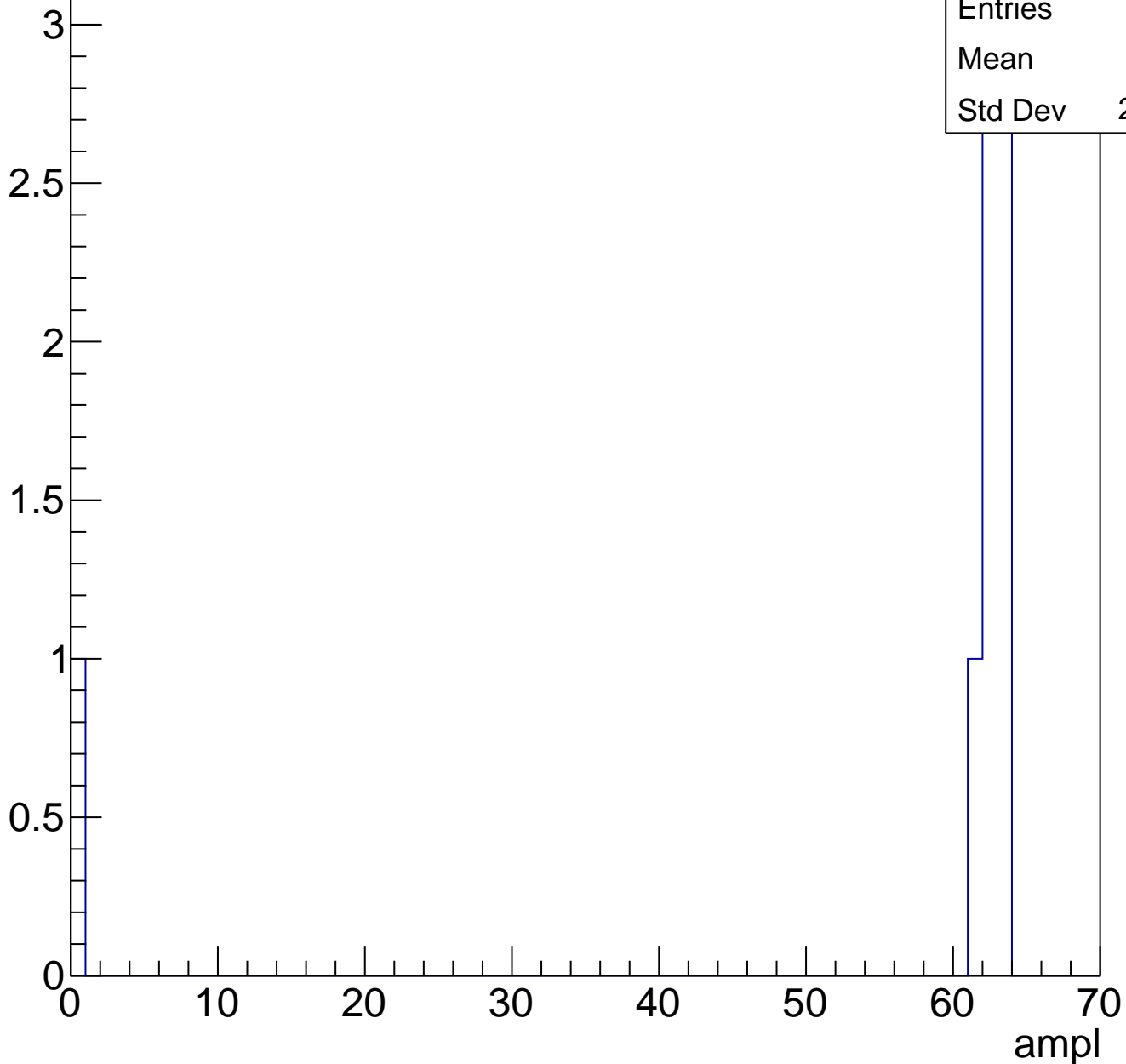
|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 60.17 |
| Std Dev | 2.421 |



# B0L002S, U1-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 11.5 |
| Std Dev | 11.5 |

# B0L002S, U1-ch16, adc0

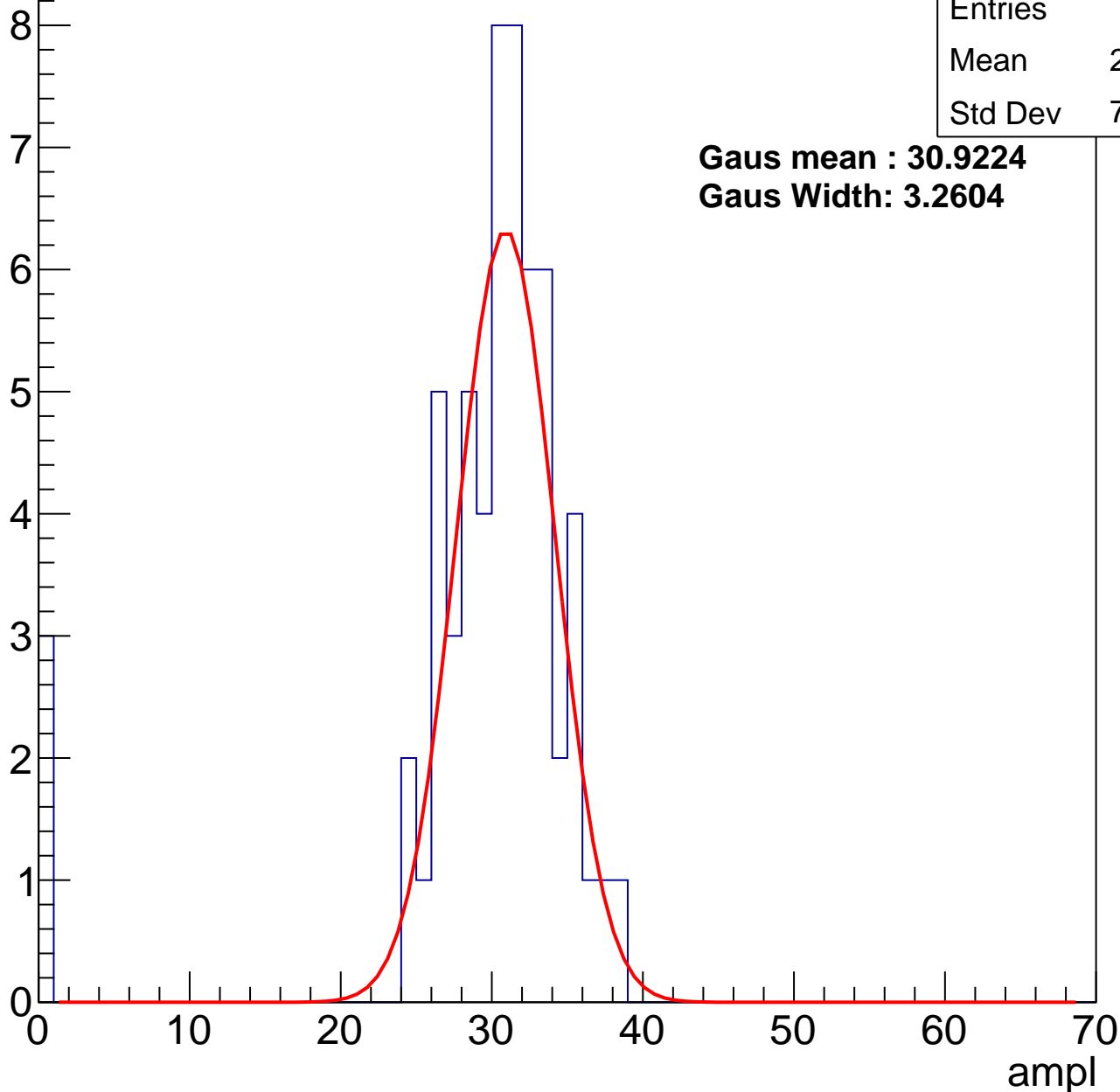
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 28.95 |
| Std Dev | 7.336 |

**Gaus mean : 30.9224**

**Gaus Width: 3.2604**



# B0L002S, U1-ch16, adc1

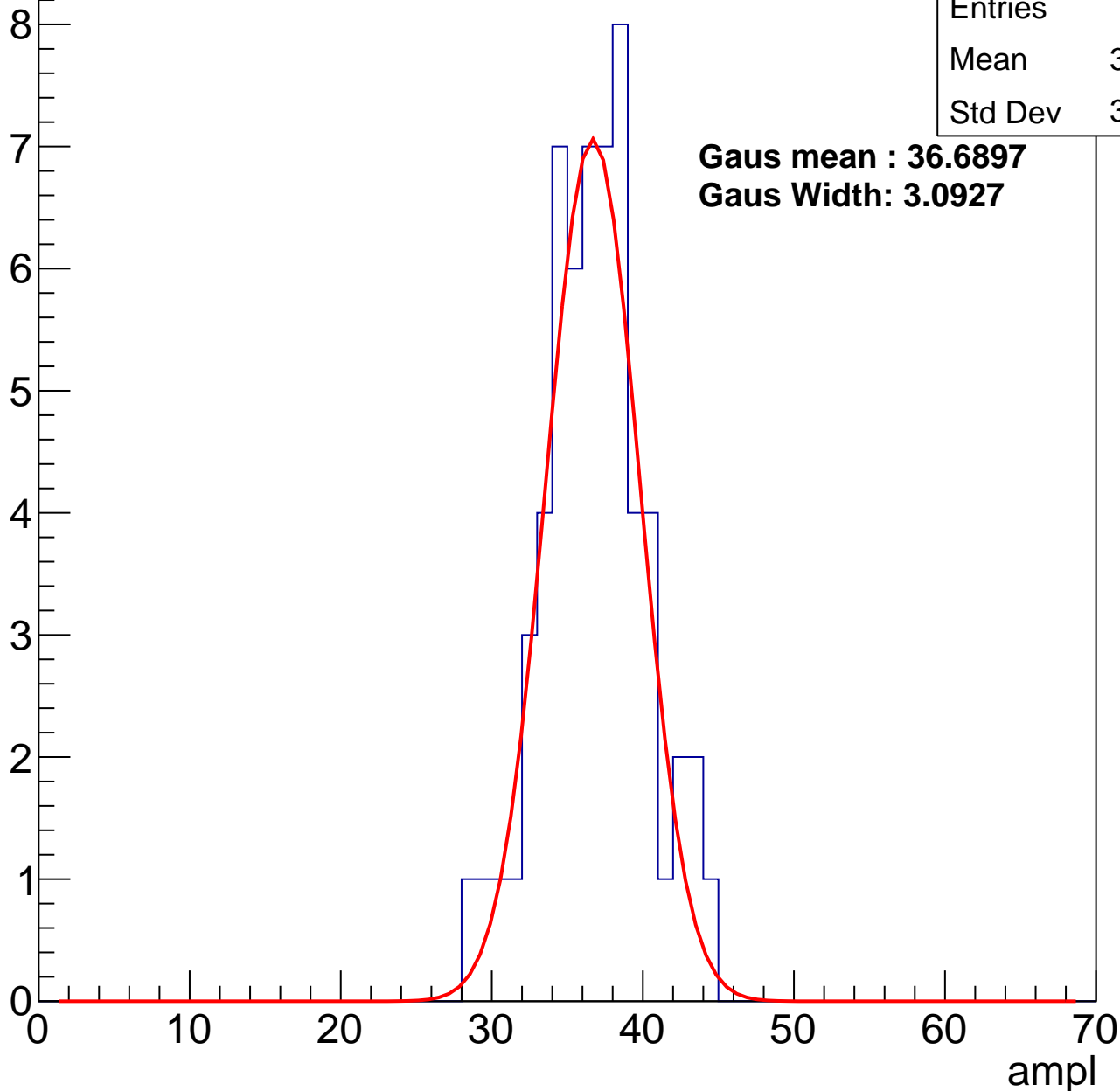
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 36.33 |
| Std Dev | 3.375 |

**Gaus mean : 36.6897**

**Gaus Width: 3.0927**



# B0L002S, U1-ch16, adc2

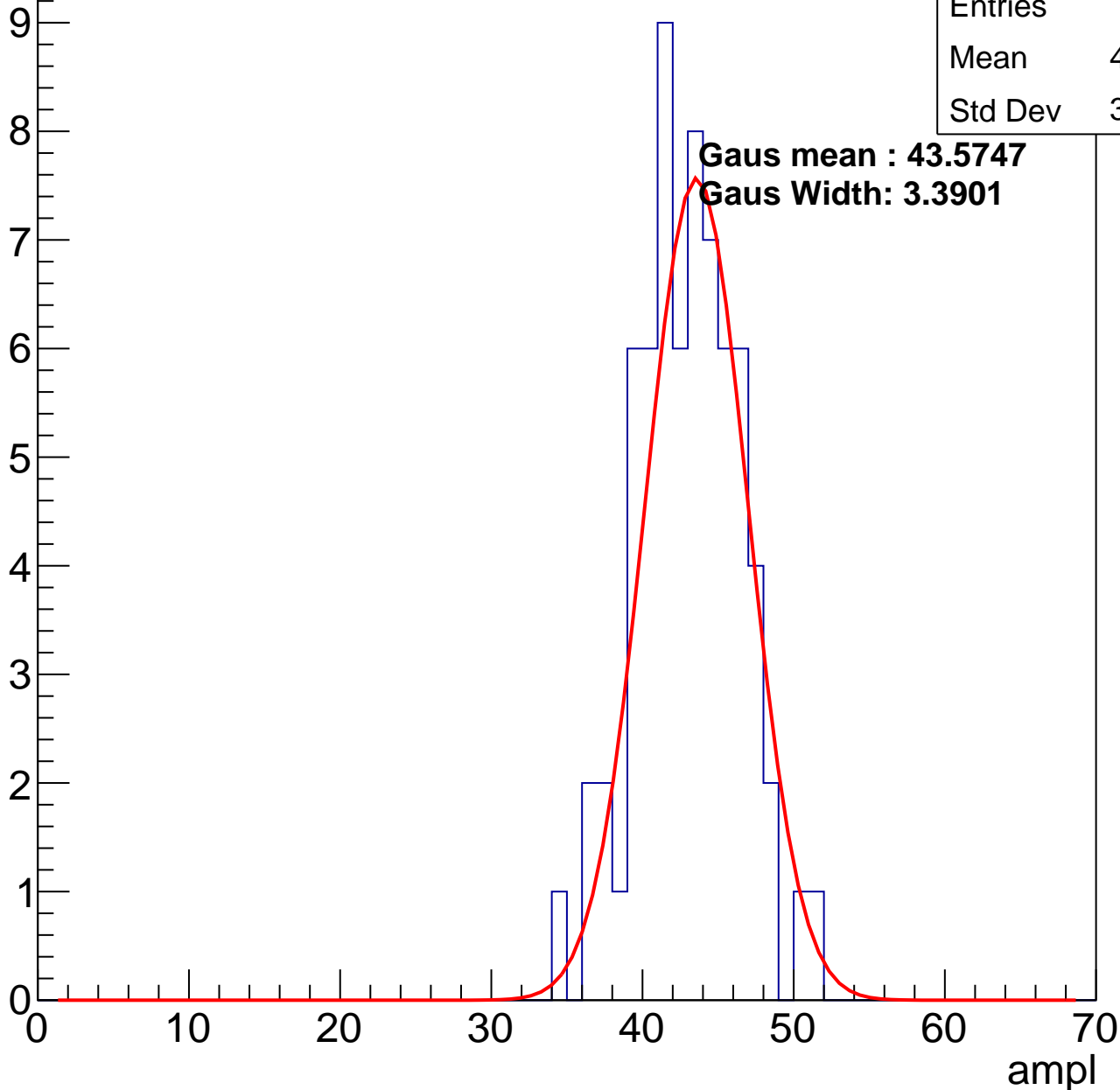
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 42.59 |
| Std Dev | 3.362 |

**Gaus mean : 43.5747**

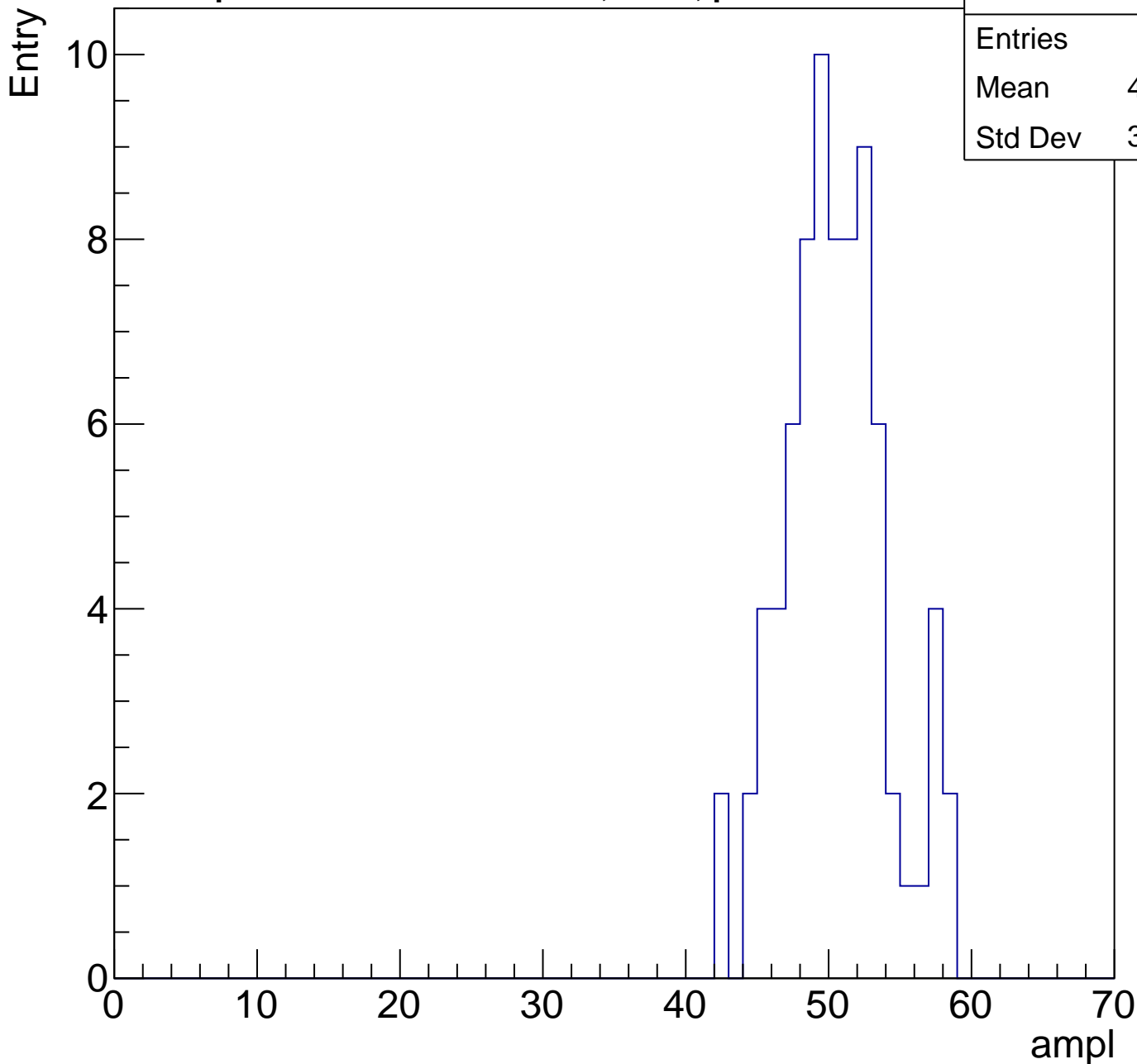
**Gaus Width: 3.3901**



# B0L002S, U1-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

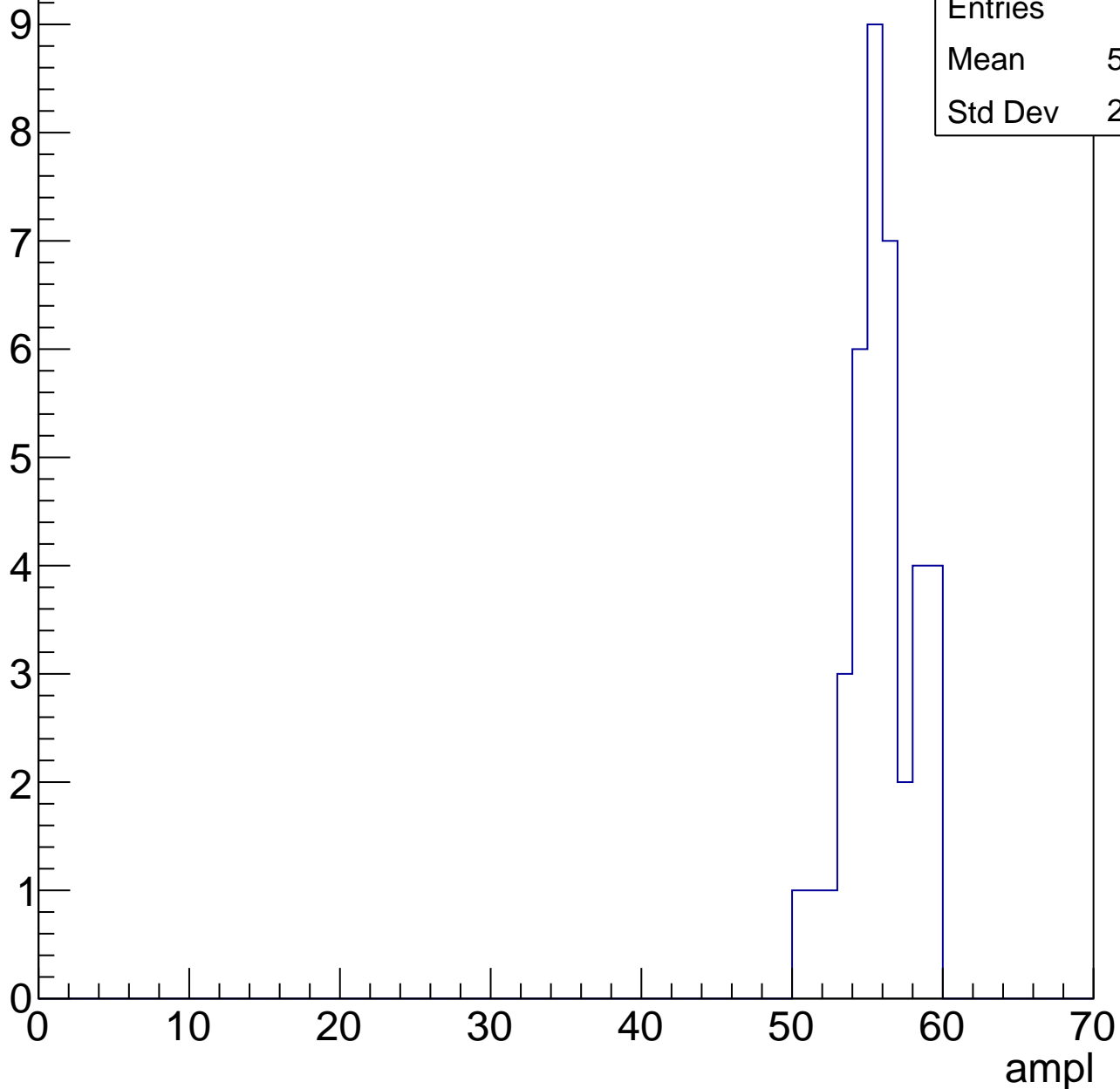
|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 49.99 |
| Std Dev | 3.558 |



# B0L002S, U1-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

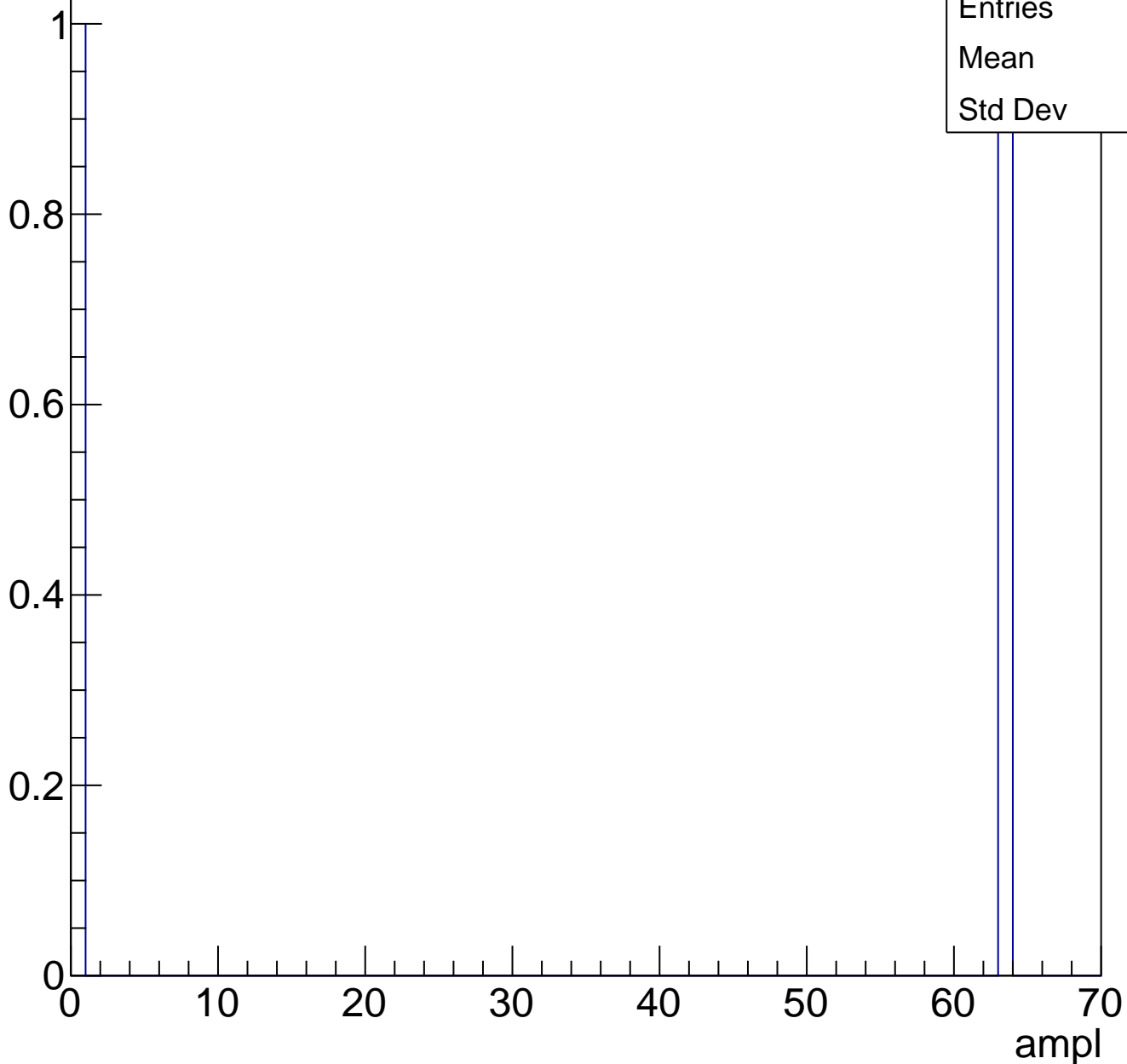
ampl

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 60.07 |
| Std Dev | 2.427 |

# B0L002S, U1-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch17, adc0

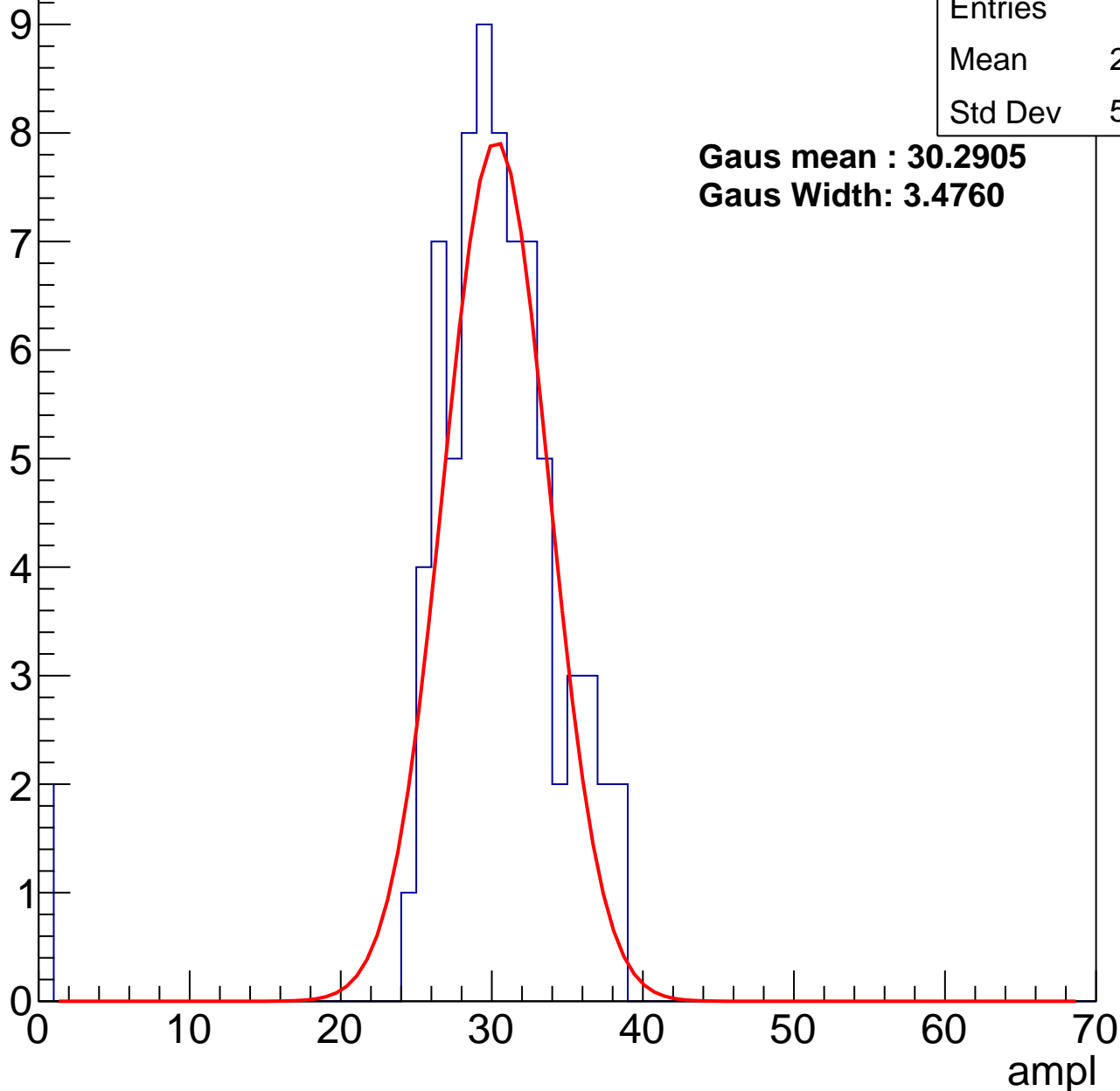
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 29.37 |
| Std Dev | 5.917 |

**Gaus mean : 30.2905**

**Gaus Width: 3.4760**



# B0L002S, U1-ch17, adc1

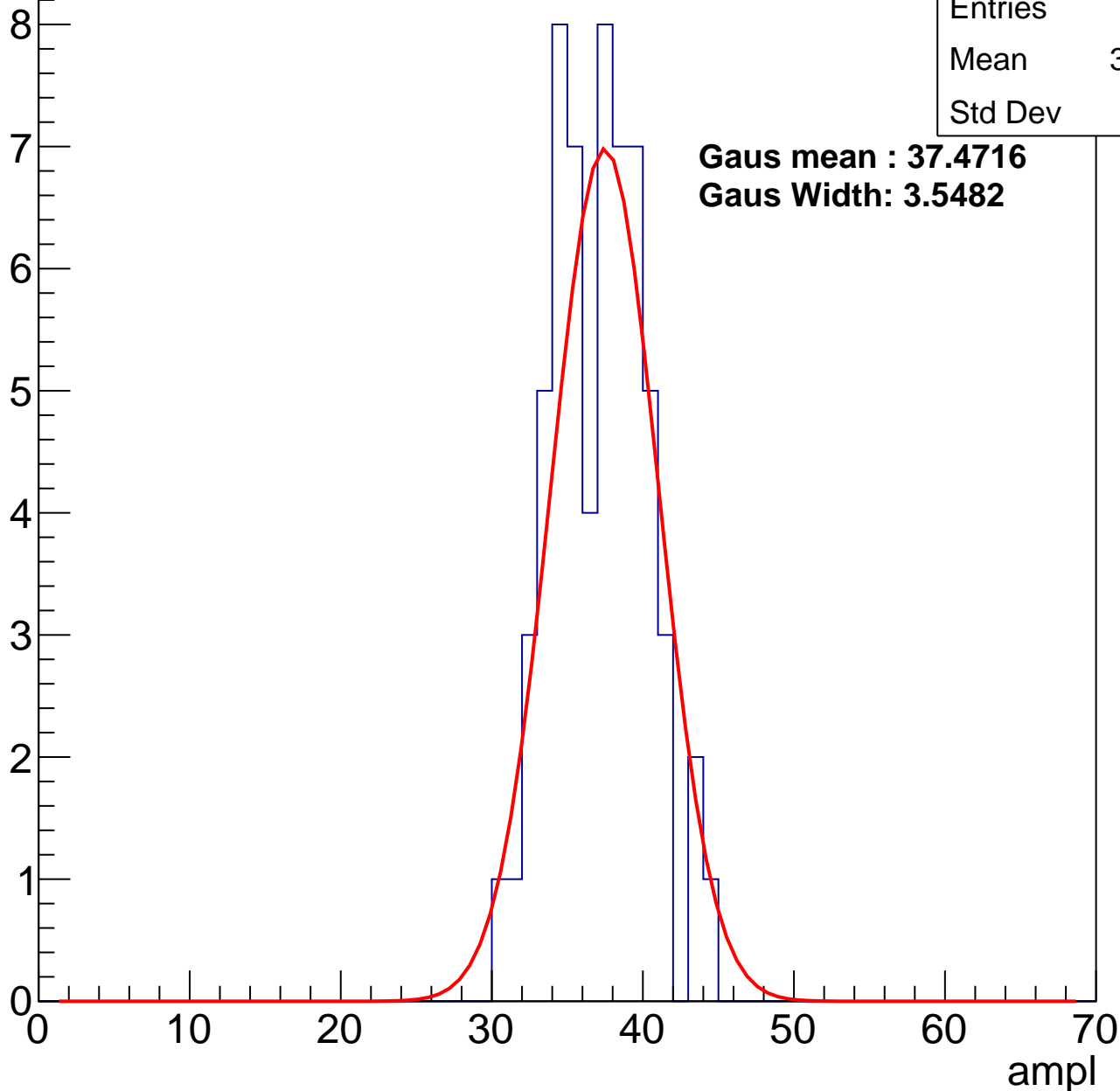
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 36.63 |
| Std Dev | 3.07  |

**Gaus mean : 37.4716**

**Gaus Width: 3.5482**



# B0L002S, U1-ch17, adc2

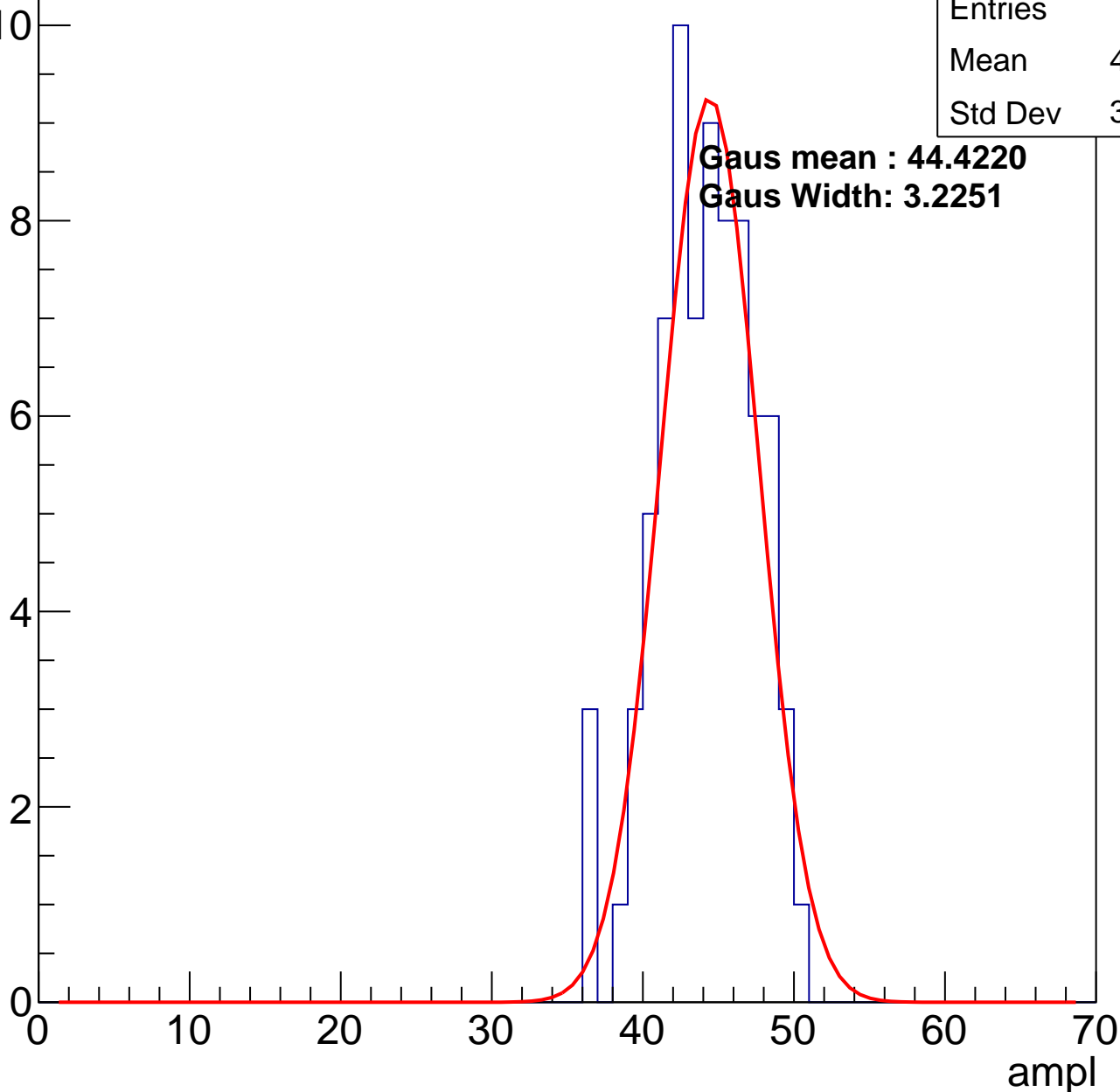
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 43.66 |
| Std Dev | 3.193 |

**Gaus mean : 44.4220**

**Gaus Width: 3.2251**



# B0L002S, U1-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 50.93 |
| Std Dev | 3.218 |

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

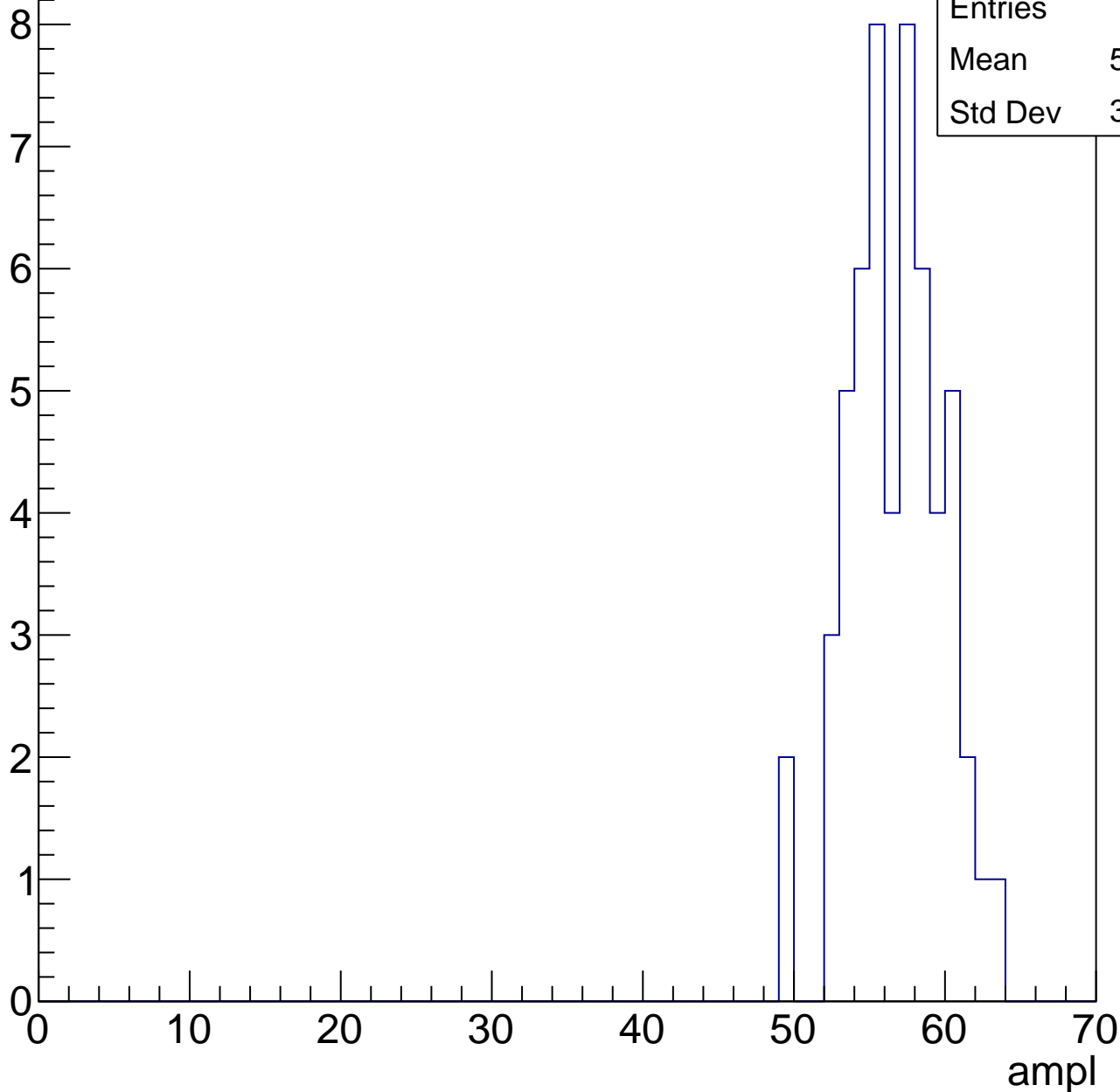
70

# B0L002S, U1-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 56.25 |
| Std Dev | 3.022 |



# B0L002S, U1-ch17, adc5

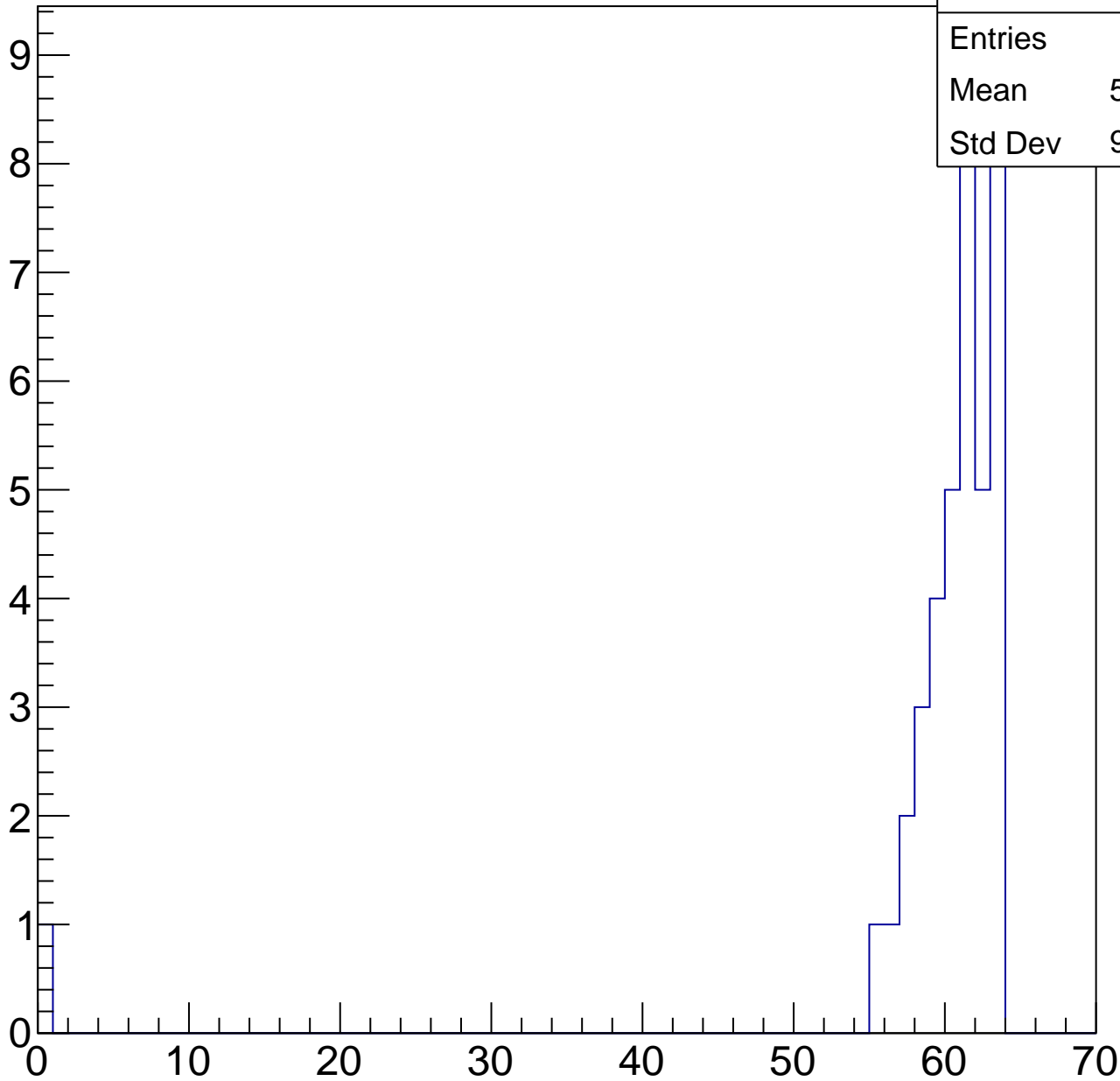
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 58.97 |
| Std Dev | 9.797 |

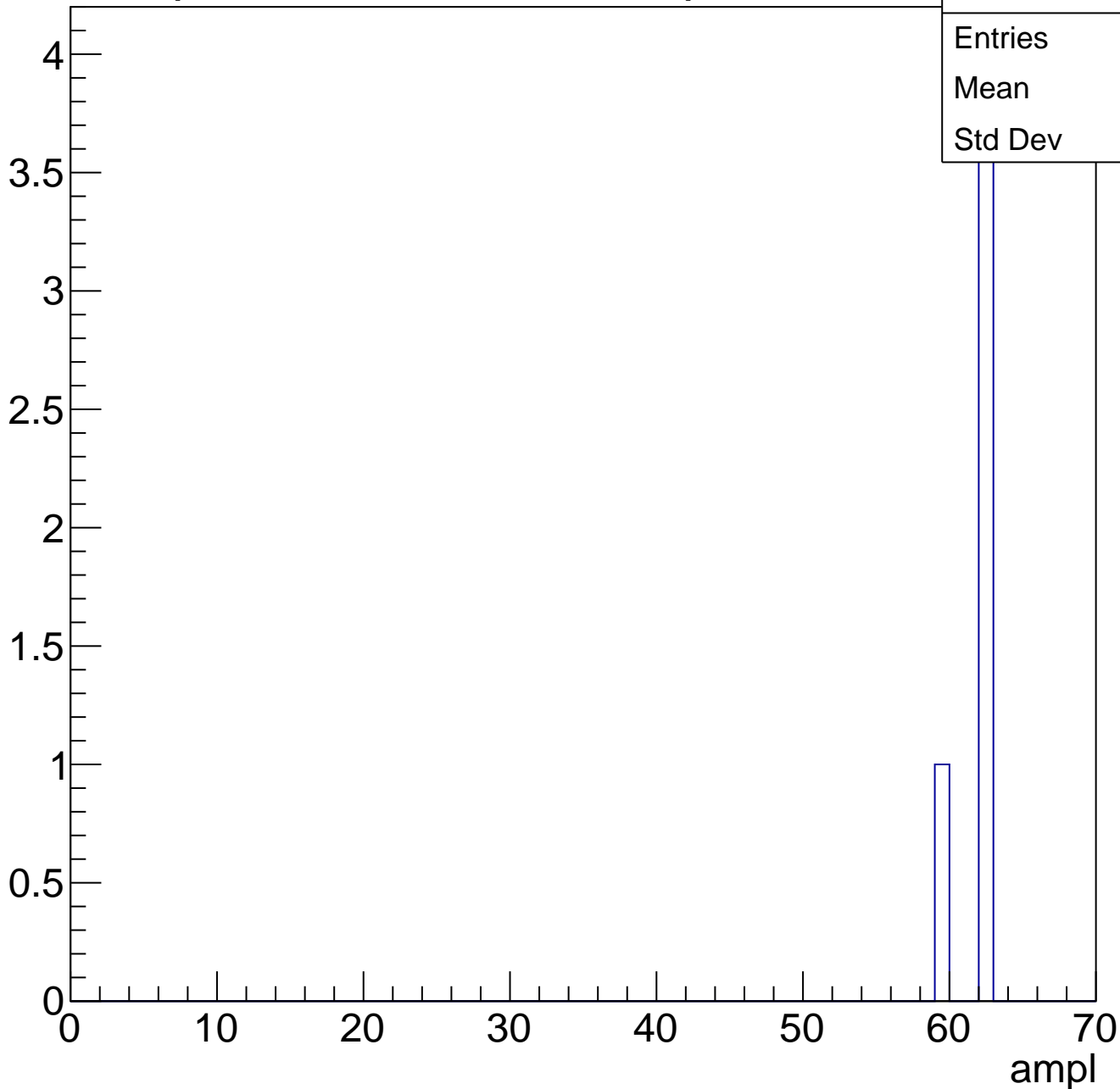
ampl



# B0L002S, U1-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 20 |
| Std Dev | 0  |

# B0L002S, U1-ch18, adc0

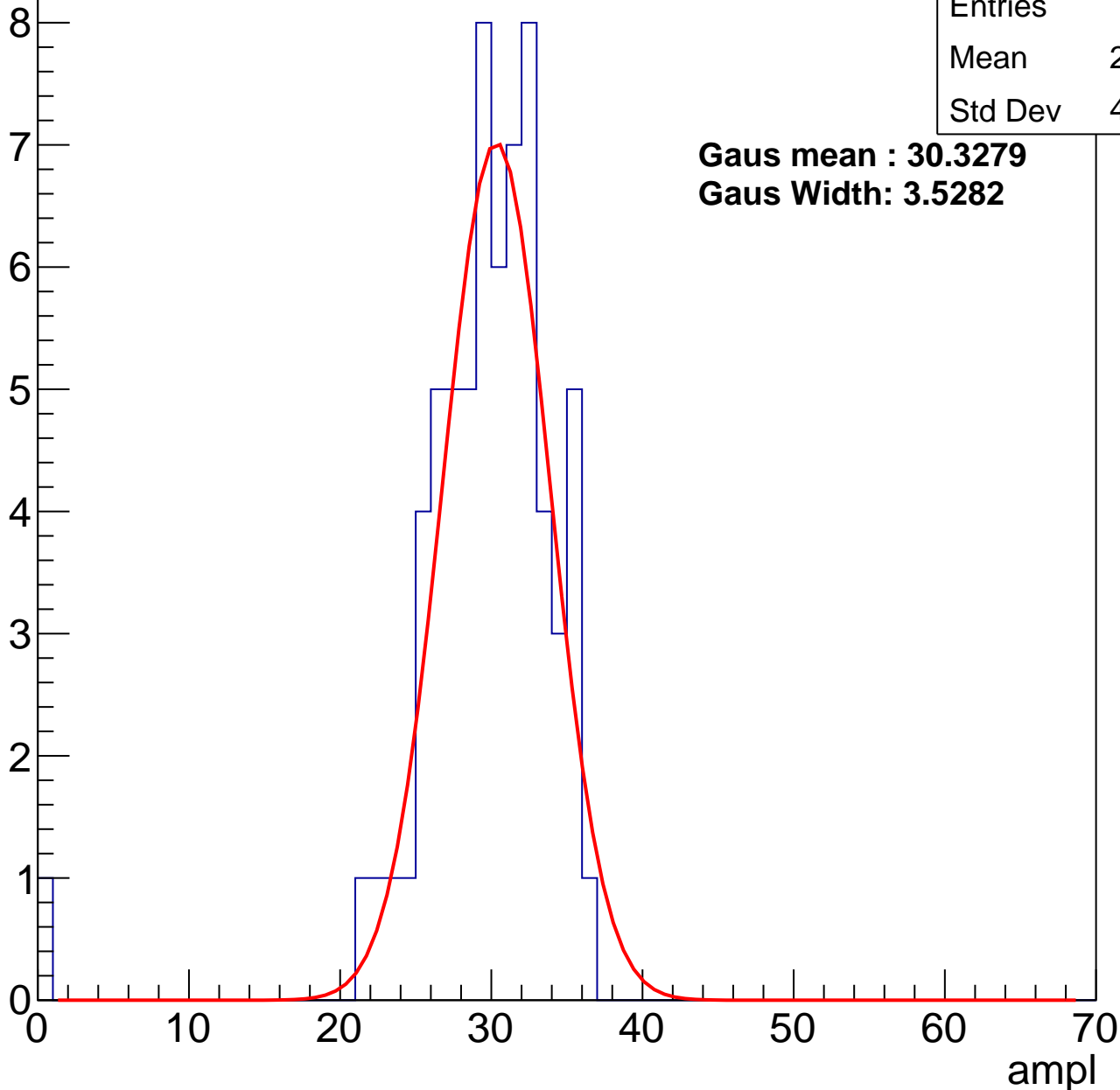
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 29.17 |
| Std Dev | 4.956 |

**Gaus mean : 30.3279**

**Gaus Width: 3.5282**

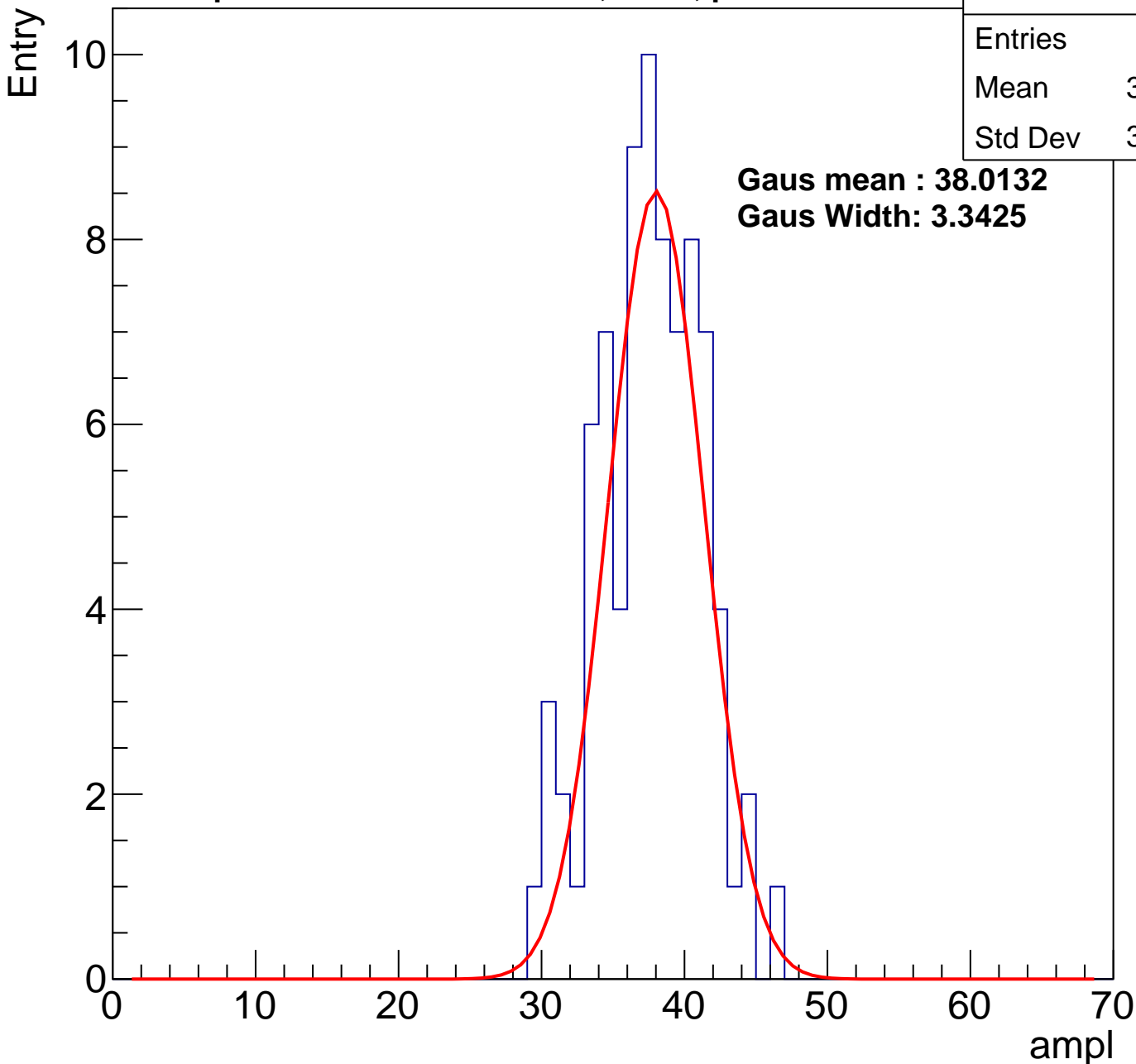


# B0L002S, U1-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 37.19 |
| Std Dev | 3.556 |

**Gaus mean : 38.0132**  
**Gaus Width: 3.3425**



# B0L002S, U1-ch18, adc2

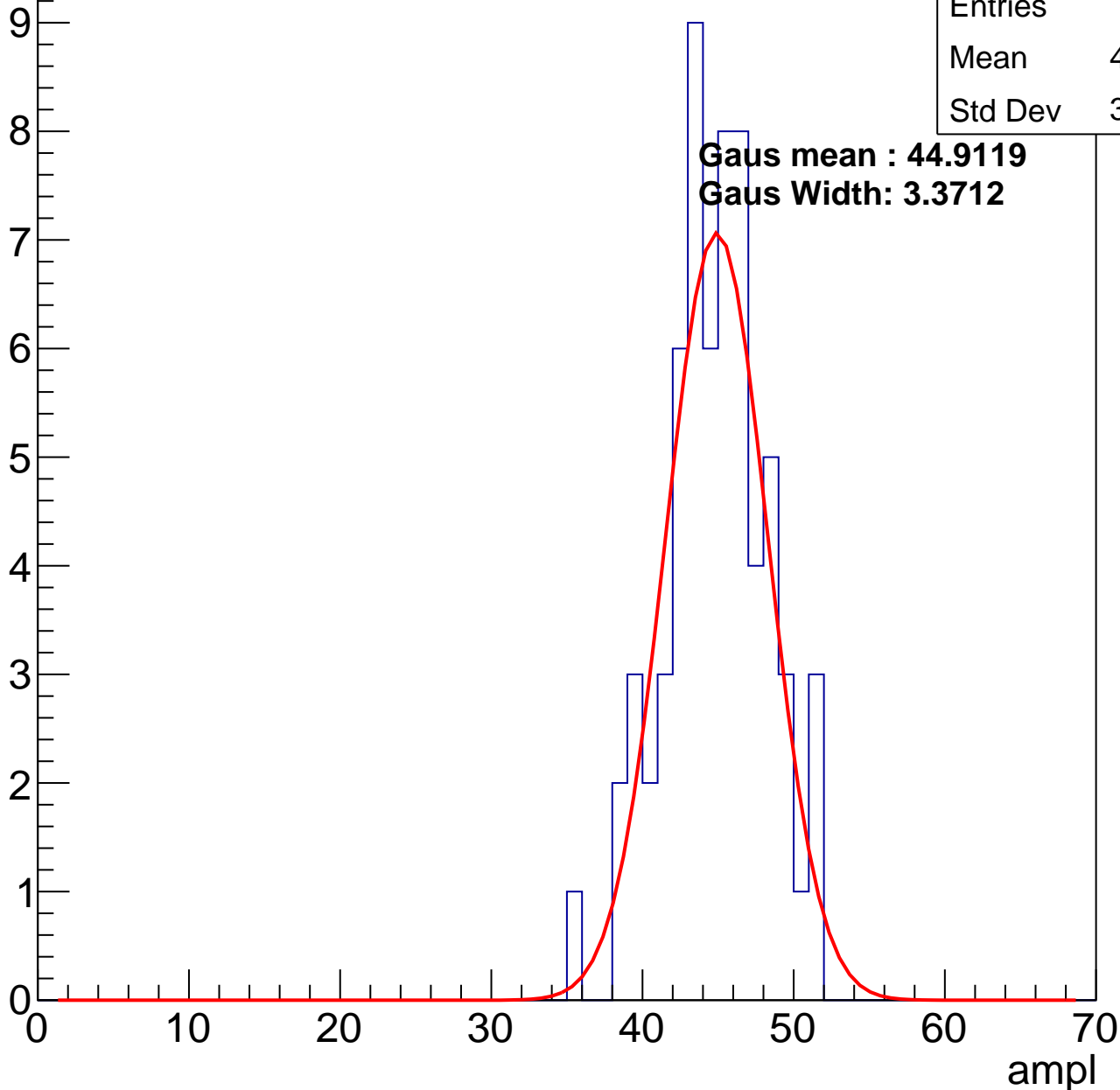
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 44.38 |
| Std Dev | 3.366 |

**Gaus mean : 44.9119**

**Gaus Width: 3.3712**

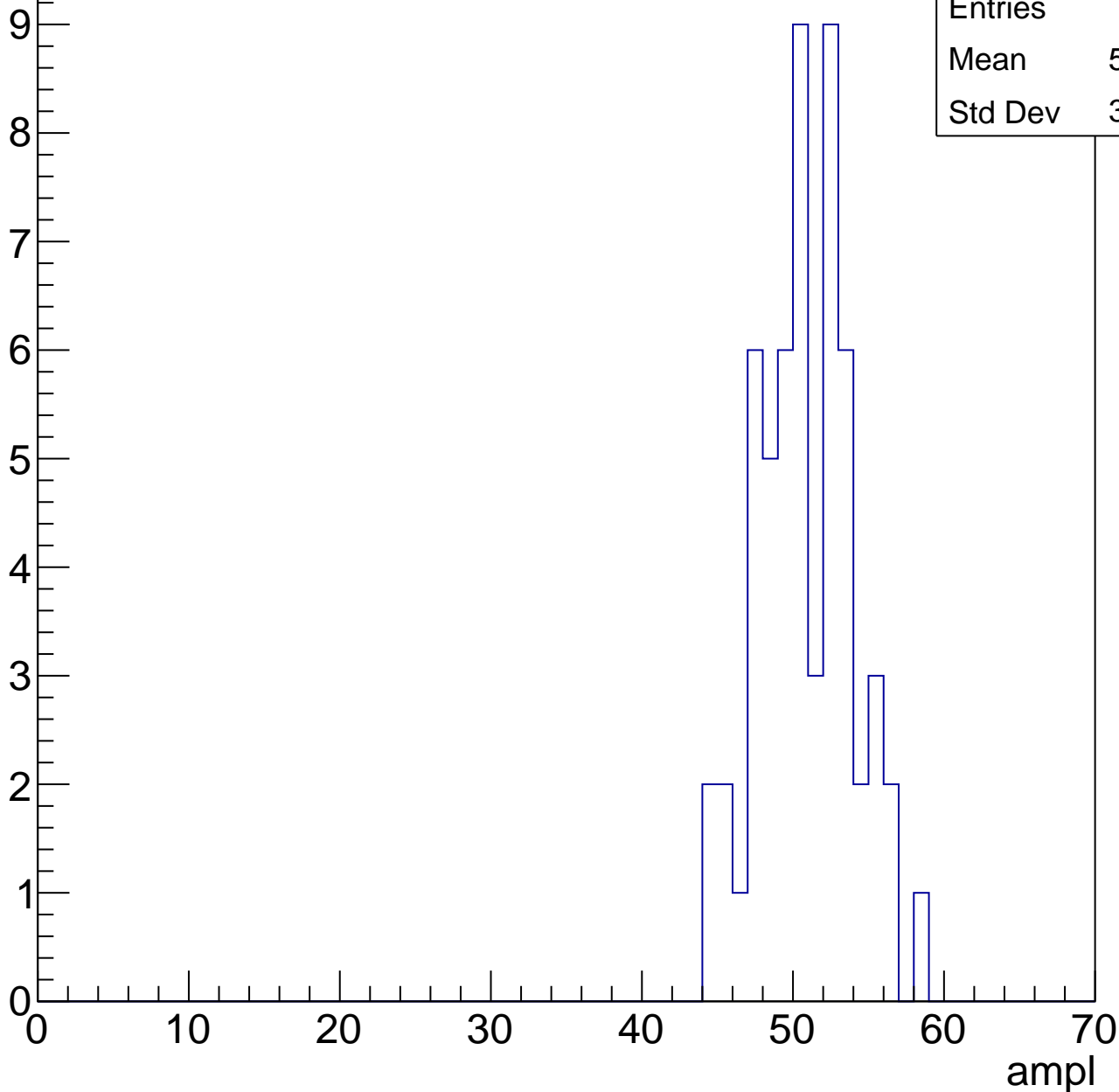


# B0L002S, U1-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

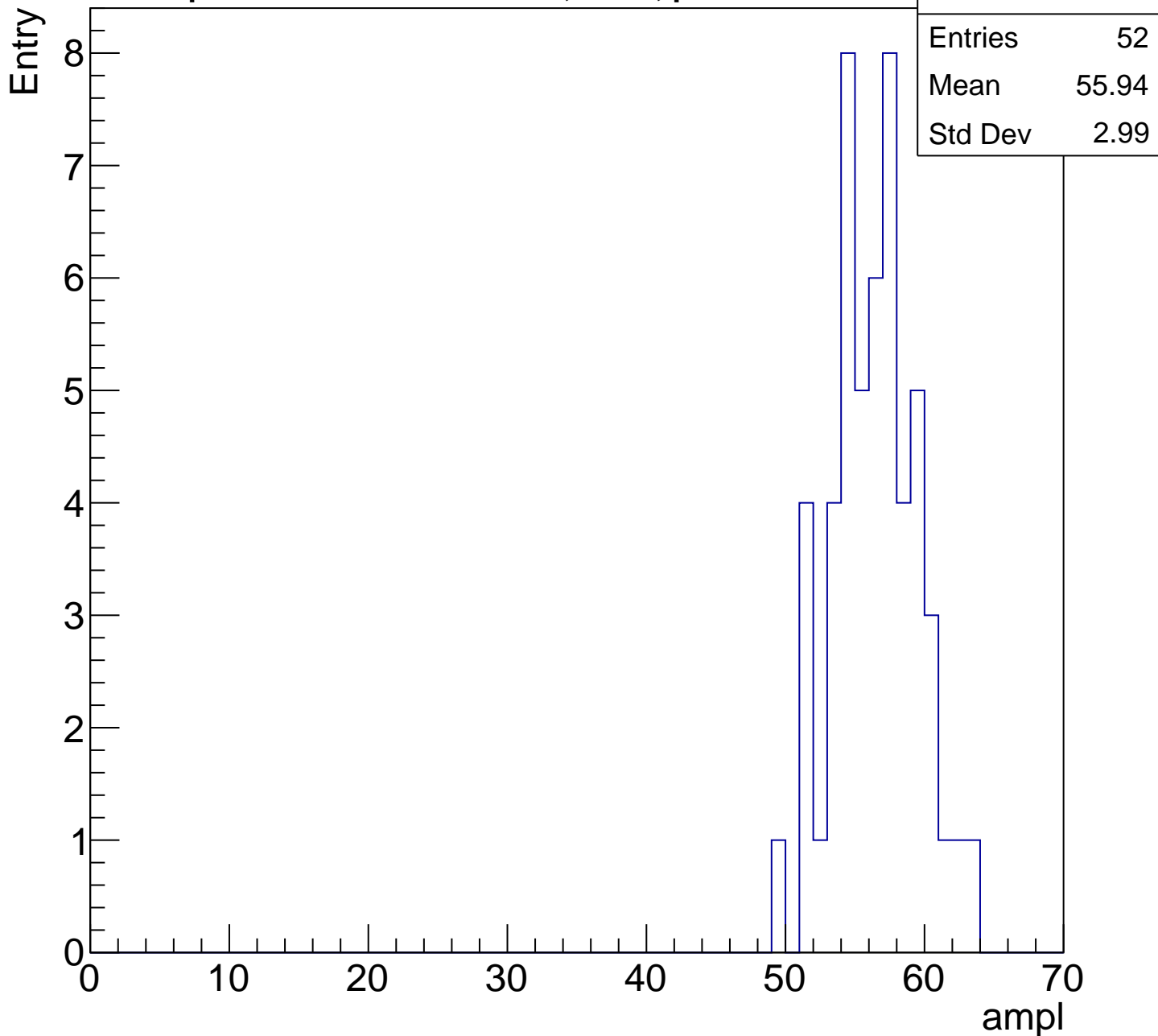
Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 50.39 |
| Std Dev | 3.094 |



# B0L002S, U1-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

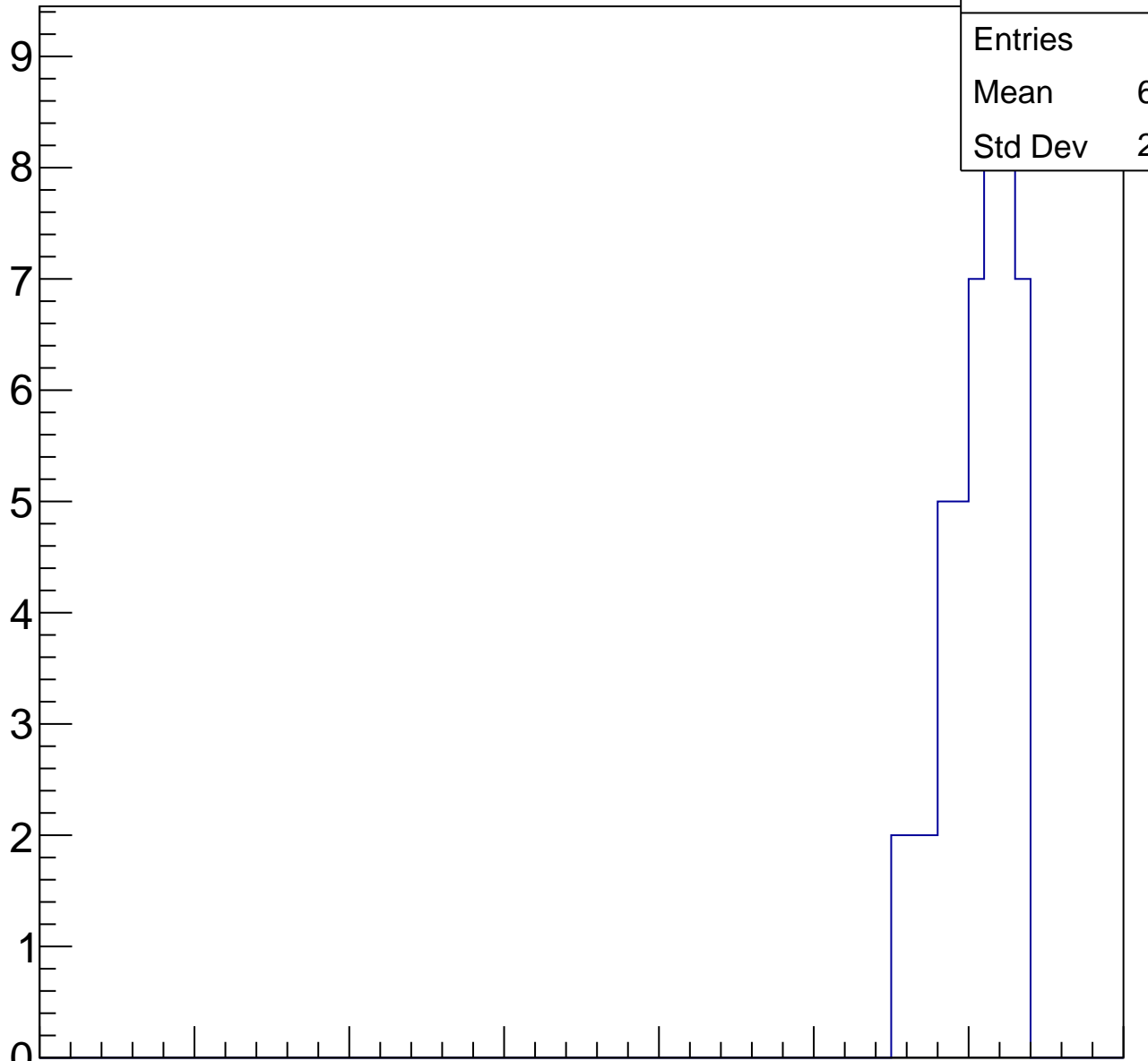
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 60.19 |
| Std Dev | 2.195 |

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch19, adc0

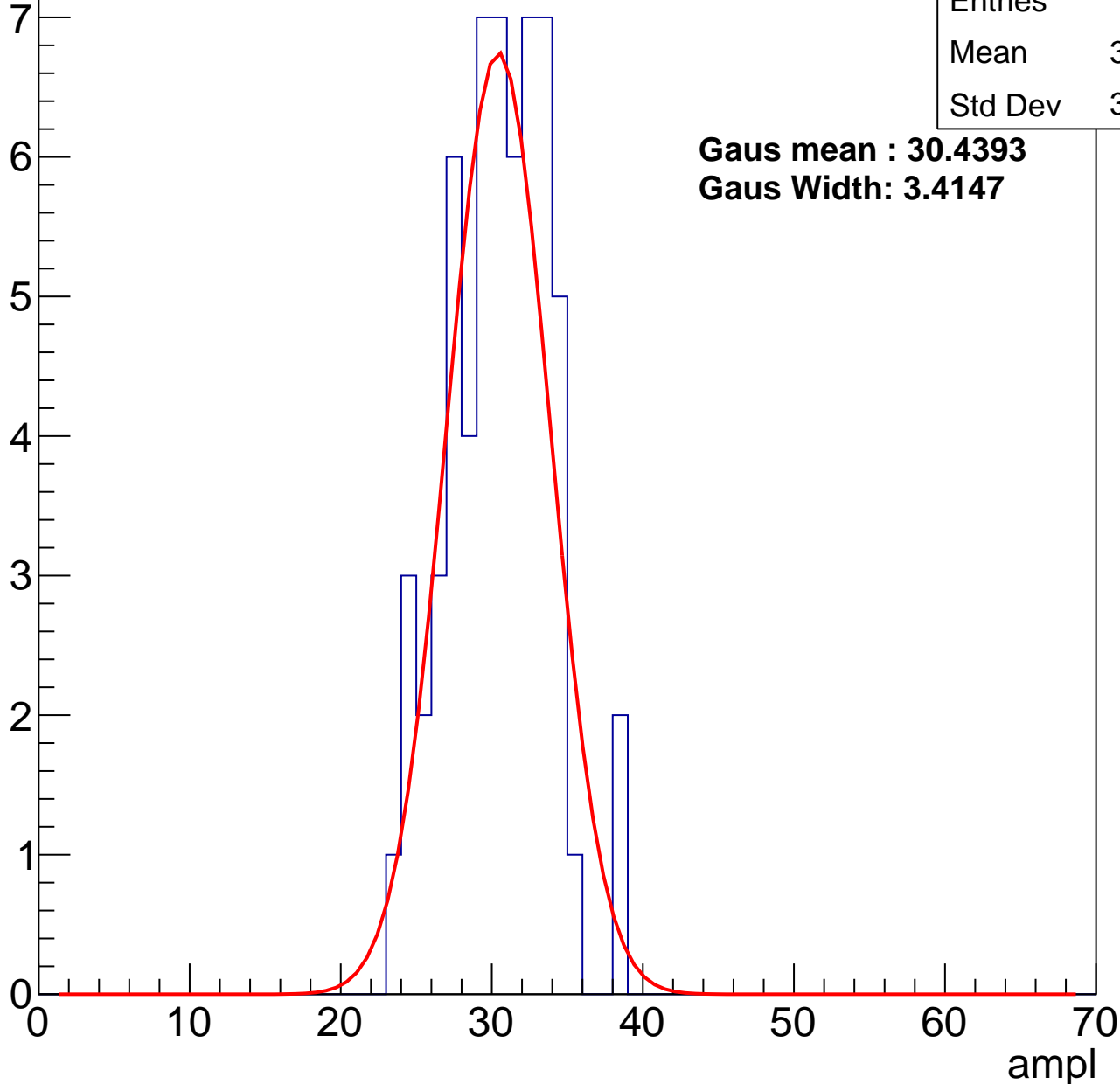
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 30.03 |
| Std Dev | 3.294 |

**Gaus mean : 30.4393**

**Gaus Width: 3.4147**



# B0L002S, U1-ch19, adc1

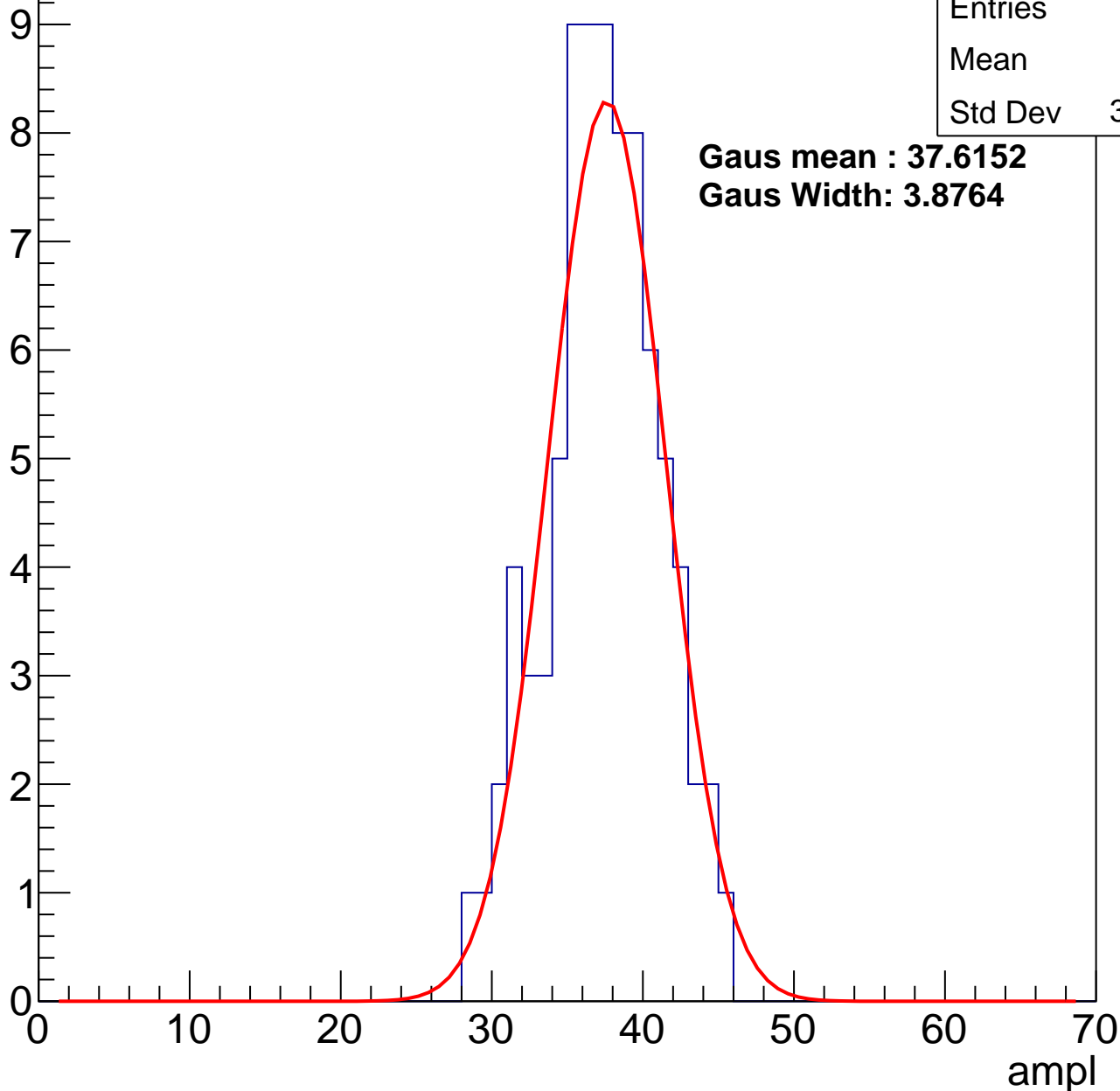
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 36.9  |
| Std Dev | 3.675 |

**Gaus mean : 37.6152**

**Gaus Width: 3.8764**



# B0L002S, U1-ch19, adc2

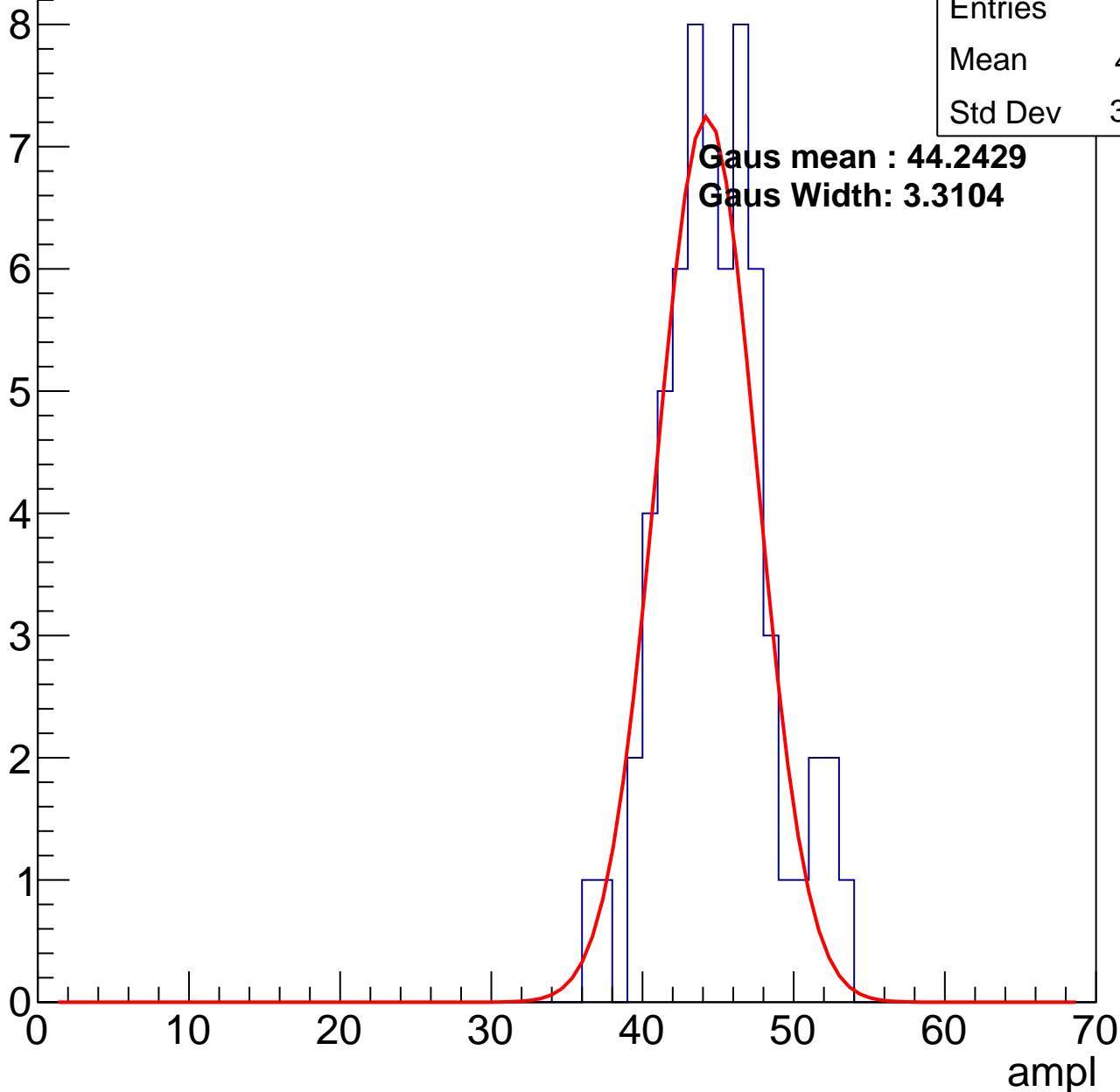
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 44.41 |
| Std Dev | 3.539 |

**Gaus mean : 44.2429**

**Gaus Width: 3.3104**

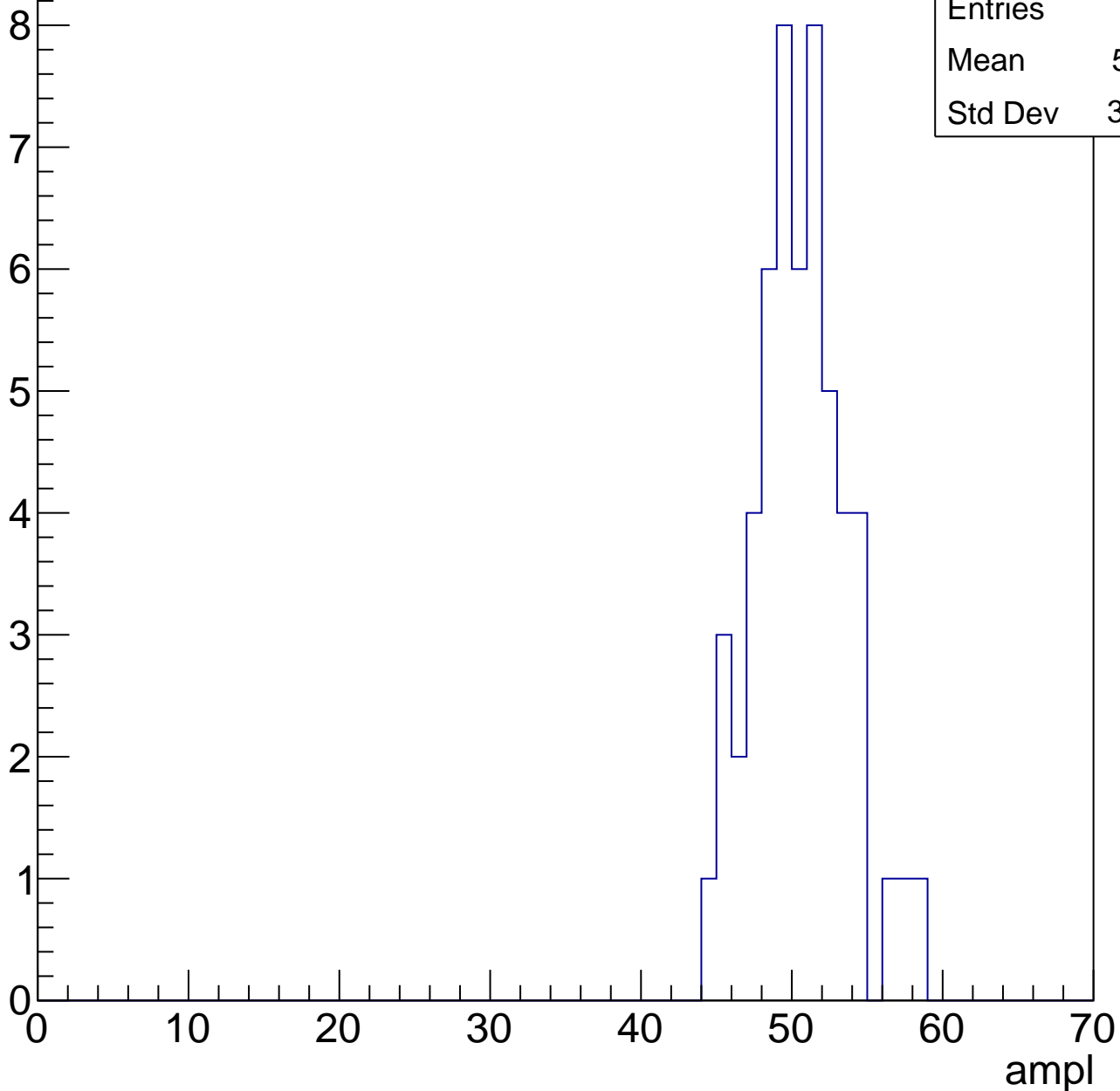


# B0L002S, U1-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 50.11 |
| Std Dev | 3.004 |

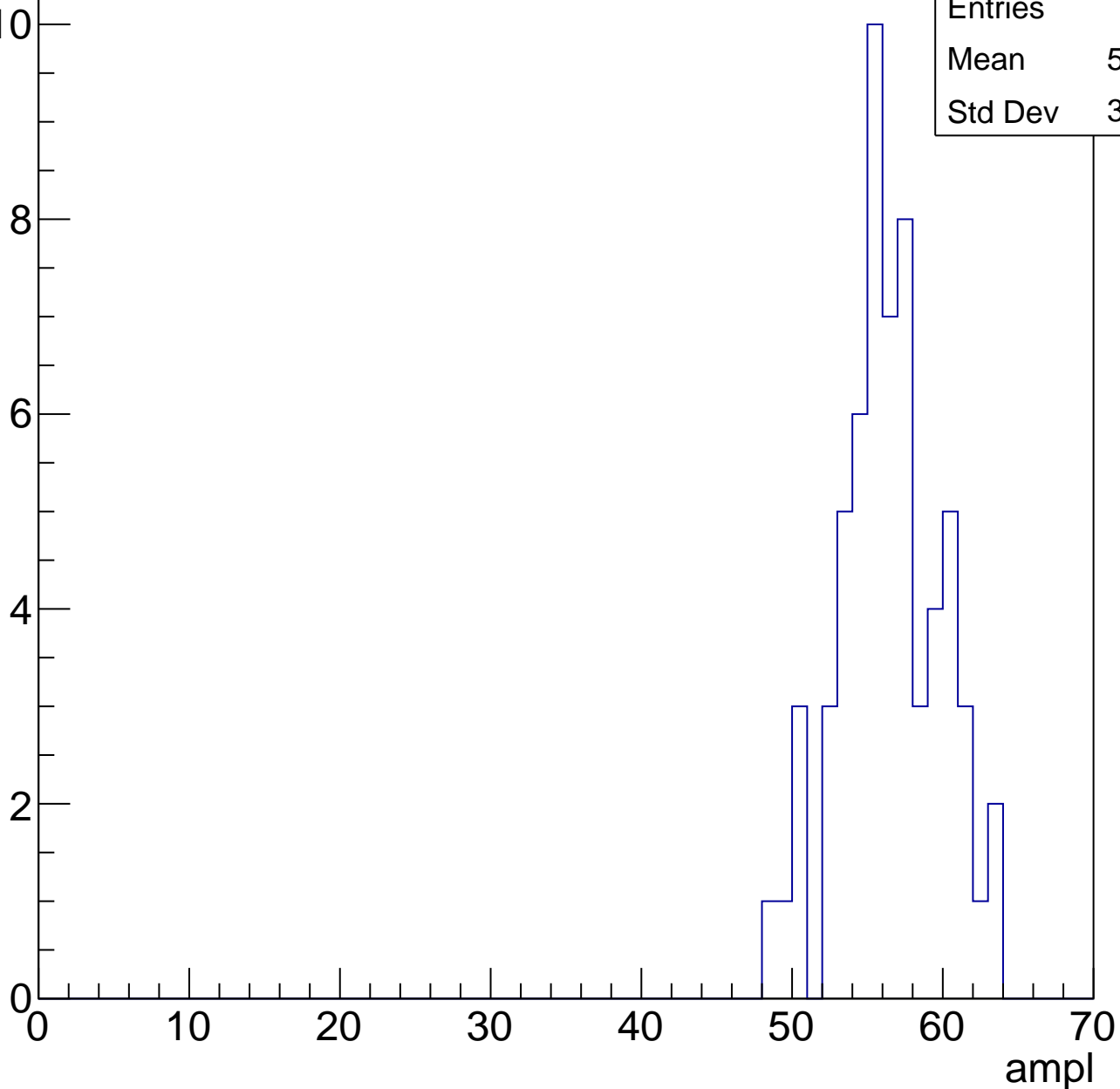


# B0L002S, U1-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 55.98 |
| Std Dev | 3.343 |

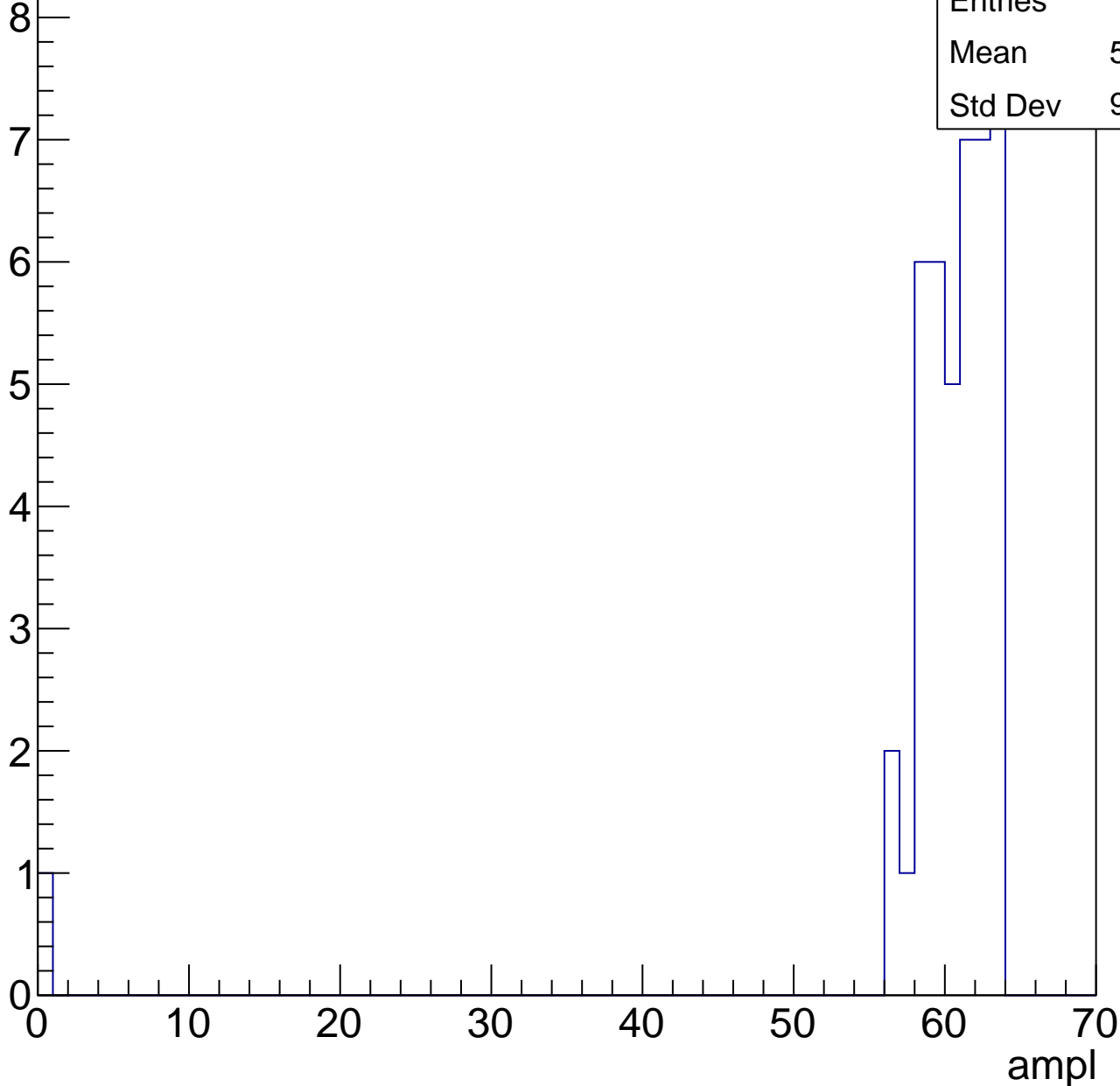


# B0L002S, U1-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 58.98 |
| Std Dev | 9.317 |



# B0L002S, U1-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 62 |
| Std Dev | 0  |

ampl



# B0L002S, U1-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch20, adc0

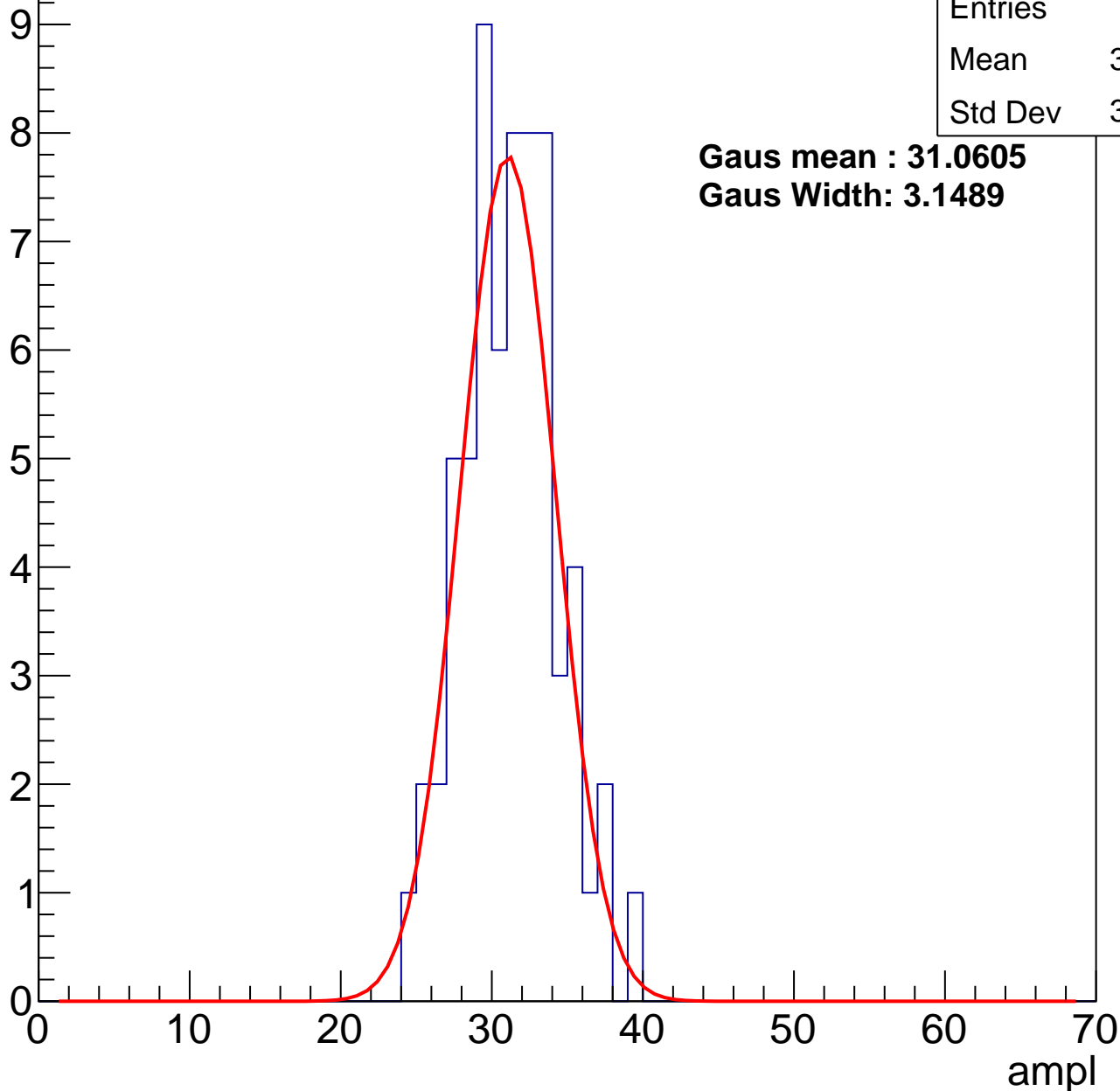
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 30.78 |
| Std Dev | 3.106 |

**Gaus mean : 31.0605**

**Gaus Width: 3.1489**



# B0L002S, U1-ch20, adc1

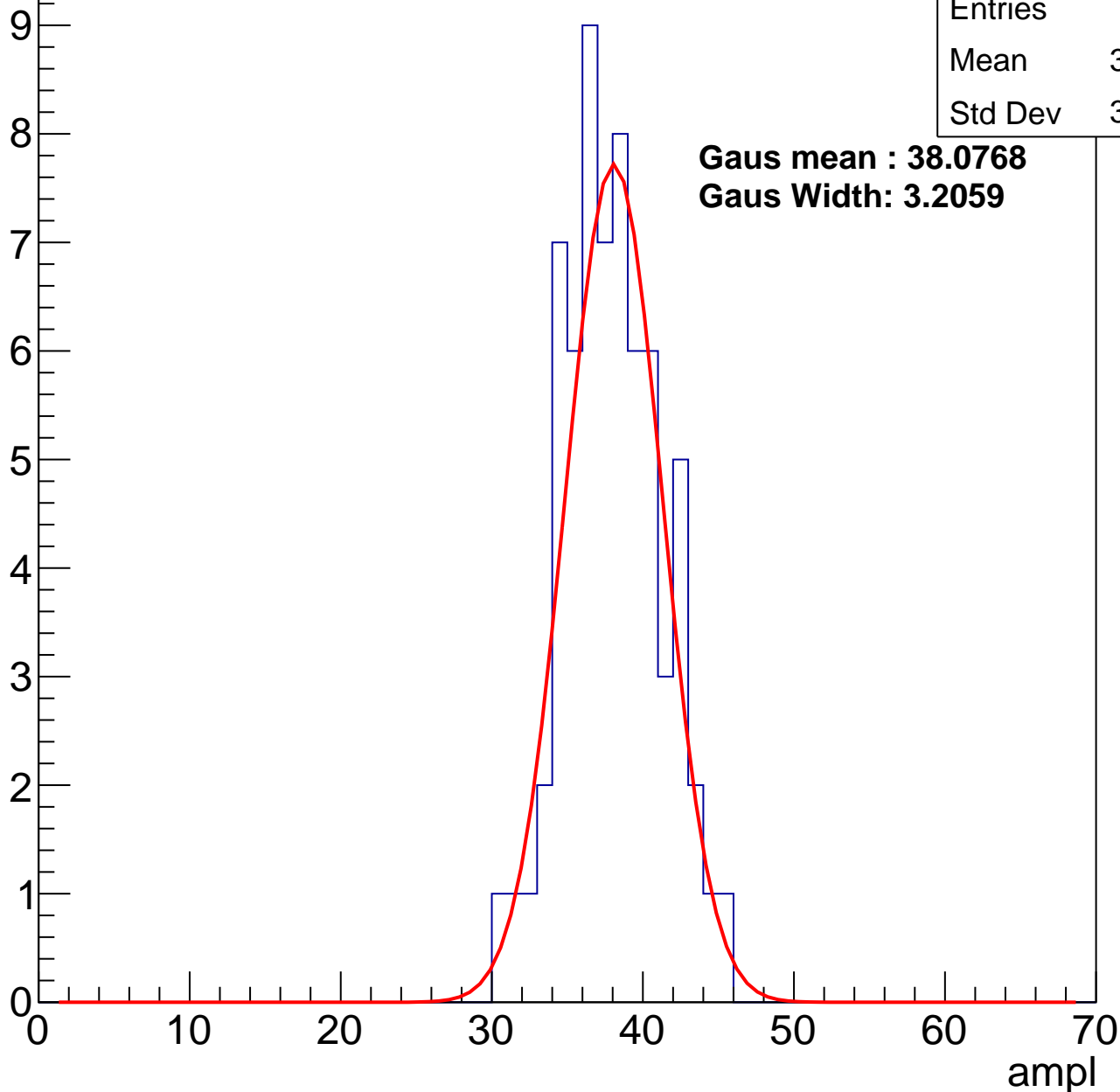
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 37.52 |
| Std Dev | 3.173 |

**Gaus mean : 38.0768**

**Gaus Width: 3.2059**



# B0L002S, U1-ch20, adc2

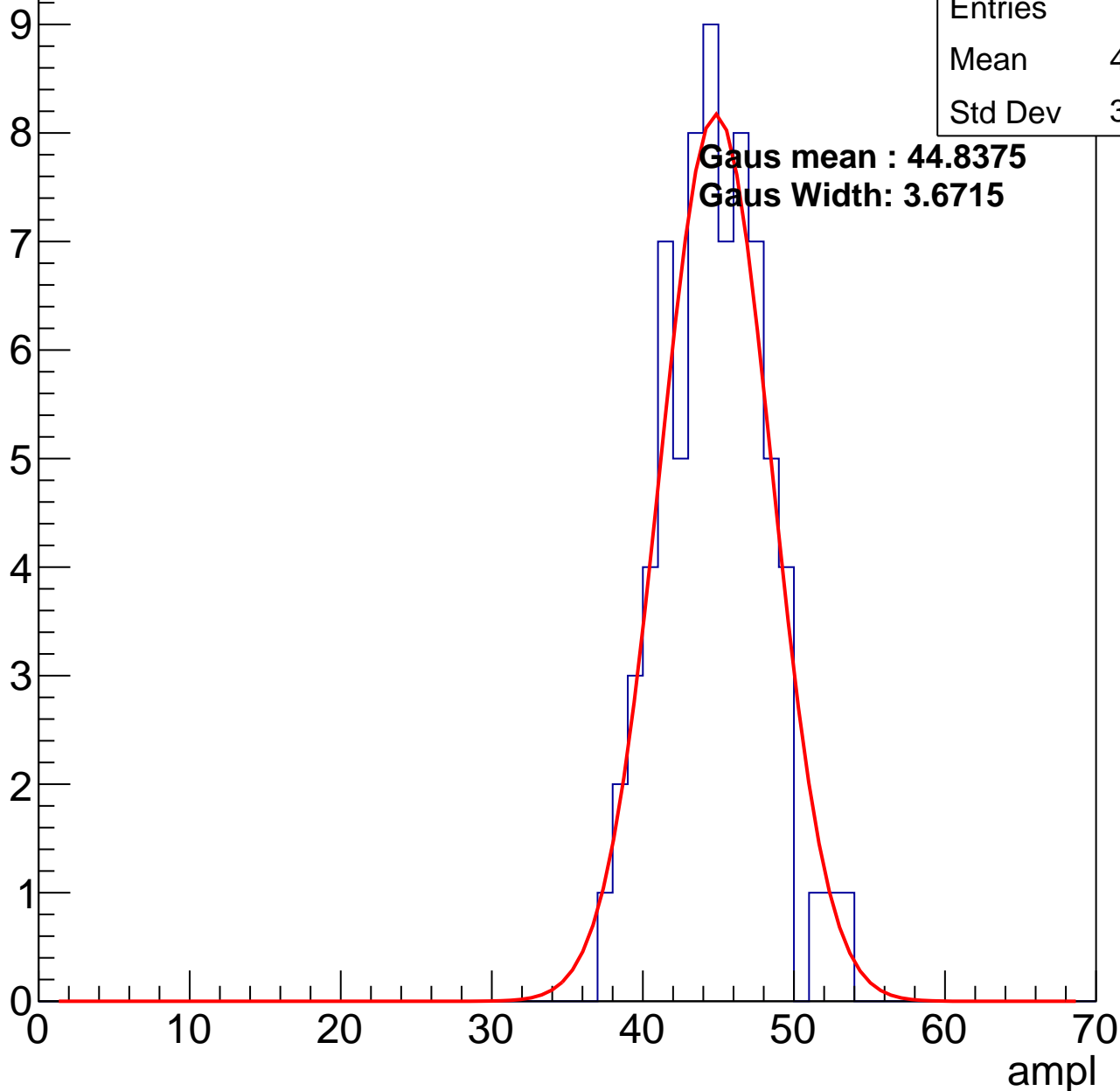
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 44.26 |
| Std Dev | 3.356 |

**Gaus mean : 44.8375**

**Gaus Width: 3.6715**



# B0L002S, U1-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

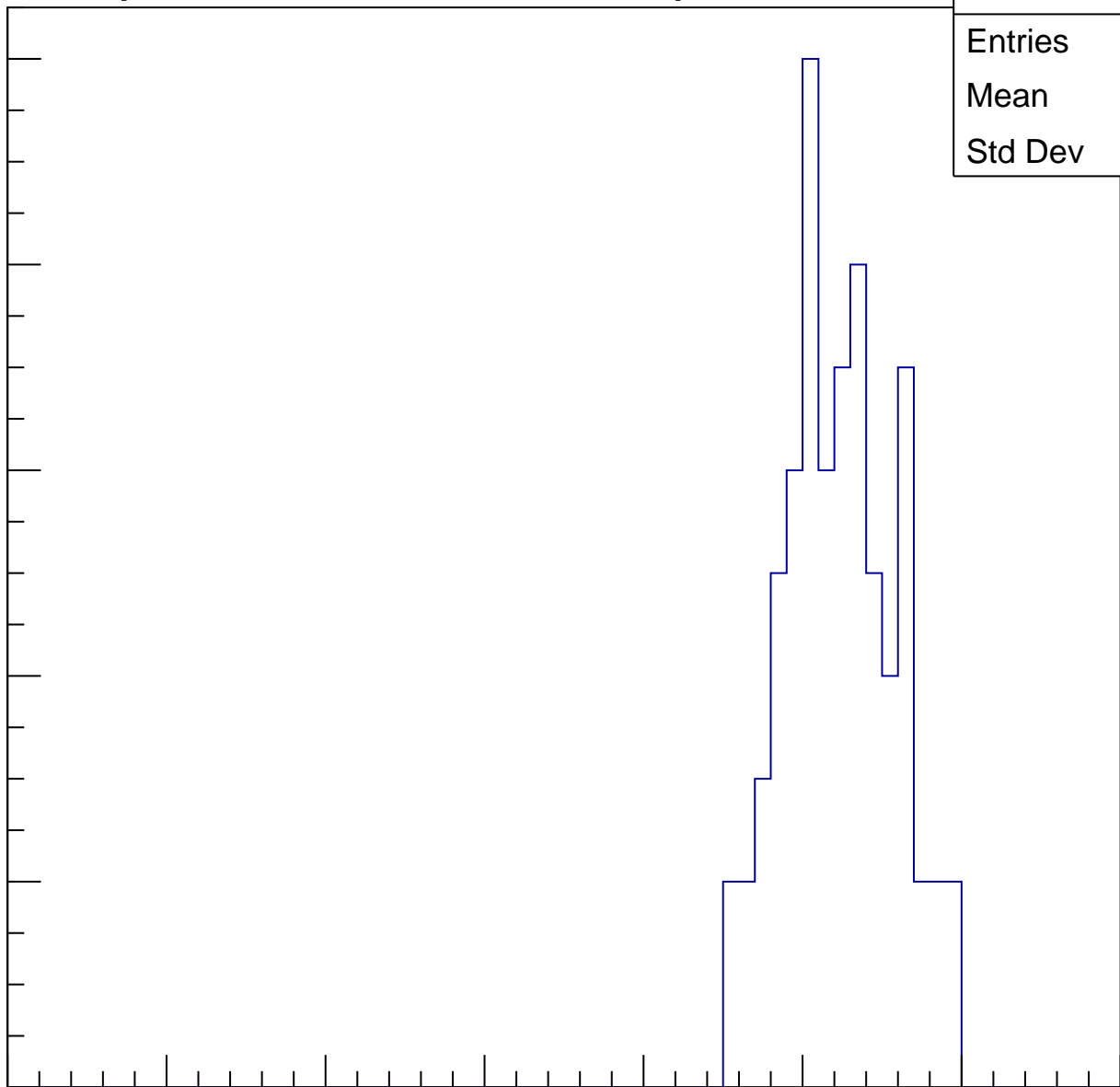
|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 51.85 |
| Std Dev | 3.397 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 57.11

Std Dev 3.087

ampl

0

10

20

30

40

50

60

70

# B0L002S, U1-ch20, adc5

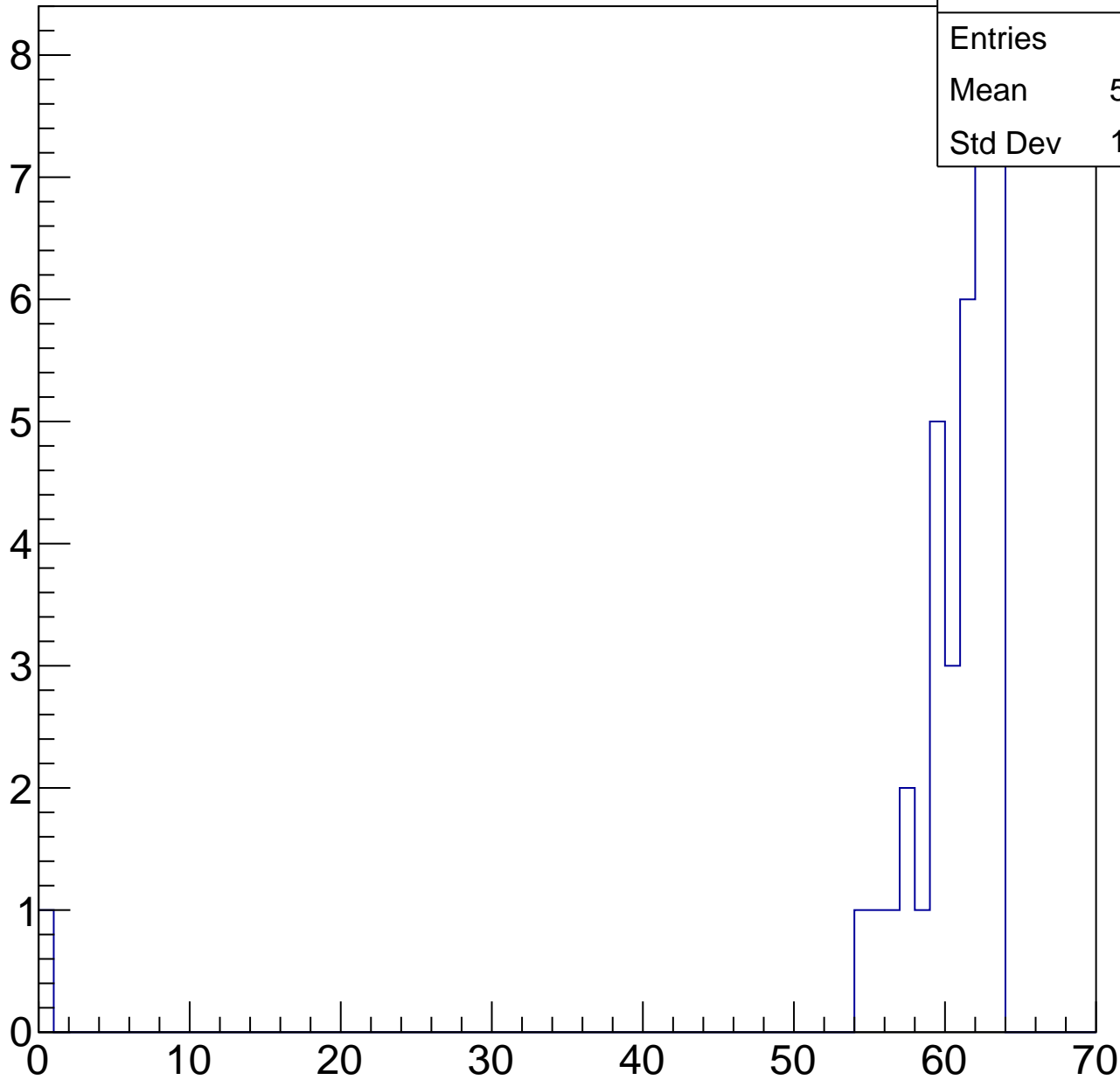
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 58.86 |
| Std Dev | 10.09 |

ampl



# B0L002S, U1-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 23 |
| Std Dev | 0  |

# B0L002S, U1-ch21, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 30.61 |
| Std Dev | 2.917 |

**Gaus mean : 31.0040**

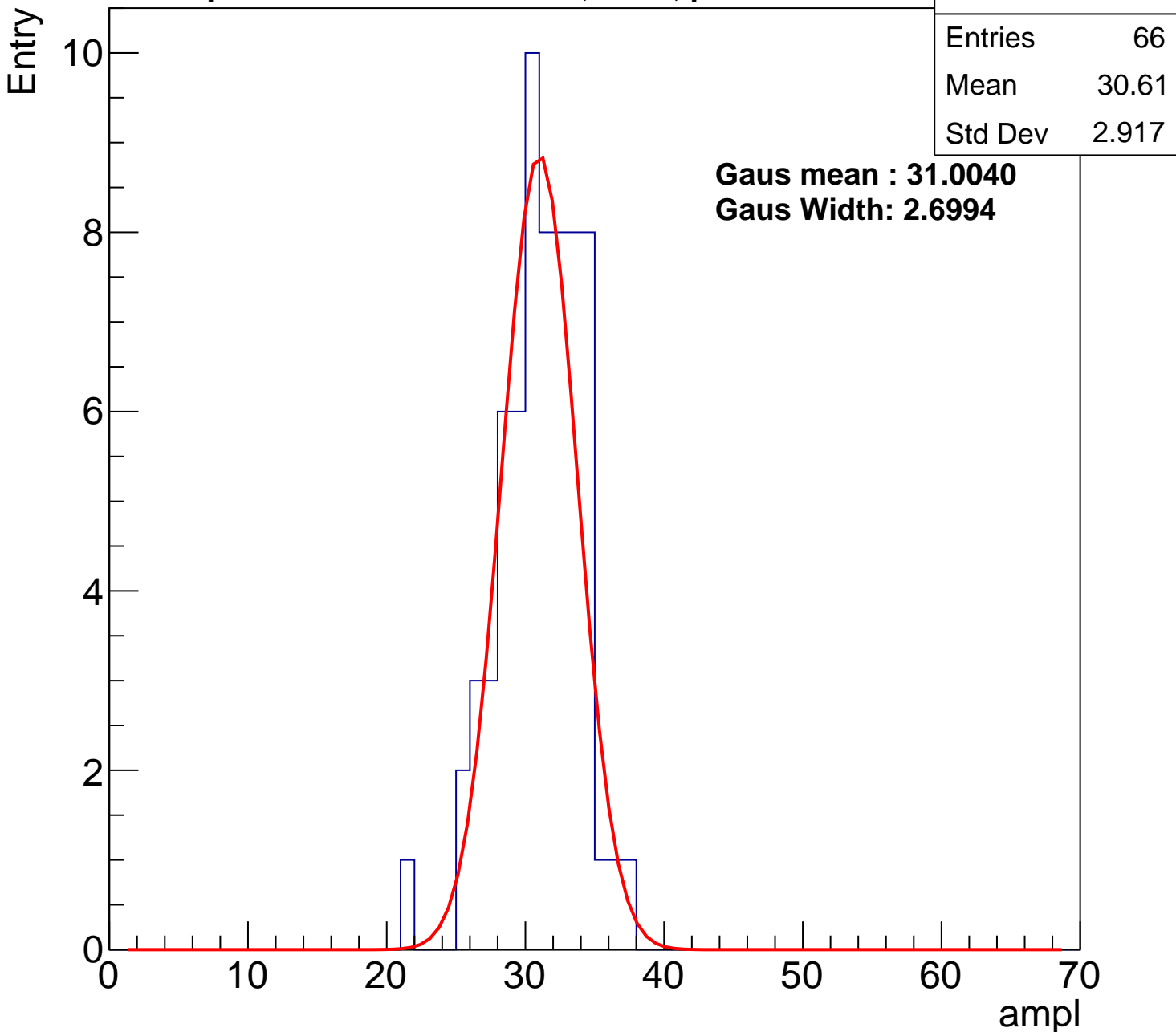
**Gaus Width: 2.6994**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch21, adc1

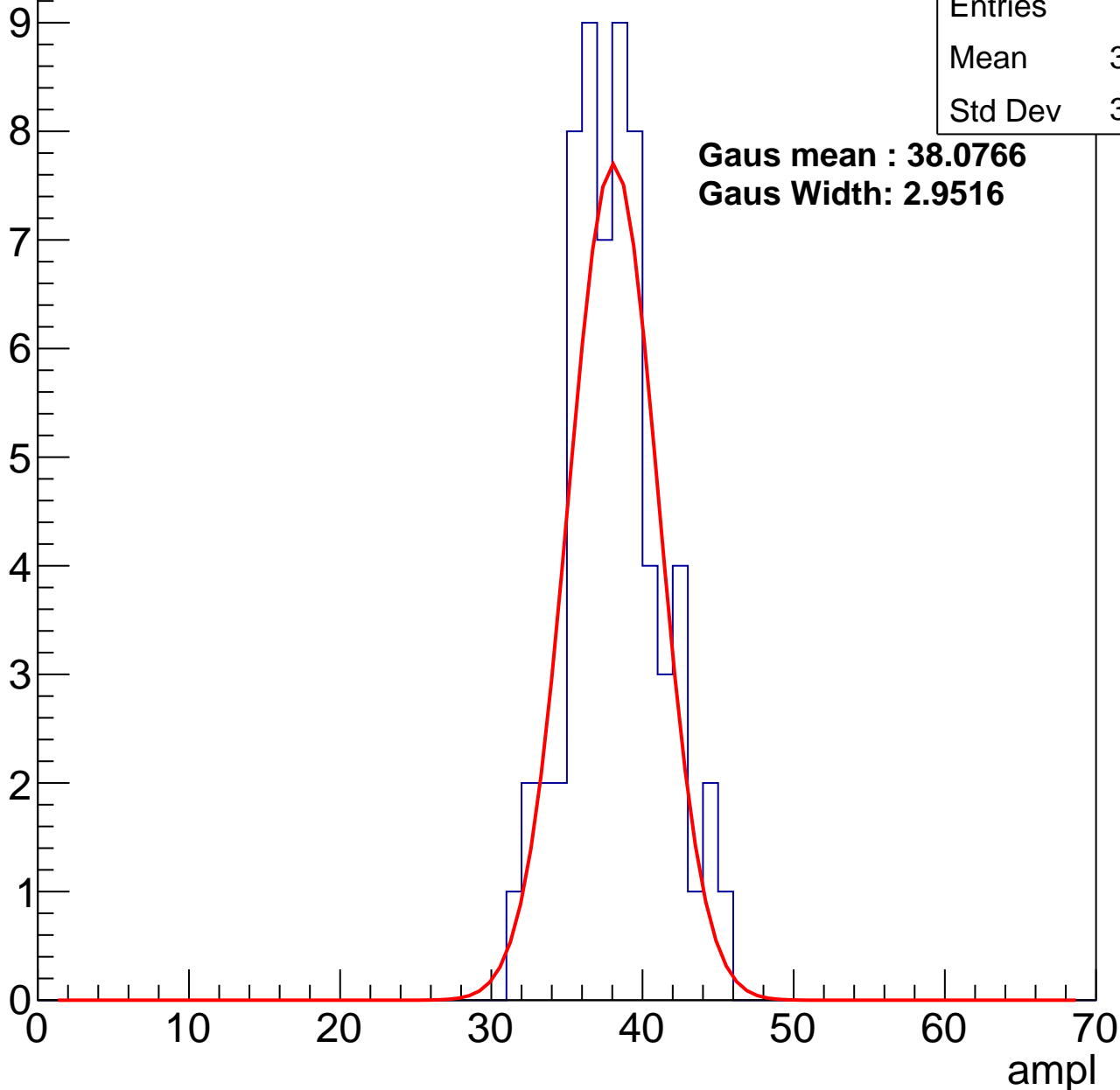
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 37.67 |
| Std Dev | 3.013 |

**Gaus mean : 38.0766**

**Gaus Width: 2.9516**

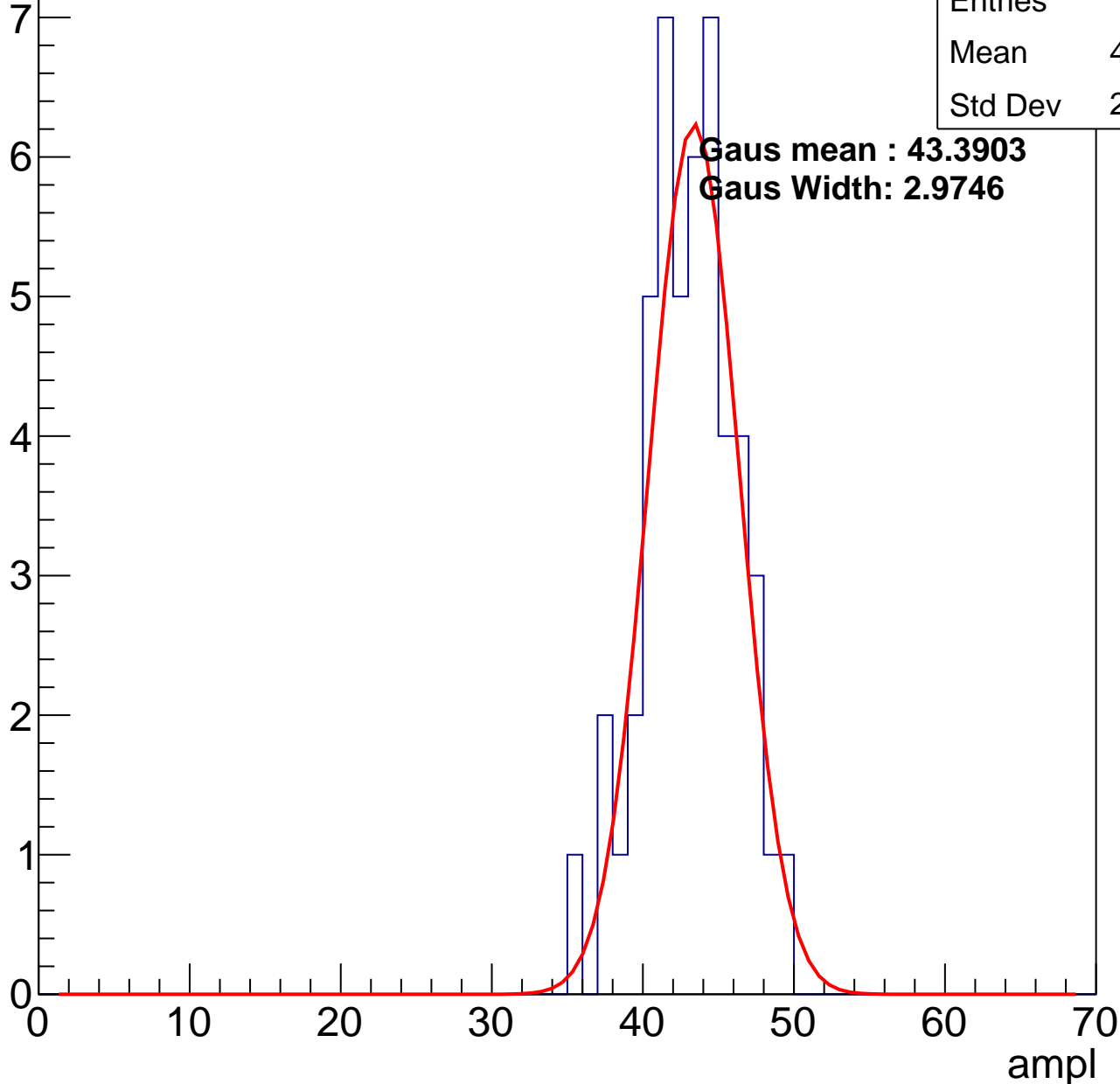


# B0L002S, U1-ch21, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 42.65 |
| Std Dev | 2.973 |

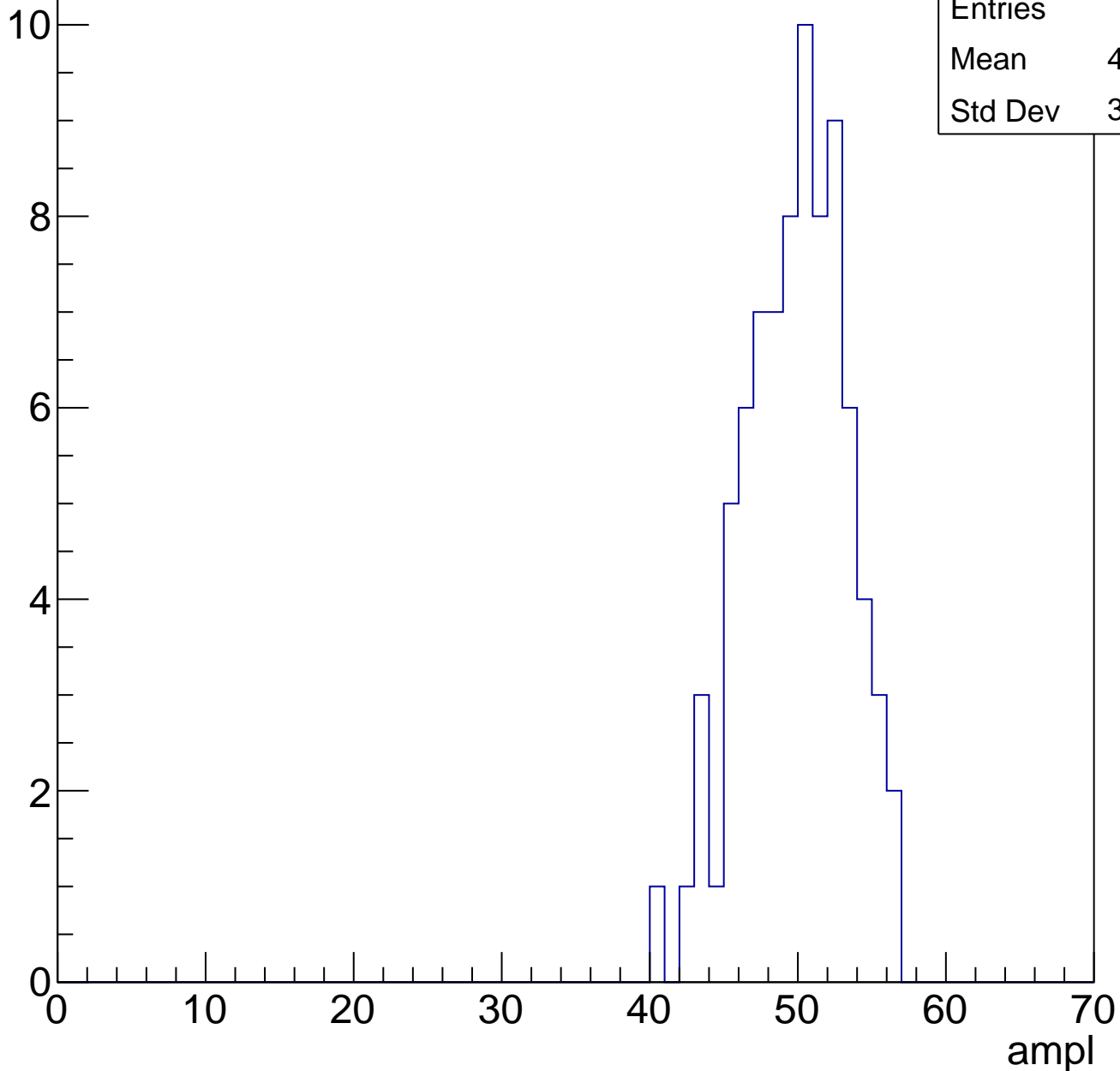


# B0L002S, U1-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 49.38 |
| Std Dev | 3.416 |

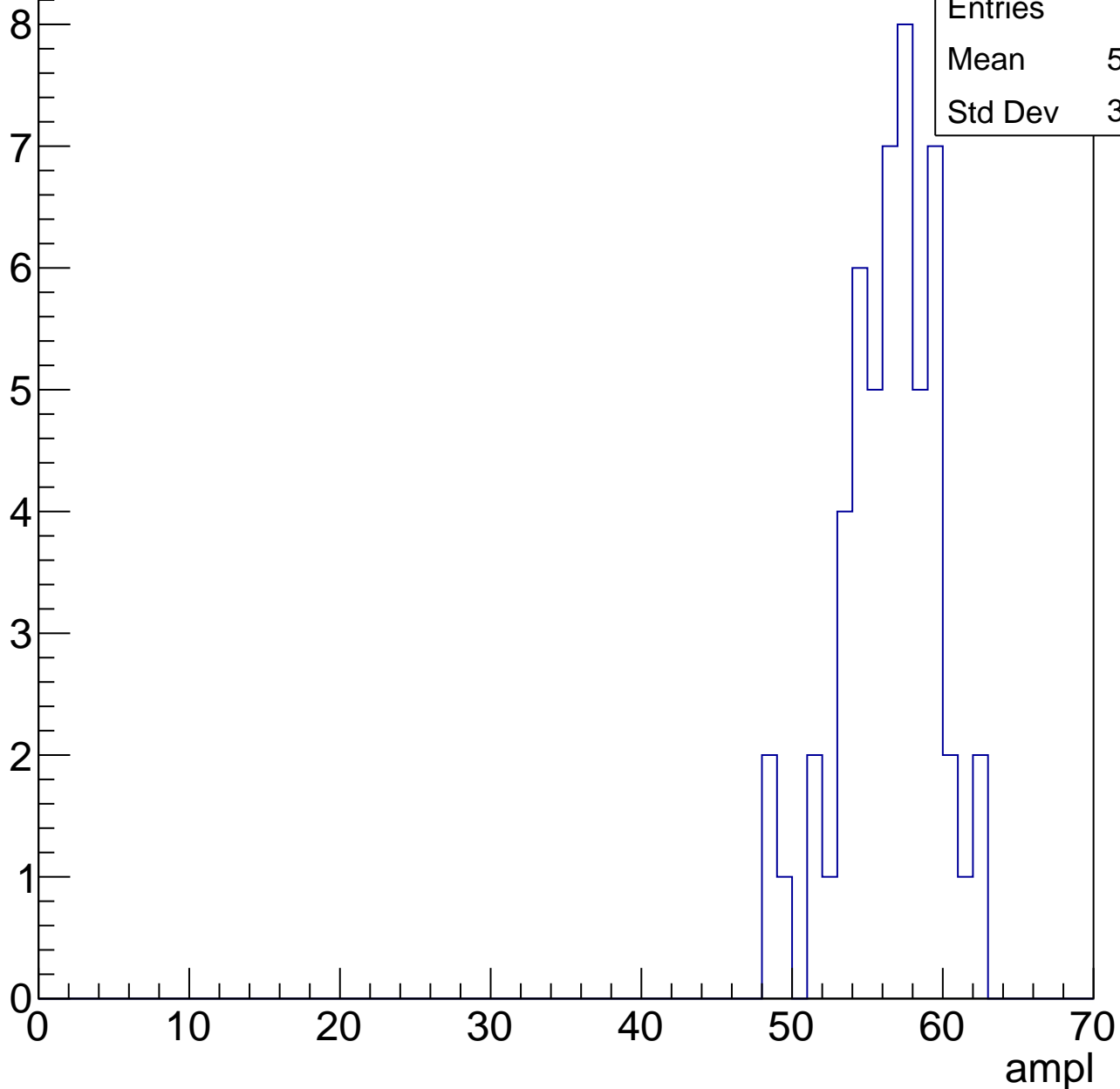
Entry



# B0L002S, U1-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch21, adc5

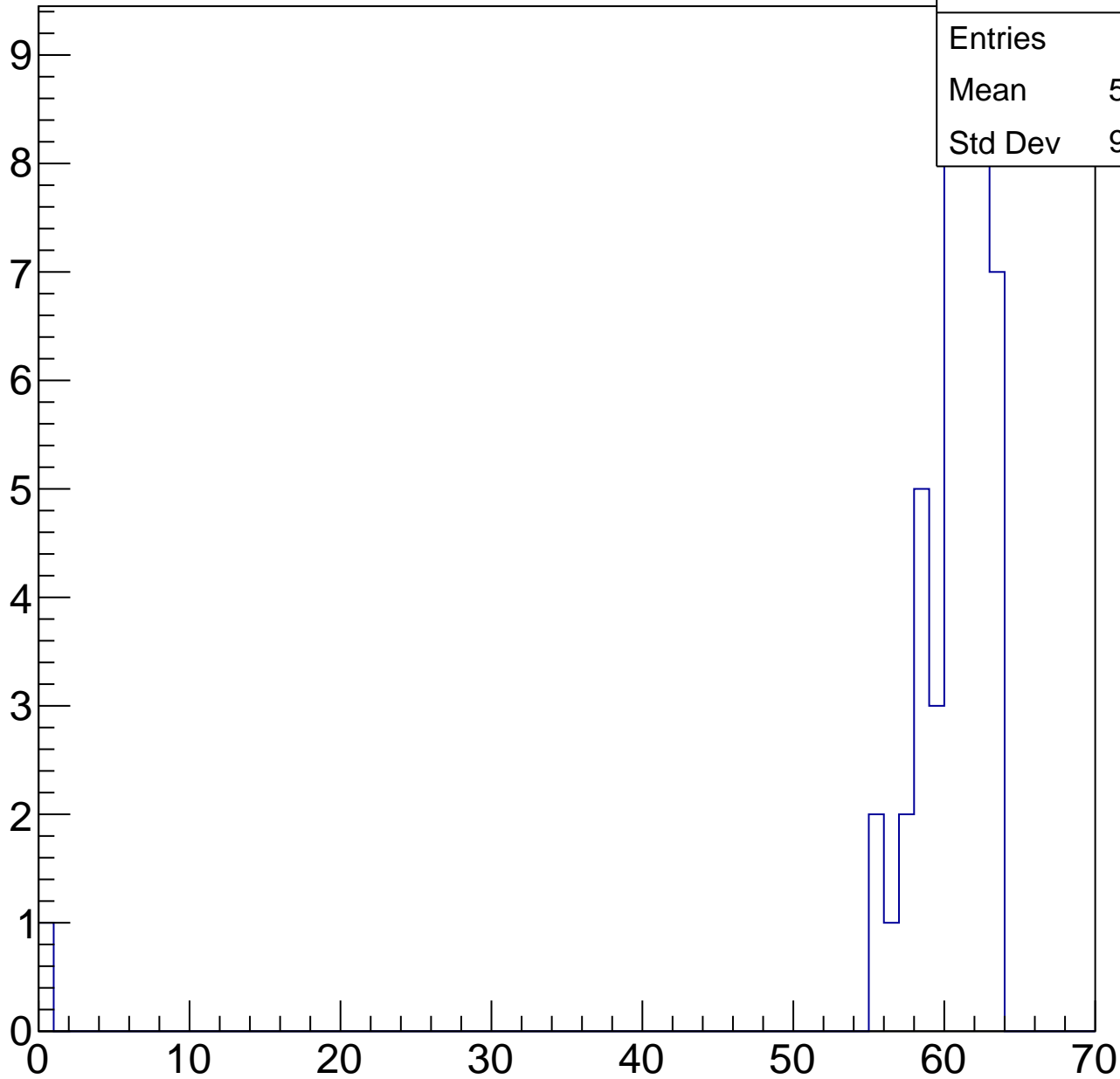
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 58.98 |
| Std Dev | 9.045 |

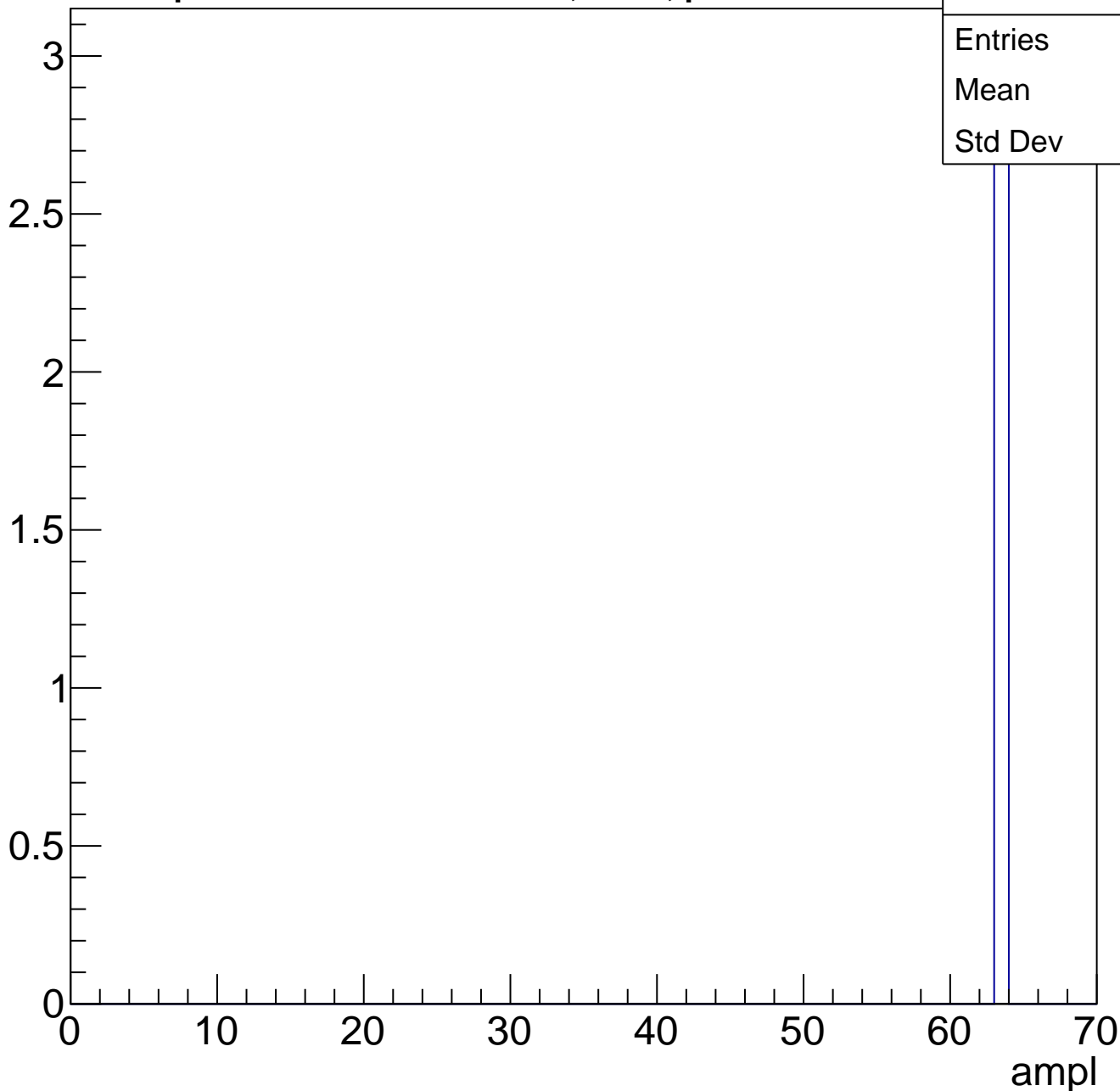
ampl



# B0L002S, U1-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch22, adc0

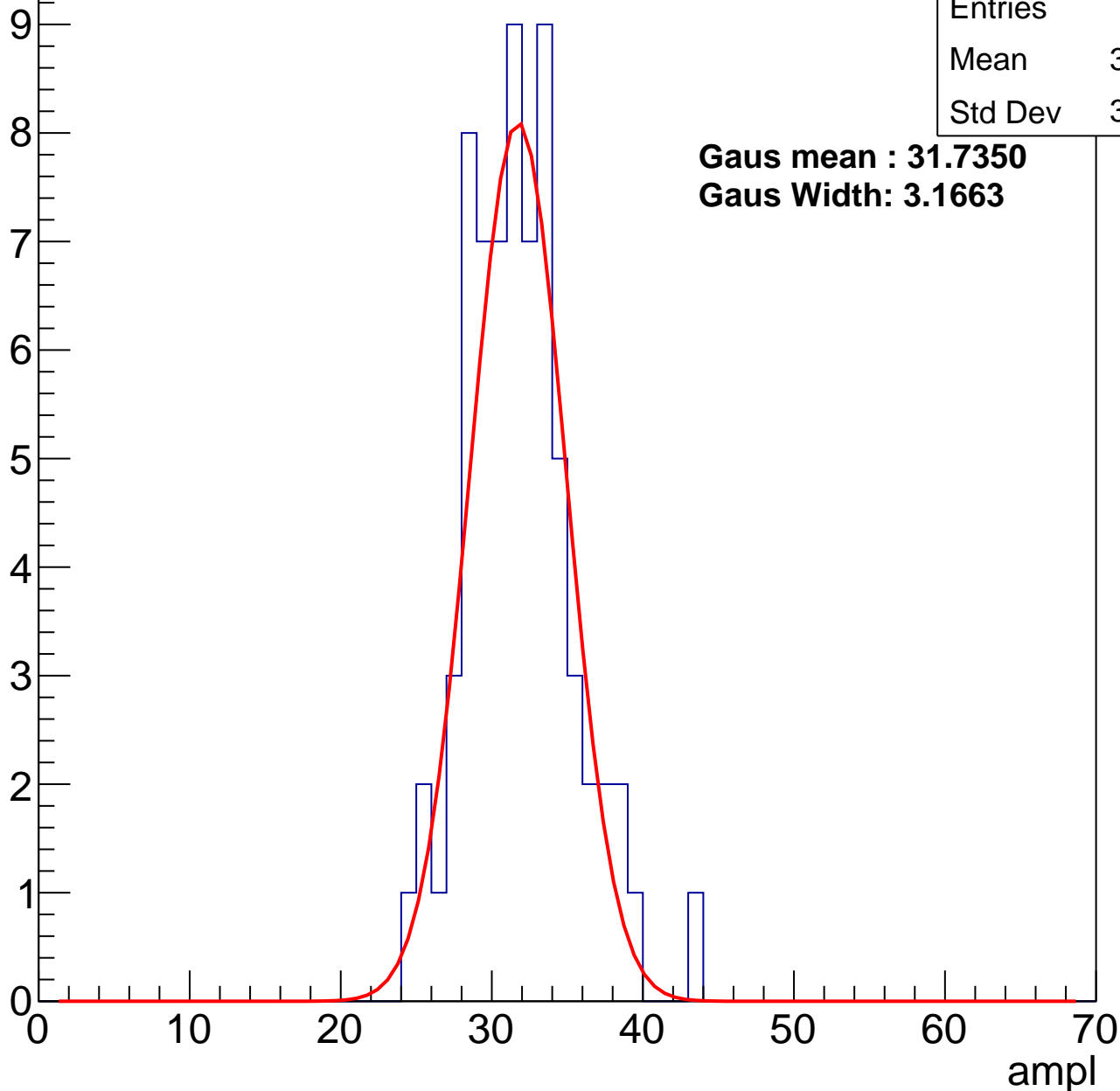
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 31.39 |
| Std Dev | 3.494 |

**Gaus mean : 31.7350**

**Gaus Width: 3.1663**



# B0L002S, U1-ch22, adc1

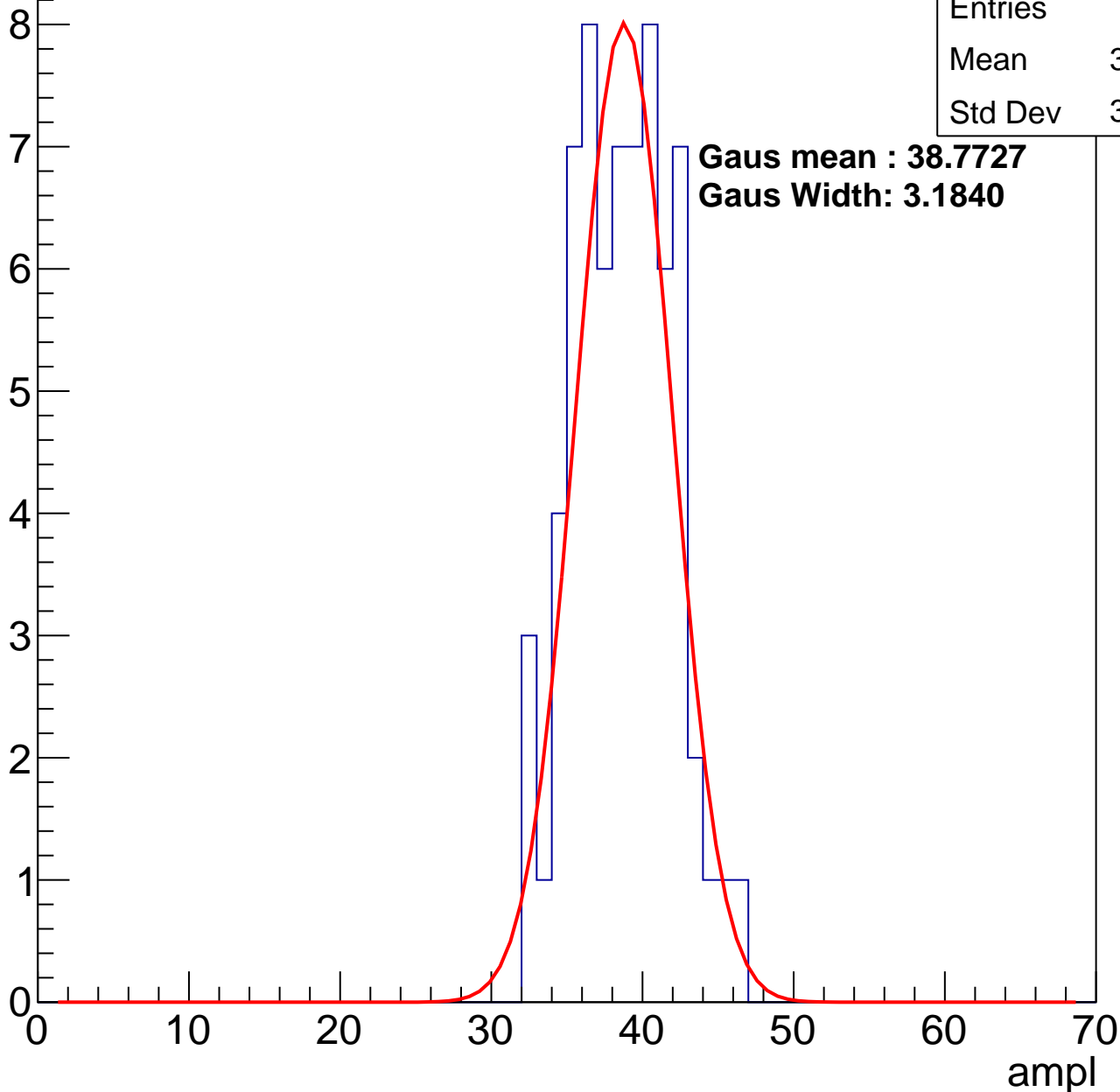
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 38.26 |
| Std Dev | 3.179 |

**Gaus mean : 38.7727**

**Gaus Width: 3.1840**



# B0L002S, U1-ch22, adc2

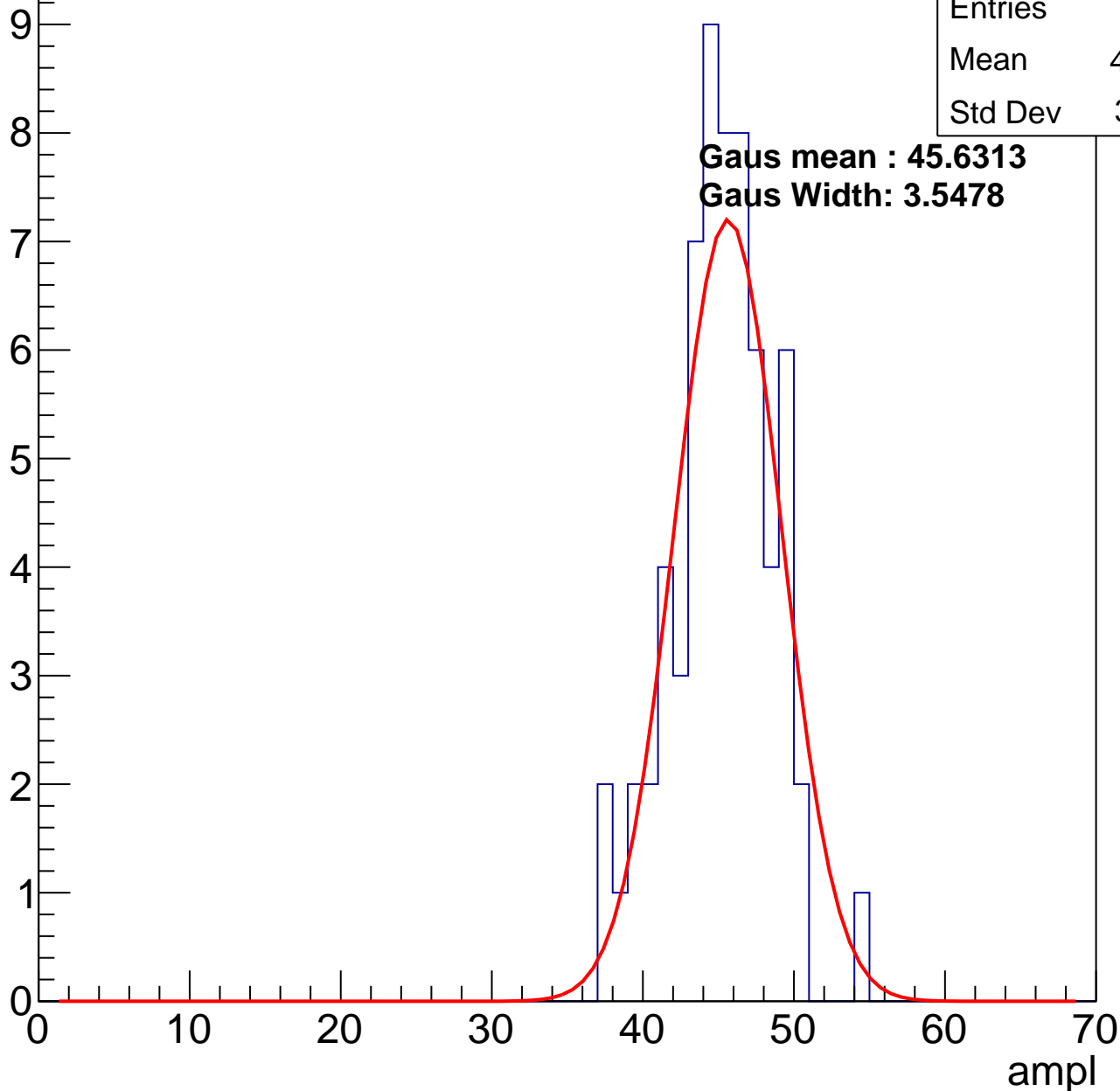
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 44.72 |
| Std Dev | 3.321 |

**Gaus mean : 45.6313**

**Gaus Width: 3.5478**

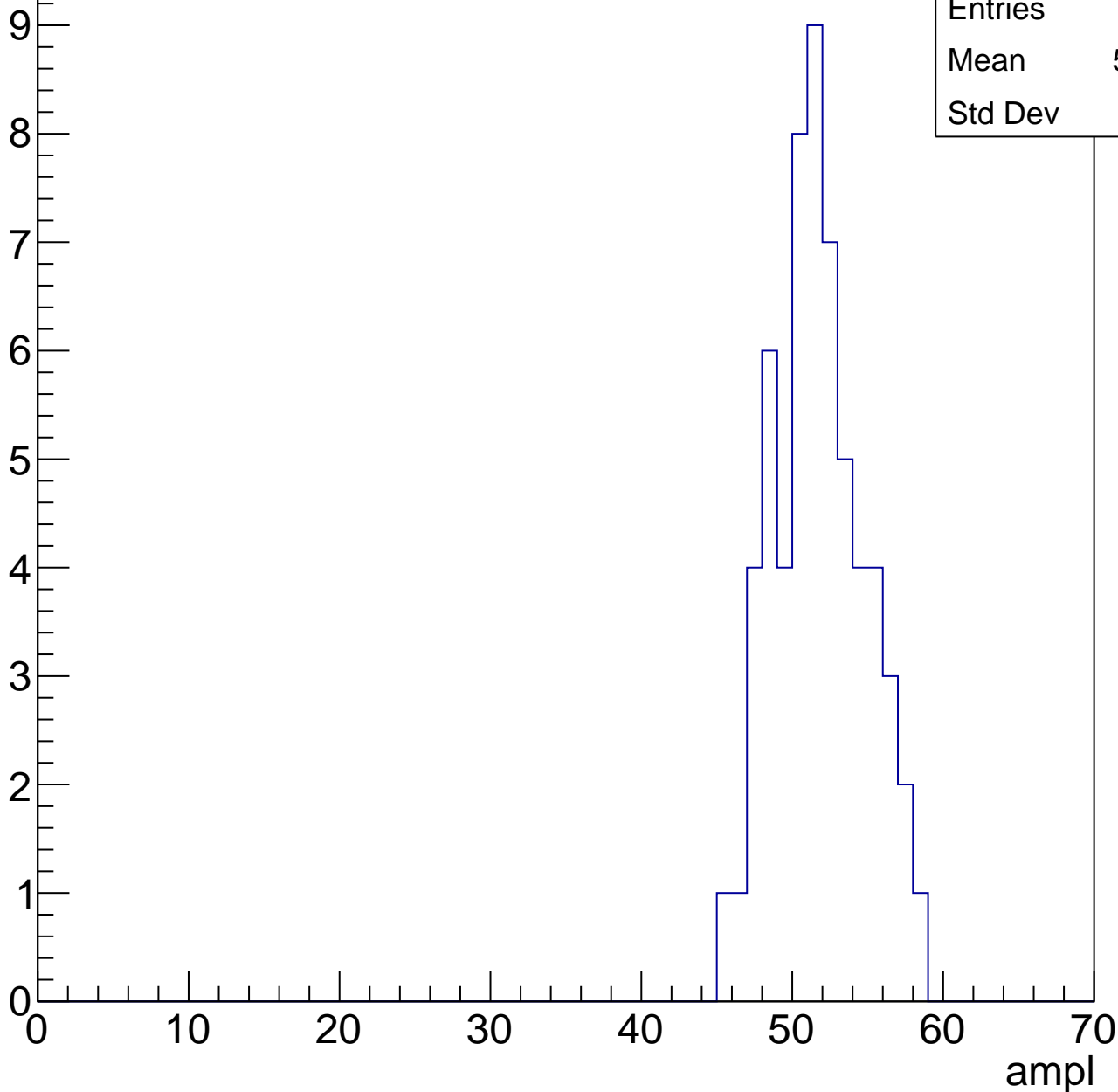


# B0L002S, U1-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 51.31 |
| Std Dev | 2.97  |

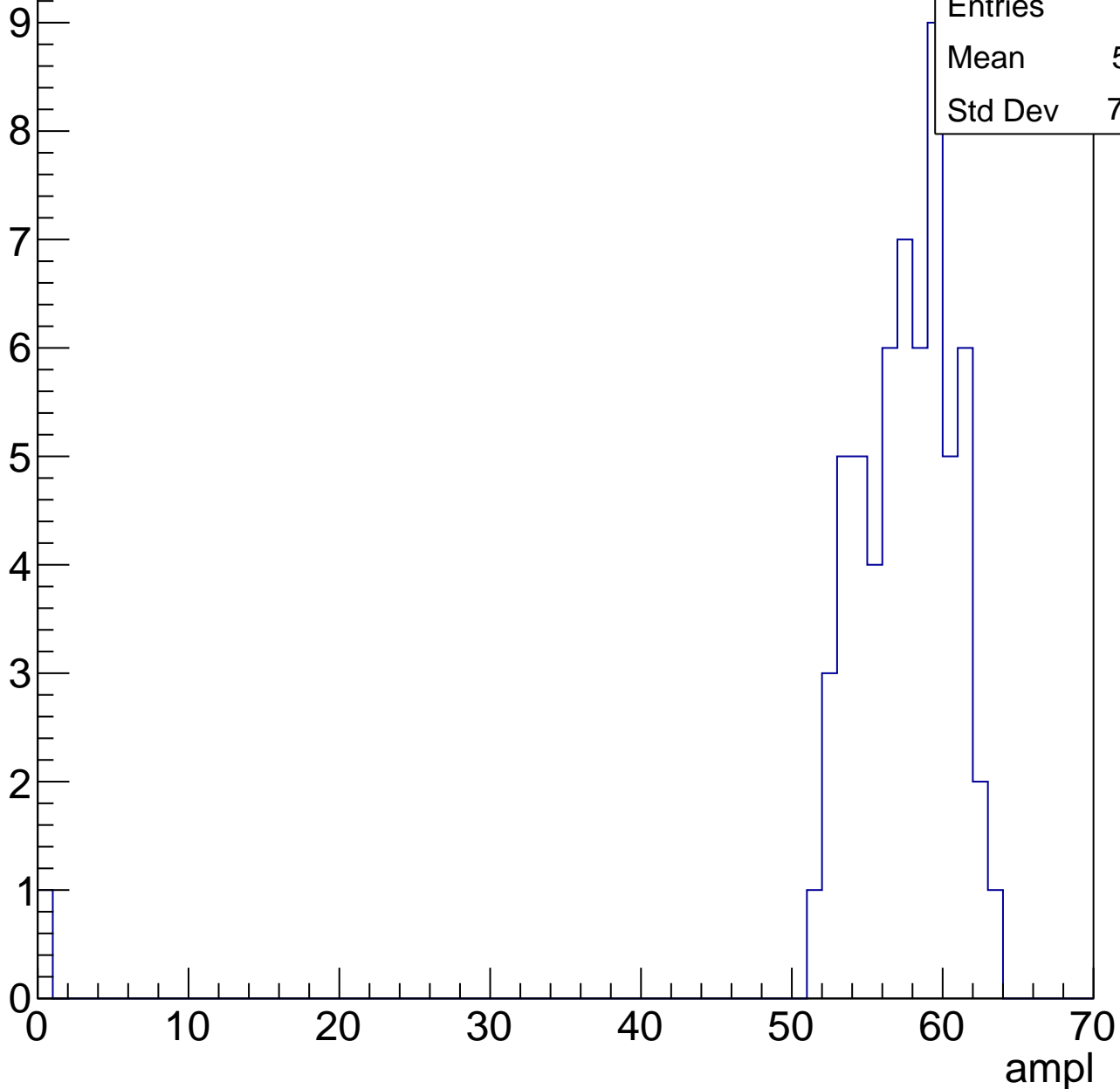


# B0L002S, U1-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

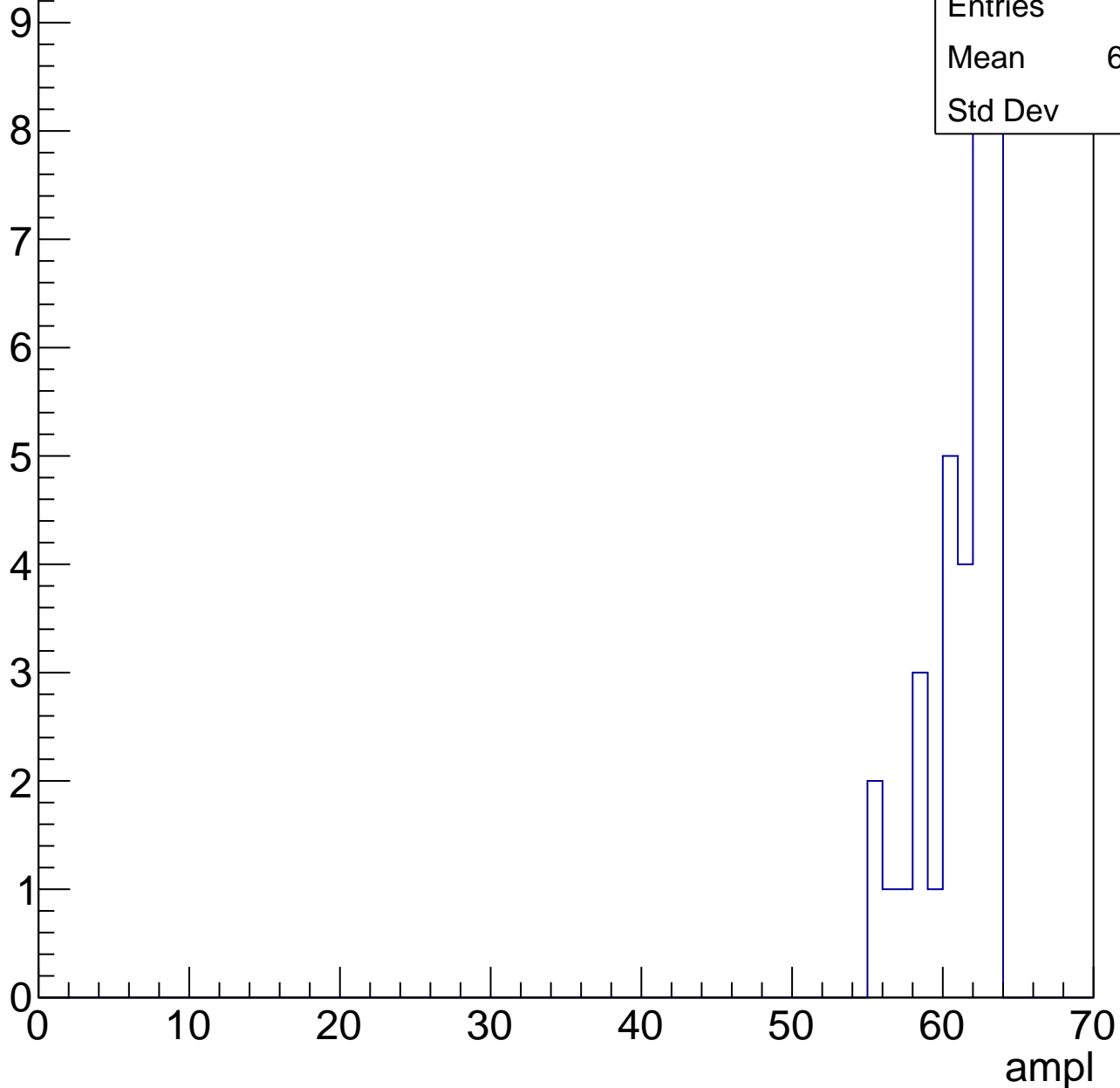
|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 56.21 |
| Std Dev | 7.828 |



# B0L002S, U1-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 23 |
| Std Dev | 0  |

# B0L002S, U1-ch23, adc0

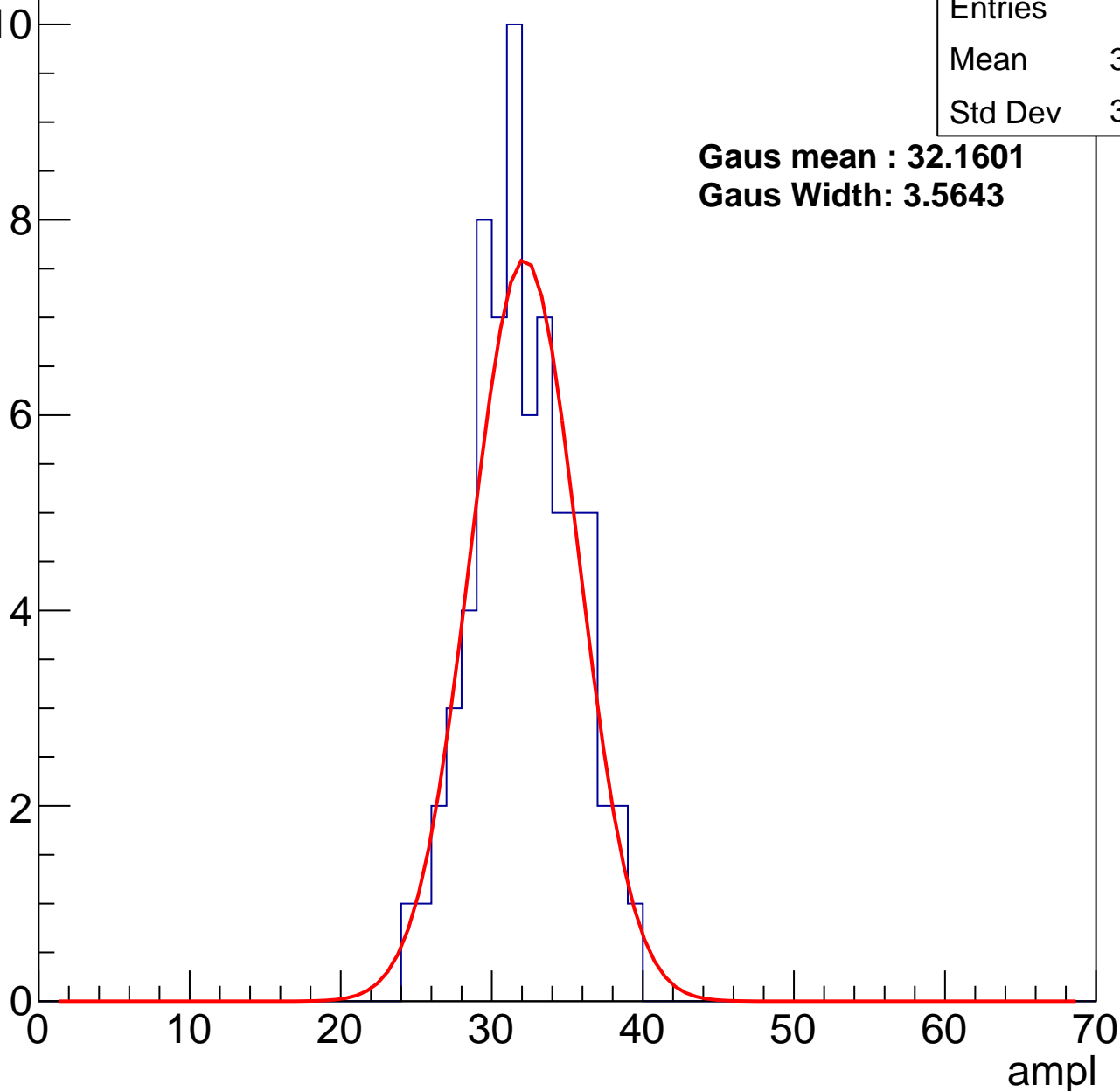
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 31.64 |
| Std Dev | 3.292 |

**Gaus mean : 32.1601**

**Gaus Width: 3.5643**



# B0L002S, U1-ch23, adc1

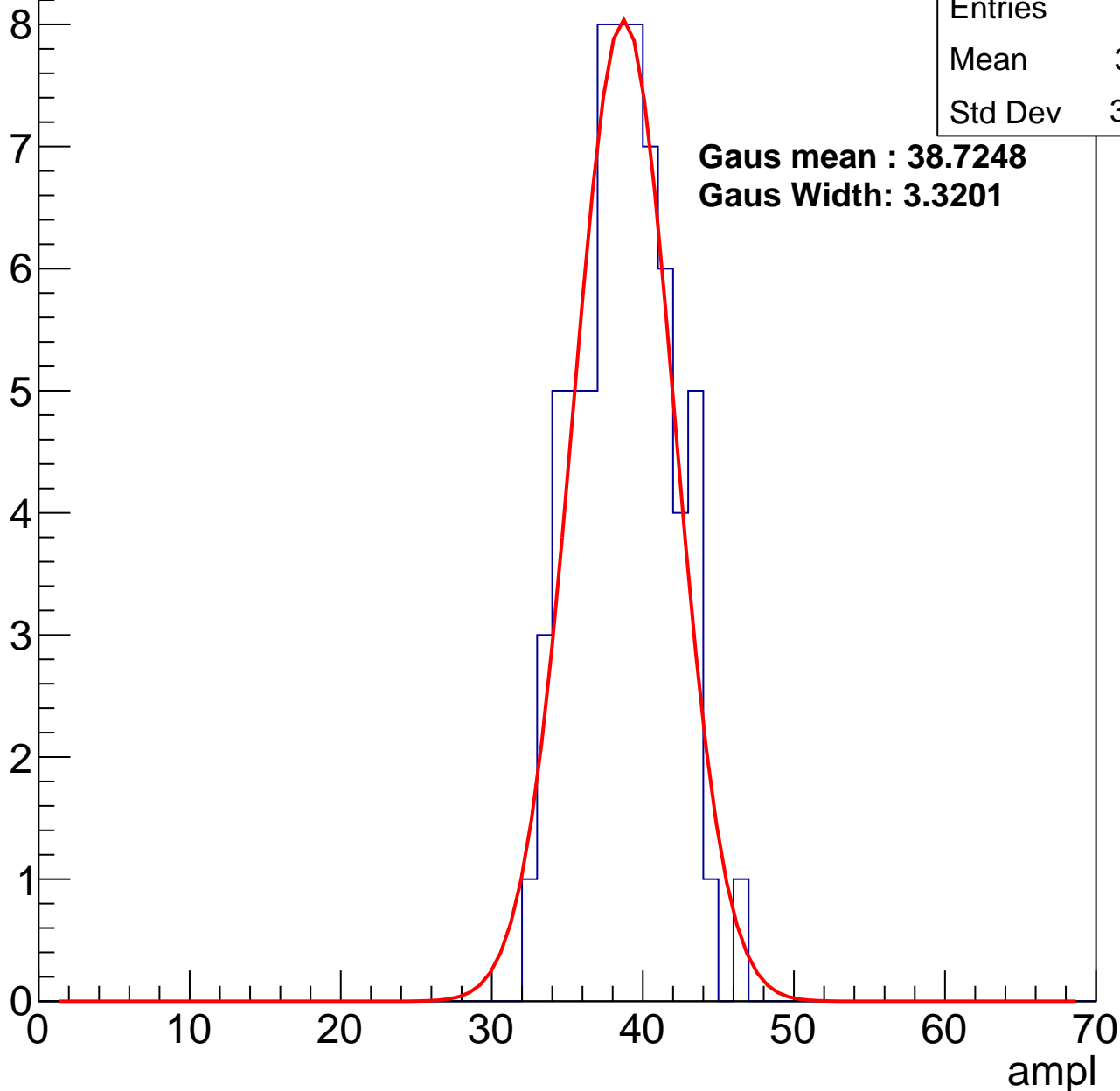
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 38.31 |
| Std Dev | 3.082 |

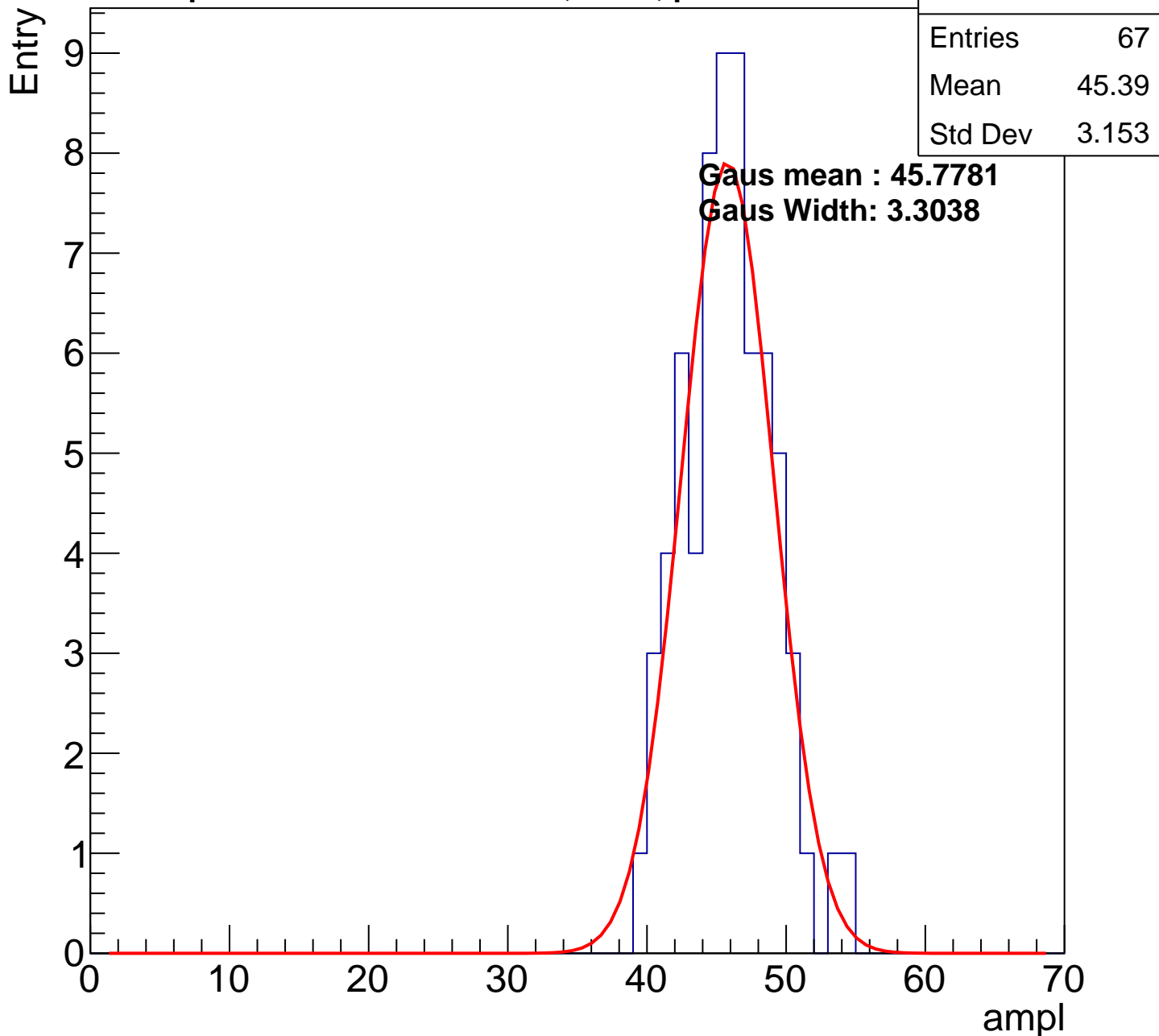
**Gaus mean : 38.7248**

**Gaus Width: 3.3201**



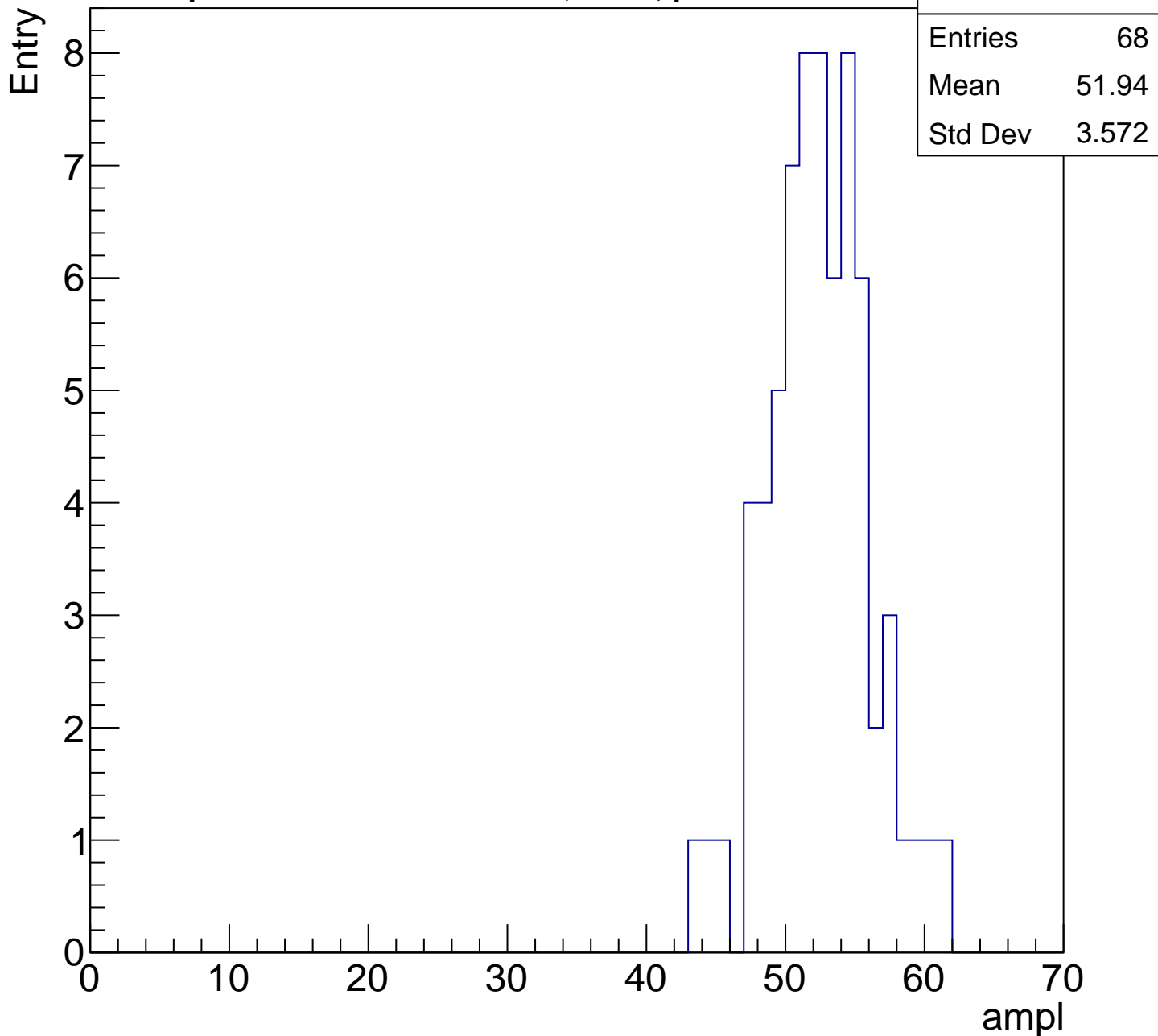
# B0L002S, U1-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch23, adc3

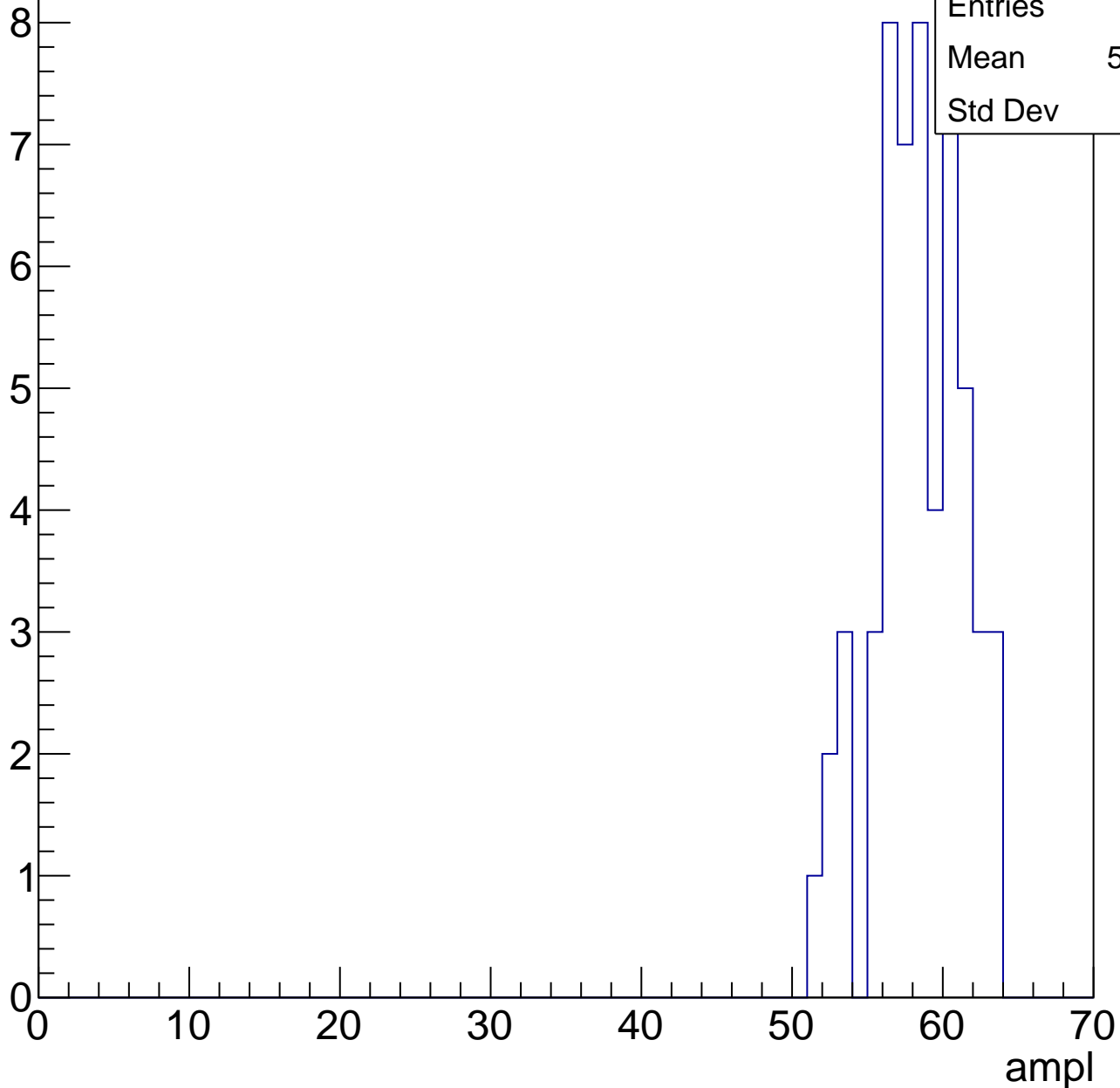
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

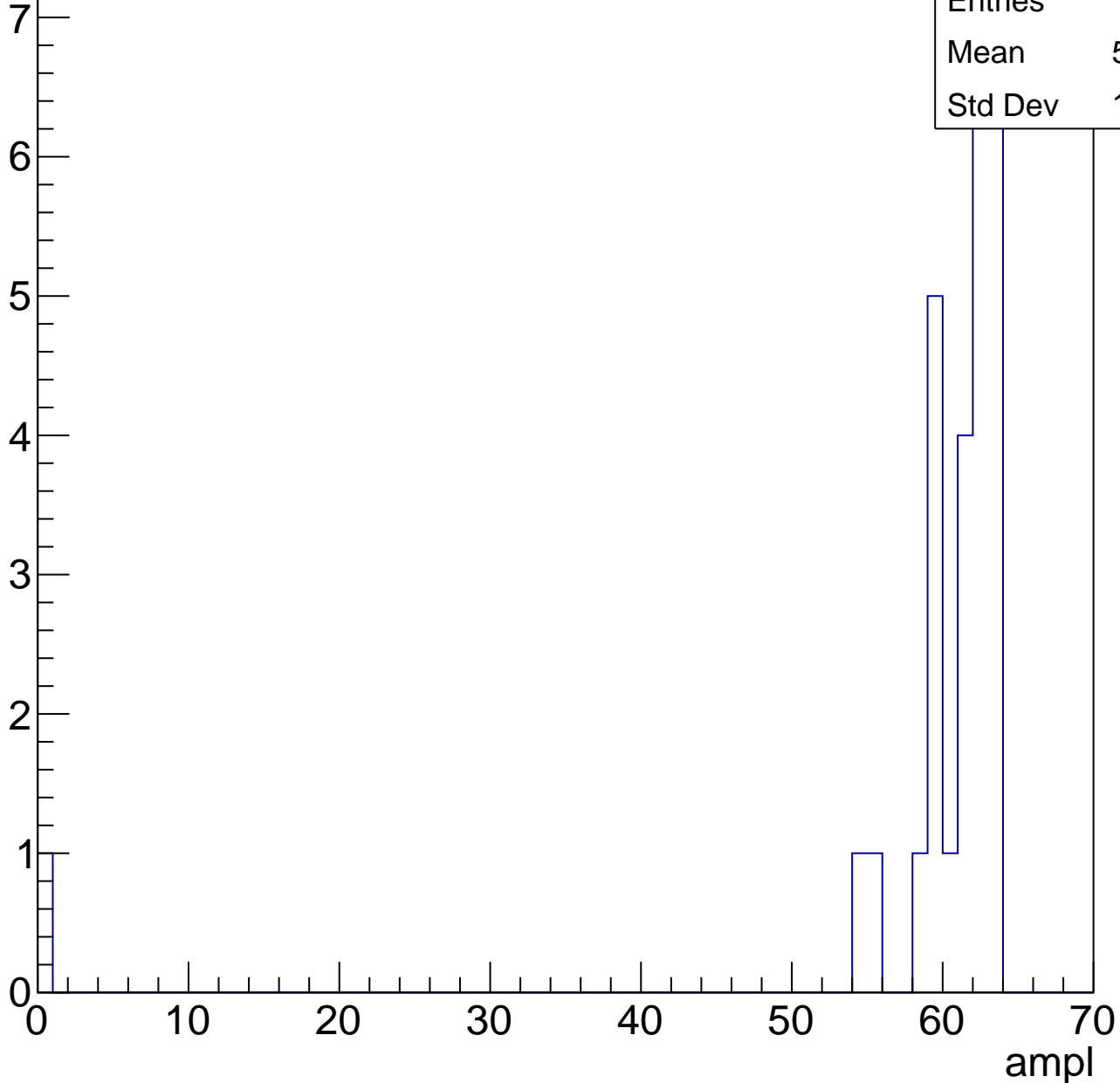


# B0L002S, U1-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 28    |
| Mean    | 58.61 |
| Std Dev | 11.51 |



# B0L002S, U1-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch24, adc0

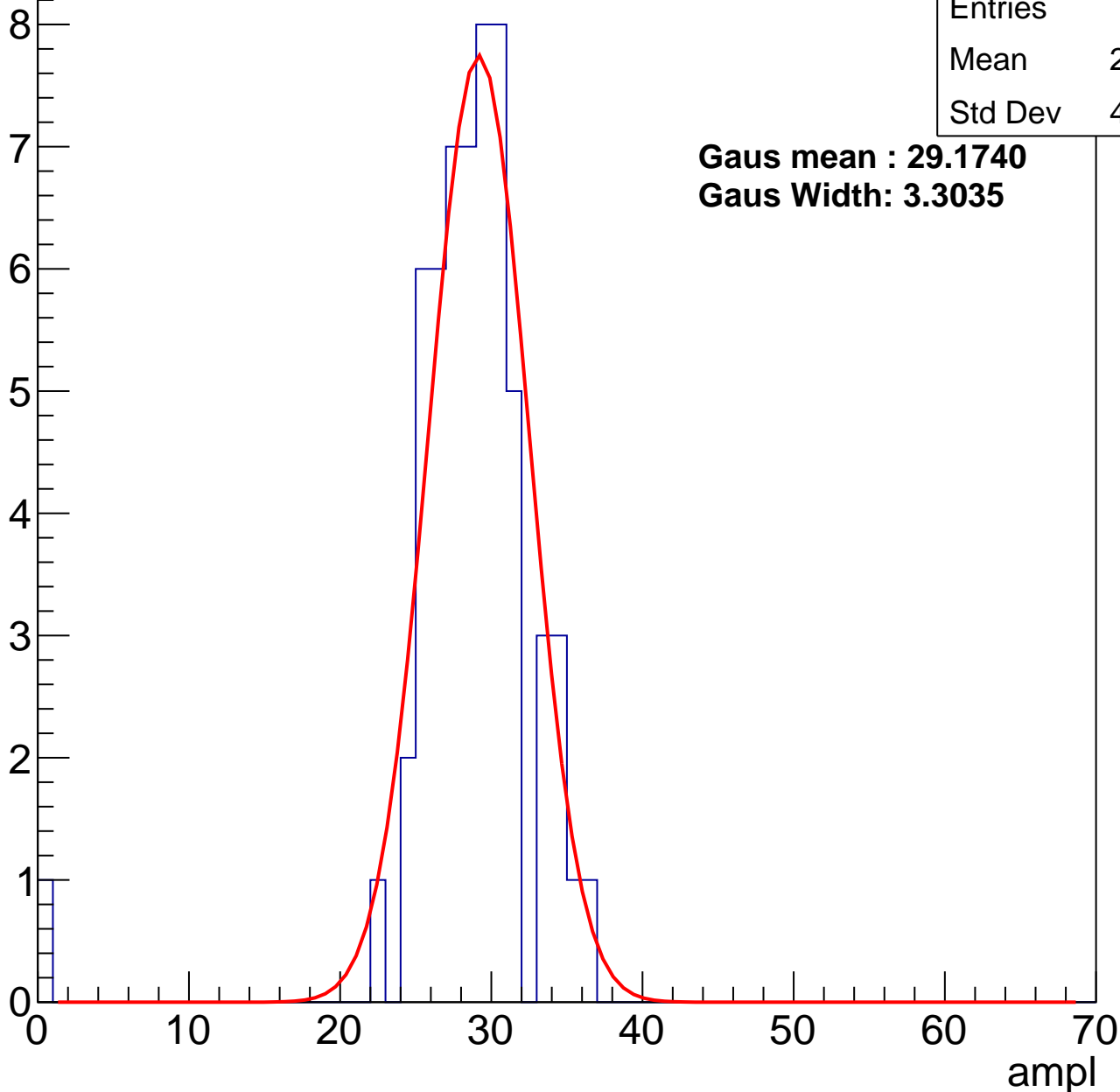
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 28.14 |
| Std Dev | 4.724 |

**Gaus mean : 29.1740**

**Gaus Width: 3.3035**



# B0L002S, U1-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 35.16 |
| Std Dev | 3.309 |

**Gaus mean : 35.6211**

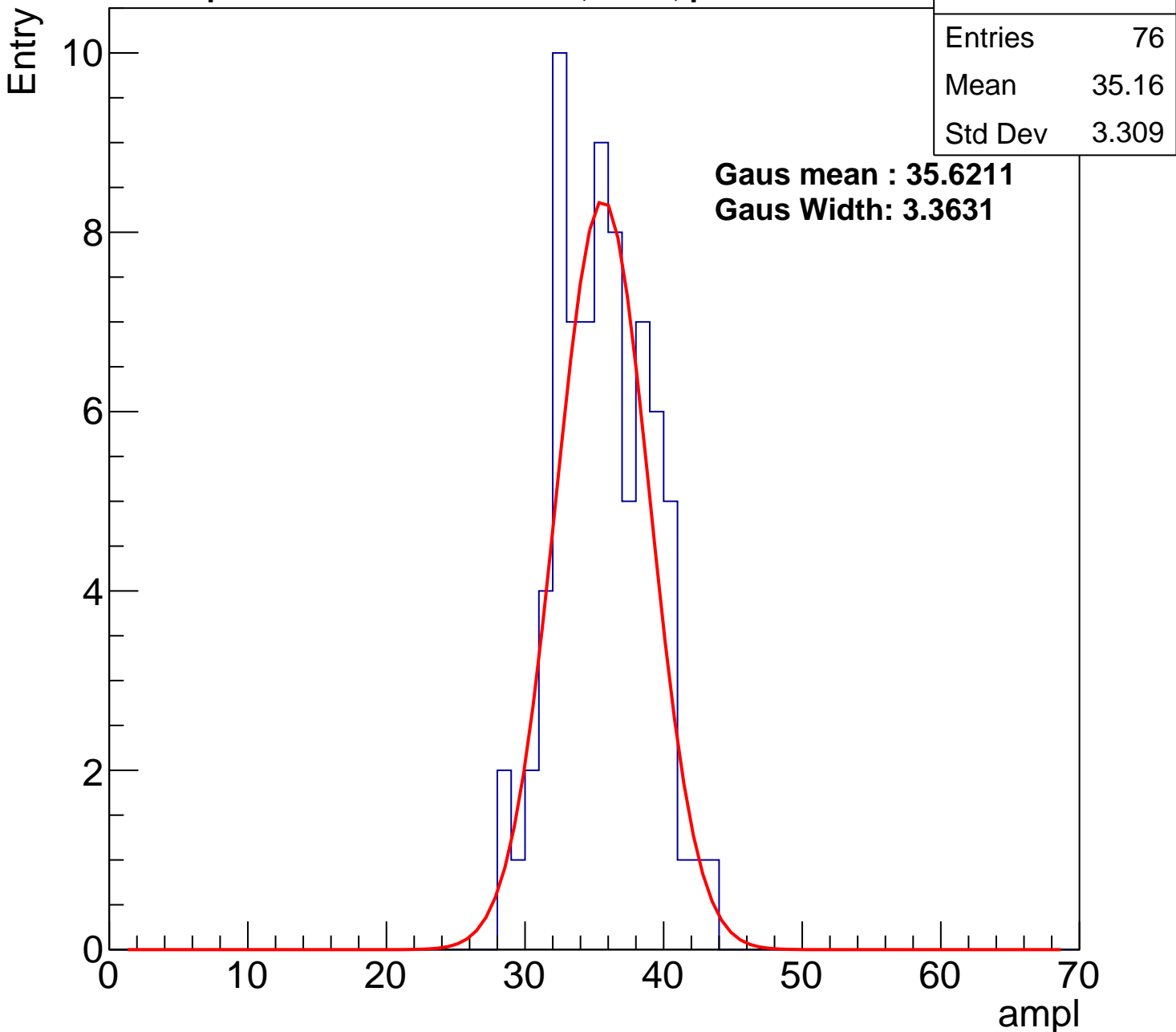
**Gaus Width: 3.3631**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch24, adc2

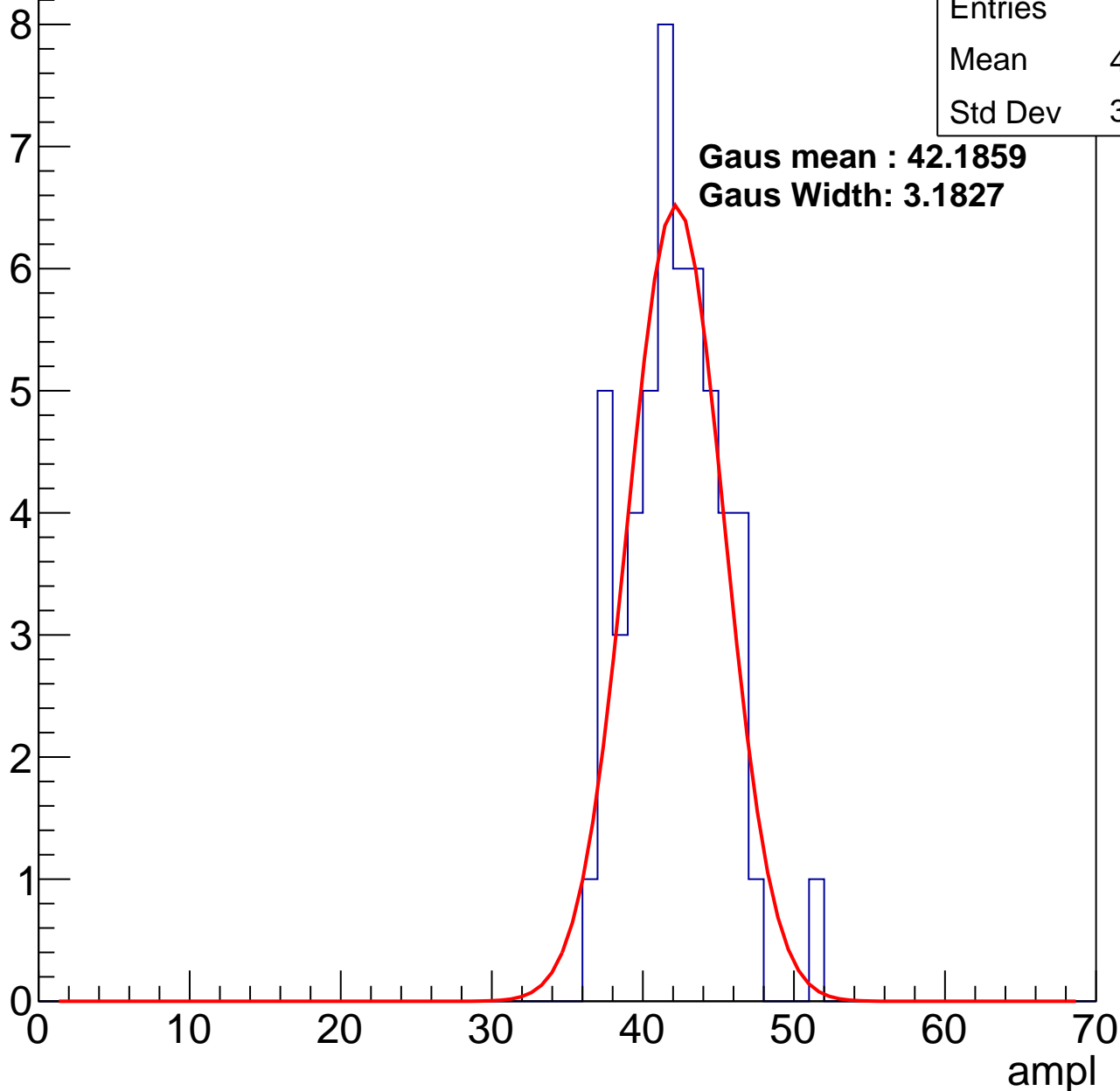
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 41.72 |
| Std Dev | 3.074 |

**Gaus mean : 42.1859**

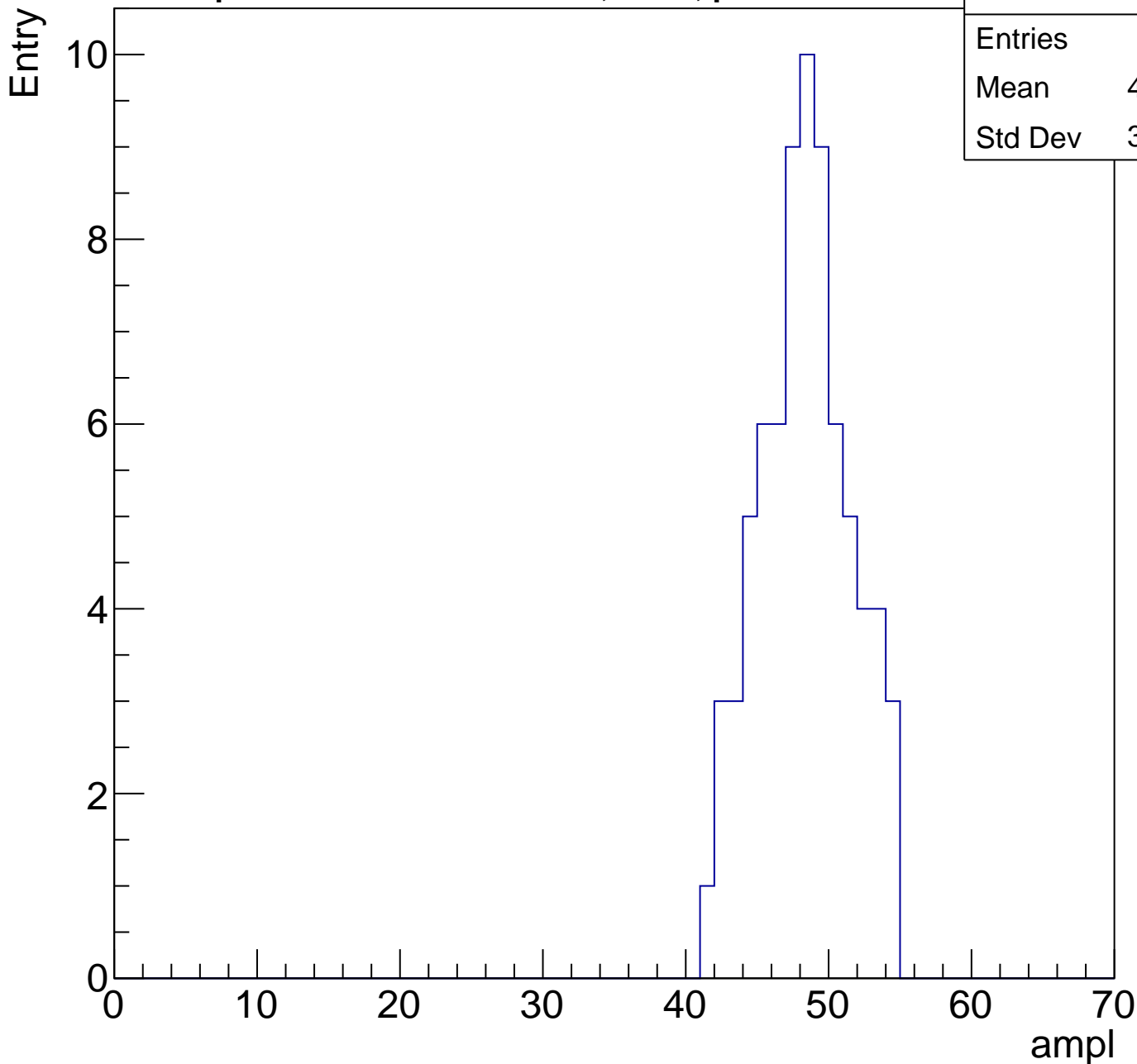
**Gaus Width: 3.1827**



# B0L002S, U1-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

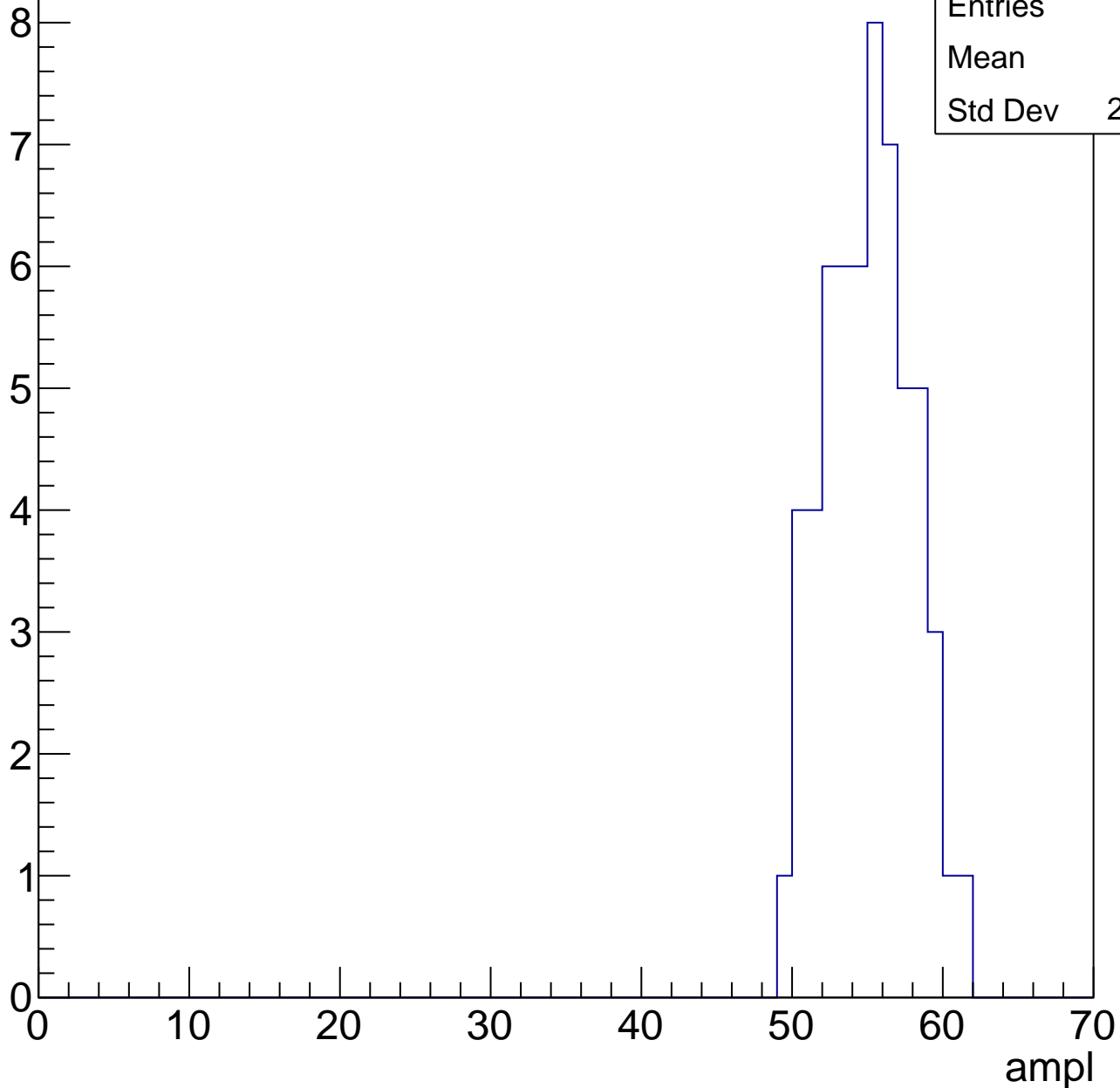
|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 47.88 |
| Std Dev | 3.179 |



# B0L002S, U1-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 49

Mean 59.51

Std Dev 2.366

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

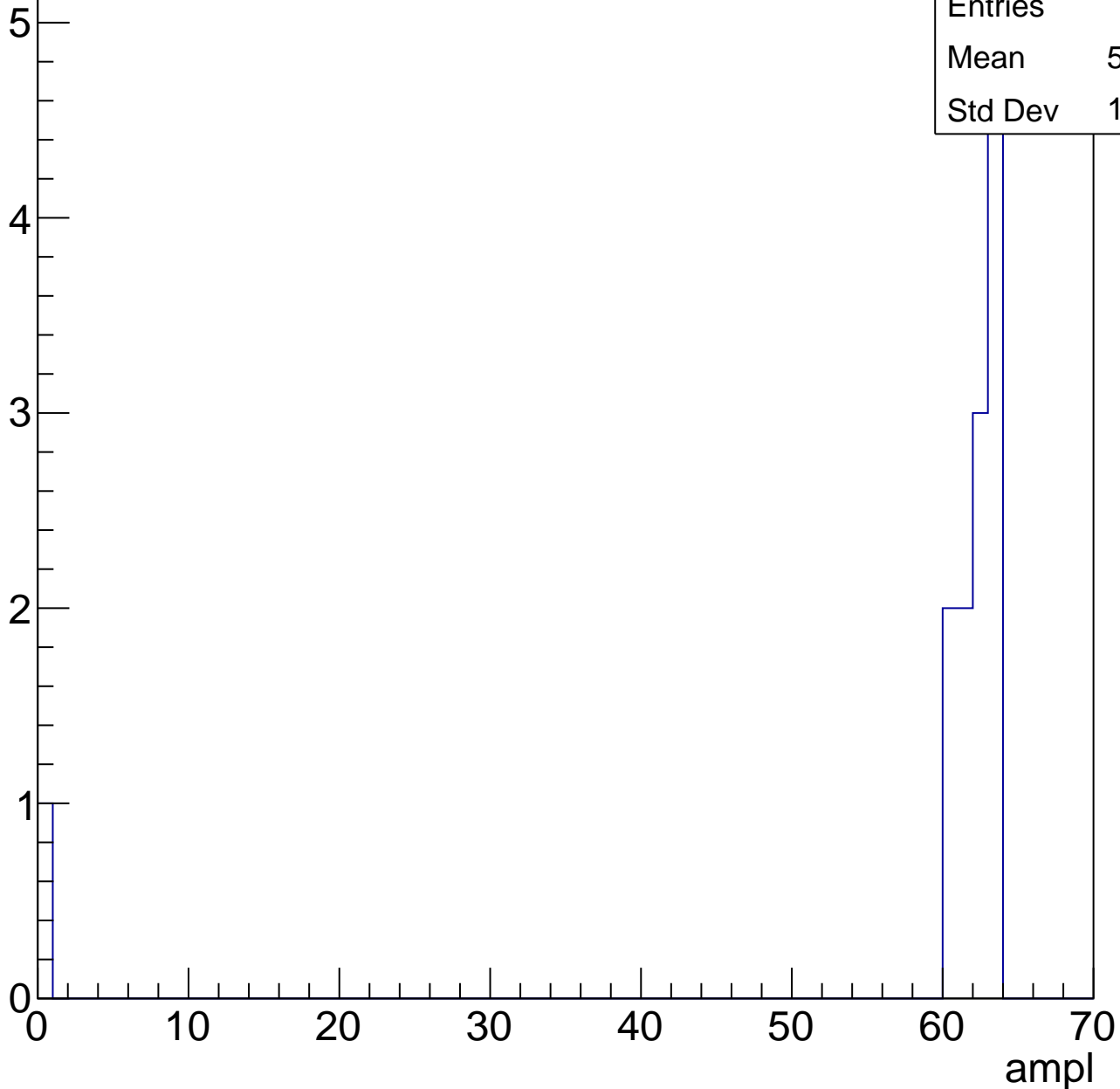
7

# B0L002S, U1-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 13    |
| Mean    | 57.15 |
| Std Dev | 16.53 |





# B0L002S, U1-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 11 |
| Std Dev | 11 |

# B0L002S, U1-ch25, adc0

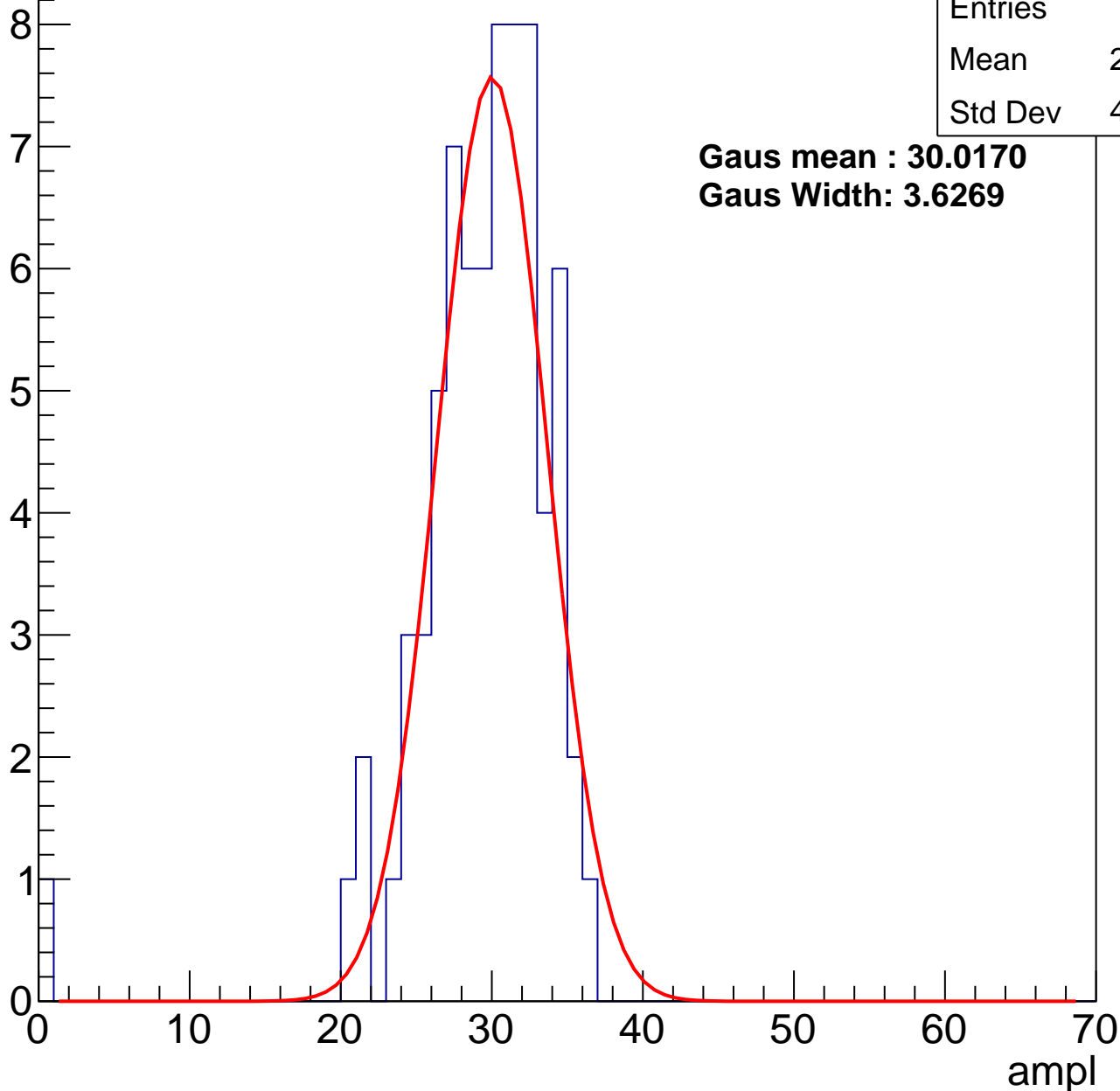
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 28.88 |
| Std Dev | 4.907 |

**Gaus mean : 30.0170**

**Gaus Width: 3.6269**



# B0L002S, U1-ch25, adc1

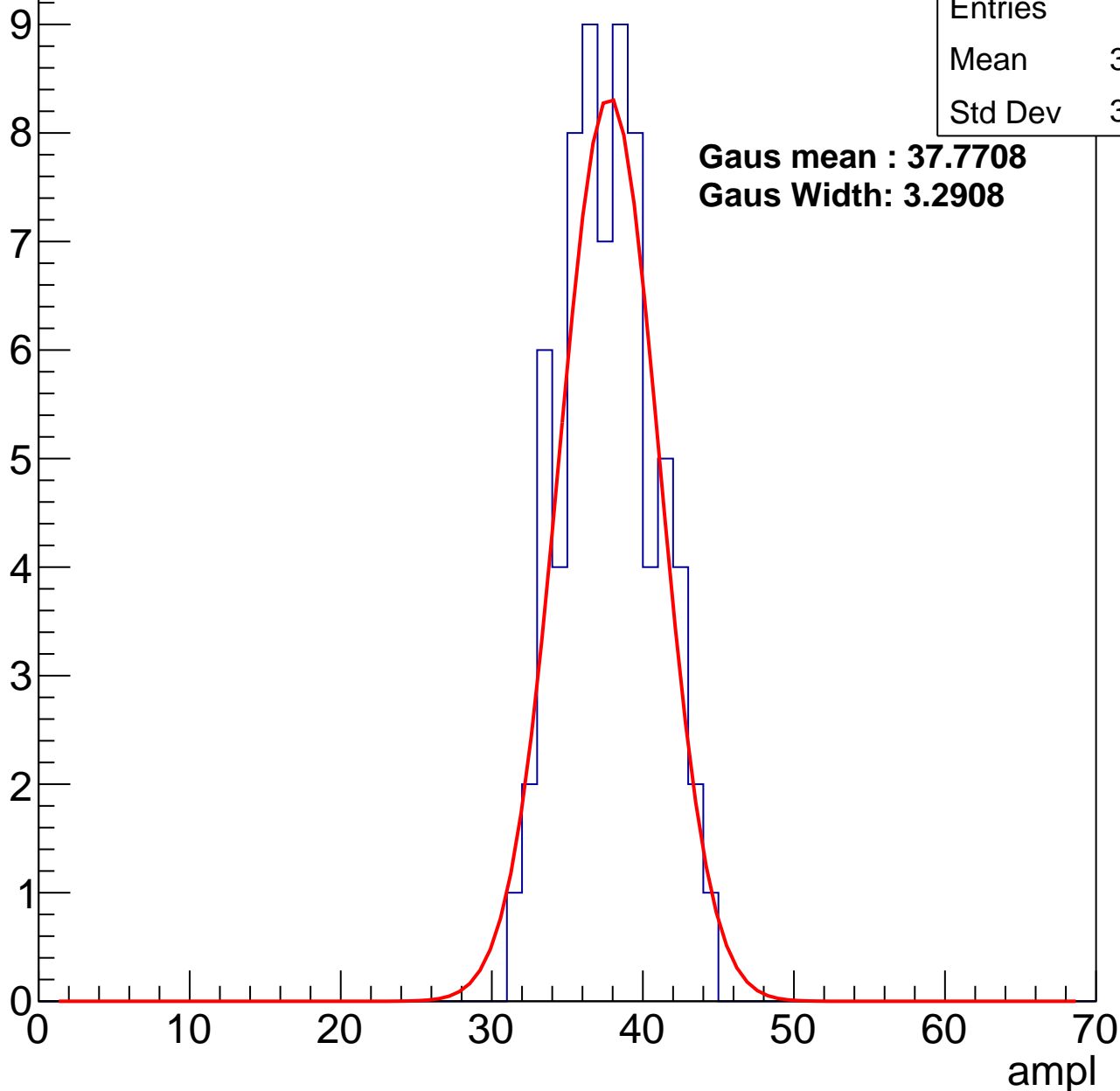
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 37.27 |
| Std Dev | 3.004 |

**Gaus mean : 37.7708**

**Gaus Width: 3.2908**

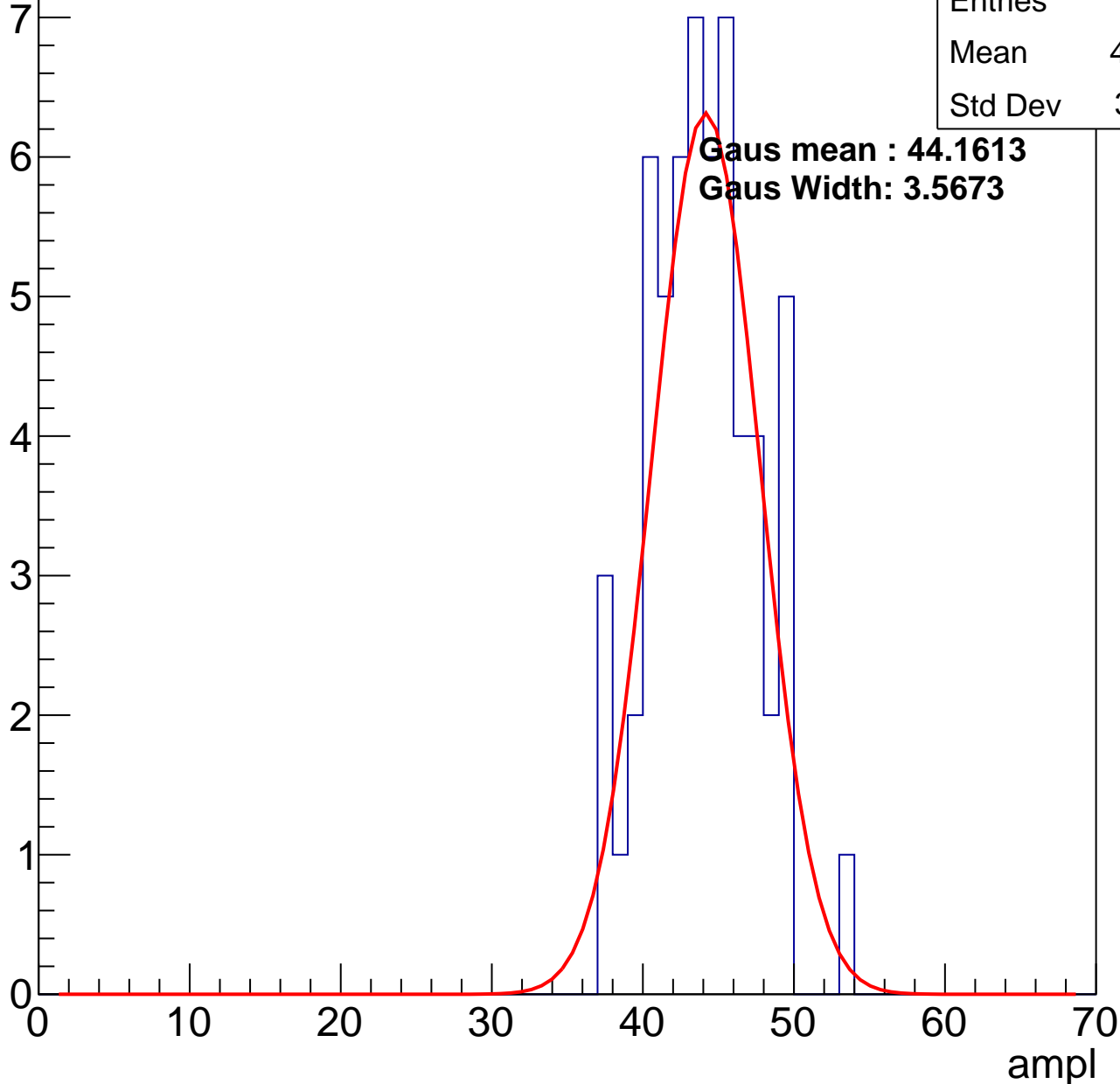


# B0L002S, U1-ch25, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

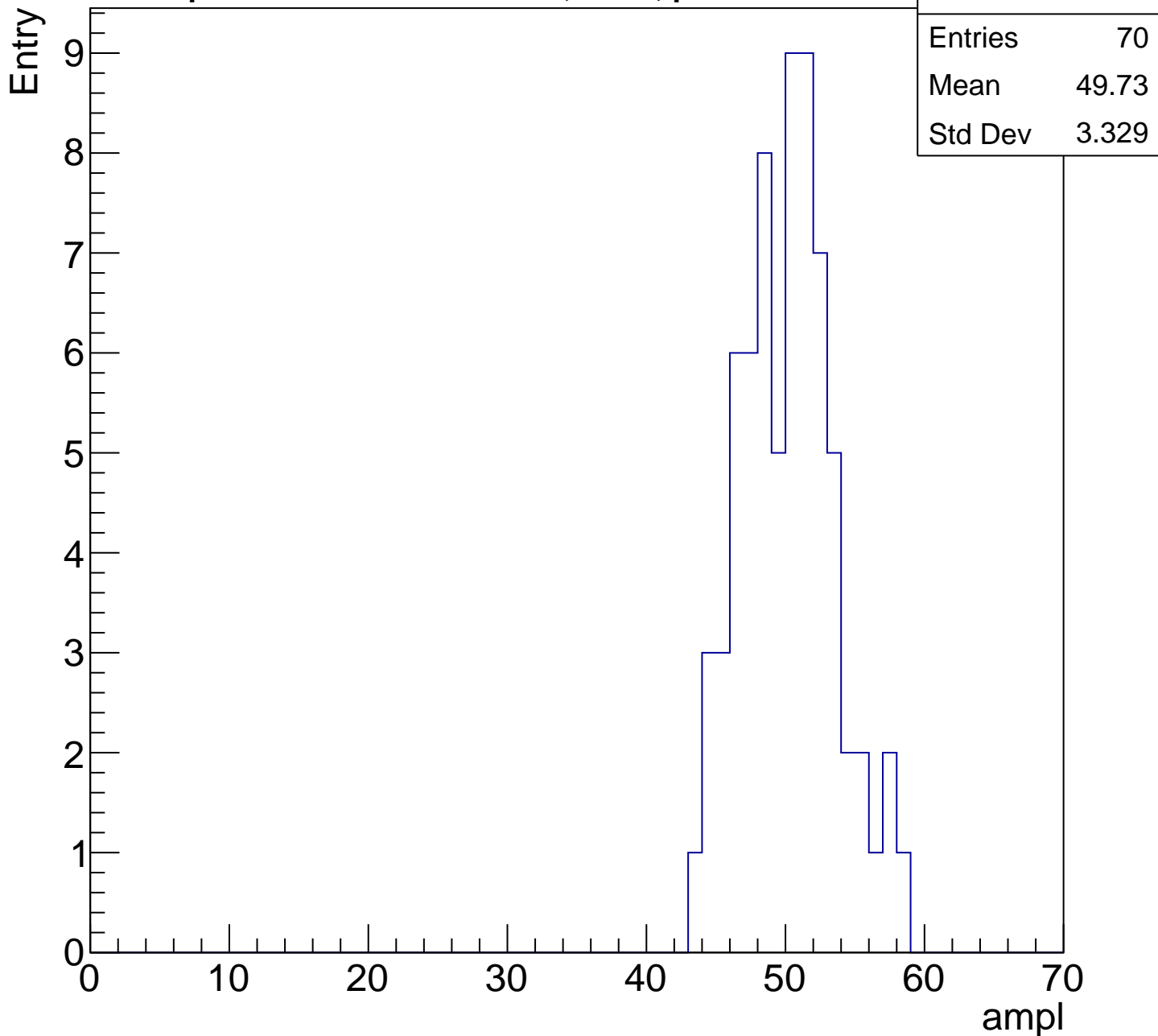
Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 43.56 |
| Std Dev | 3.421 |



# B0L002S, U1-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

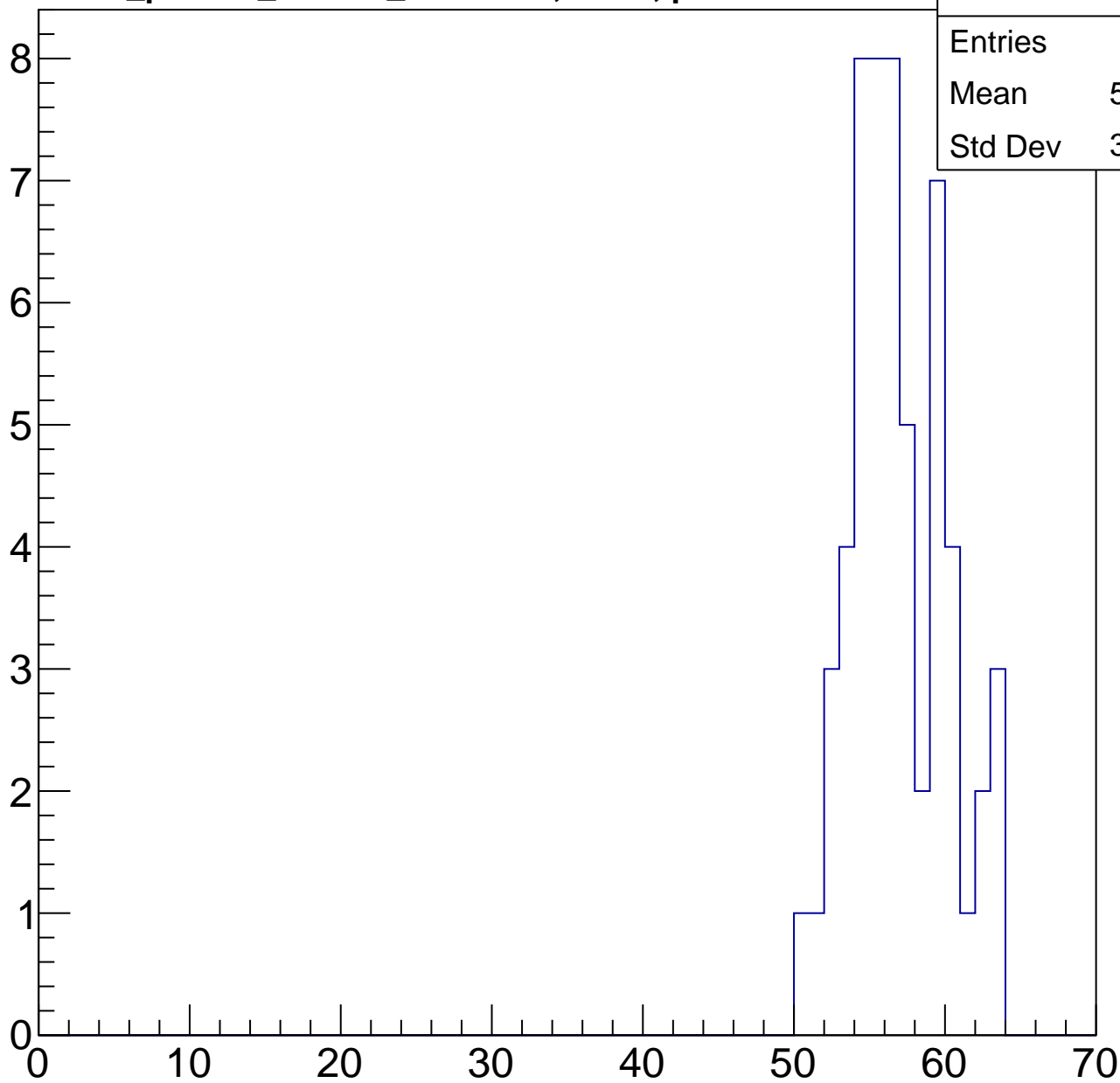
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 56.44 |
| Std Dev | 3.129 |

ampl

0 10 20 30 40 50 60 70

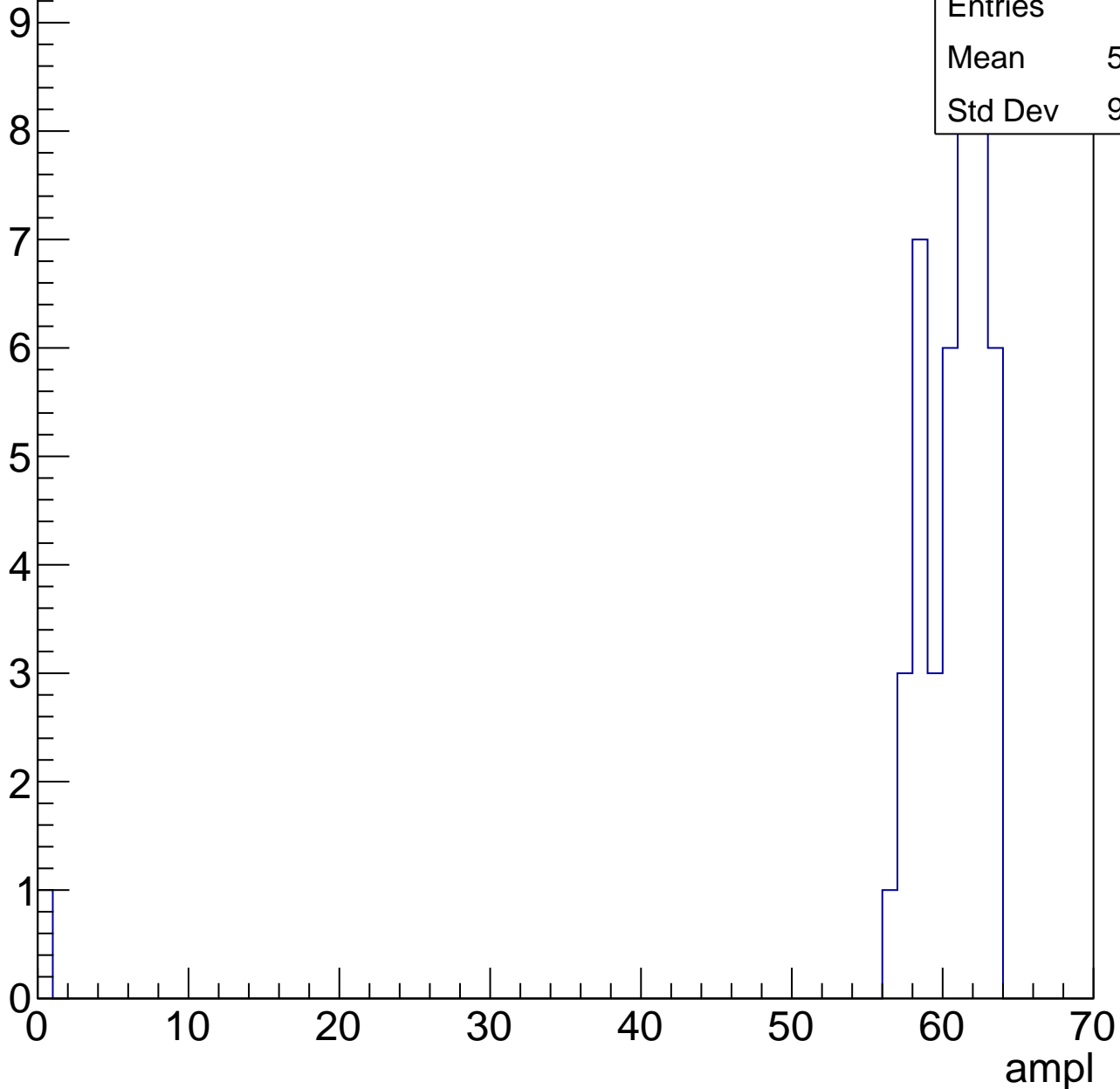


# B0L002S, U1-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

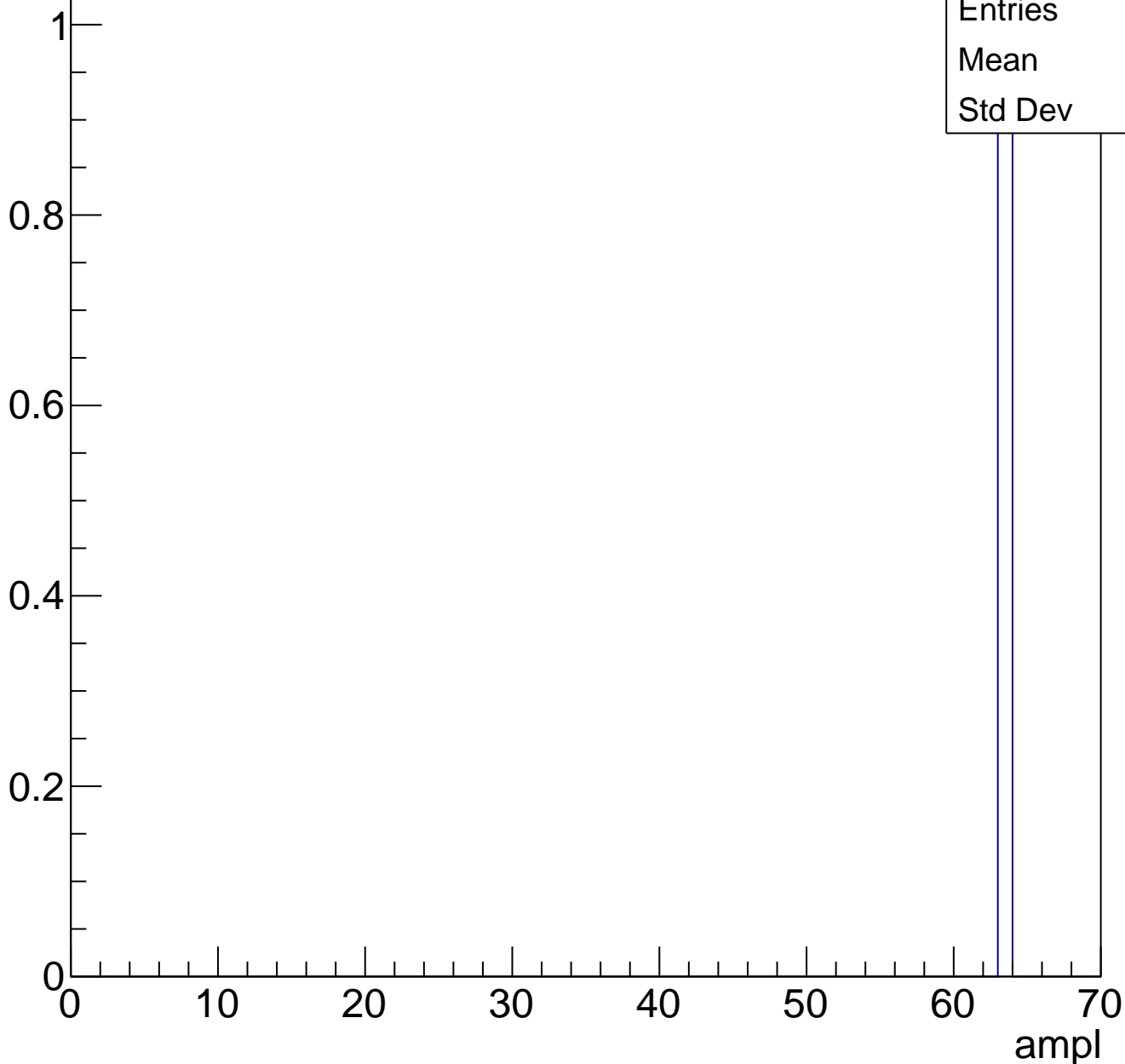
|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 58.93 |
| Std Dev | 9.193 |



# B0L002S, U1-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch26, adc0

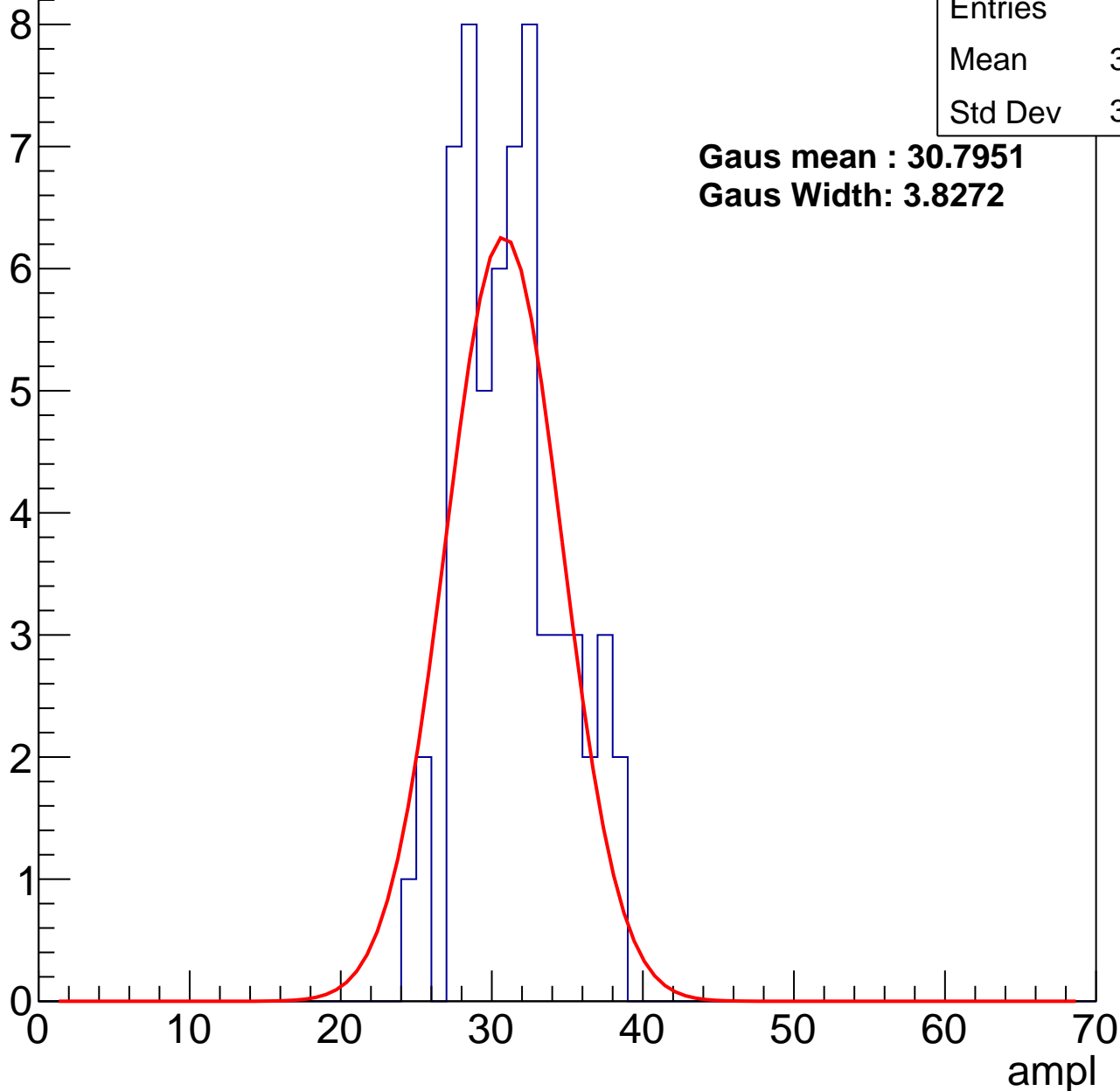
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 30.83 |
| Std Dev | 3.367 |

**Gaus mean : 30.7951**

**Gaus Width: 3.8272**



# B0L002S, U1-ch26, adc1

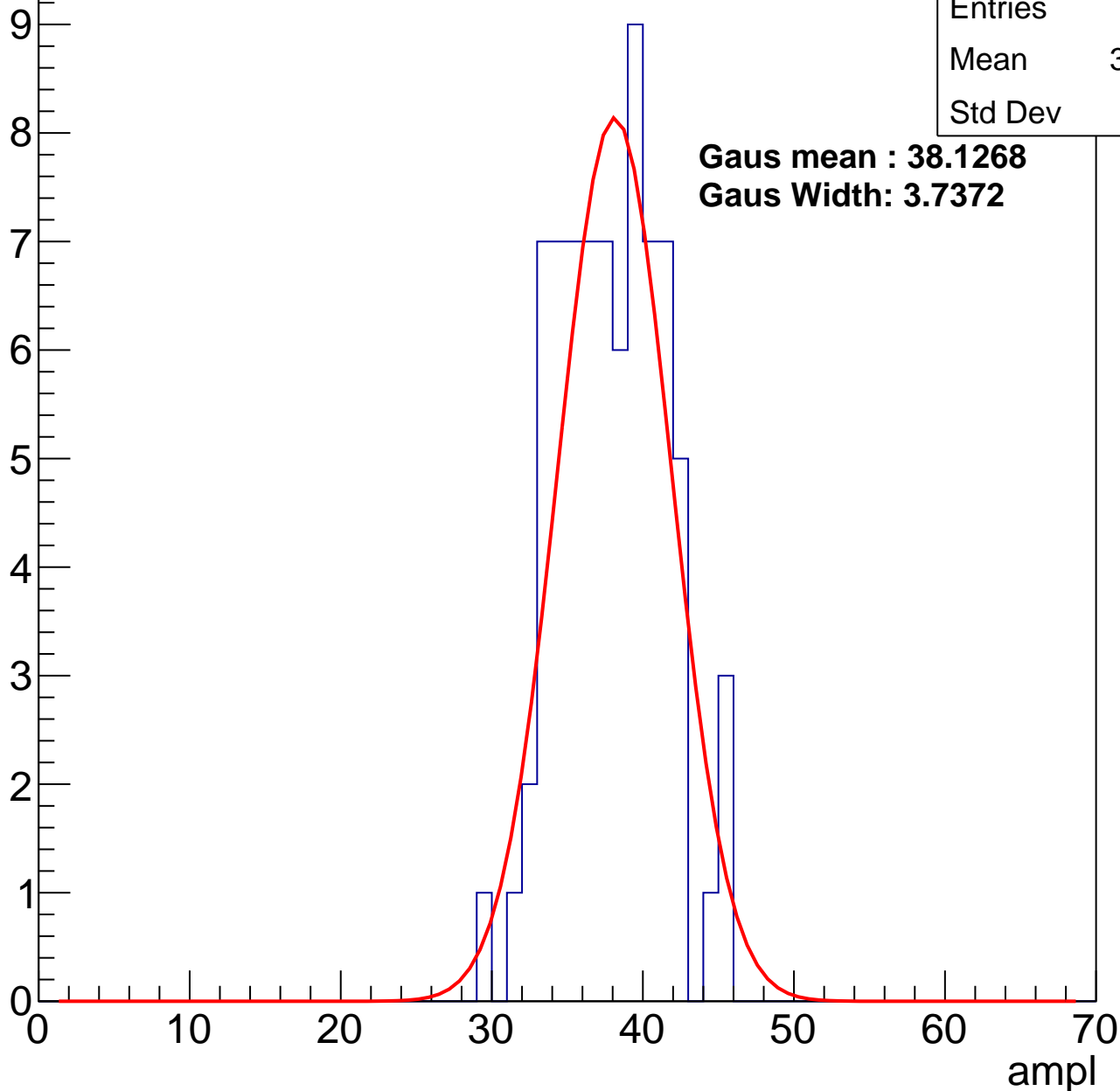
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 37.45 |
| Std Dev | 3.47  |

**Gaus mean : 38.1268**

**Gaus Width: 3.7372**



# B0L002S, U1-ch26, adc2

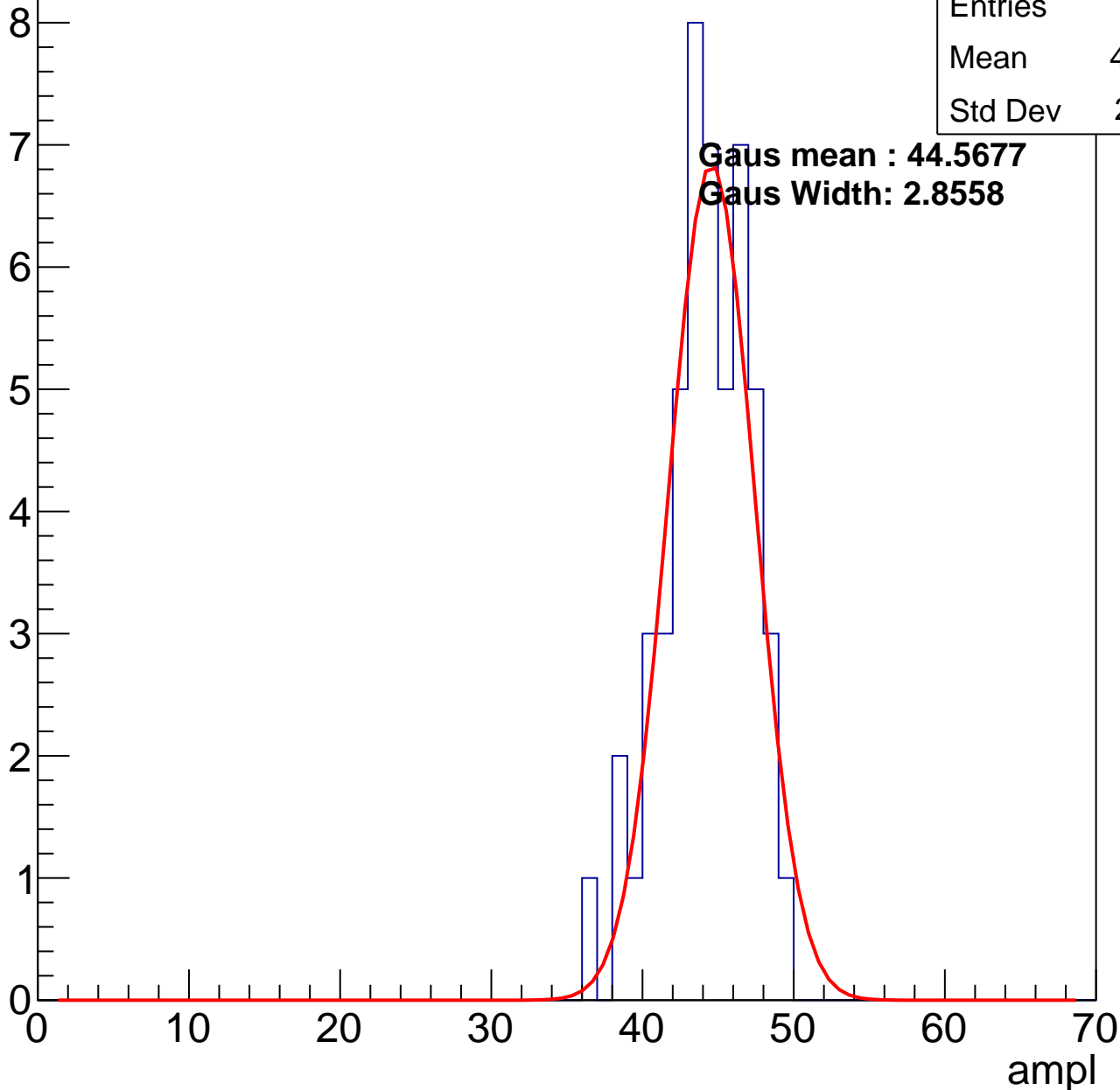
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 43.75 |
| Std Dev | 2.841 |

**Gaus mean : 44.5677**

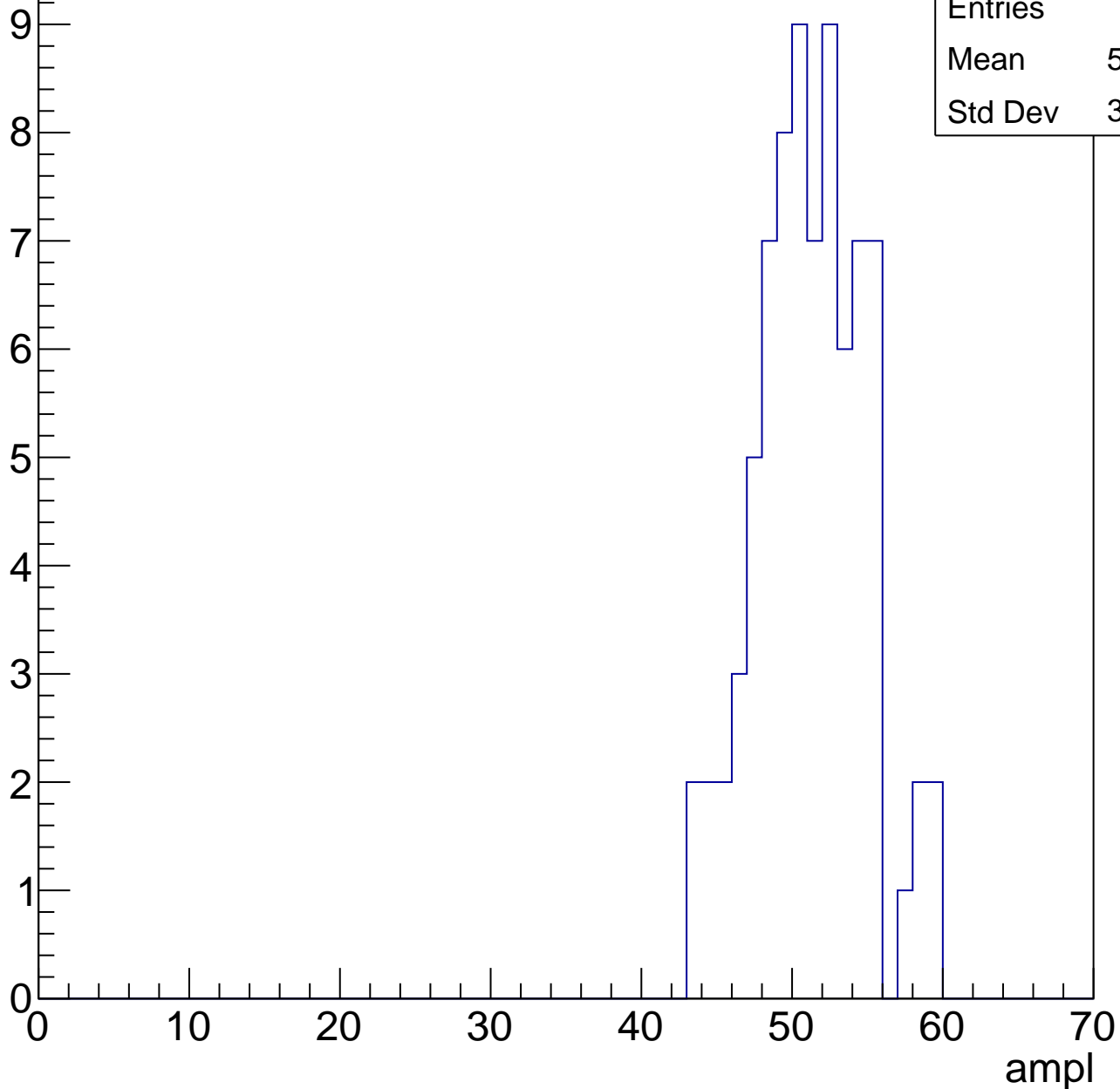
**Gaus Width: 2.8558**



# B0L002S, U1-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

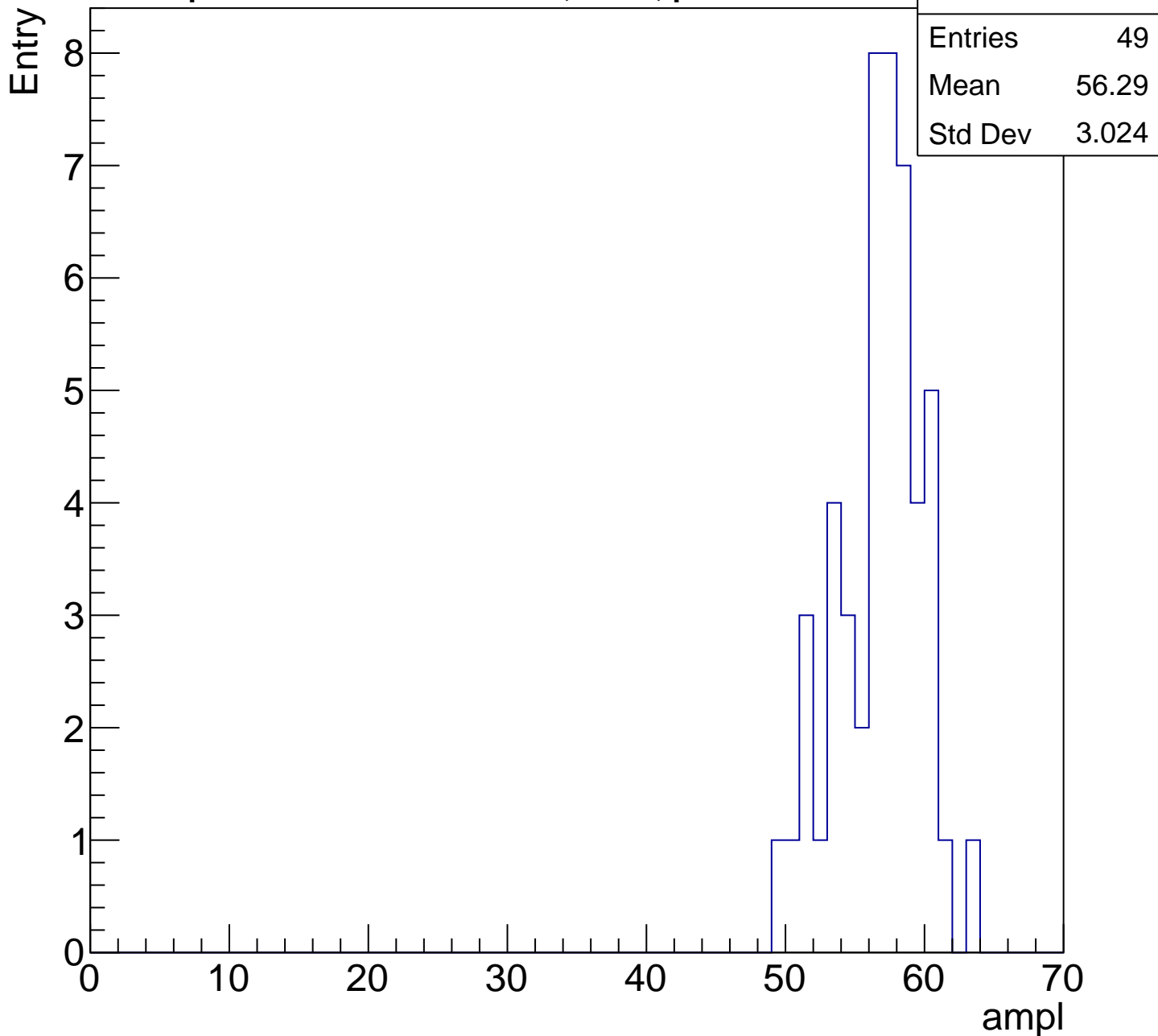
Entry



|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 50.78 |
| Std Dev | 3.599 |

# B0L002S, U1-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

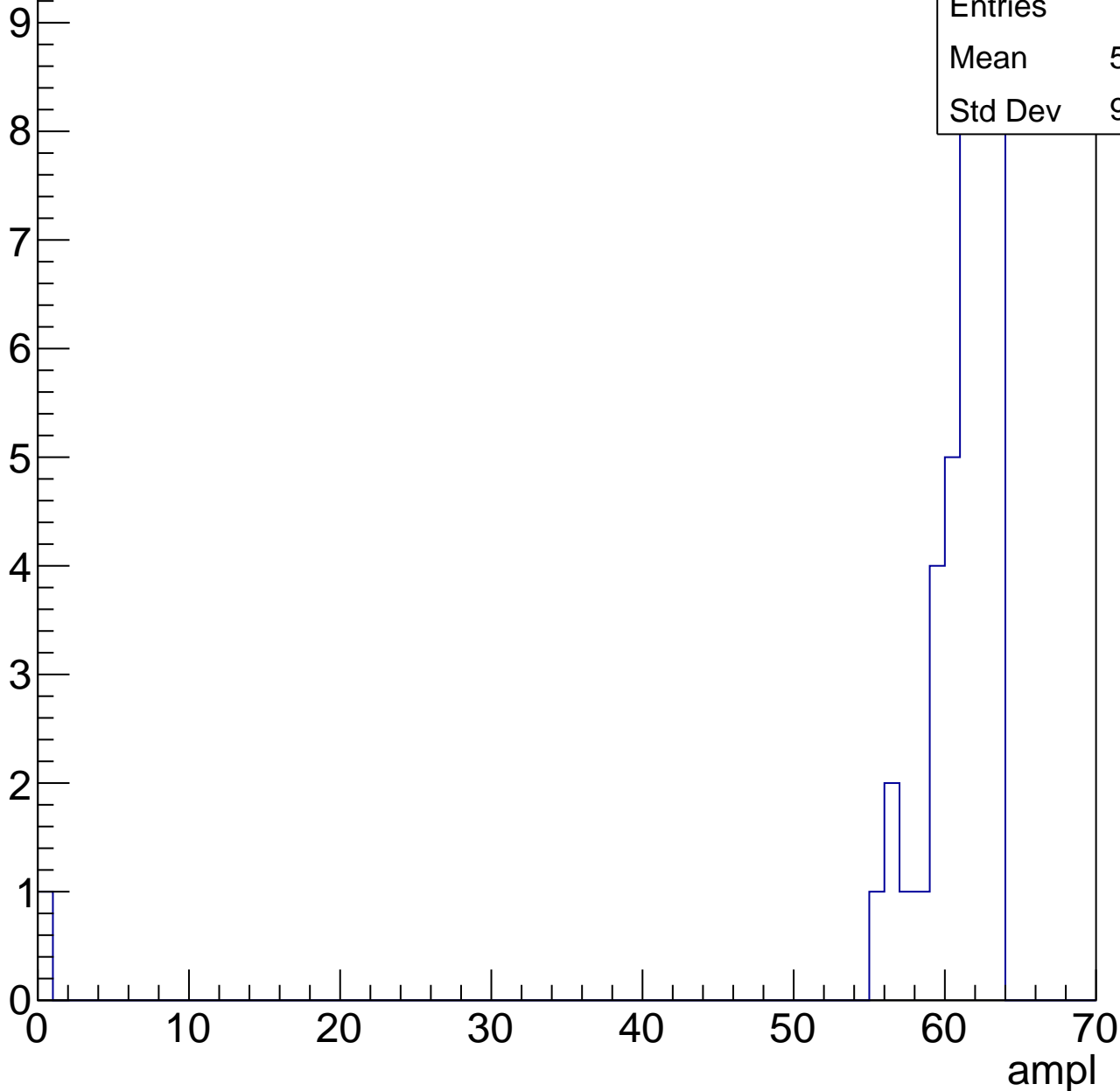


# B0L002S, U1-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

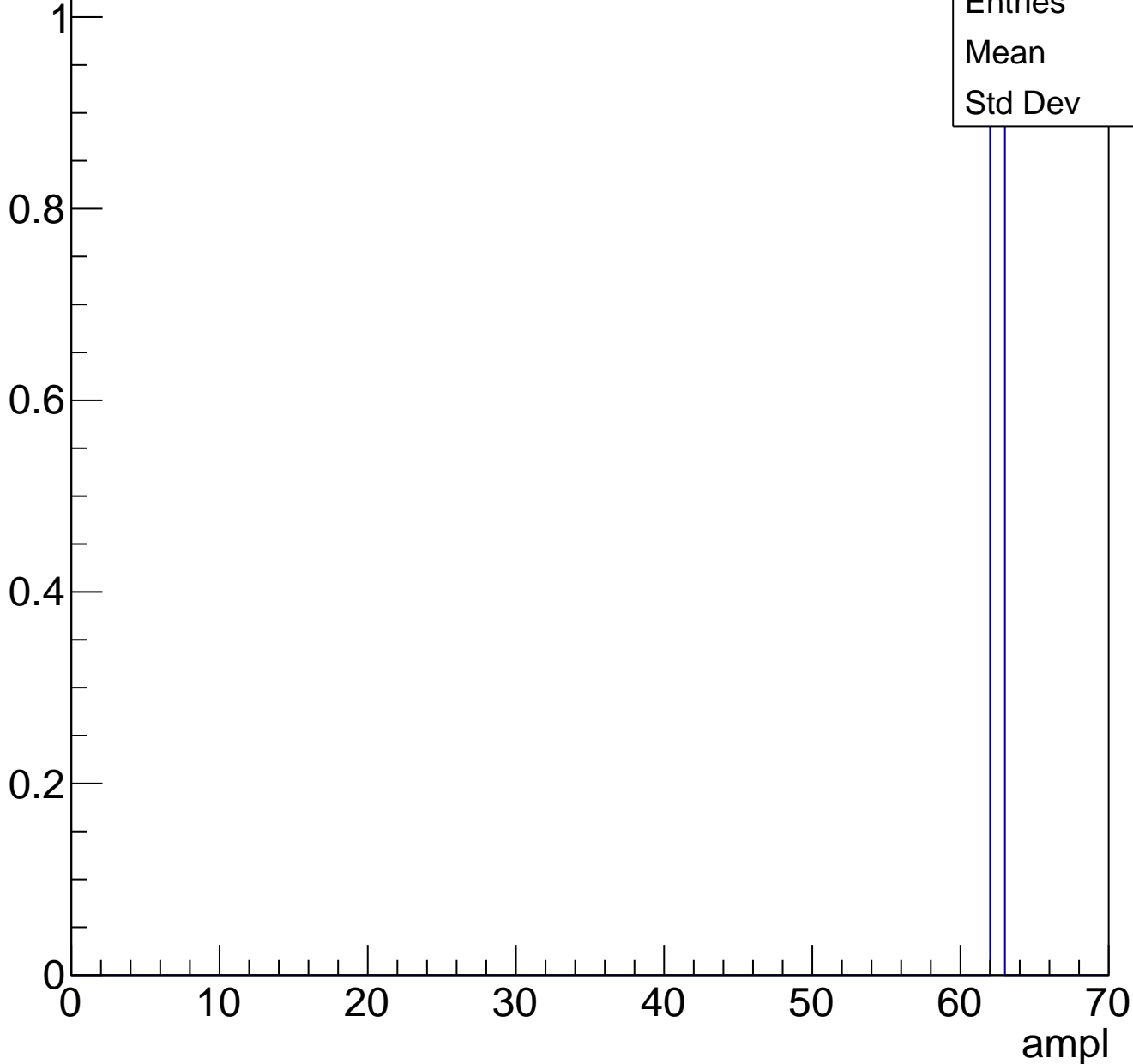
|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 59.33 |
| Std Dev | 9.489 |



# B0L002S, U1-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 24 |
| Std Dev | 0  |

# B0L002S, U1-ch27, adc0

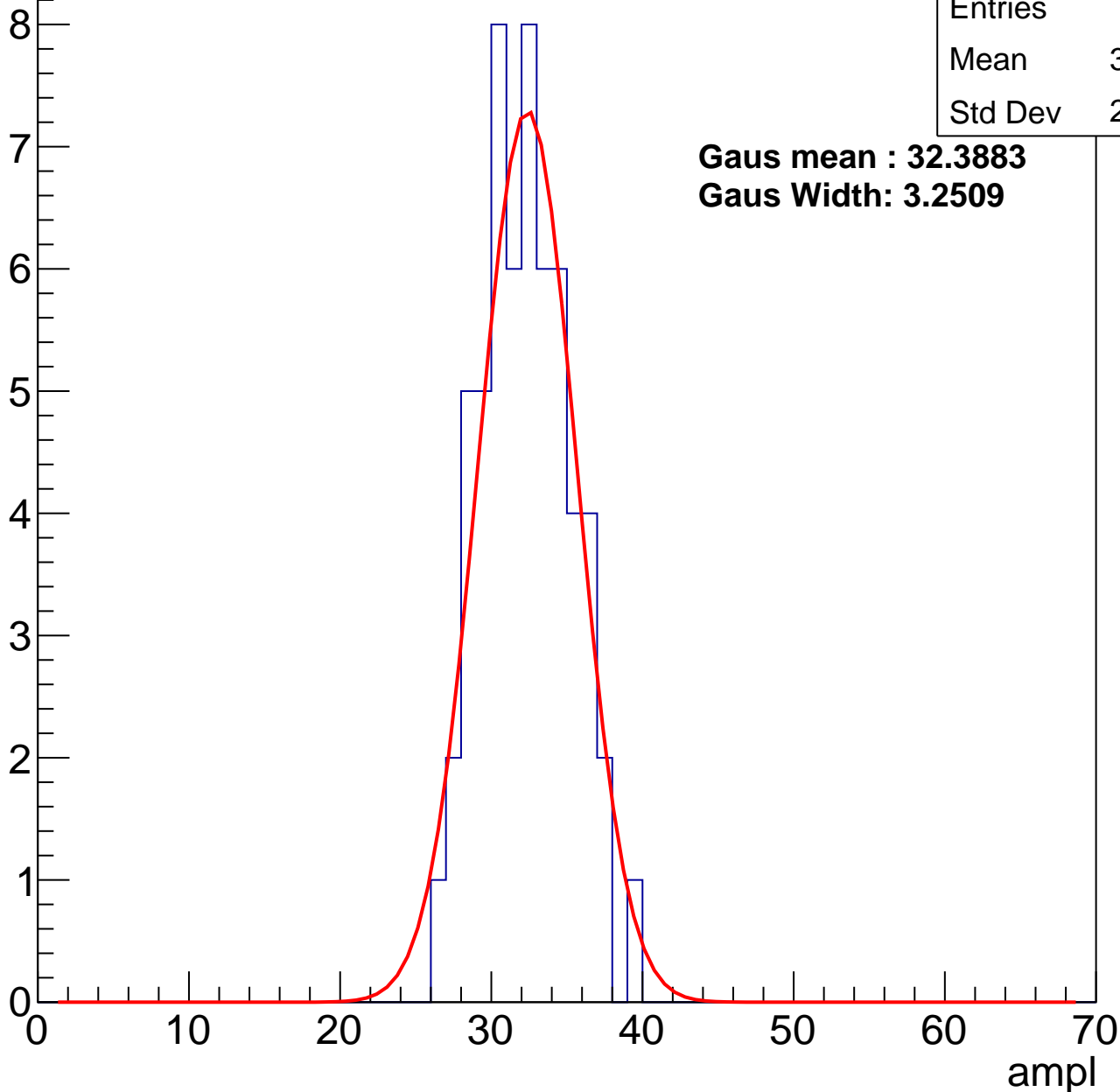
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 31.83 |
| Std Dev | 2.866 |

**Gaus mean : 32.3883**

**Gaus Width: 3.2509**



# B0L002S, U1-ch27, adc1

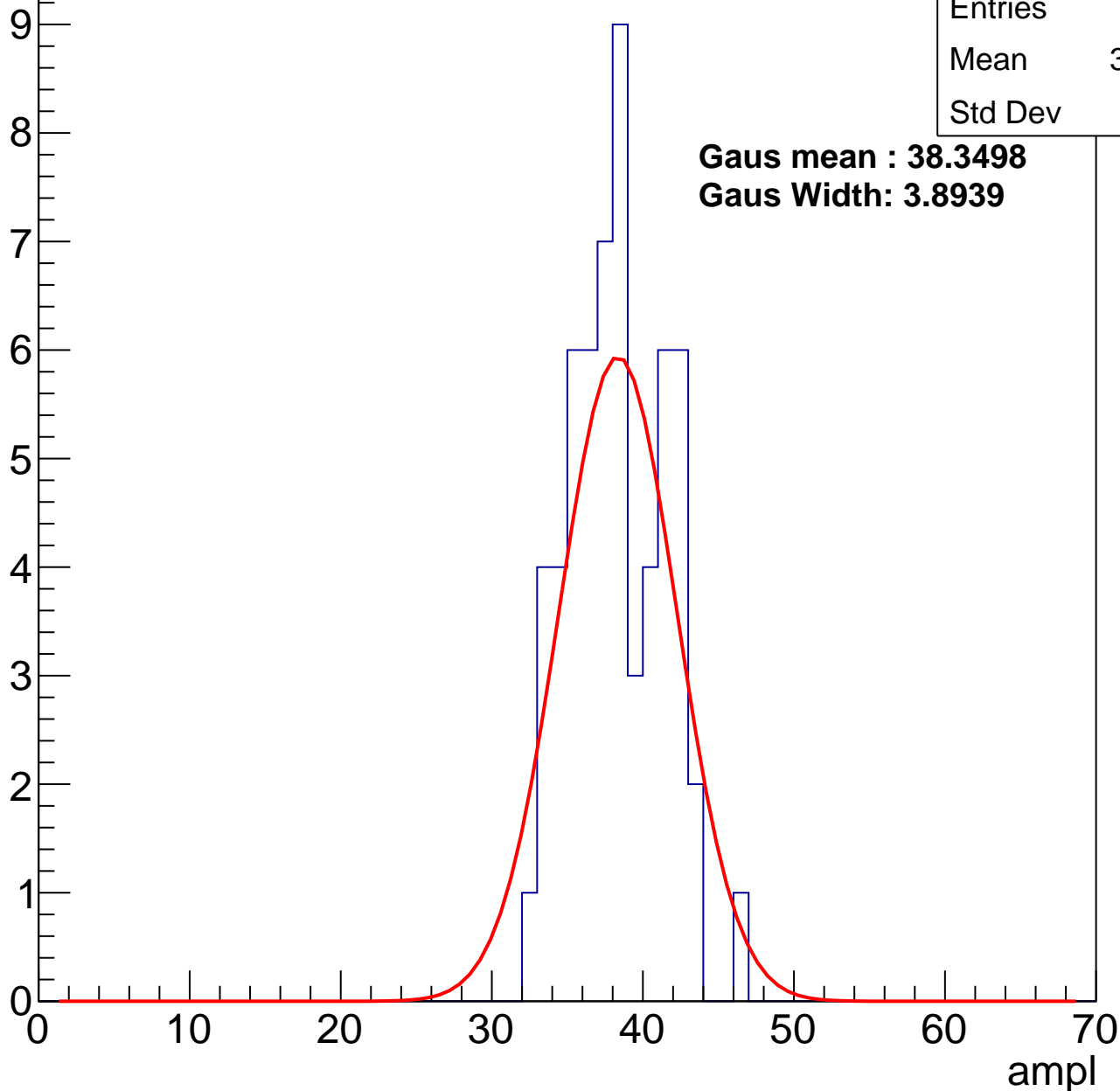
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 37.86 |
| Std Dev | 3.1   |

**Gaus mean : 38.3498**

**Gaus Width: 3.8939**



# B0L002S, U1-ch27, adc2

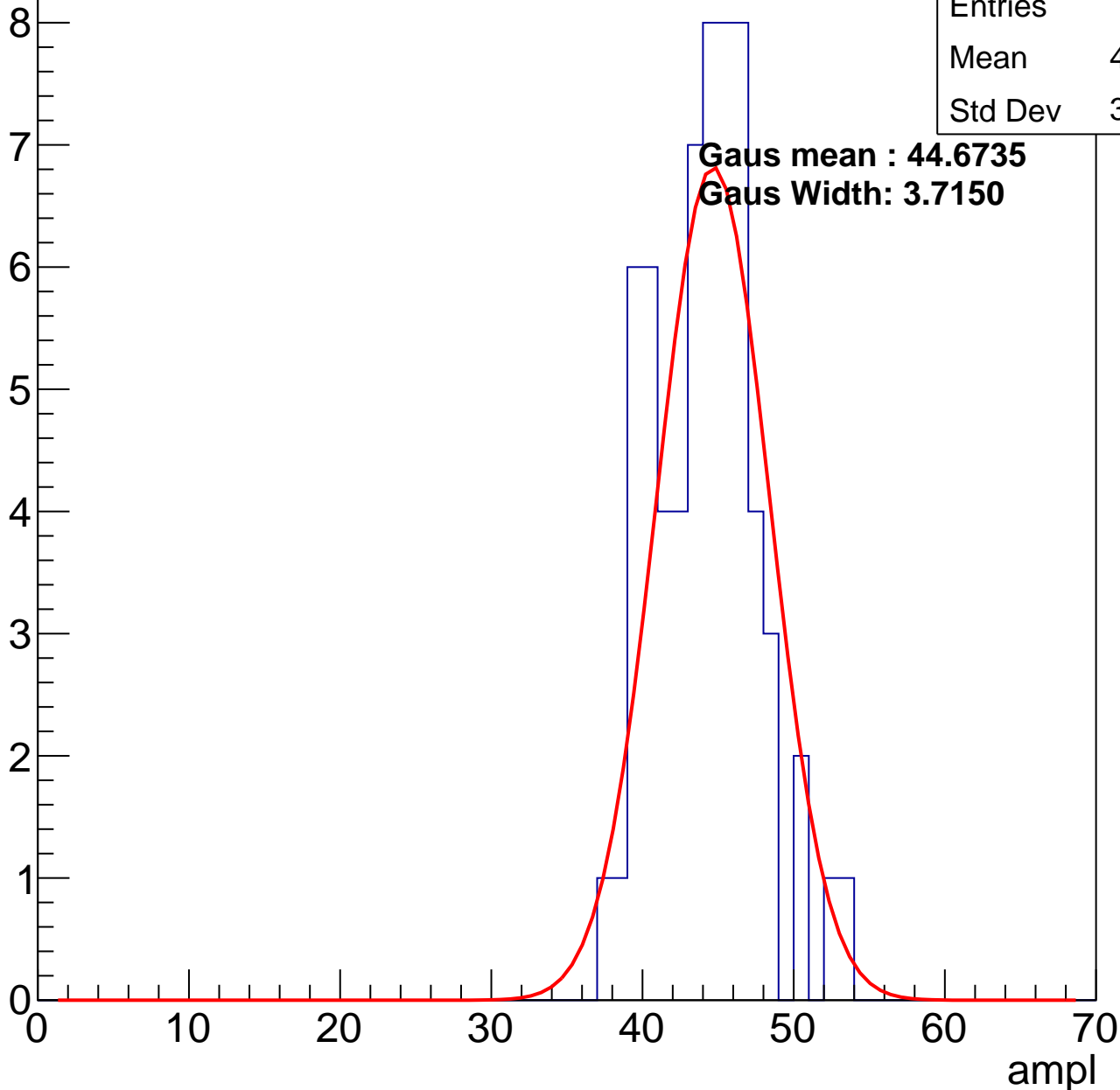
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 43.73 |
| Std Dev | 3.369 |

**Gaus mean : 44.6735**

**Gaus Width: 3.7150**

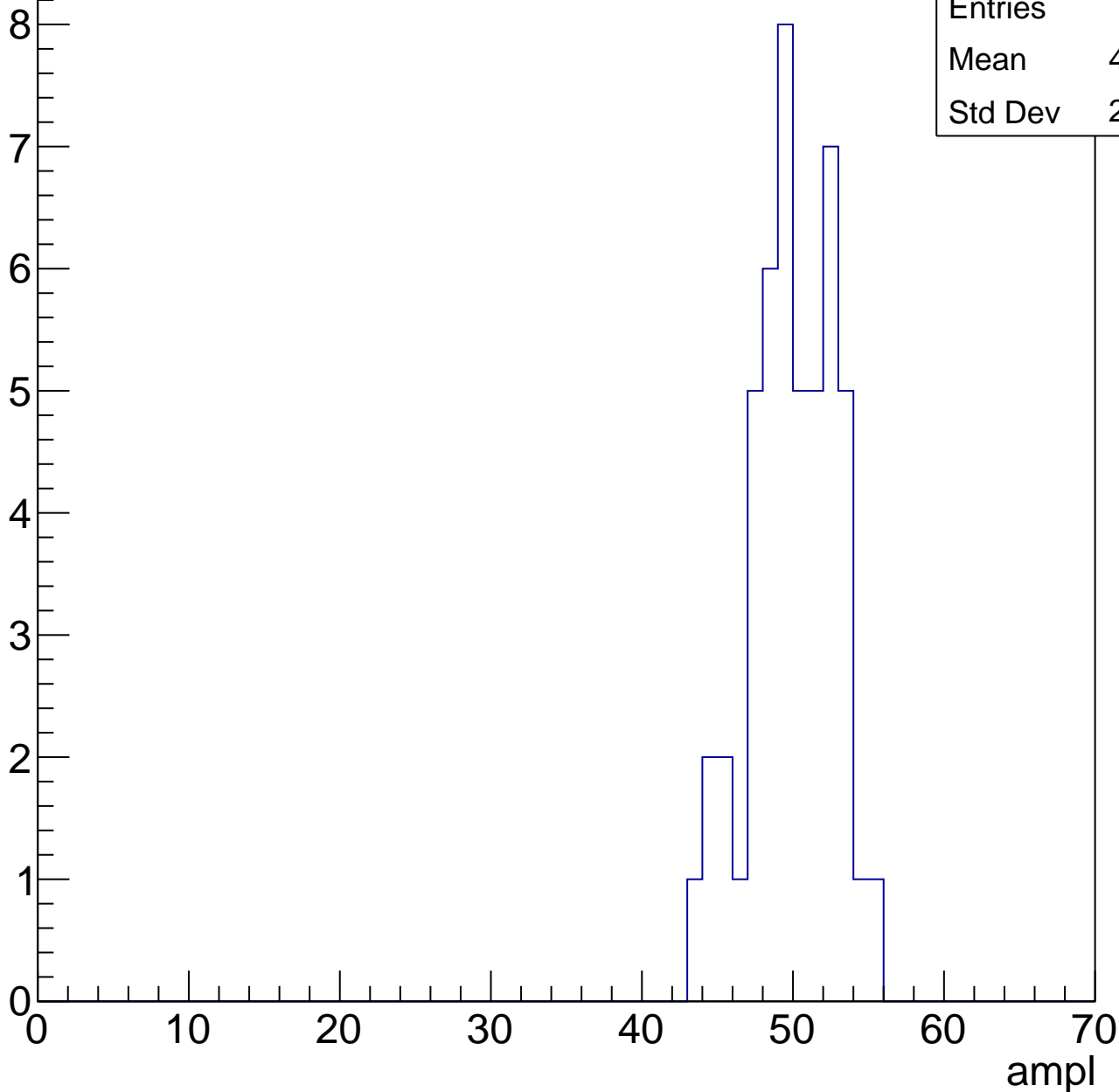


# B0L002S, U1-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 49.49 |
| Std Dev | 2.749 |



# B0L002S, U1-ch27, adc4

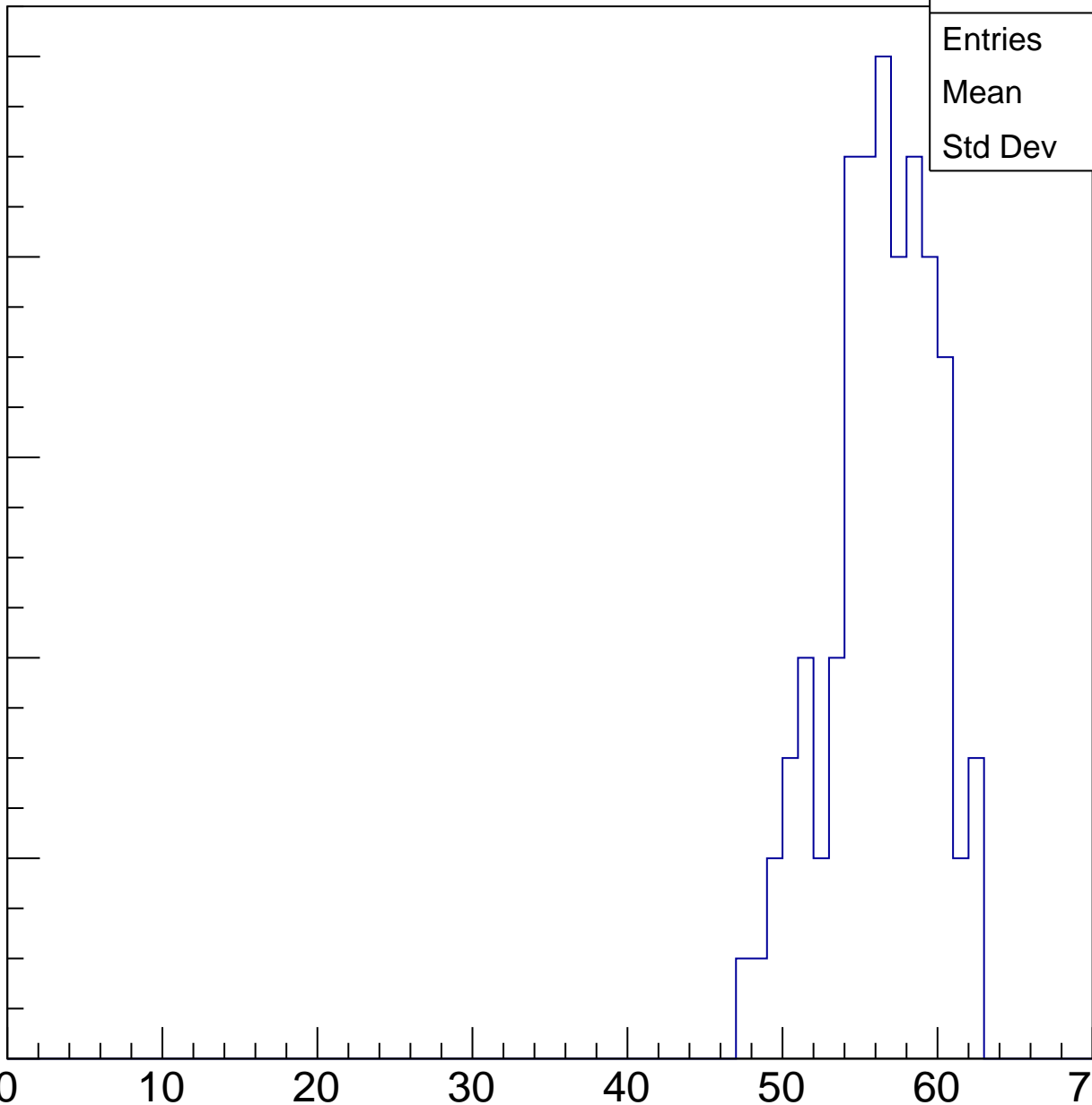
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10  
8  
6  
4  
2  
0

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 55.88 |
| Std Dev | 3.402 |

ampl

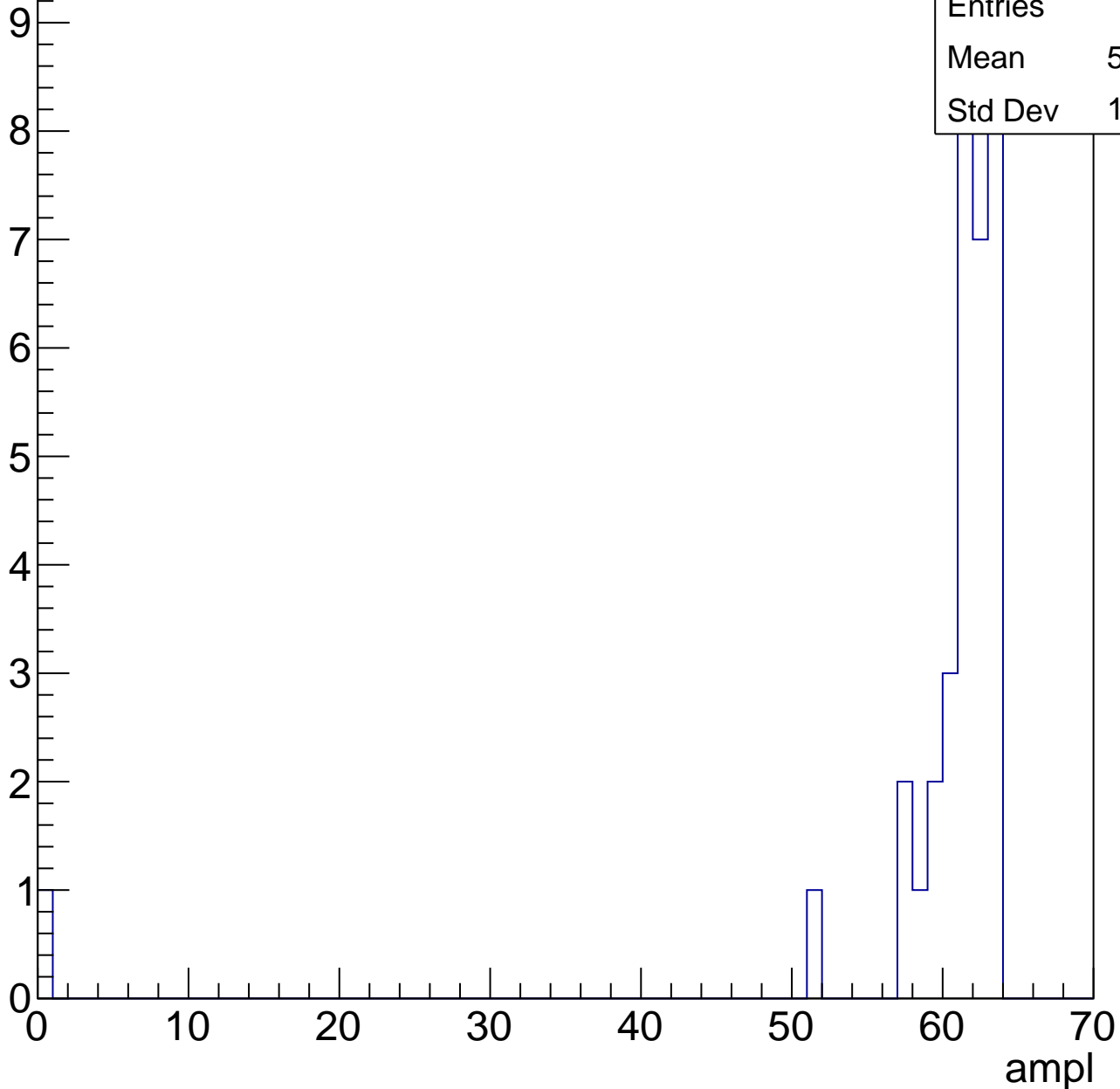


# B0L002S, U1-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 59.12 |
| Std Dev | 10.57 |



# B0L002S, U1-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch28, adc0

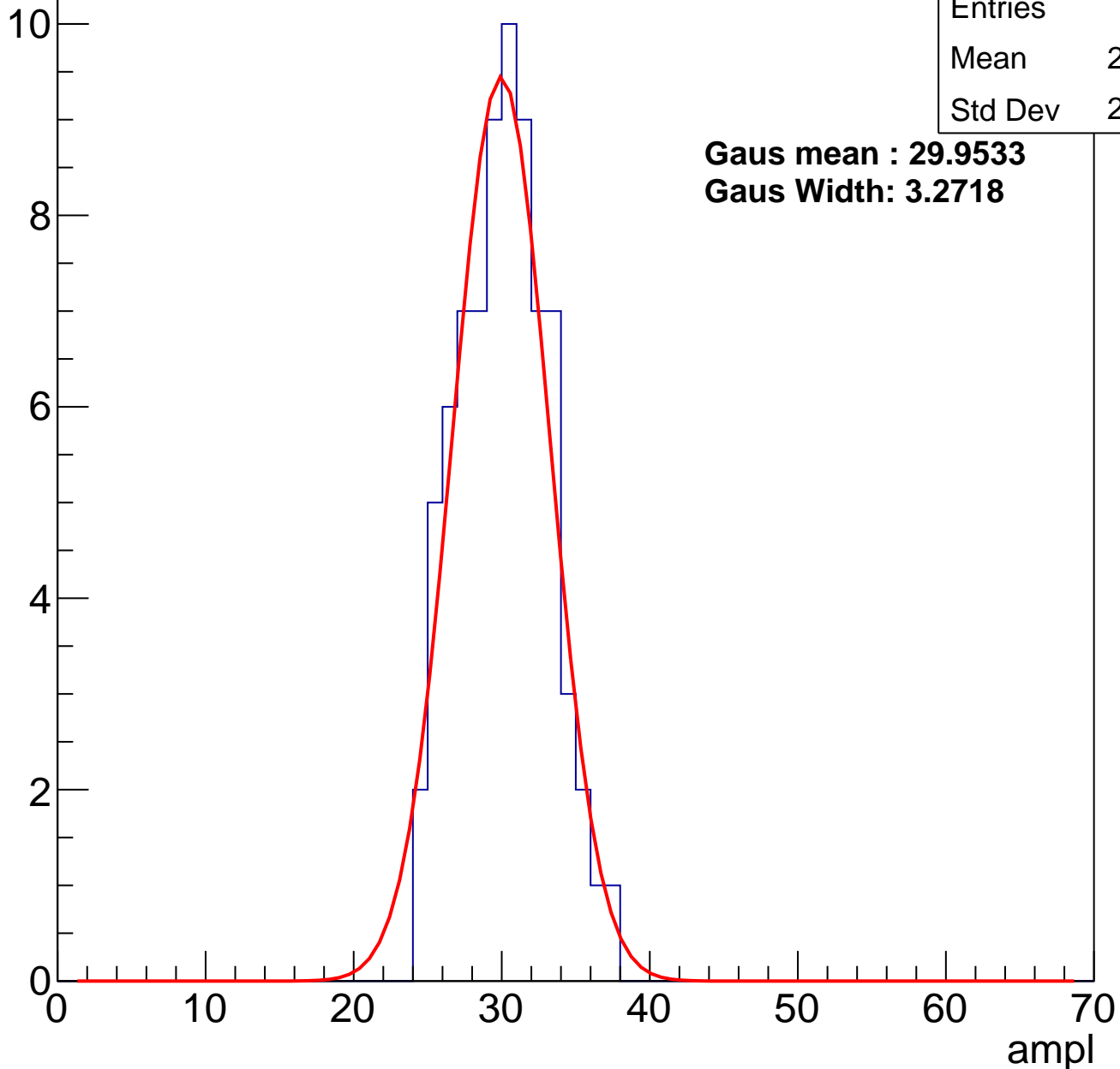
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 29.66 |
| Std Dev | 2.963 |

**Gaus mean : 29.9533**

**Gaus Width: 3.2718**

Entry



# B0L002S, U1-ch28, adc1

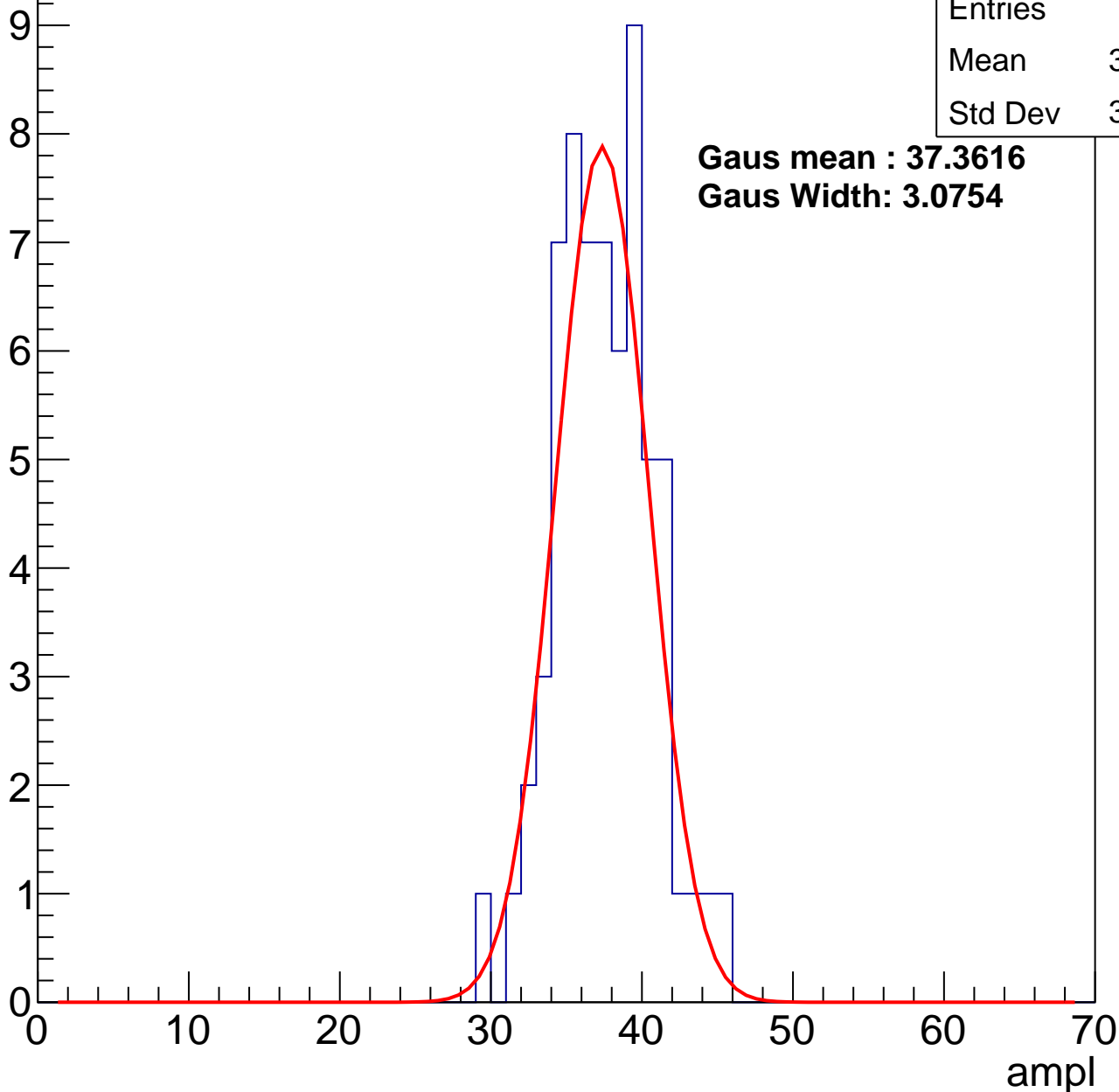
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 37.08 |
| Std Dev | 3.139 |

**Gaus mean : 37.3616**

**Gaus Width: 3.0754**



# B0L002S, U1-ch28, adc2

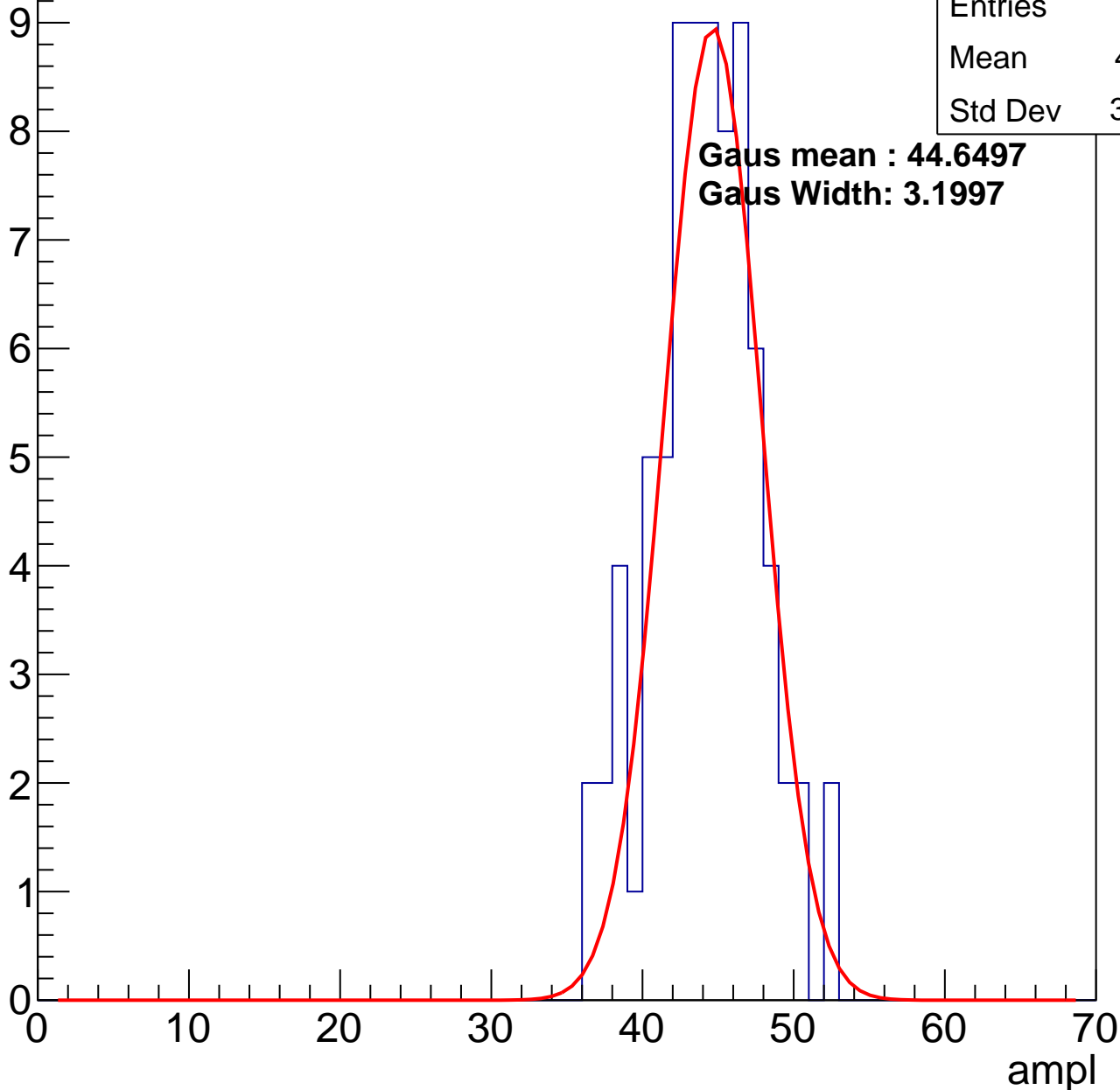
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 43.71 |
| Std Dev | 3.505 |

**Gaus mean : 44.6497**

**Gaus Width: 3.1997**

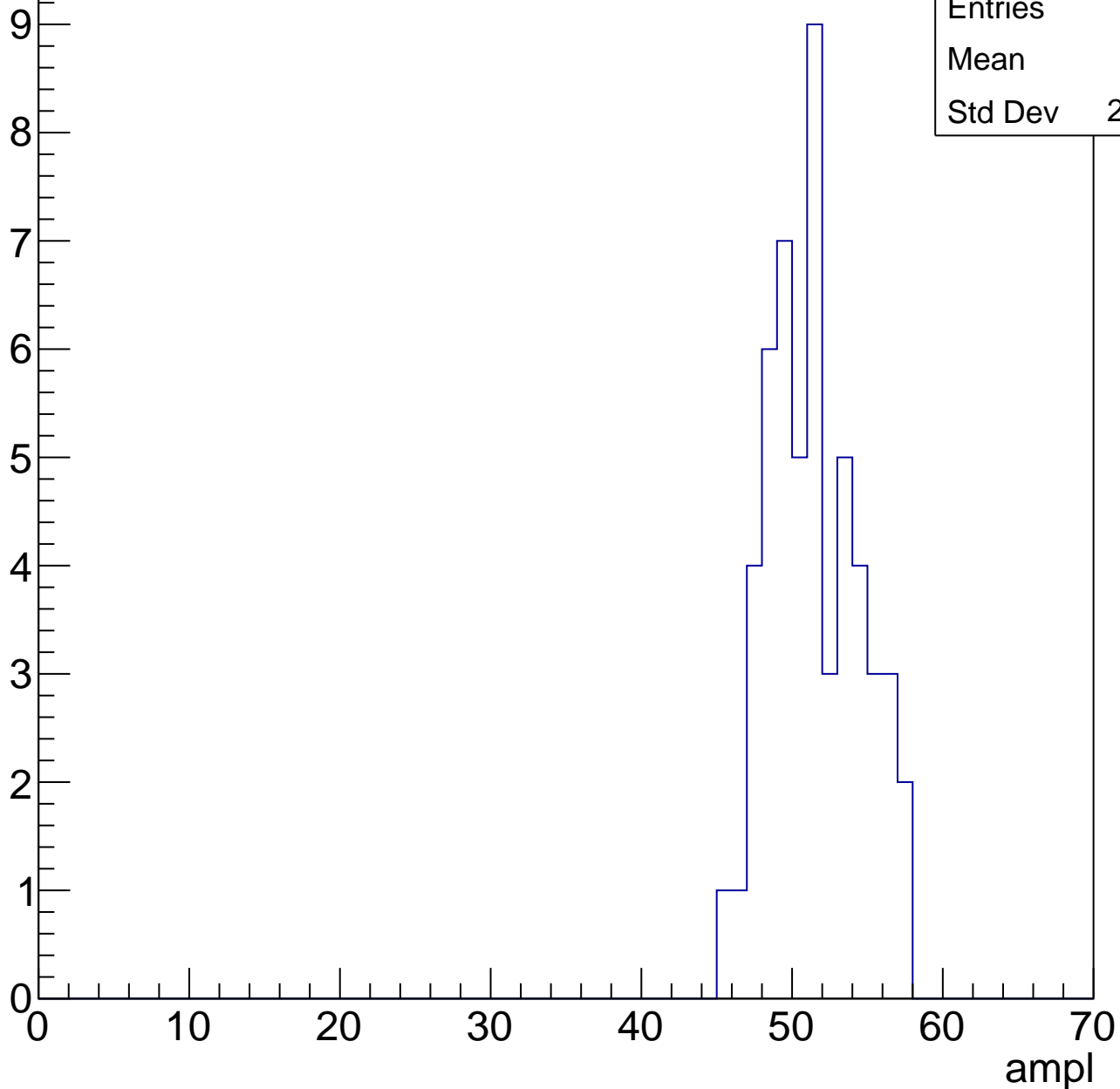


# B0L002S, U1-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

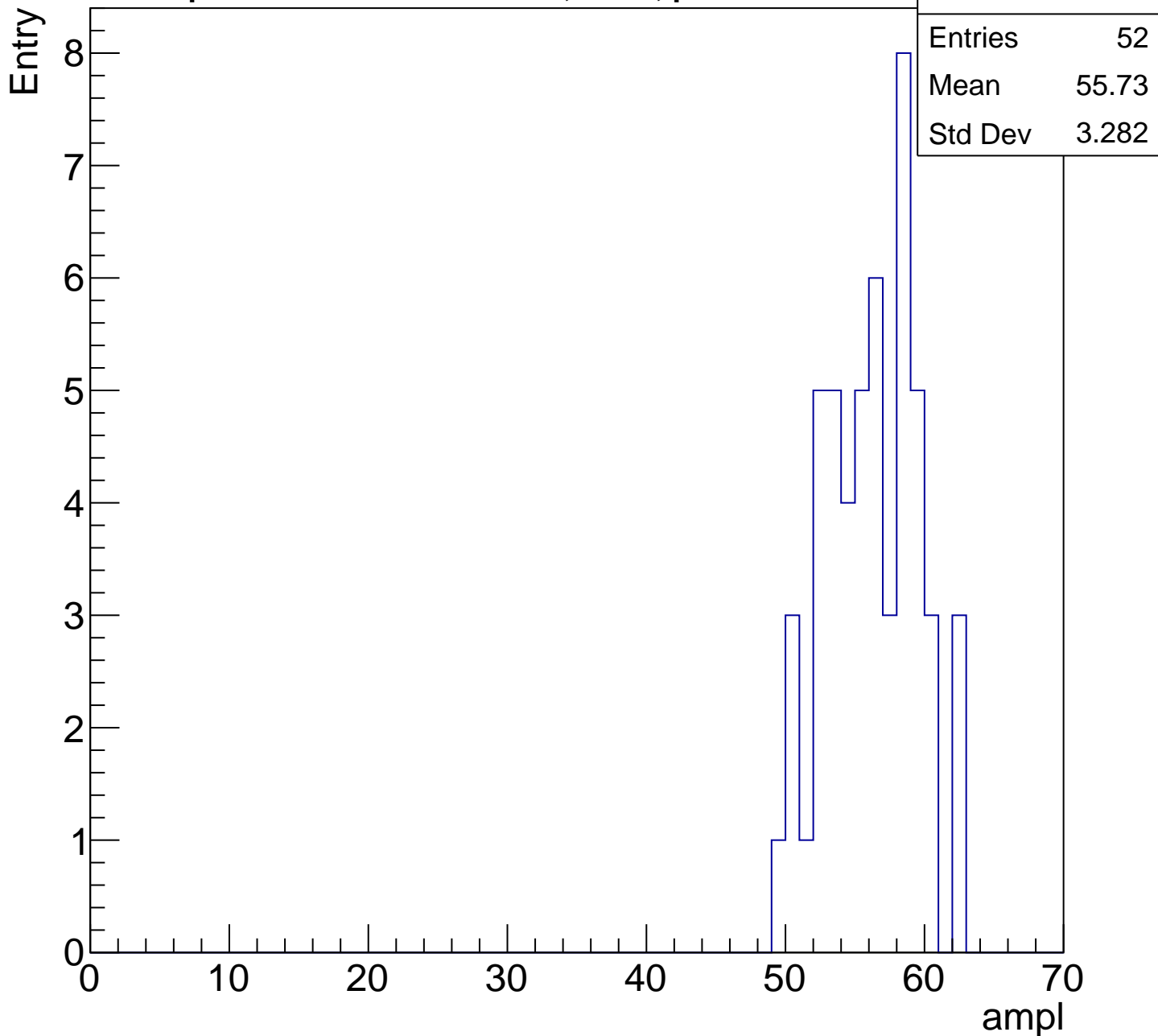
Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 51    |
| Std Dev | 2.965 |



# B0L002S, U1-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch28, adc5

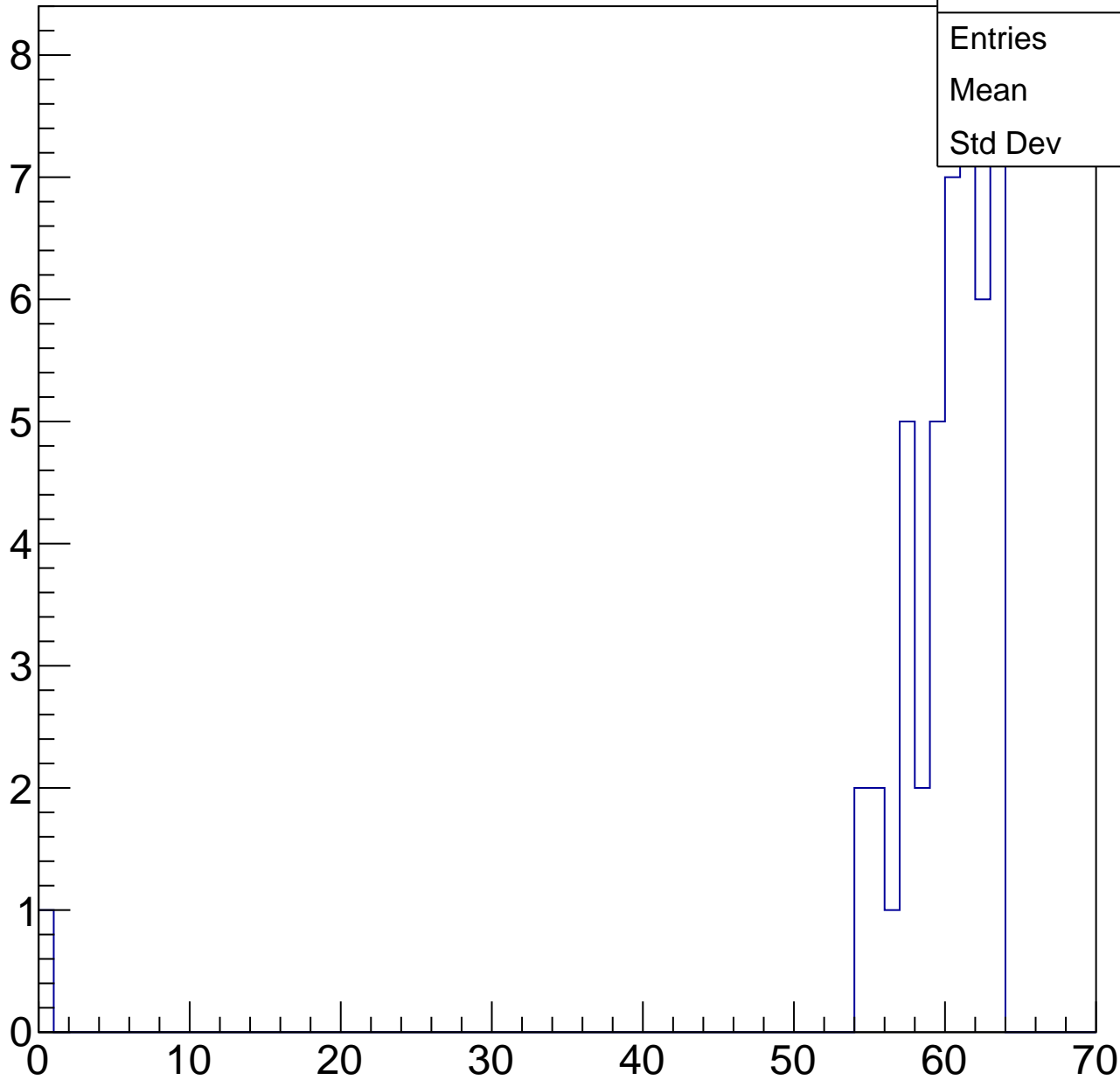
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |      |
|---------|------|
| Entries | 47   |
| Mean    | 58.6 |
| Std Dev | 9    |

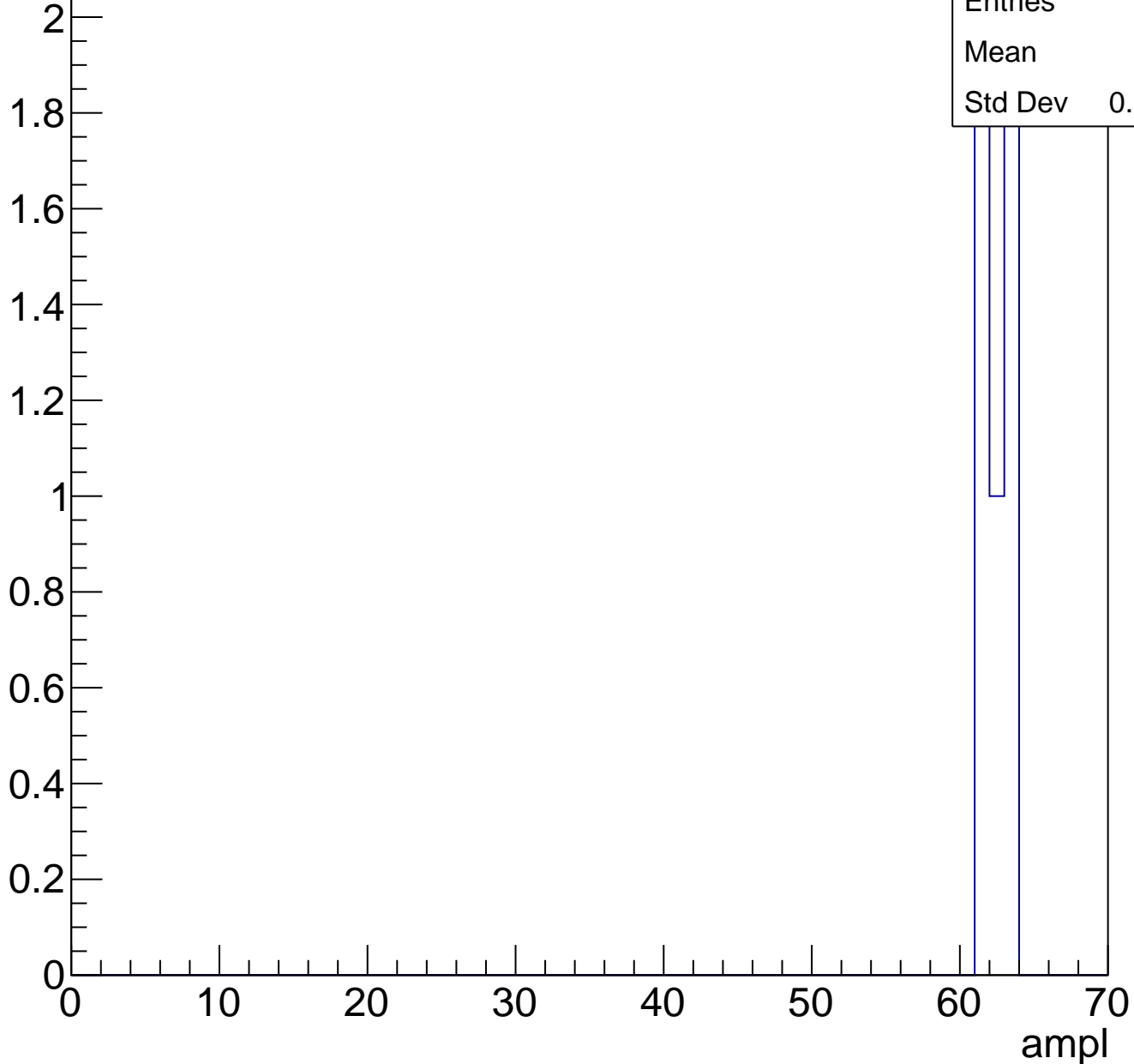
ampl



# B0L002S, U1-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 11 |
| Std Dev | 11 |

# B0L002S, U1-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 31.7  |
| Std Dev | 3.443 |

**Gaus mean : 32.0982**

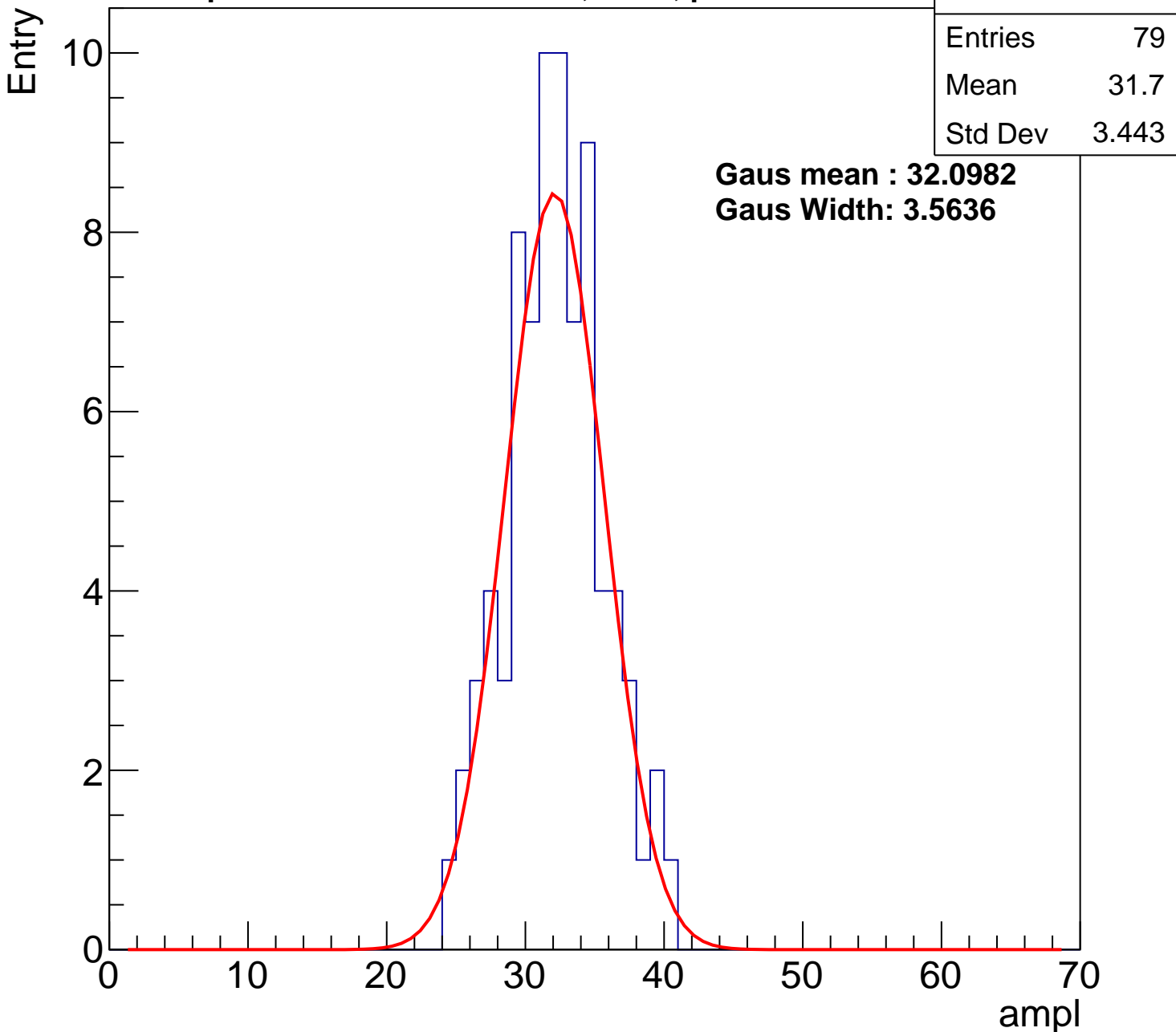
**Gaus Width: 3.5636**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch29, adc1

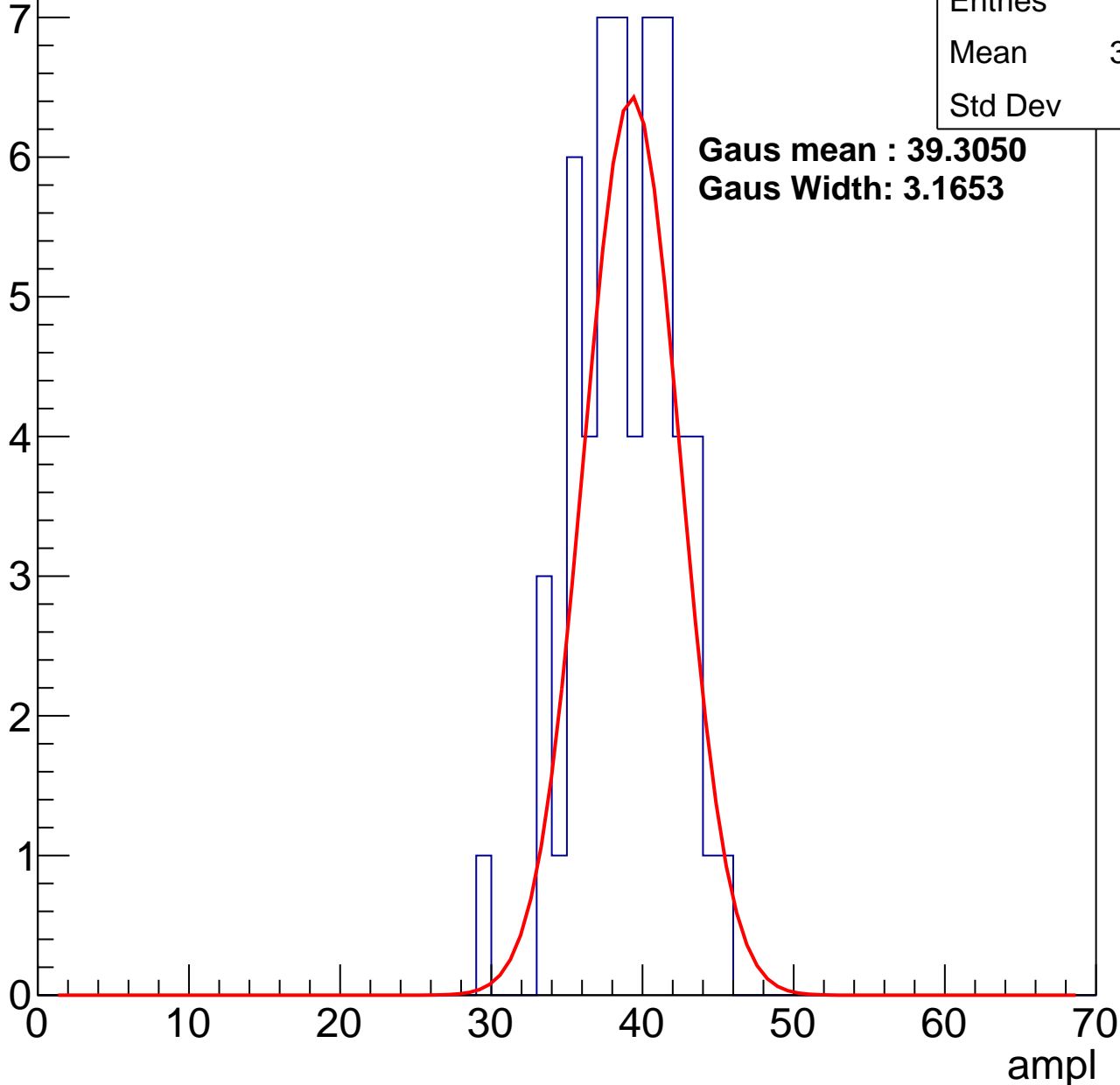
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 38.47 |
| Std Dev | 3.19  |

**Gaus mean : 39.3050**

**Gaus Width: 3.1653**



# B0L002S, U1-ch29, adc2

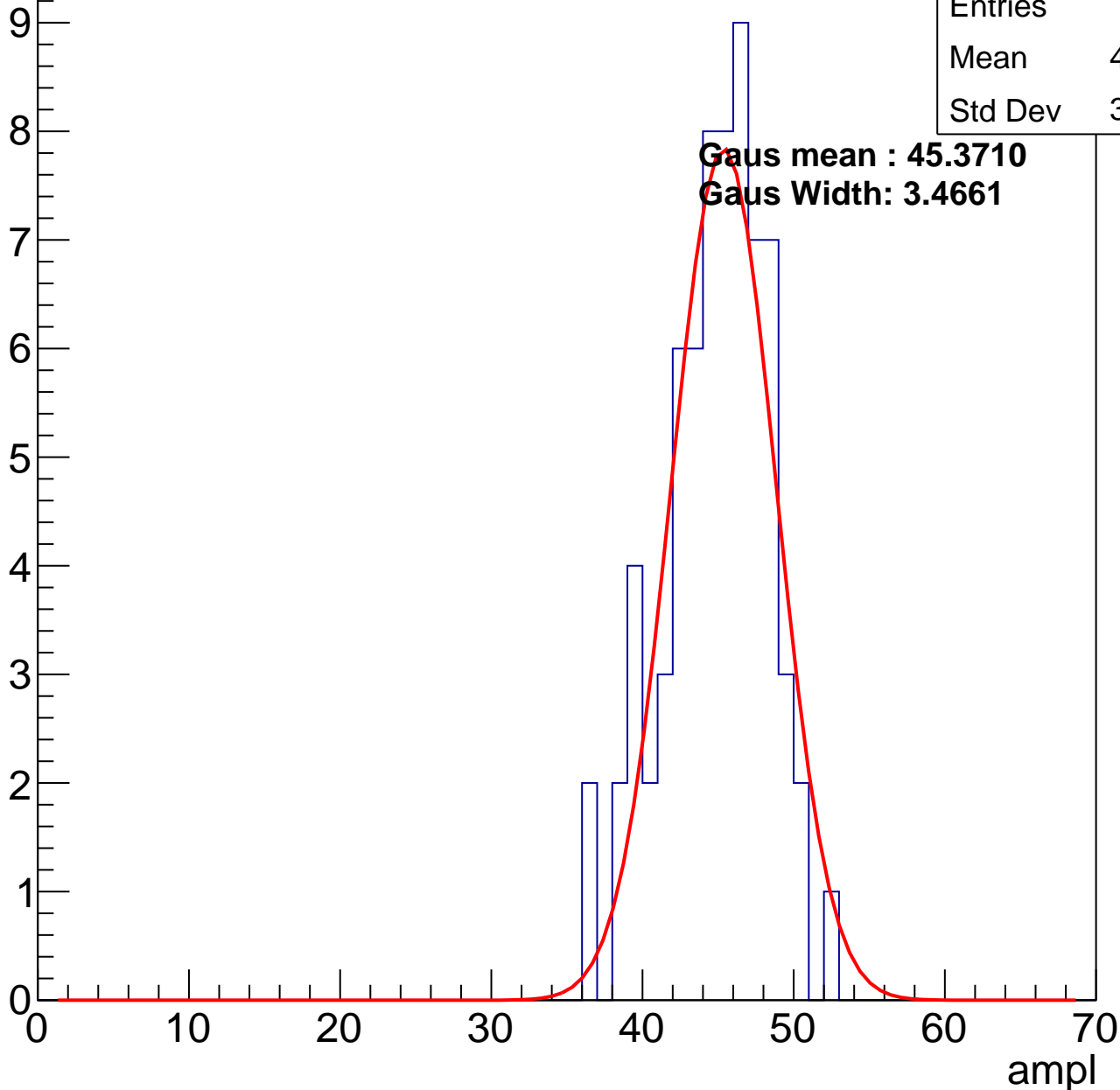
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 44.39 |
| Std Dev | 3.399 |

**Gaus mean : 45.3710**

**Gaus Width: 3.4661**

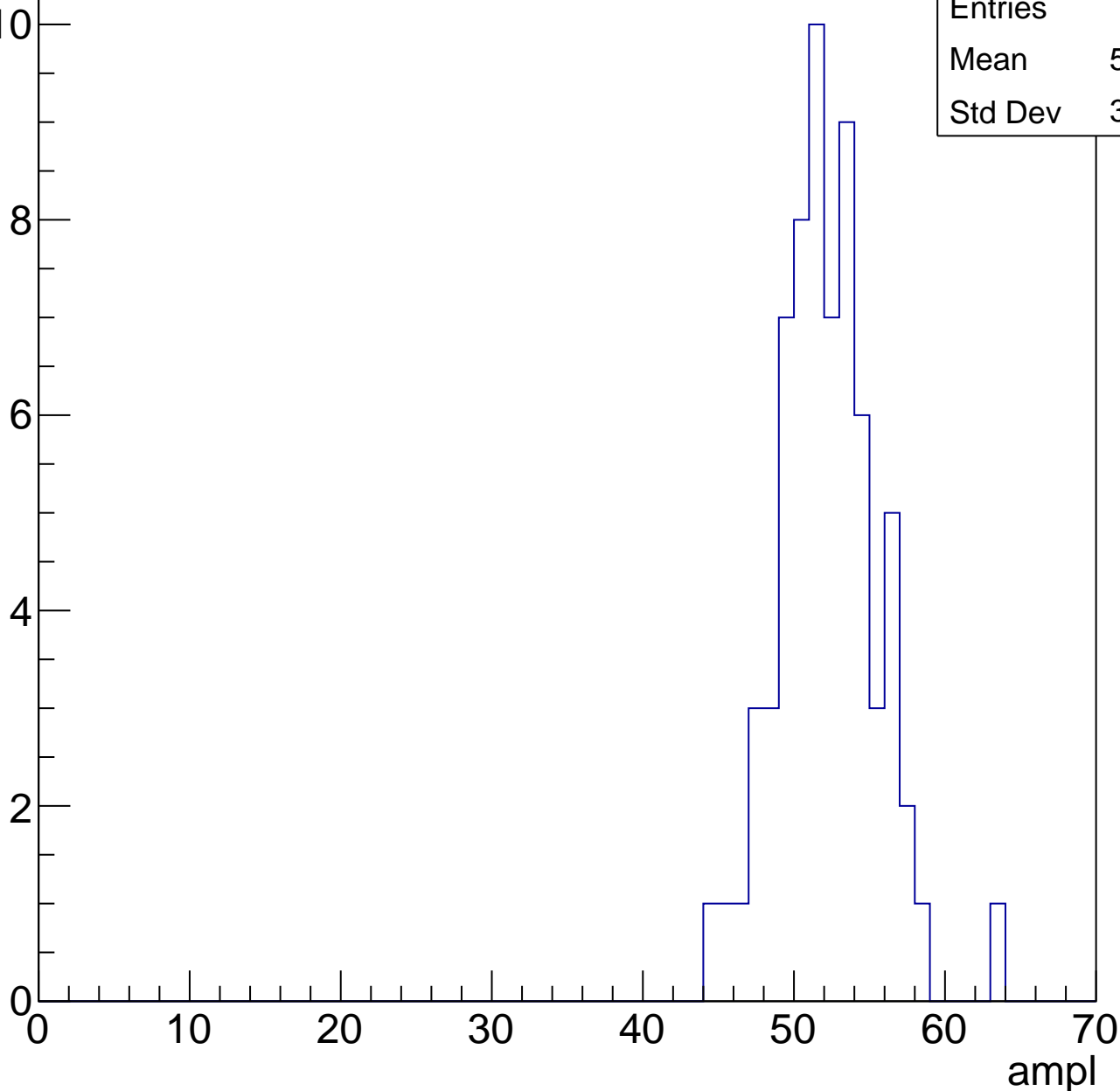


# B0L002S, U1-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 51.74 |
| Std Dev | 3.252 |



# B0L002S, U1-ch29, adc4

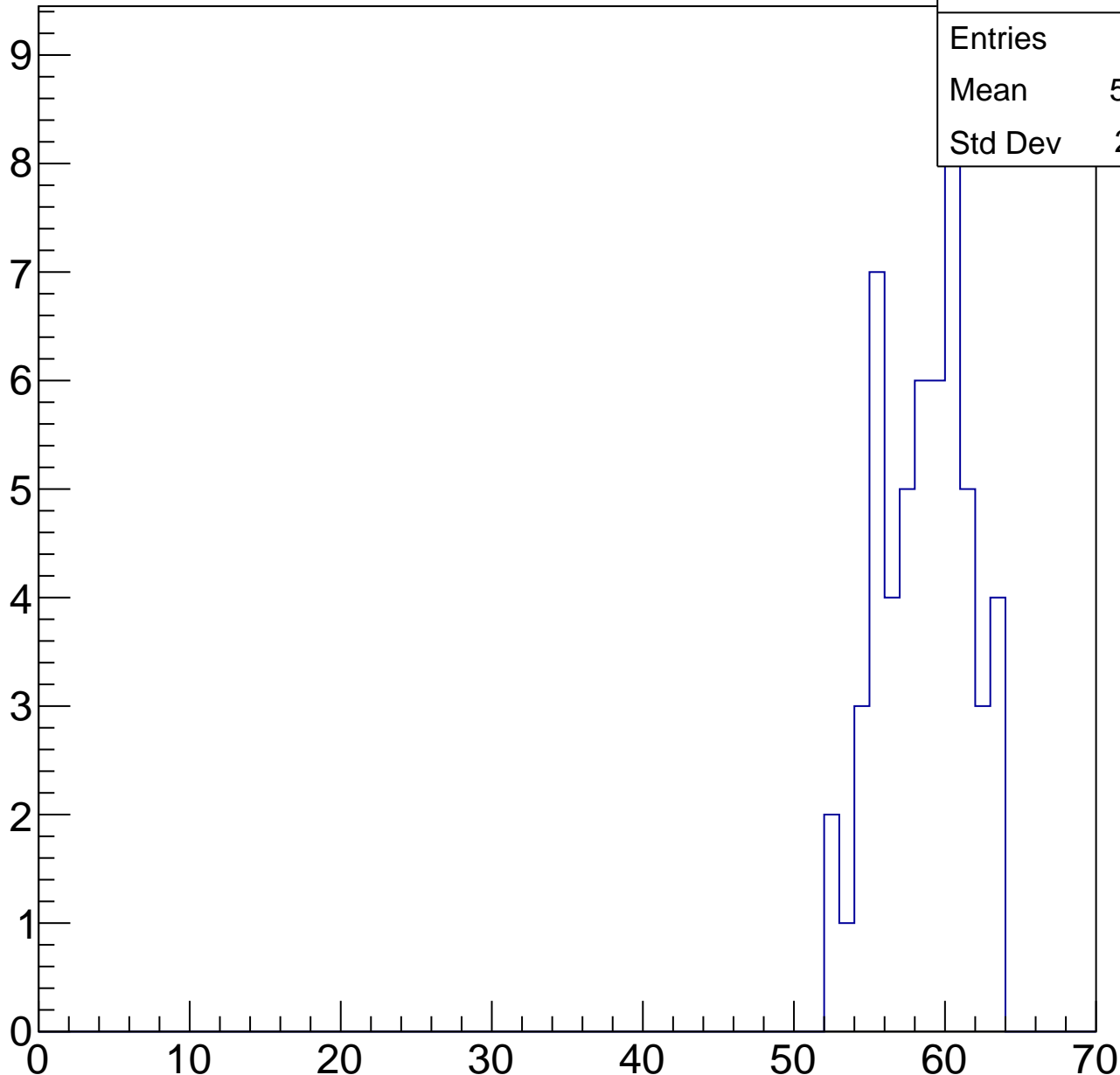
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 58.15 |
| Std Dev | 2.901 |

ampl

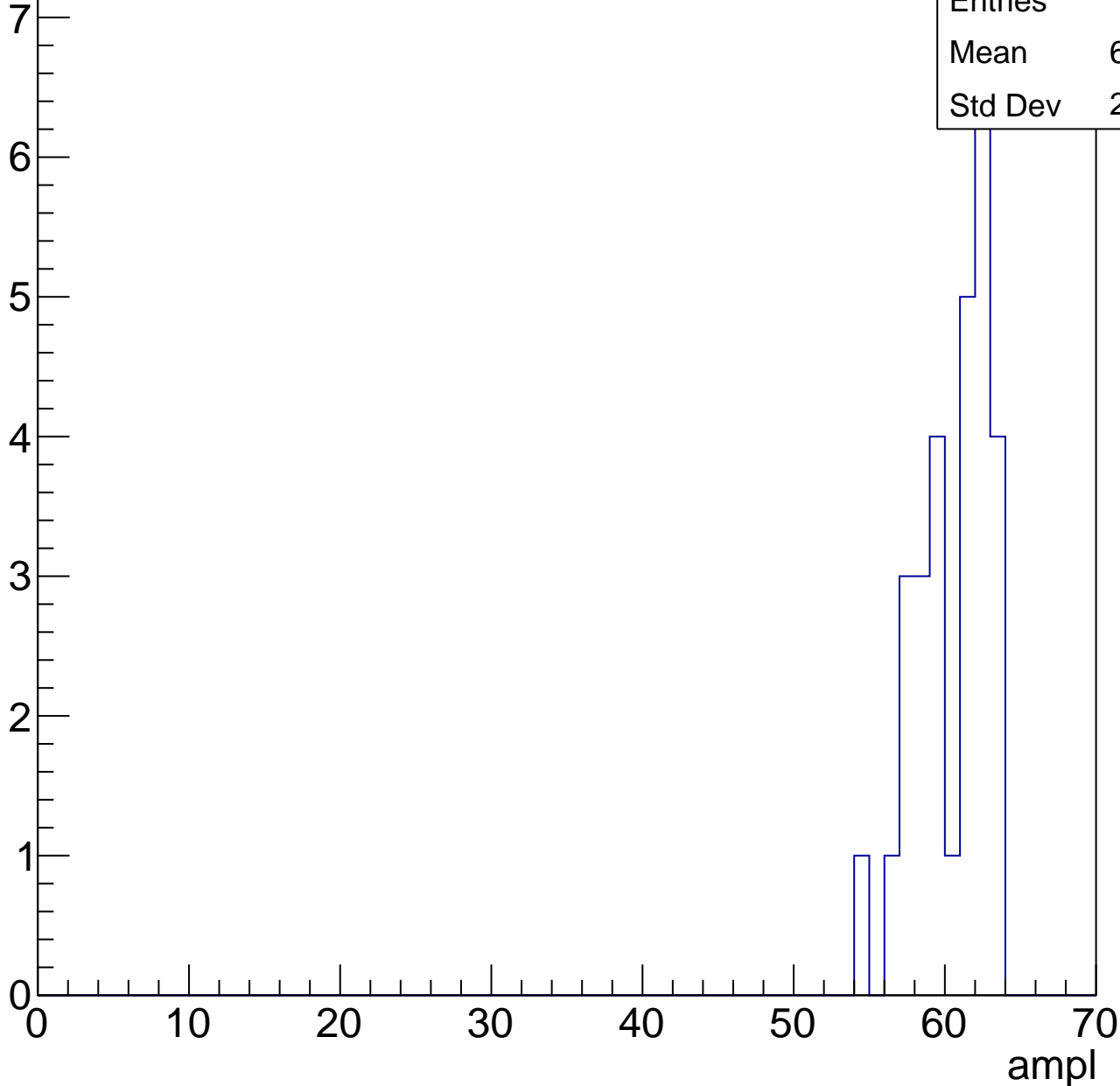


# B0L002S, U1-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 29    |
| Mean    | 60.07 |
| Std Dev | 2.377 |



# B0L002S, U1-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch30, adc0

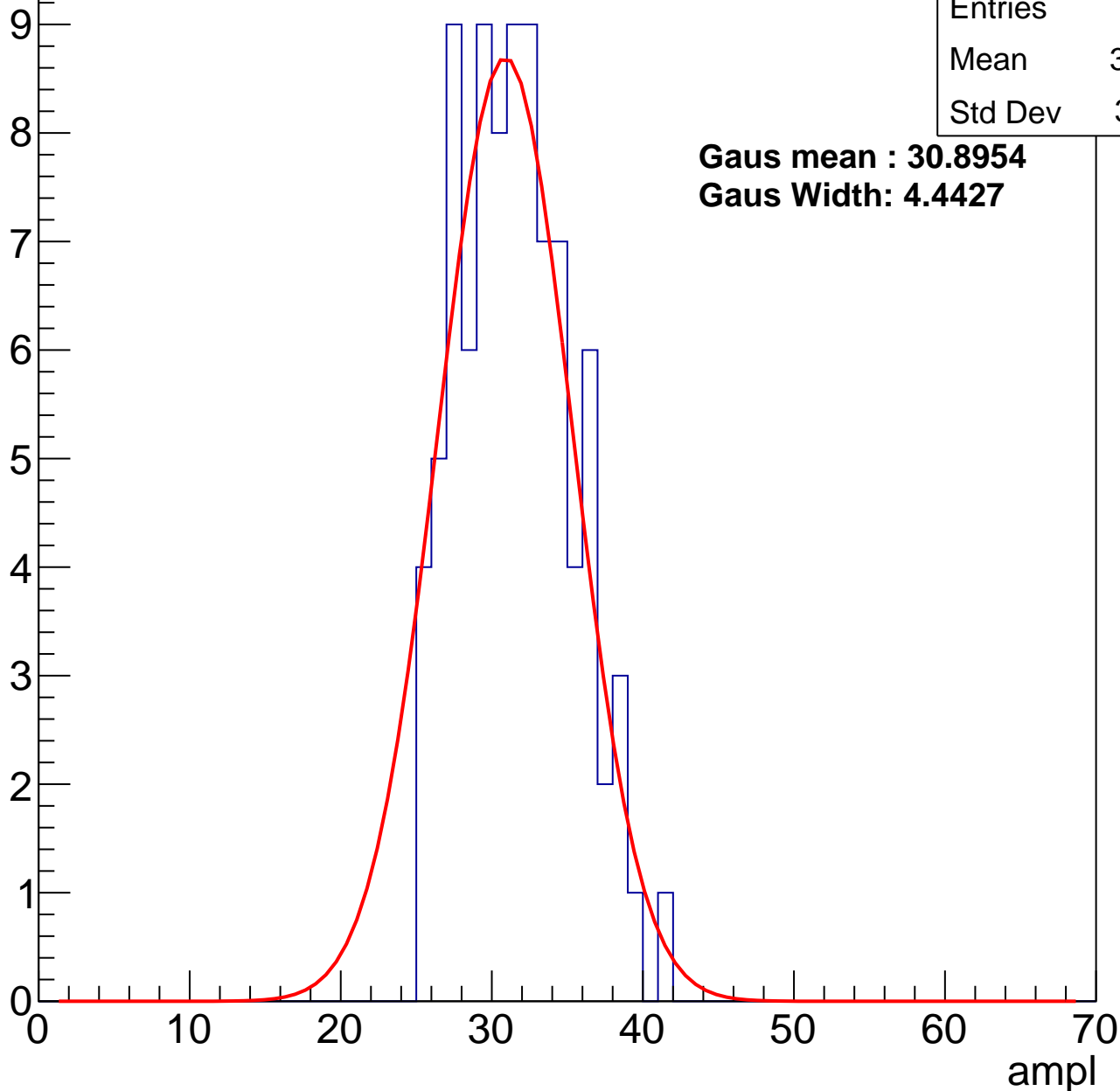
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 31.13 |
| Std Dev | 3.661 |

**Gaus mean : 30.8954**

**Gaus Width: 4.4427**



# B0L002S, U1-ch30, adc1

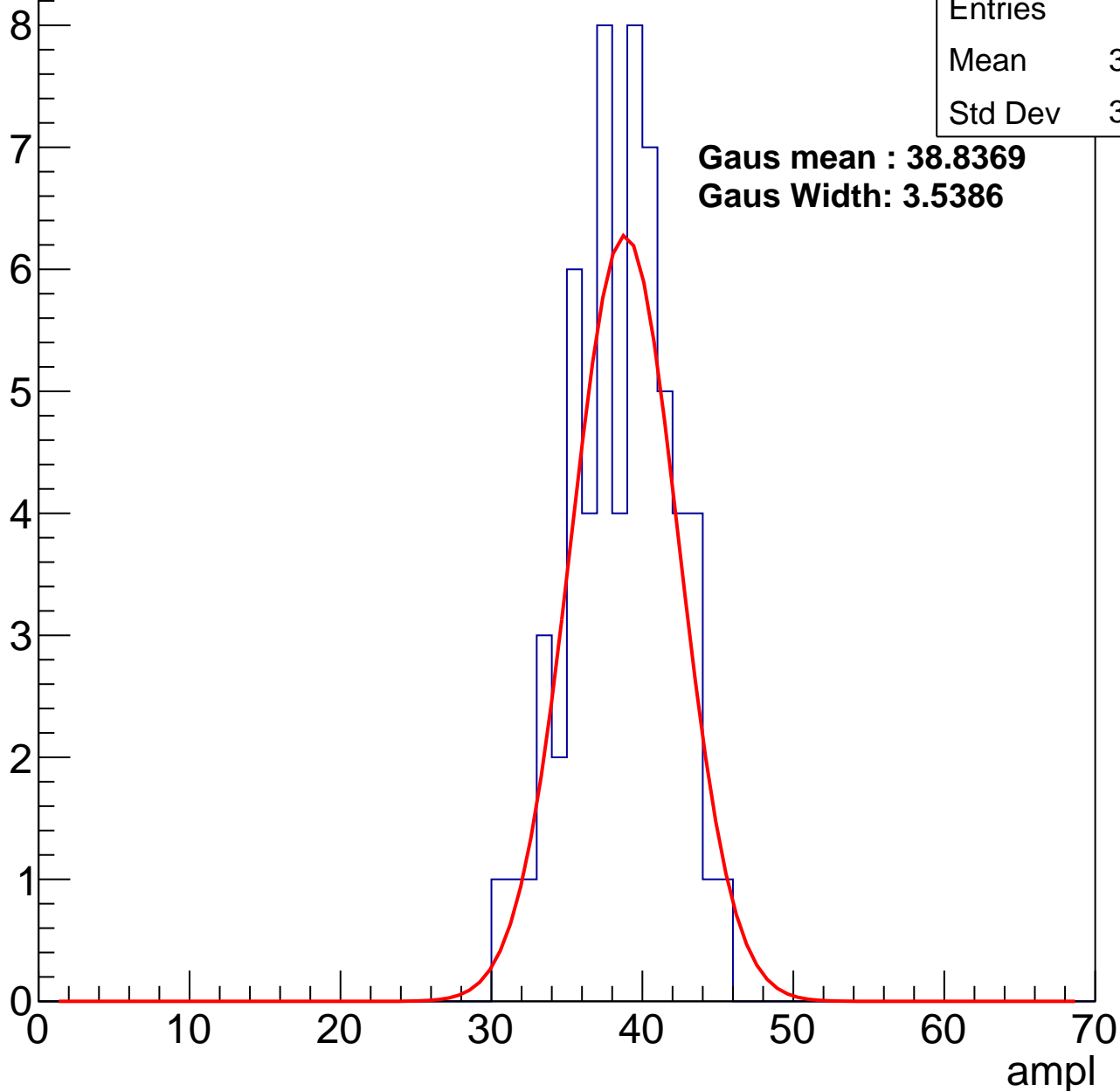
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 38.13 |
| Std Dev | 3.324 |

**Gaus mean : 38.8369**

**Gaus Width: 3.5386**



# B0L002S, U1-ch30, adc2

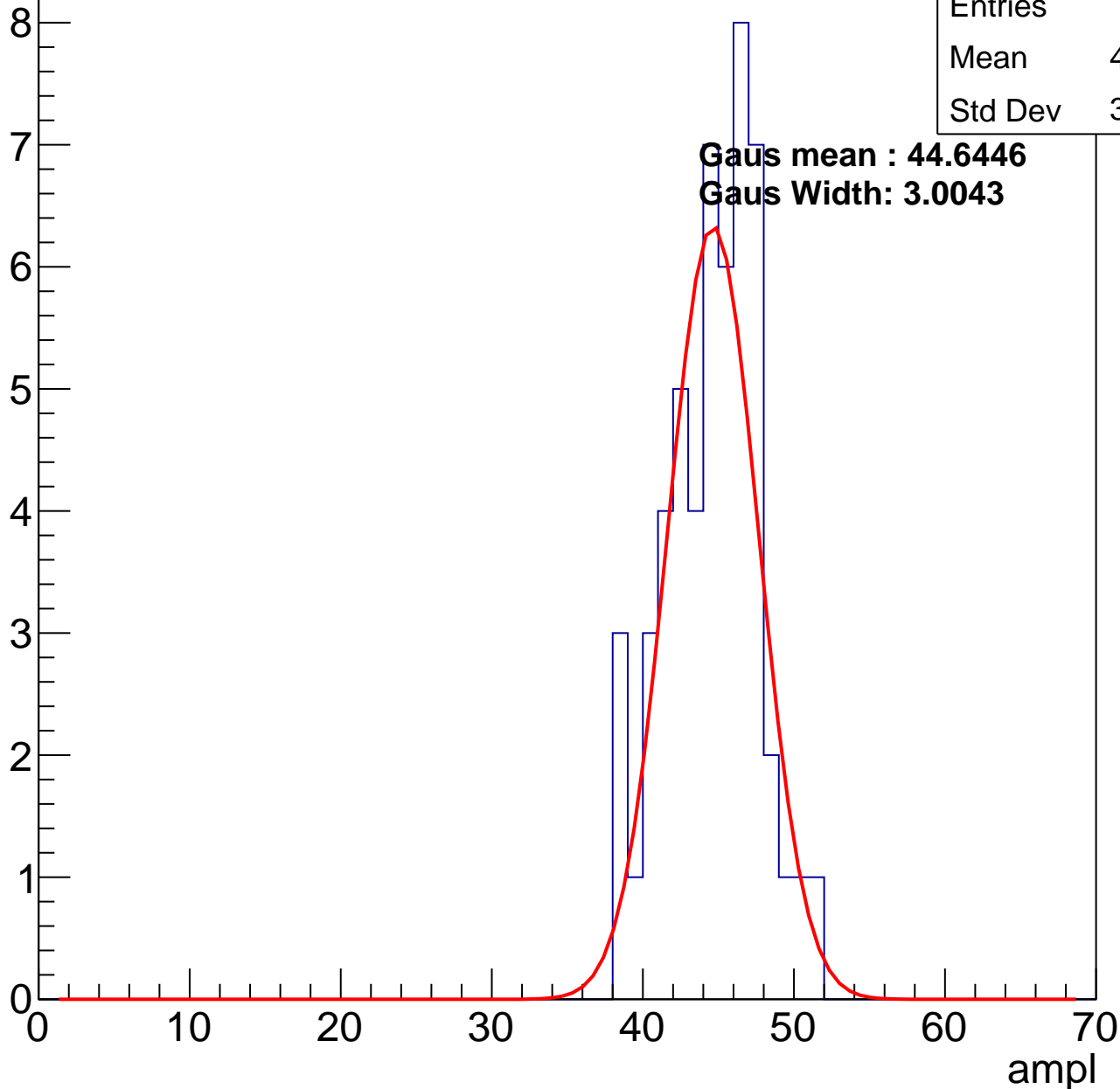
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 44.15 |
| Std Dev | 3.018 |

**Gaus mean : 44.6446**

**Gaus Width: 3.0043**

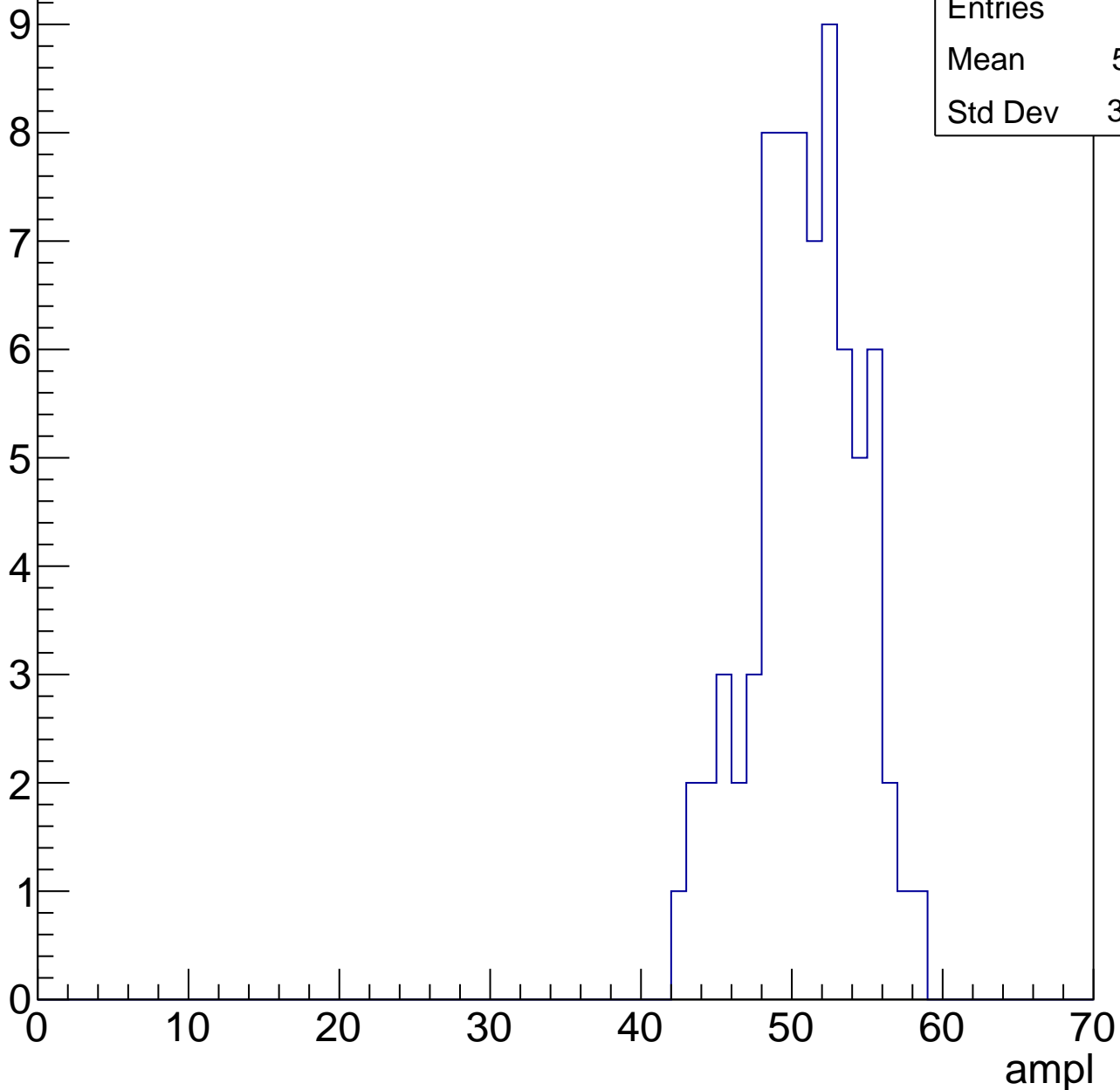


# B0L002S, U1-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

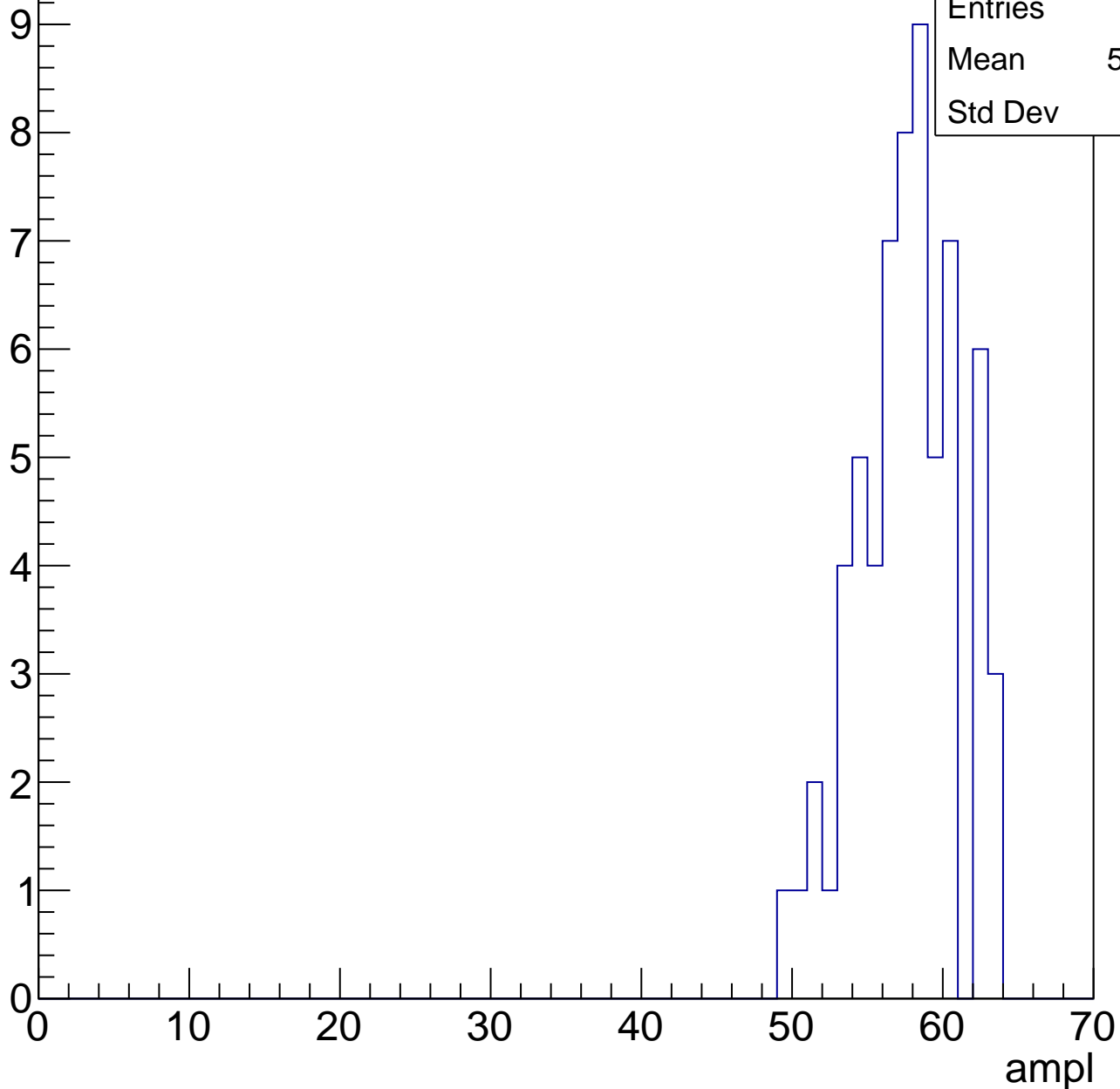
|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 50.41 |
| Std Dev | 3.503 |



# B0L002S, U1-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



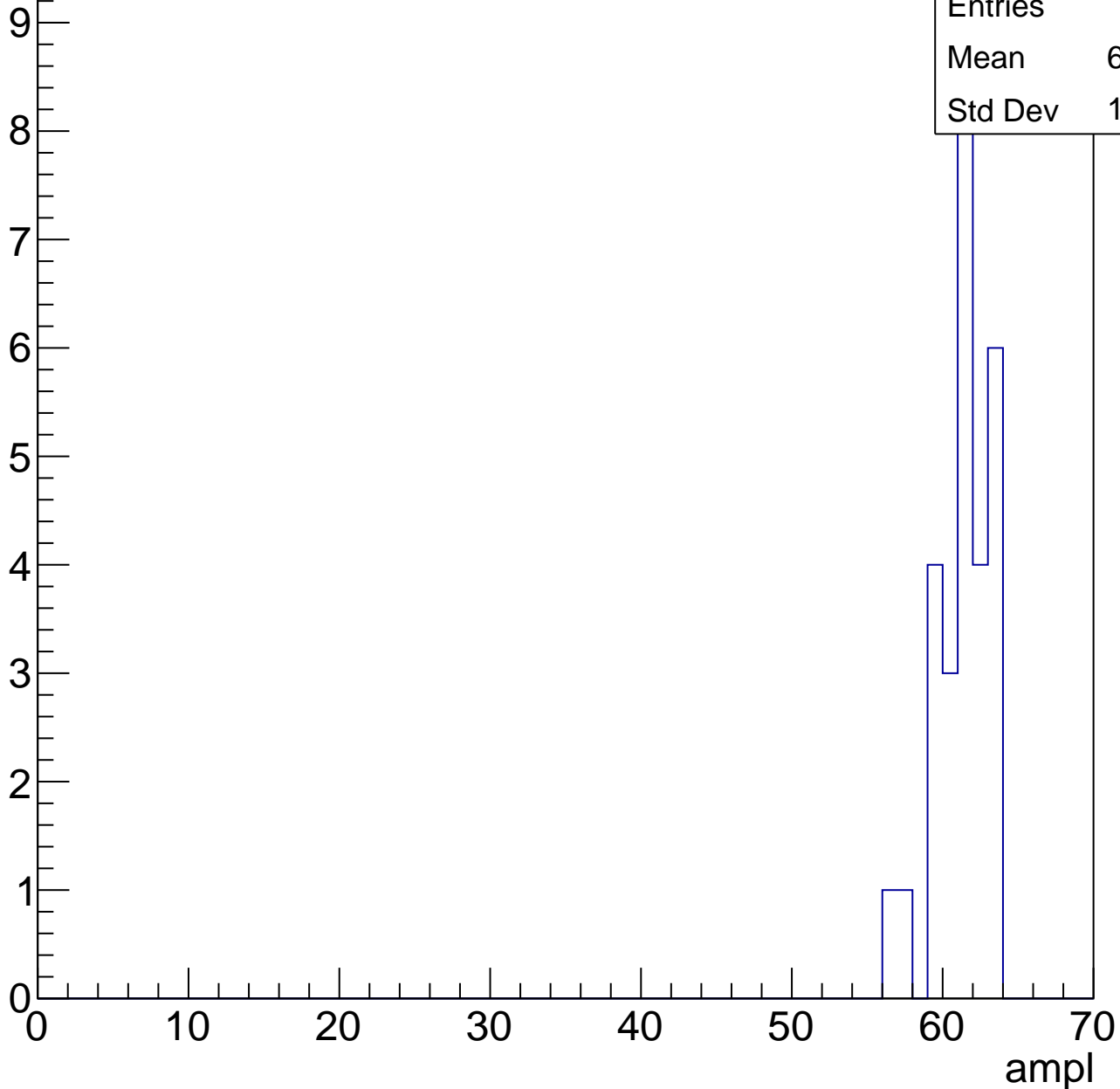
|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 57.16 |
| Std Dev | 3.31  |

# B0L002S, U1-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

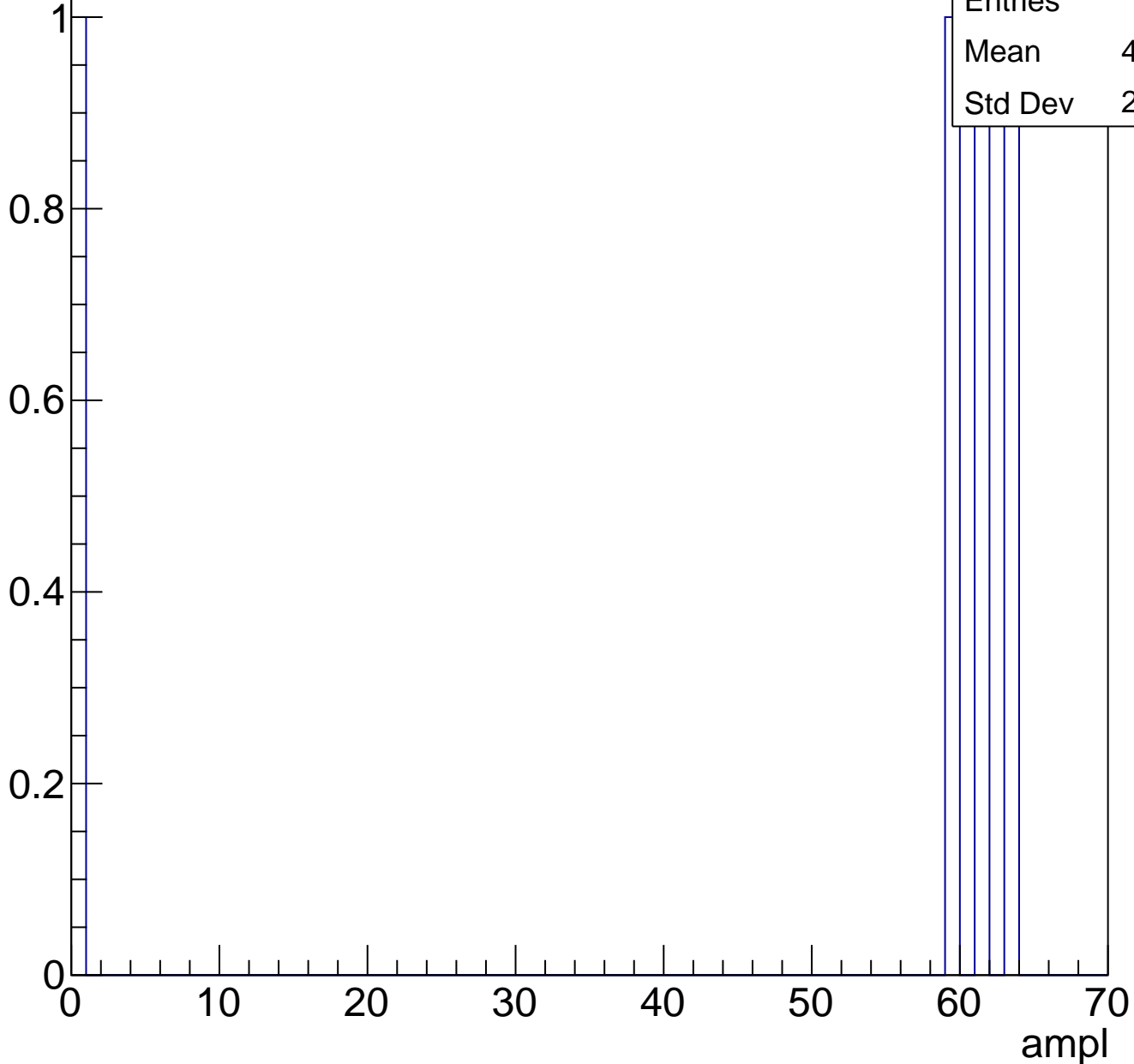
|         |       |
|---------|-------|
| Entries | 28    |
| Mean    | 60.86 |
| Std Dev | 1.767 |



# B0L002S, U1-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 11.5 |
| Std Dev | 11.5 |

# B0L002S, U1-ch31, adc0

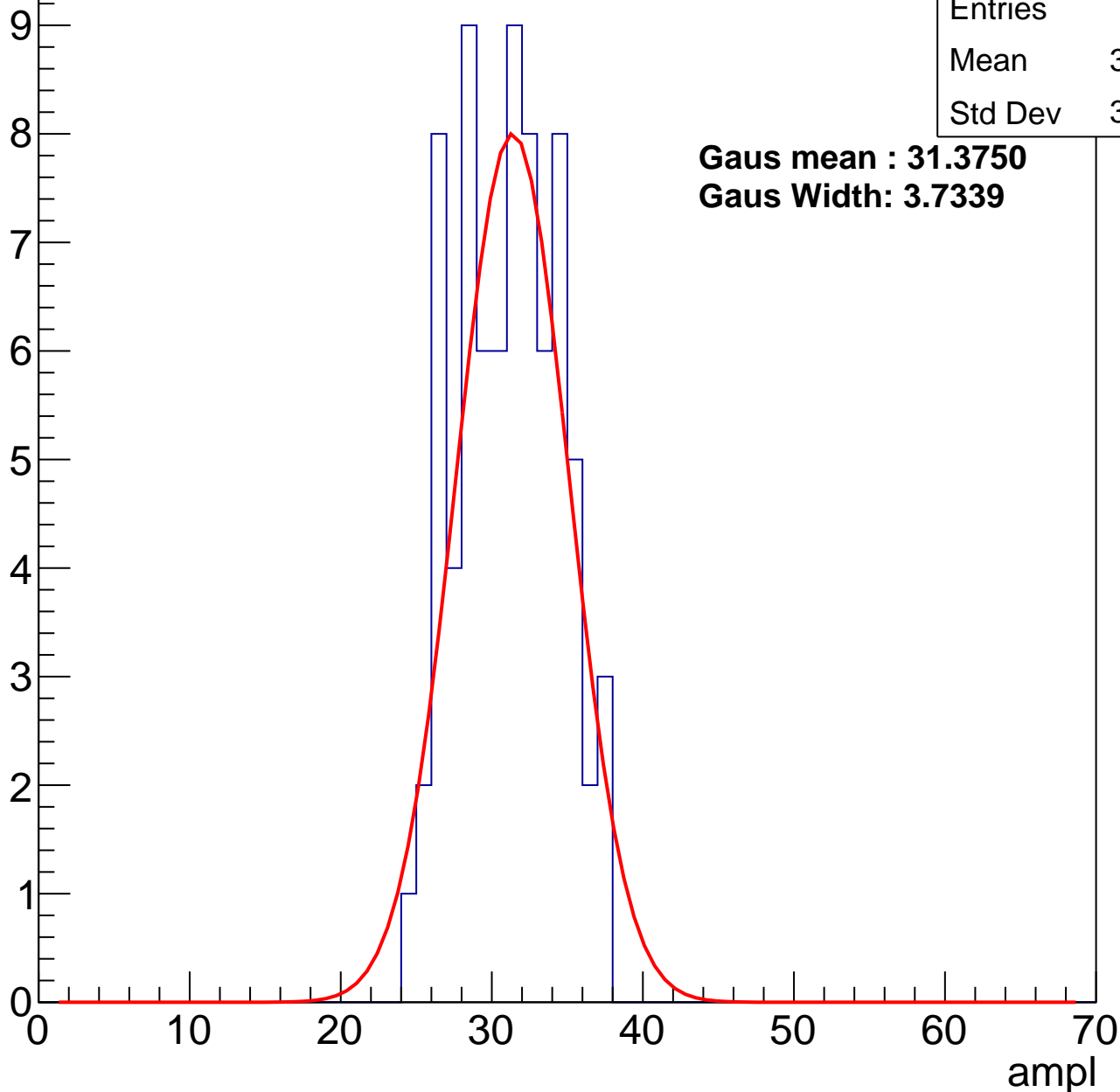
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 30.64 |
| Std Dev | 3.279 |

**Gaus mean : 31.3750**

**Gaus Width: 3.7339**



# B0L002S, U1-ch31, adc1

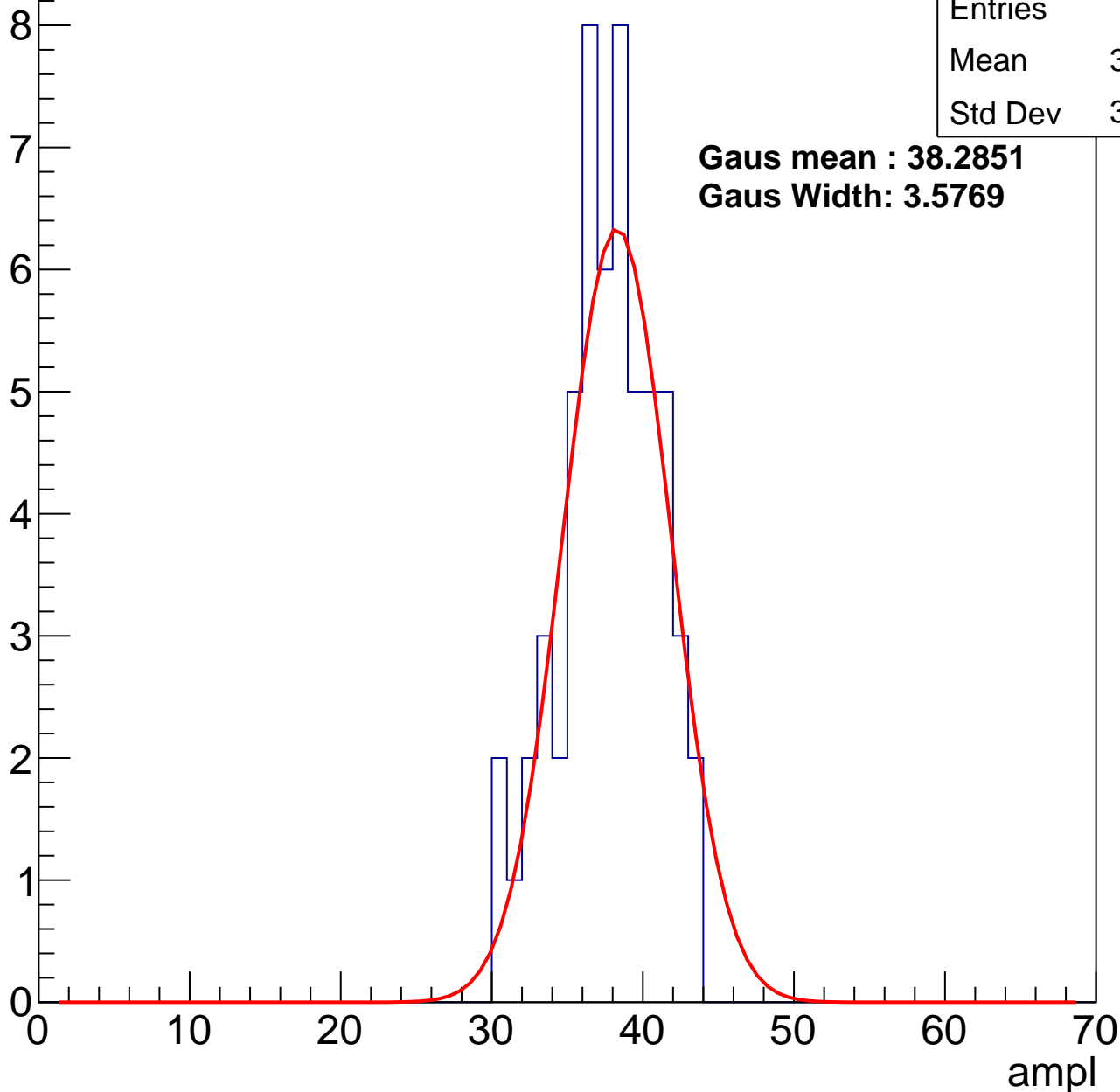
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 37.25 |
| Std Dev | 3.175 |

**Gaus mean : 38.2851**

**Gaus Width: 3.5769**



# B0L002S, U1-ch31, adc2

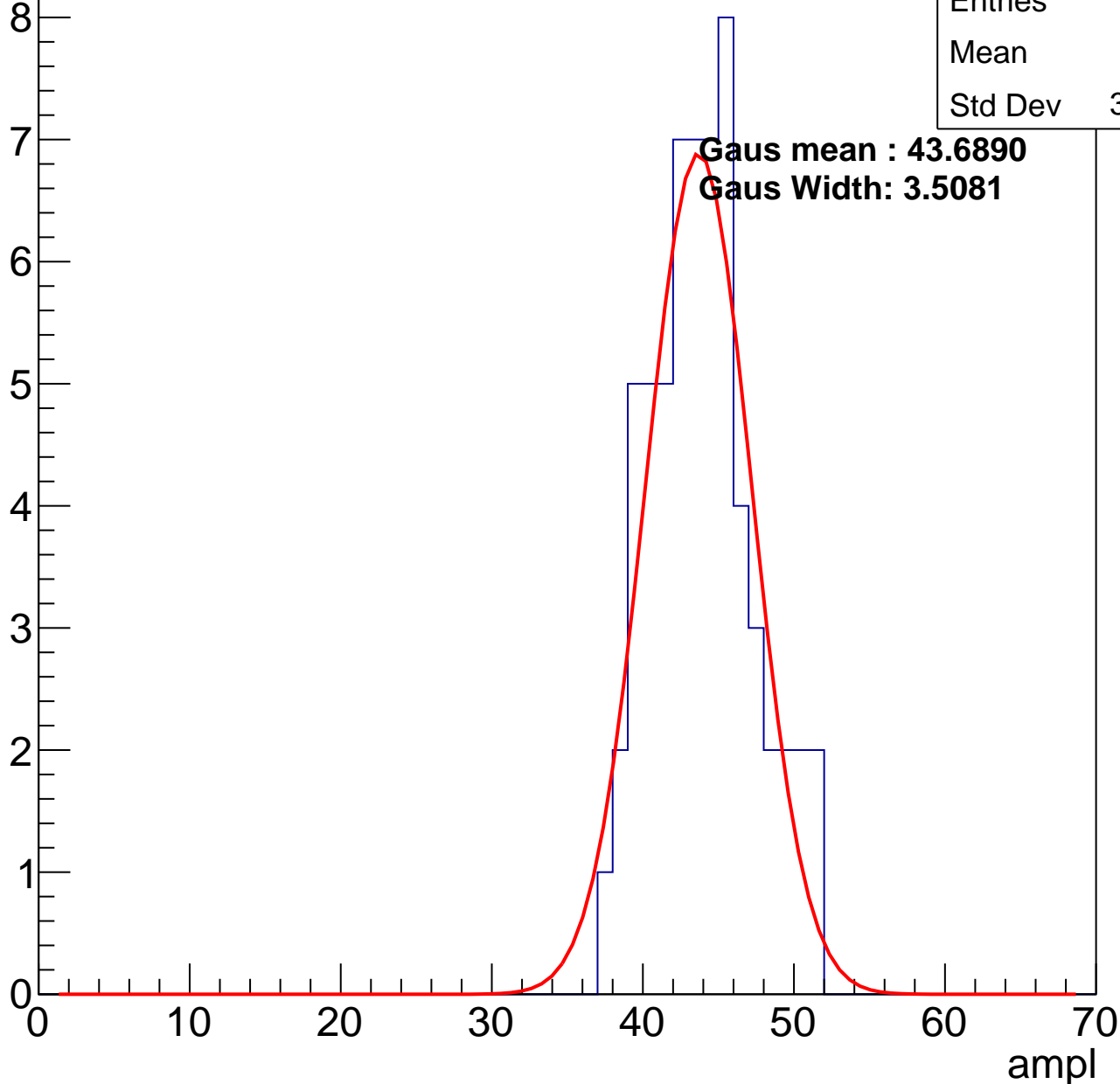
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 43.5  |
| Std Dev | 3.344 |

**Gaus mean : 43.6890**

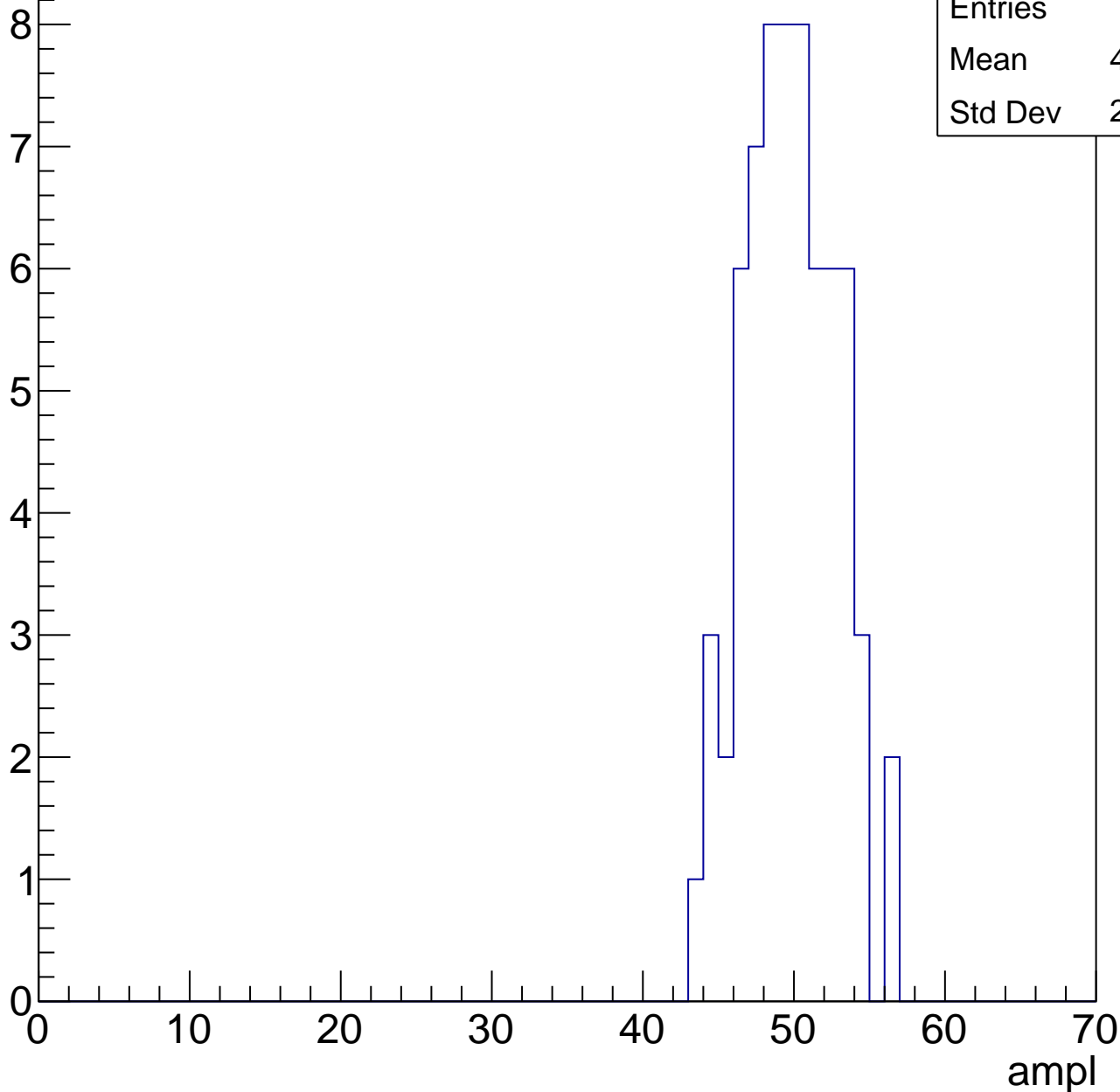
**Gaus Width: 3.5081**



# B0L002S, U1-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

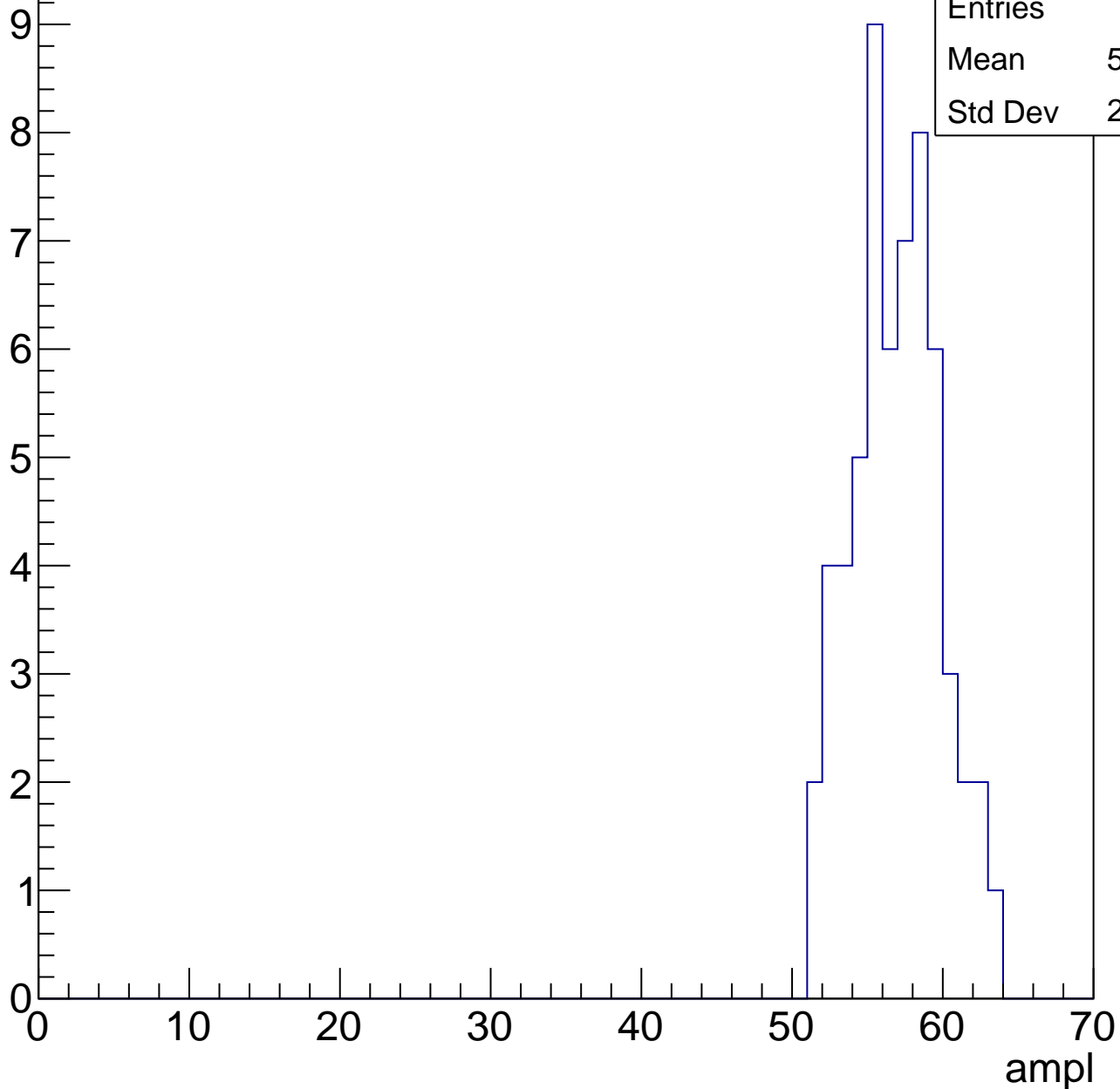


|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 49.33 |
| Std Dev | 2.966 |

# B0L002S, U1-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



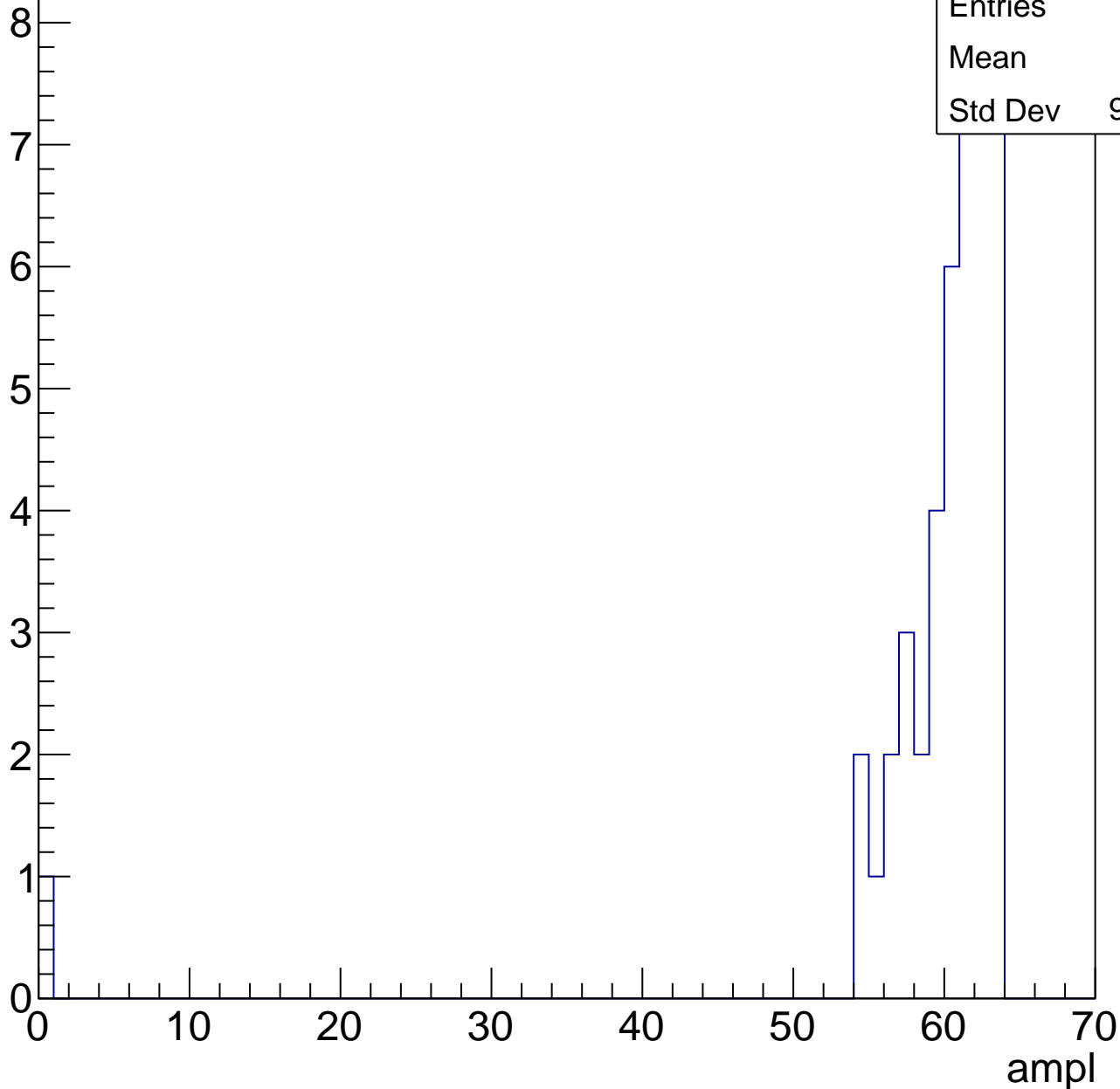
|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 56.42 |
| Std Dev | 2.854 |

# B0L002S, U1-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 58.8  |
| Std Dev | 9.208 |



# B0L002S, U1-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch32, adc0

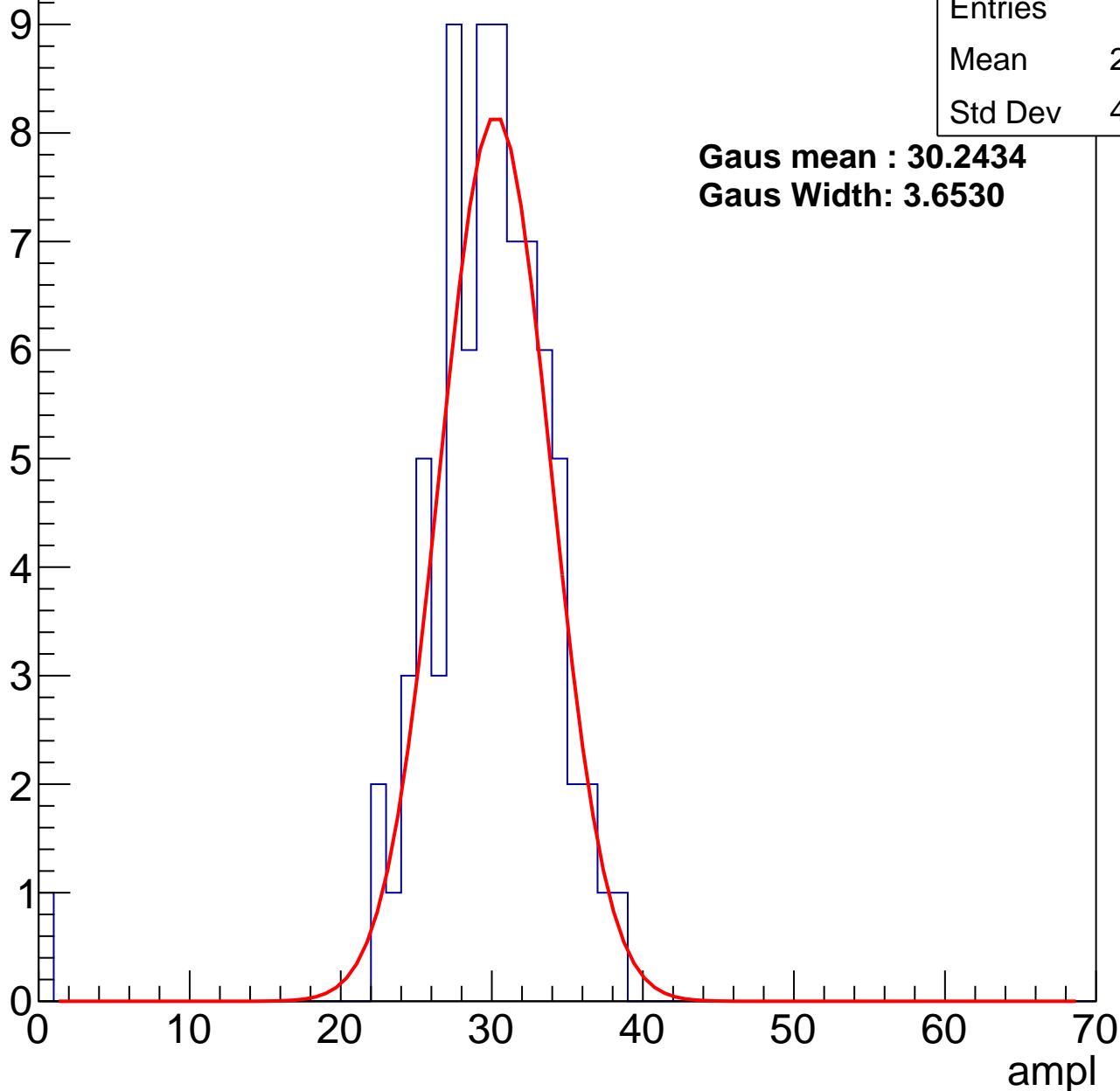
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 29.24 |
| Std Dev | 4.814 |

**Gaus mean : 30.2434**

**Gaus Width: 3.6530**



# B0L002S, U1-ch32, adc1

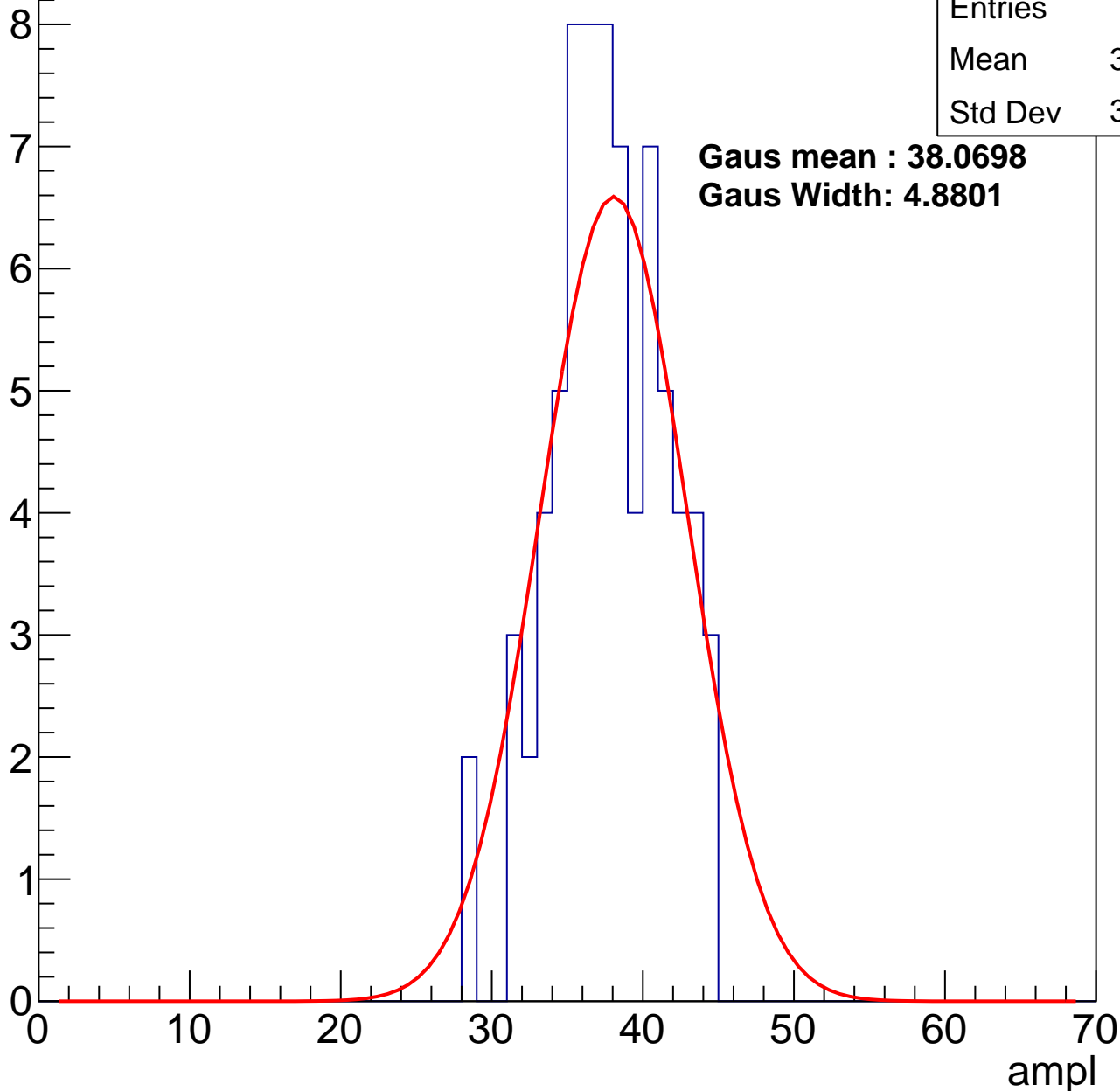
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 37.27 |
| Std Dev | 3.714 |

**Gaus mean : 38.0698**

**Gaus Width: 4.8801**



# B0L002S, U1-ch32, adc2

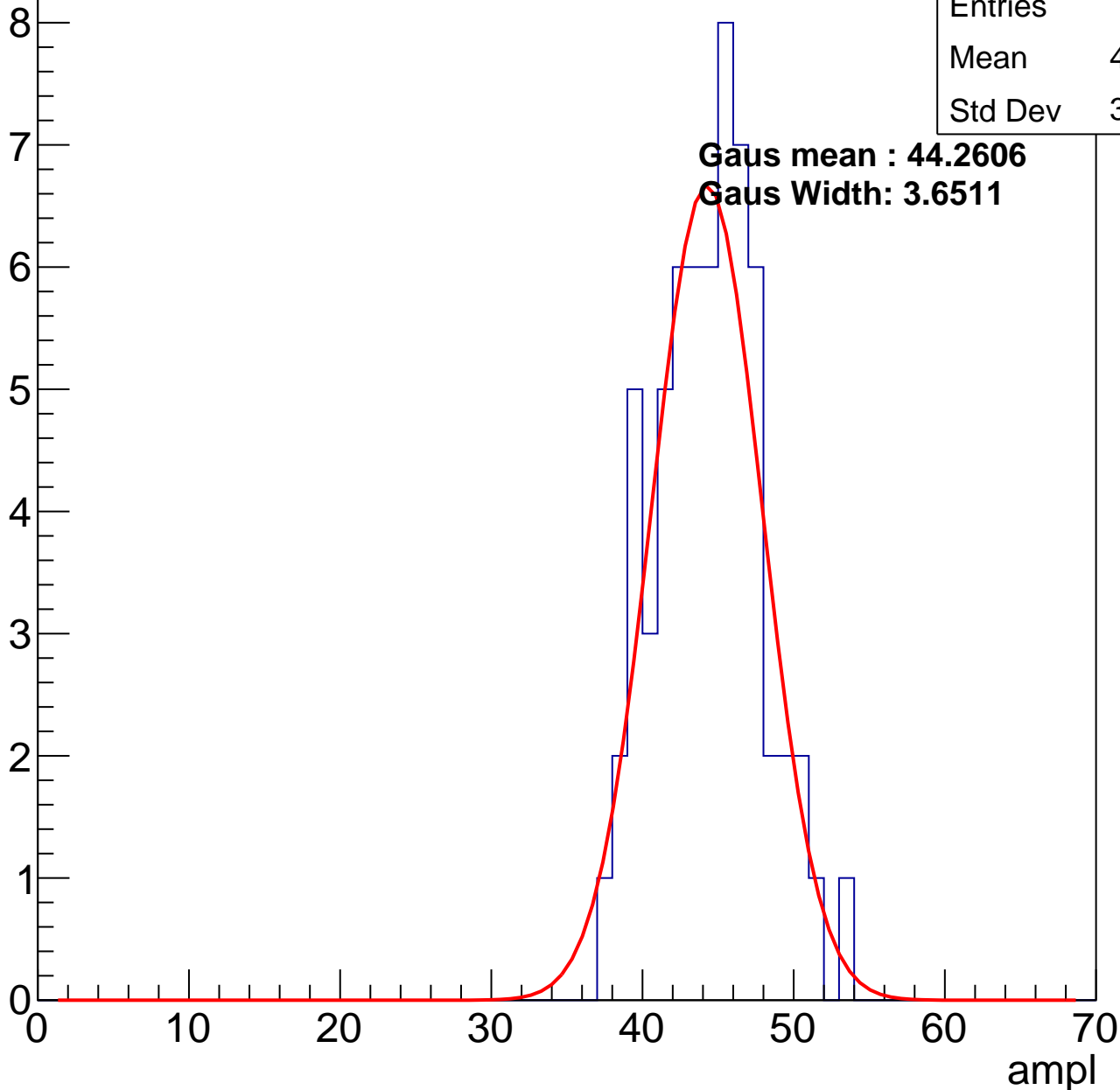
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 43.95 |
| Std Dev | 3.434 |

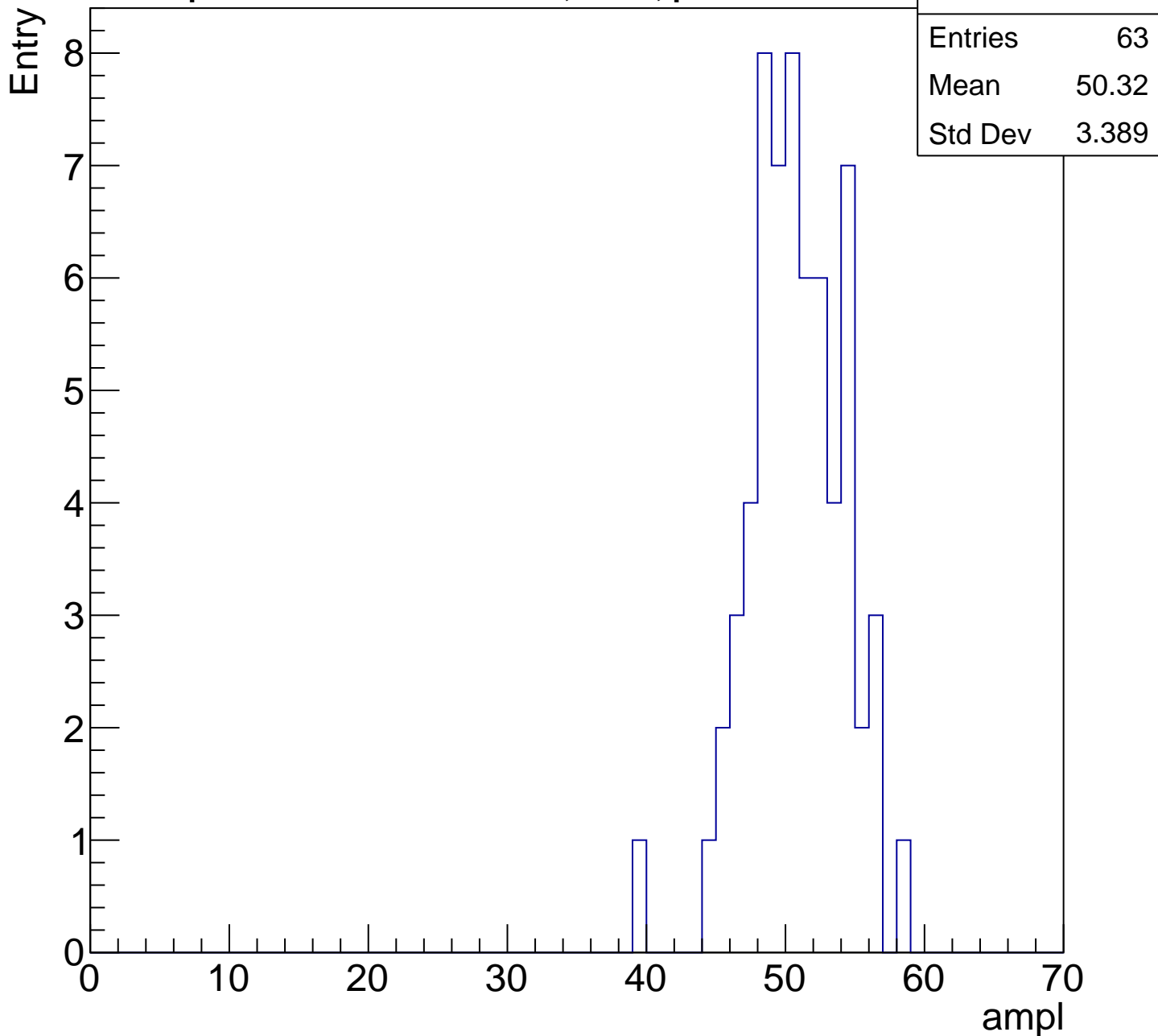
**Gaus mean : 44.2606**

**Gaus Width: 3.6511**



# B0L002S, U1-ch32, adc3

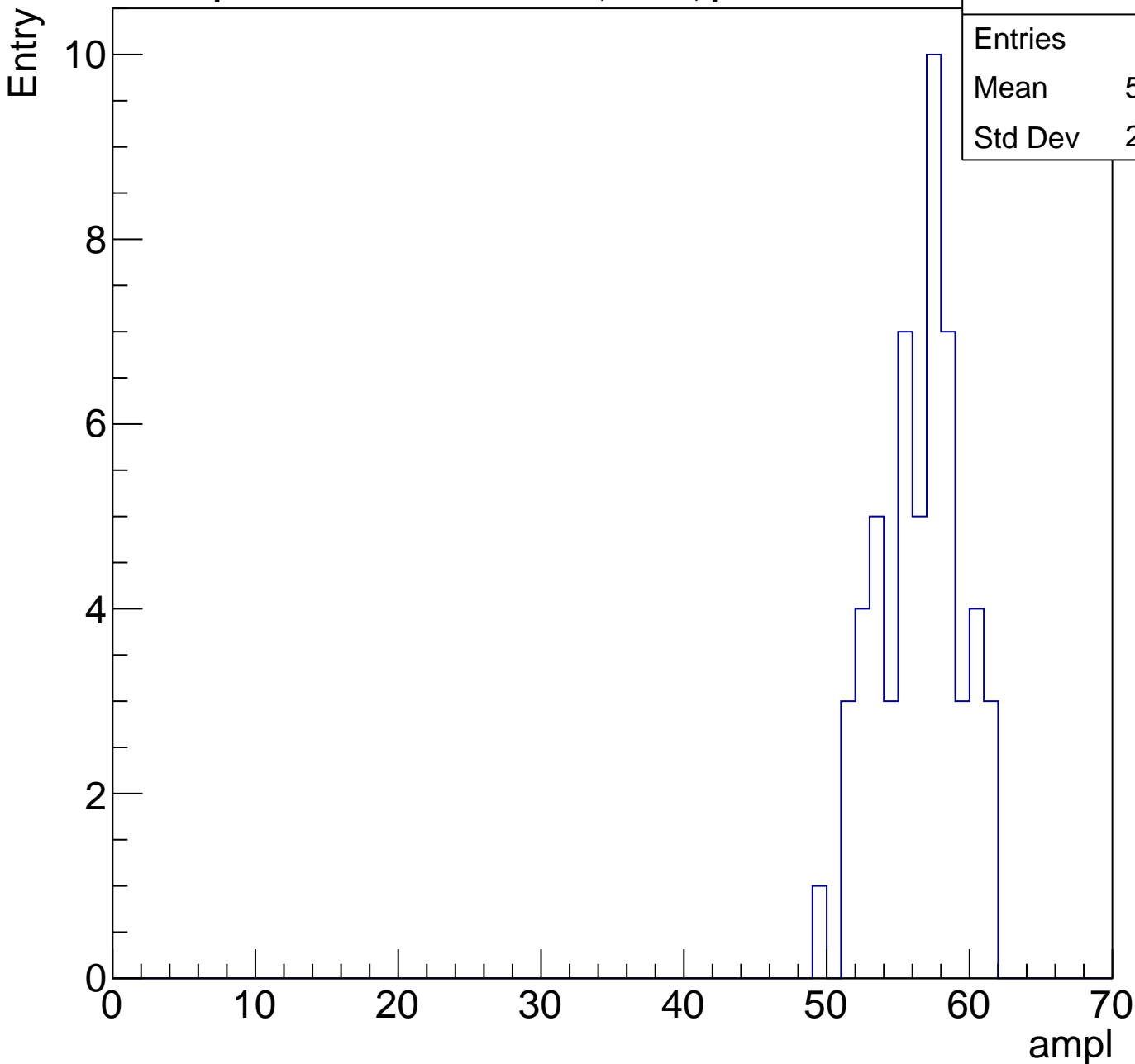
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 55.96 |
| Std Dev | 2.879 |

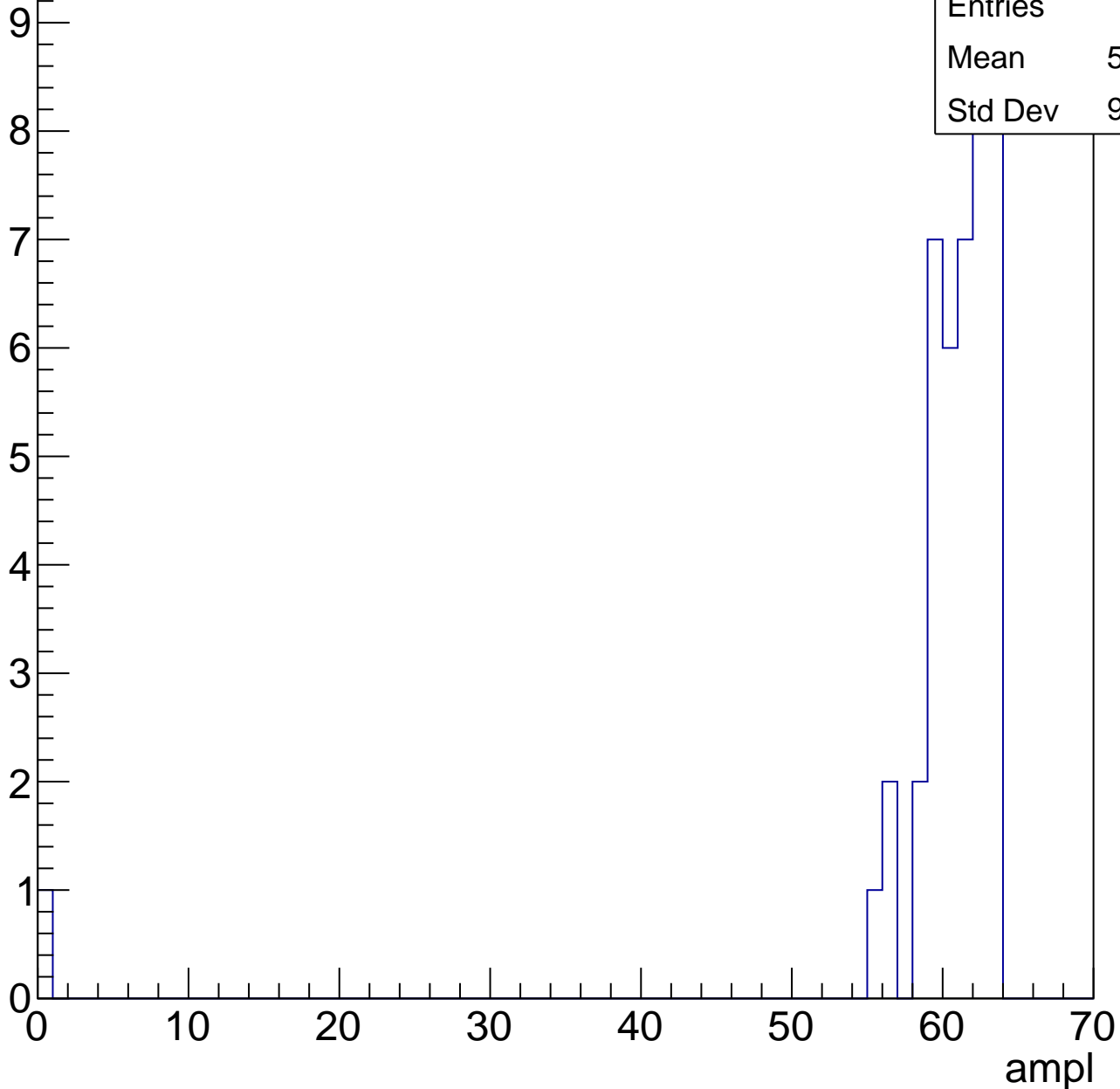


# B0L002S, U1-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 59.19 |
| Std Dev | 9.349 |



# B0L002S, U1-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 11 |
| Std Dev | 11 |

# B0L002S, U1-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 29.74 |
| Std Dev | 5.836 |

**Gaus mean : 31.0279**

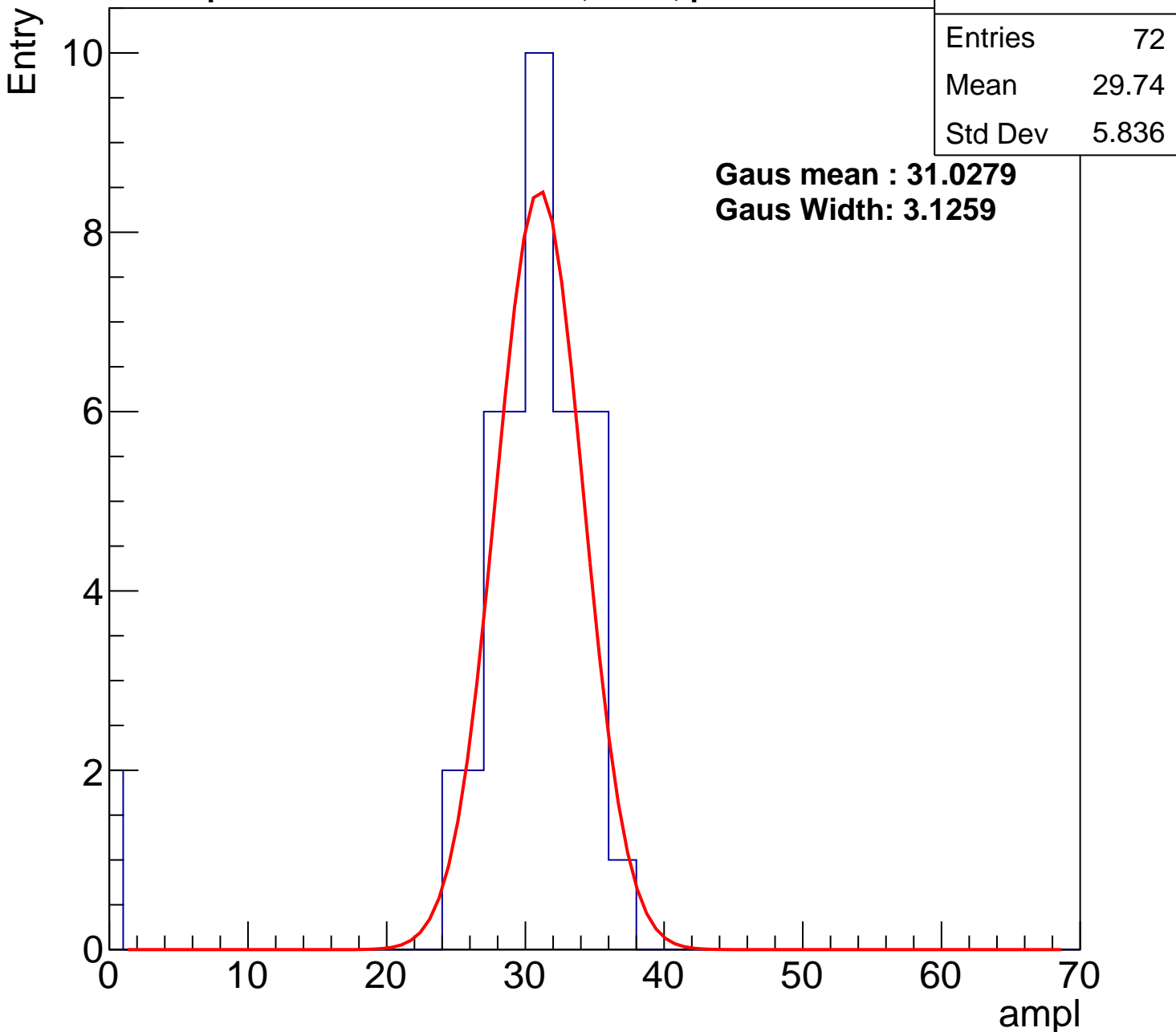
**Gaus Width: 3.1259**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch33, adc1

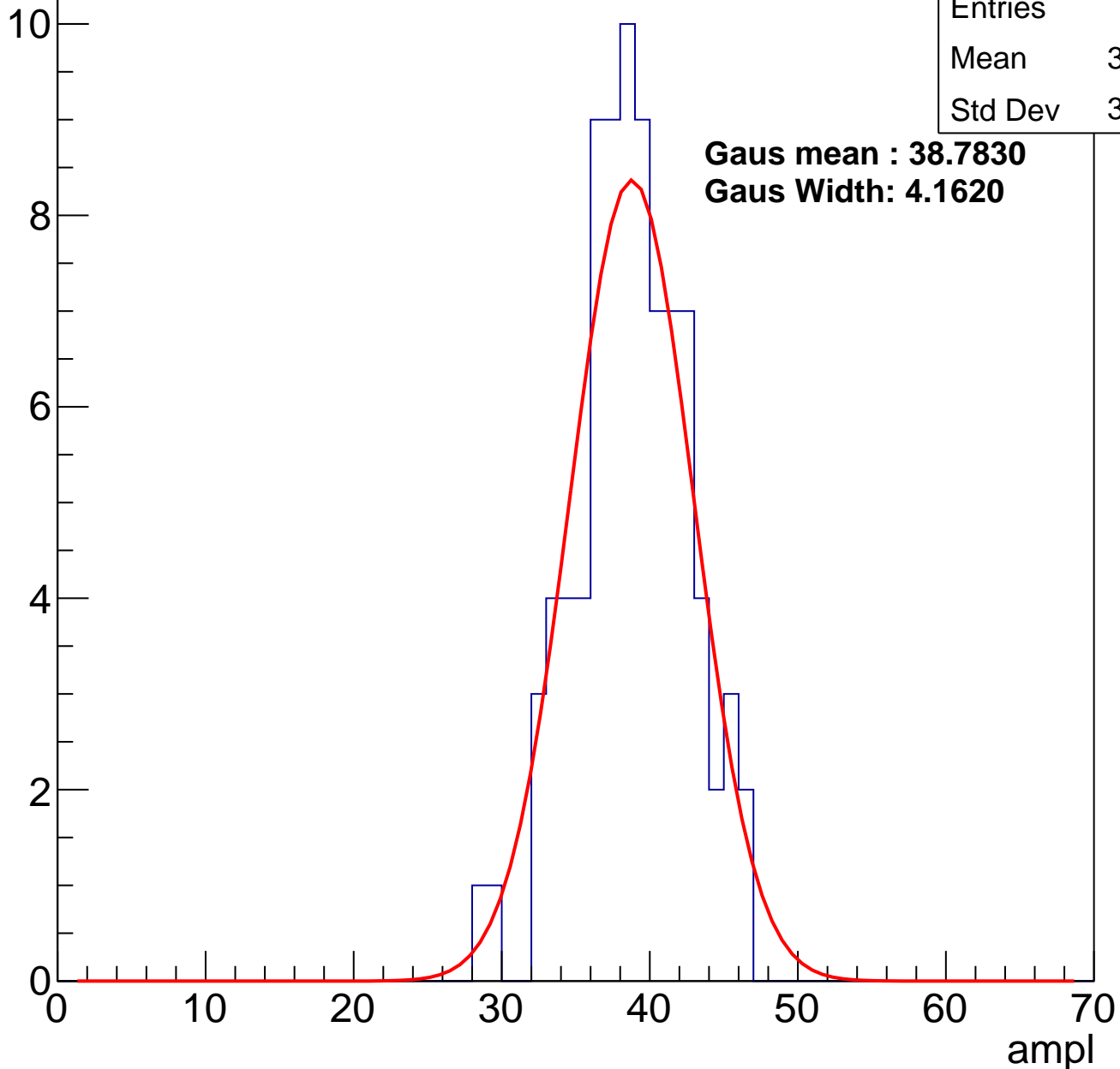
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 38.34 |
| Std Dev | 3.709 |

**Gaus mean : 38.7830**

**Gaus Width: 4.1620**

Entry

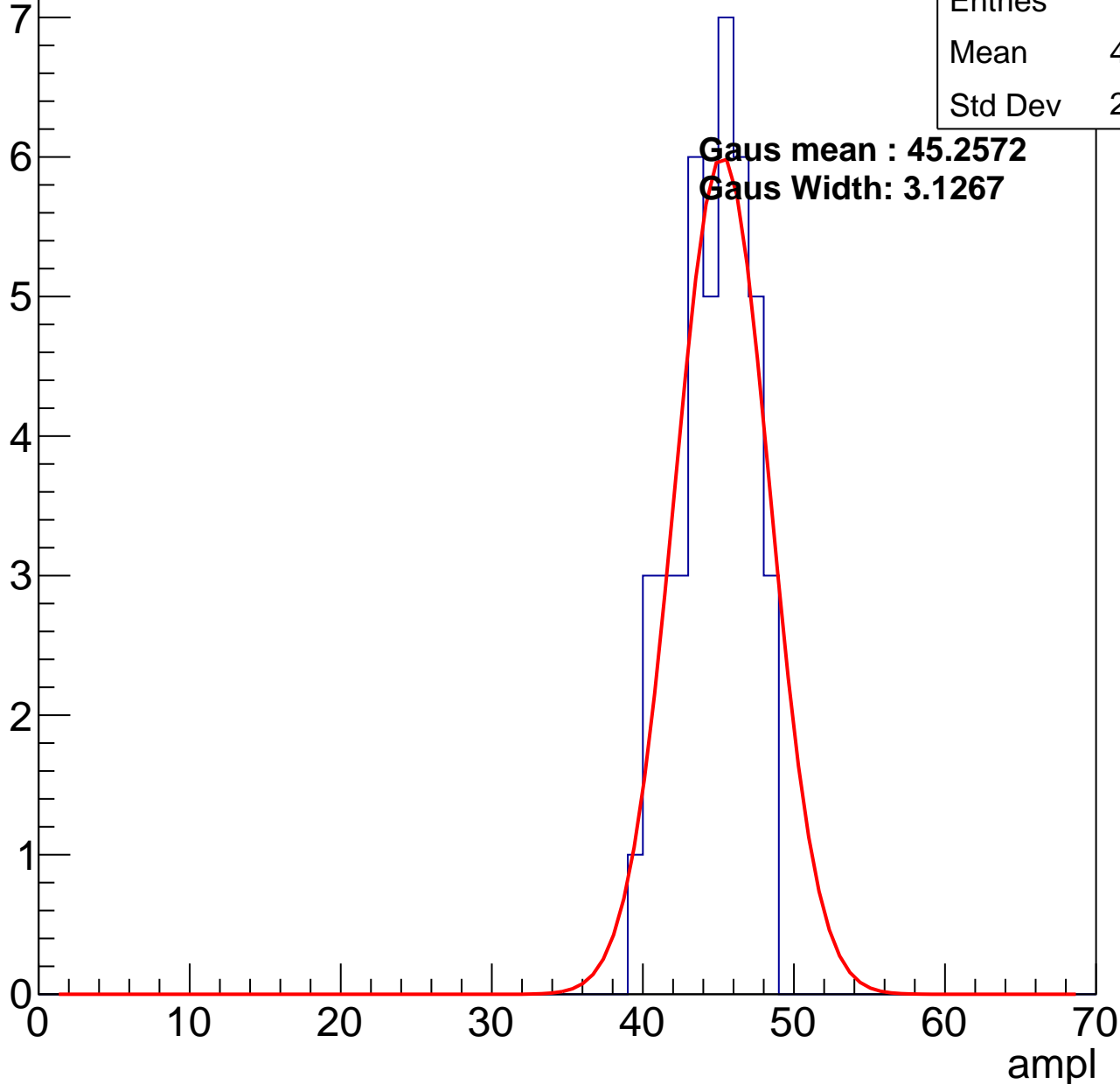


# B0L002S, U1-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 44.19 |
| Std Dev | 2.393 |



# B0L002S, U1-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

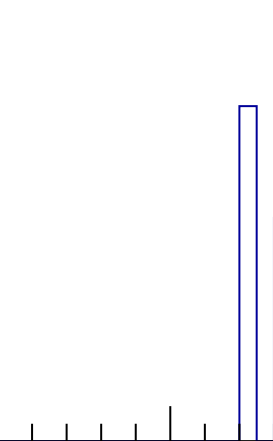
|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 50.99 |
| Std Dev | 3.311 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

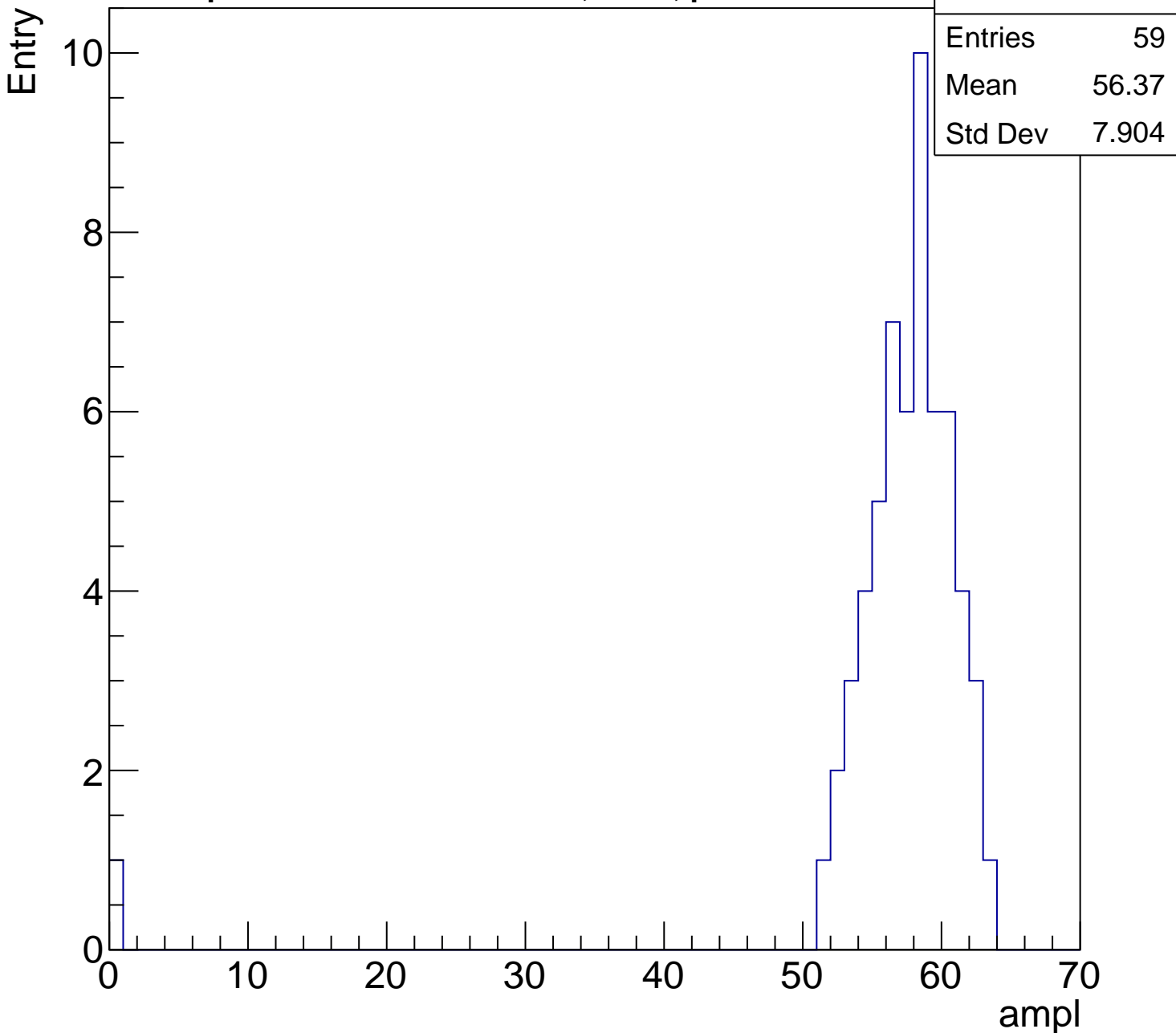
|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 56.37 |
| Std Dev | 7.904 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

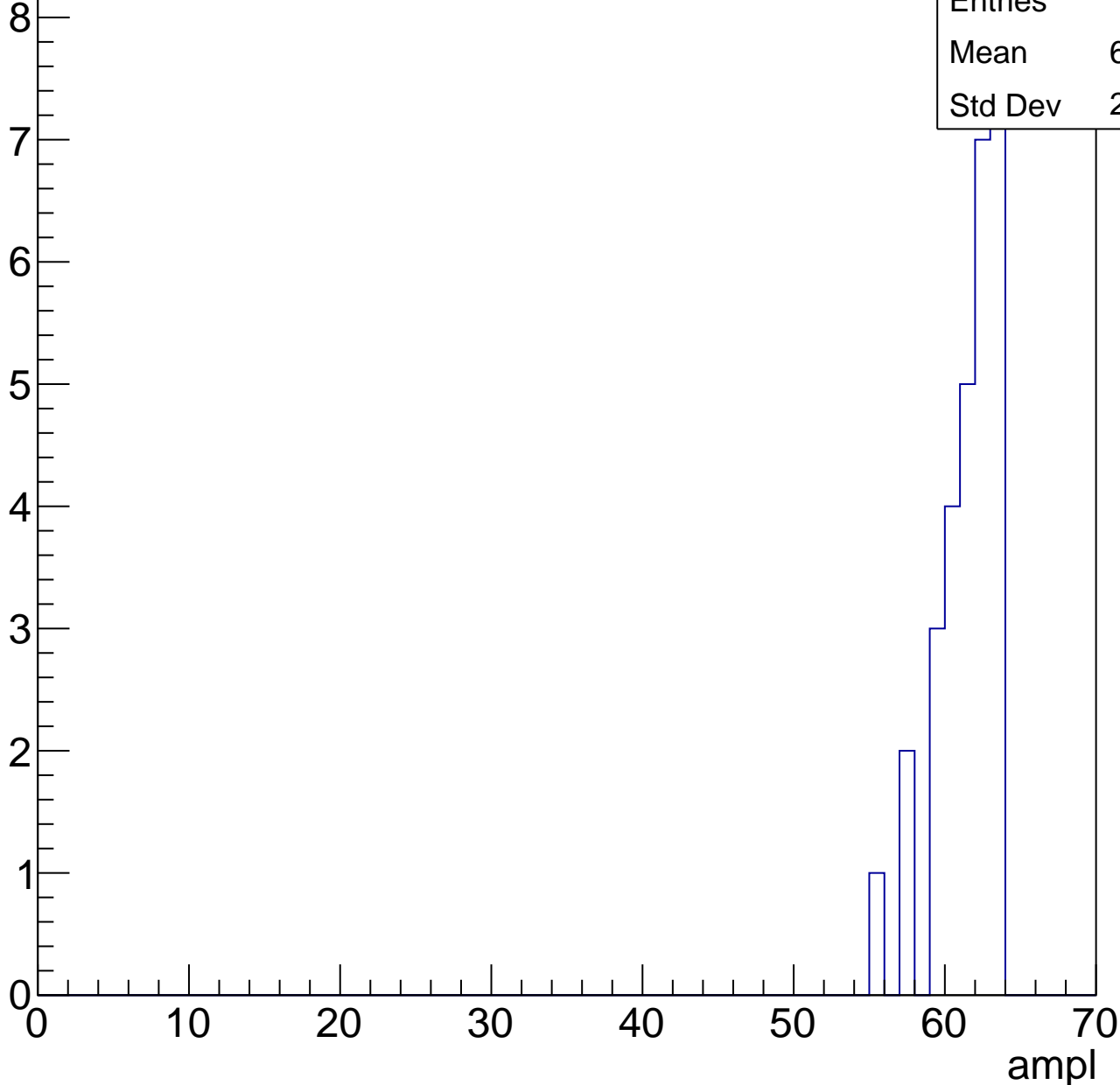


# B0L002S, U1-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 30    |
| Mean    | 60.97 |
| Std Dev | 2.025 |



# B0L002S, U1-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch34, adc0

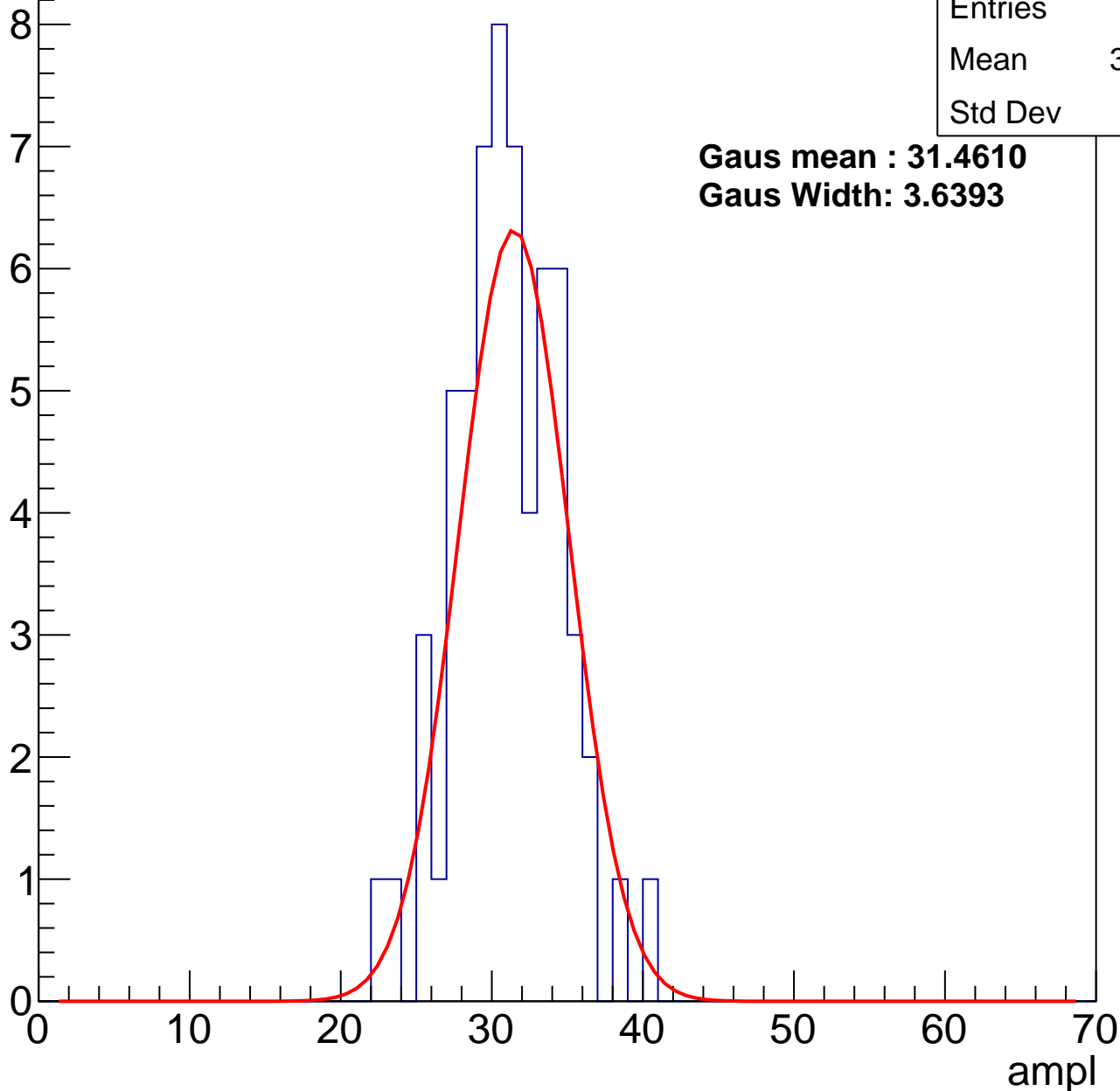
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 30.59 |
| Std Dev | 3.48  |

**Gaus mean : 31.4610**

**Gaus Width: 3.6393**



# B0L002S, U1-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 37.44 |
| Std Dev | 3.131 |

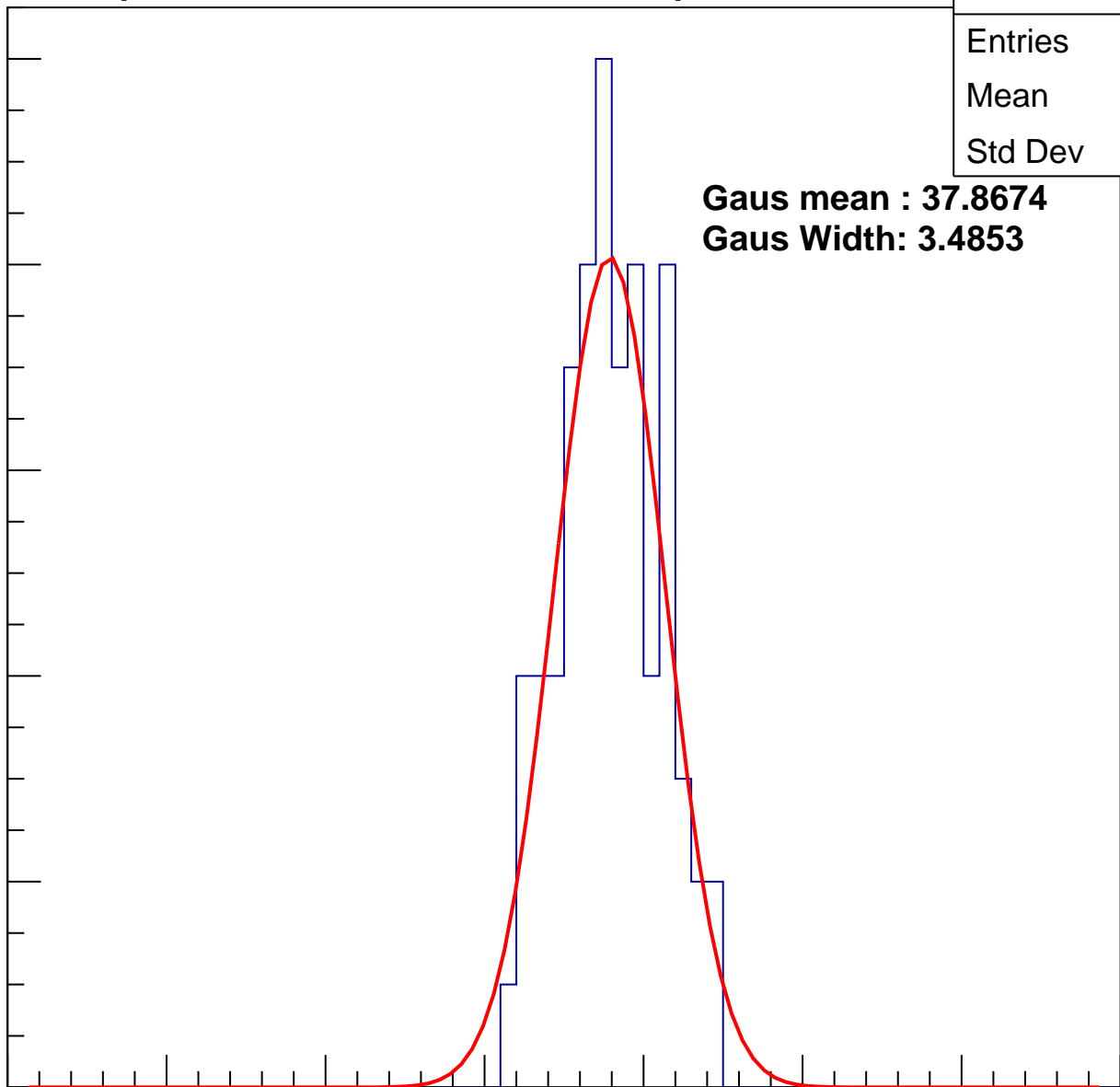
**Gaus mean : 37.8674**

**Gaus Width: 3.4853**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch34, adc2

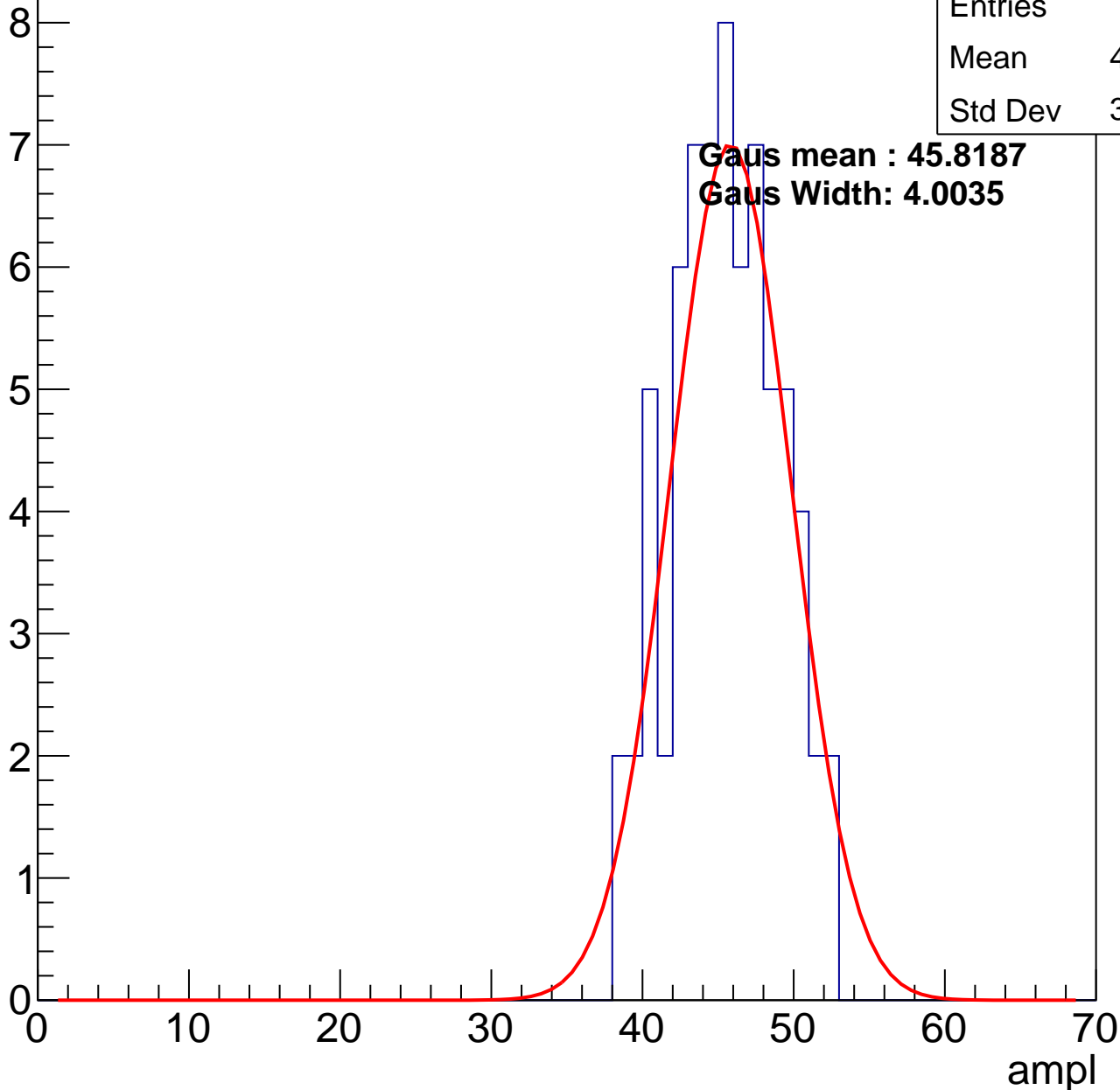
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 45.04 |
| Std Dev | 3.474 |

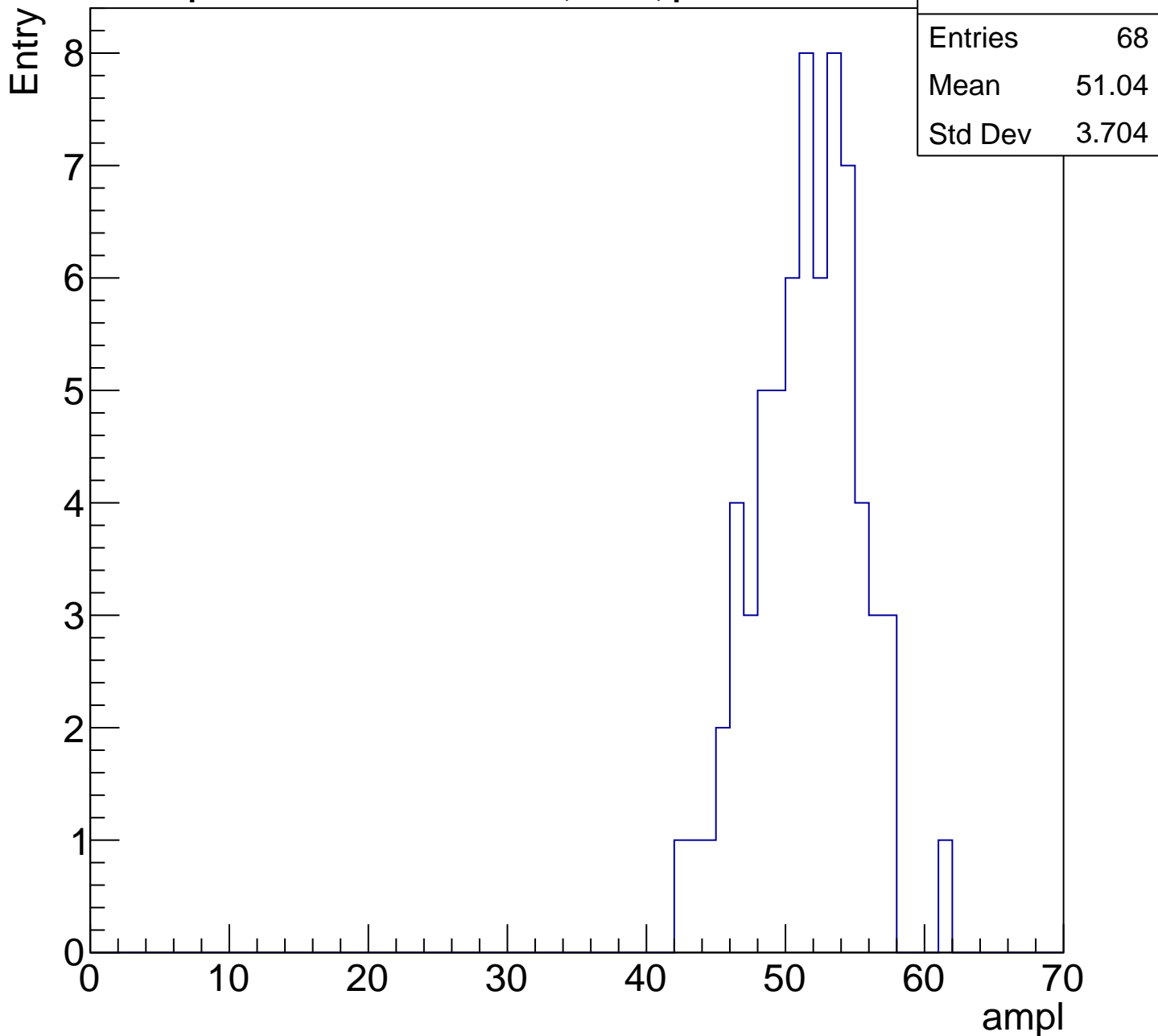
**Gaus mean : 45.8187**

**Gaus Width: 4.0035**



# B0L002S, U1-ch34, adc3

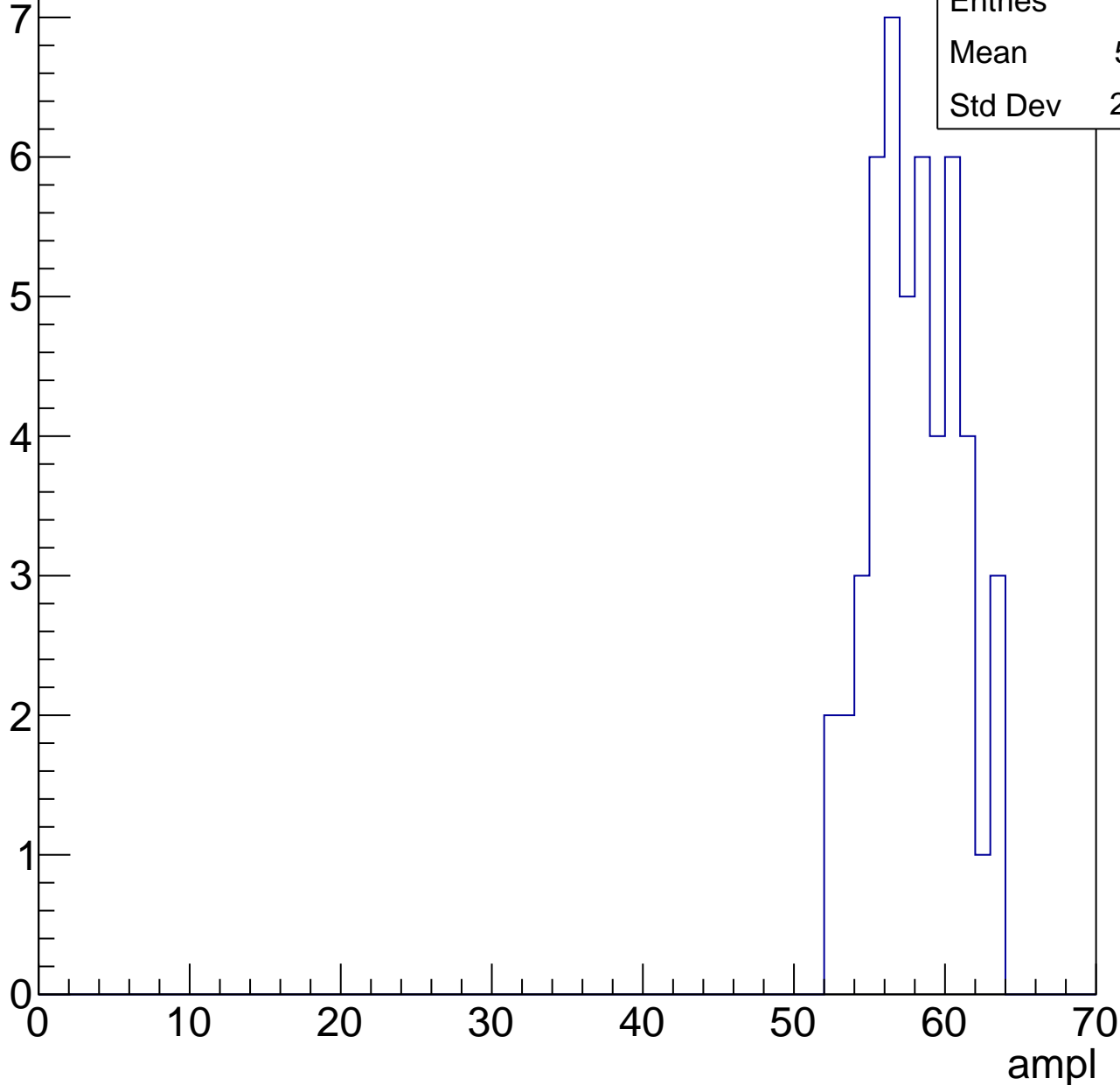
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



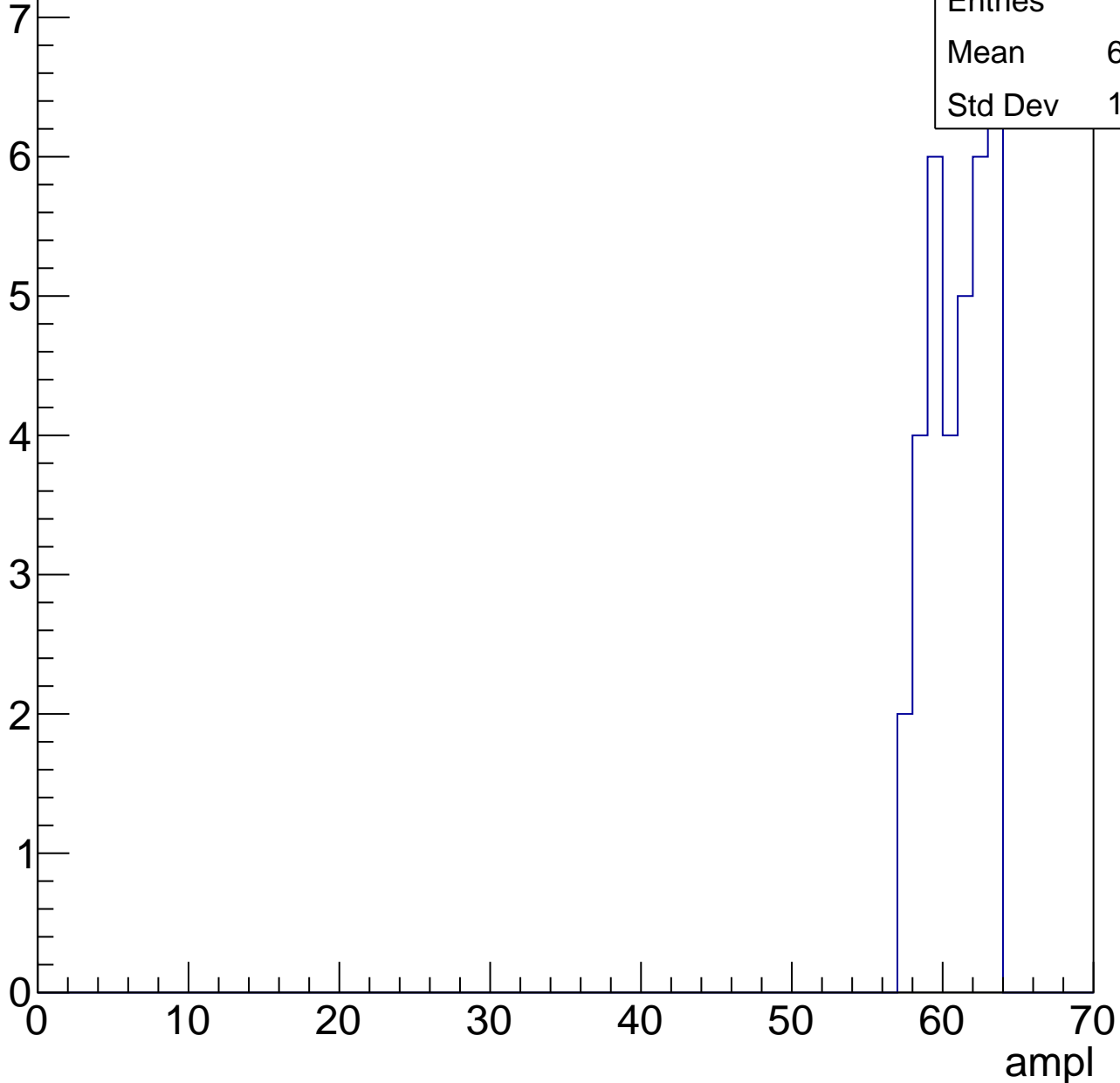
|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 57.51 |
| Std Dev | 2.858 |

# B0L002S, U1-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

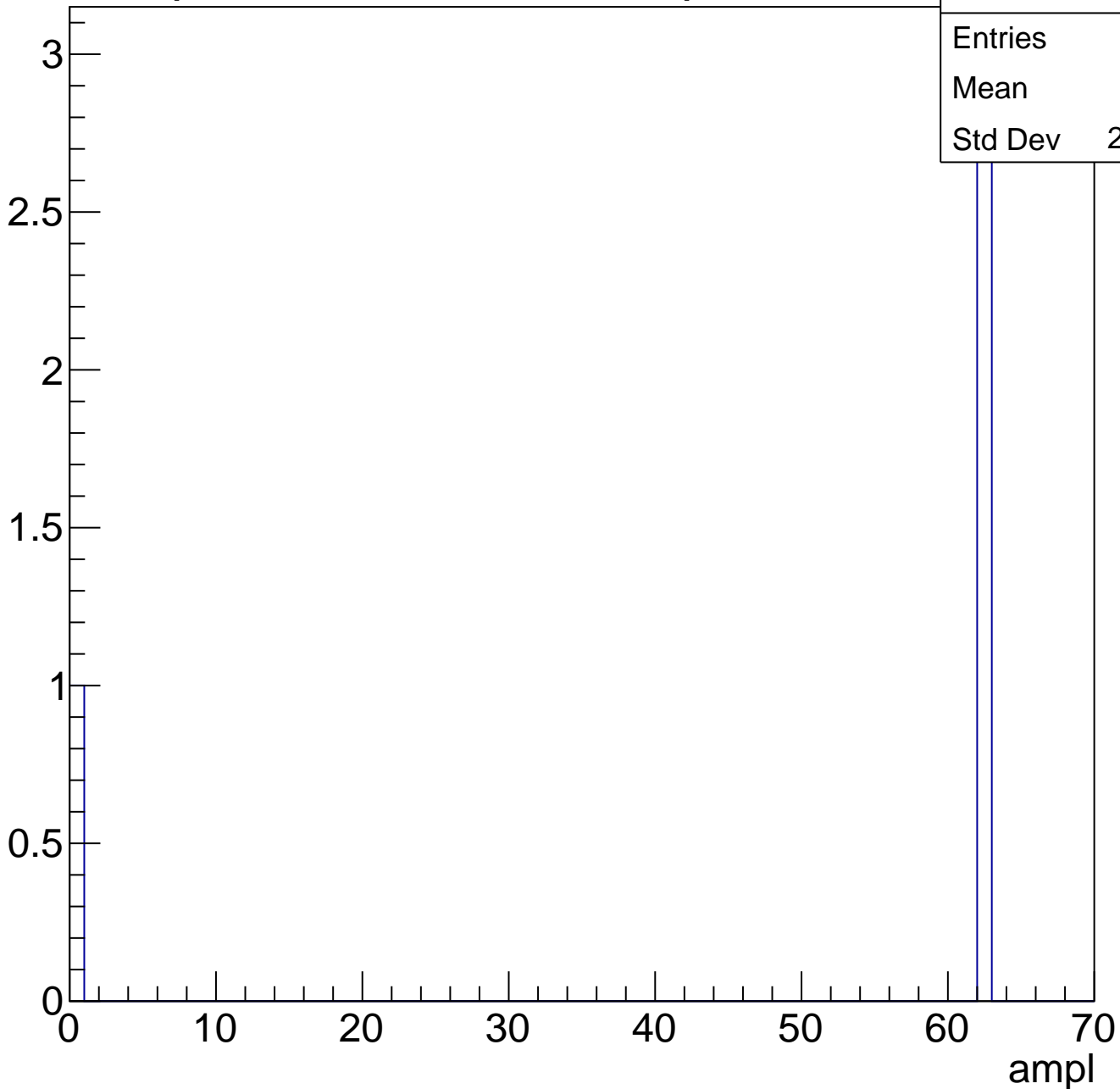
|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 60.53 |
| Std Dev | 1.898 |



# B0L002S, U1-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

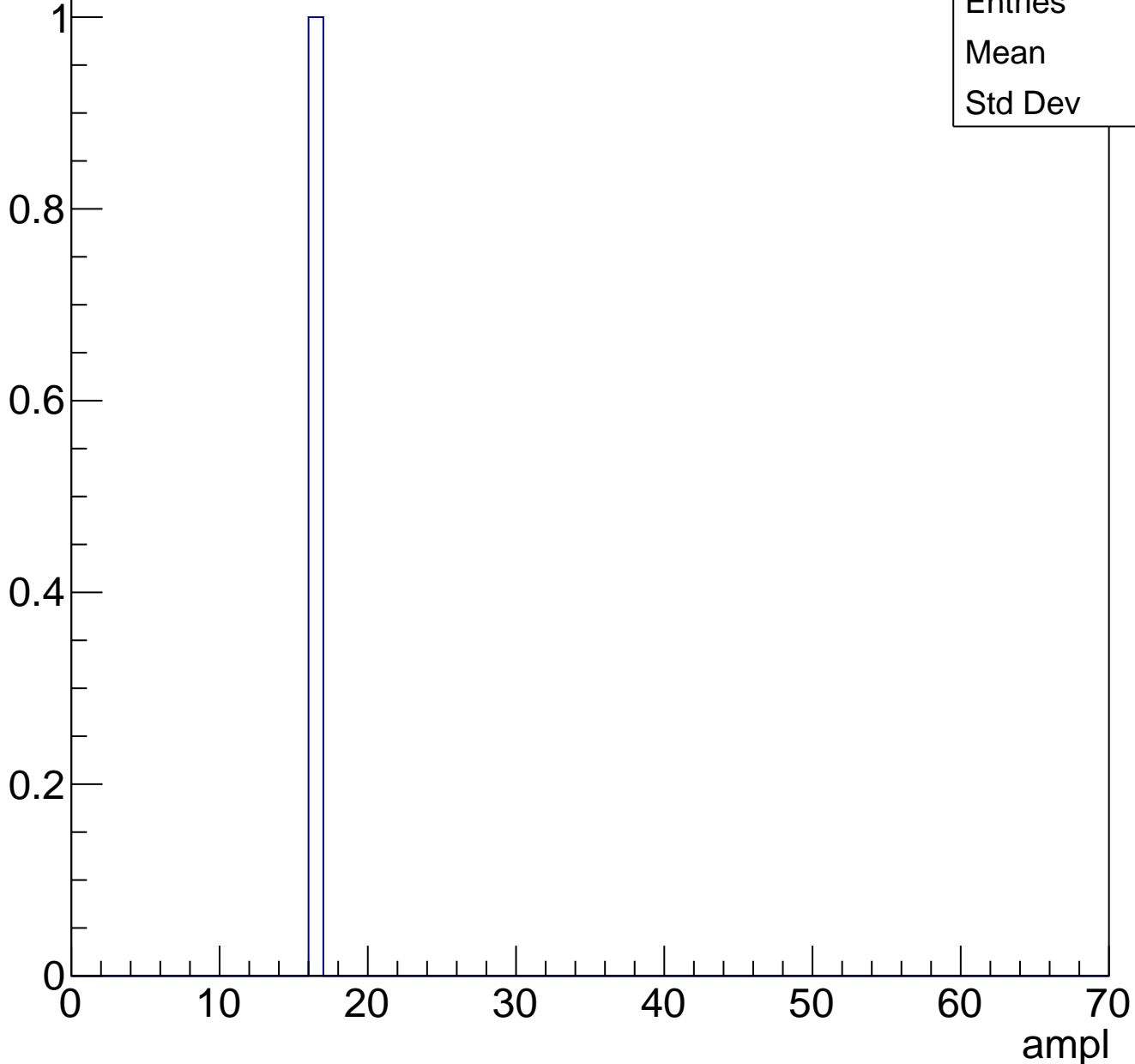




# B0L002S, U1-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 16 |
| Std Dev | 0  |

# B0L002S, U1-ch35, adc0

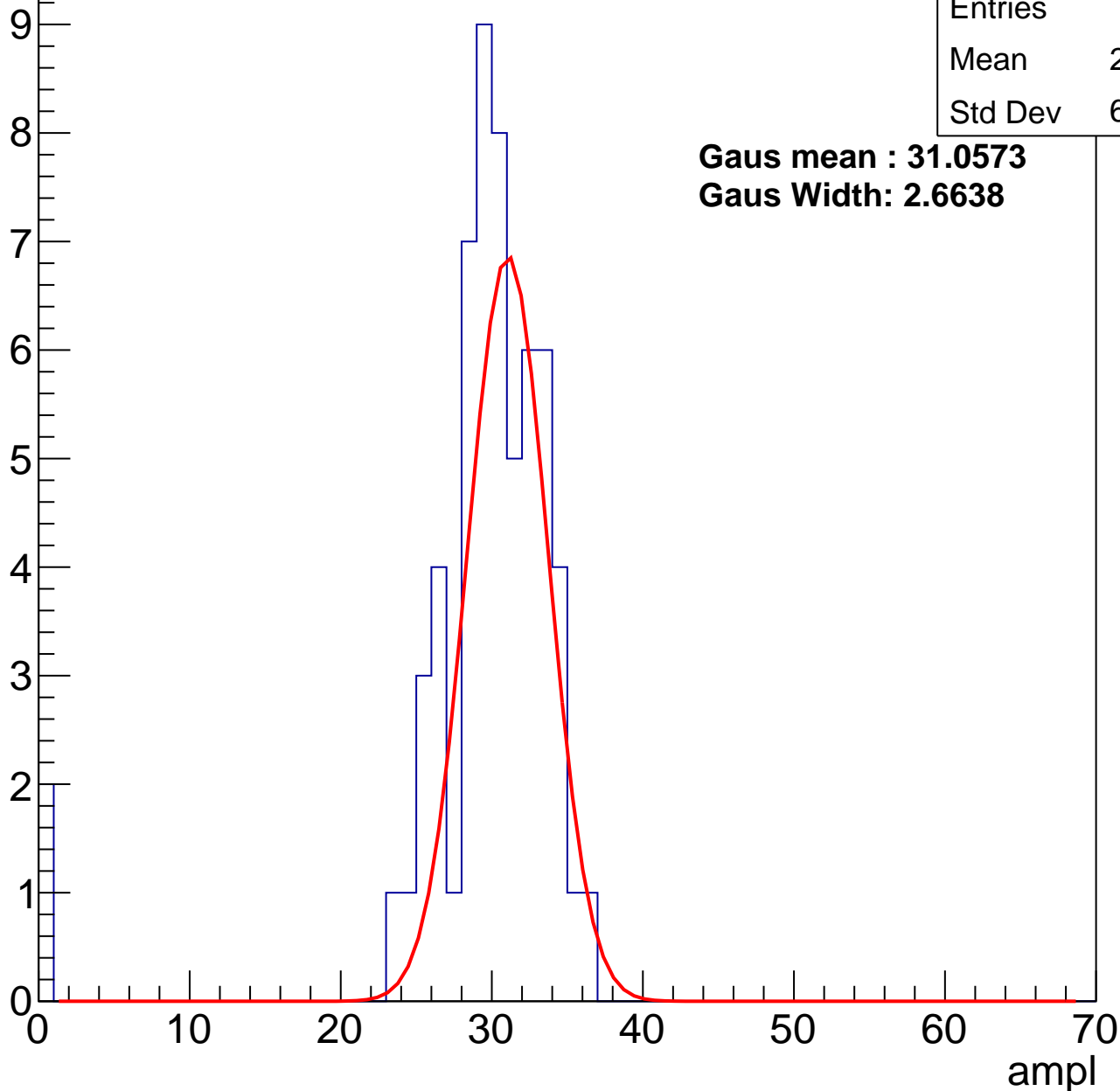
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 28.85 |
| Std Dev | 6.106 |

**Gaus mean : 31.0573**

**Gaus Width: 2.6638**



# B0L002S, U1-ch35, adc1

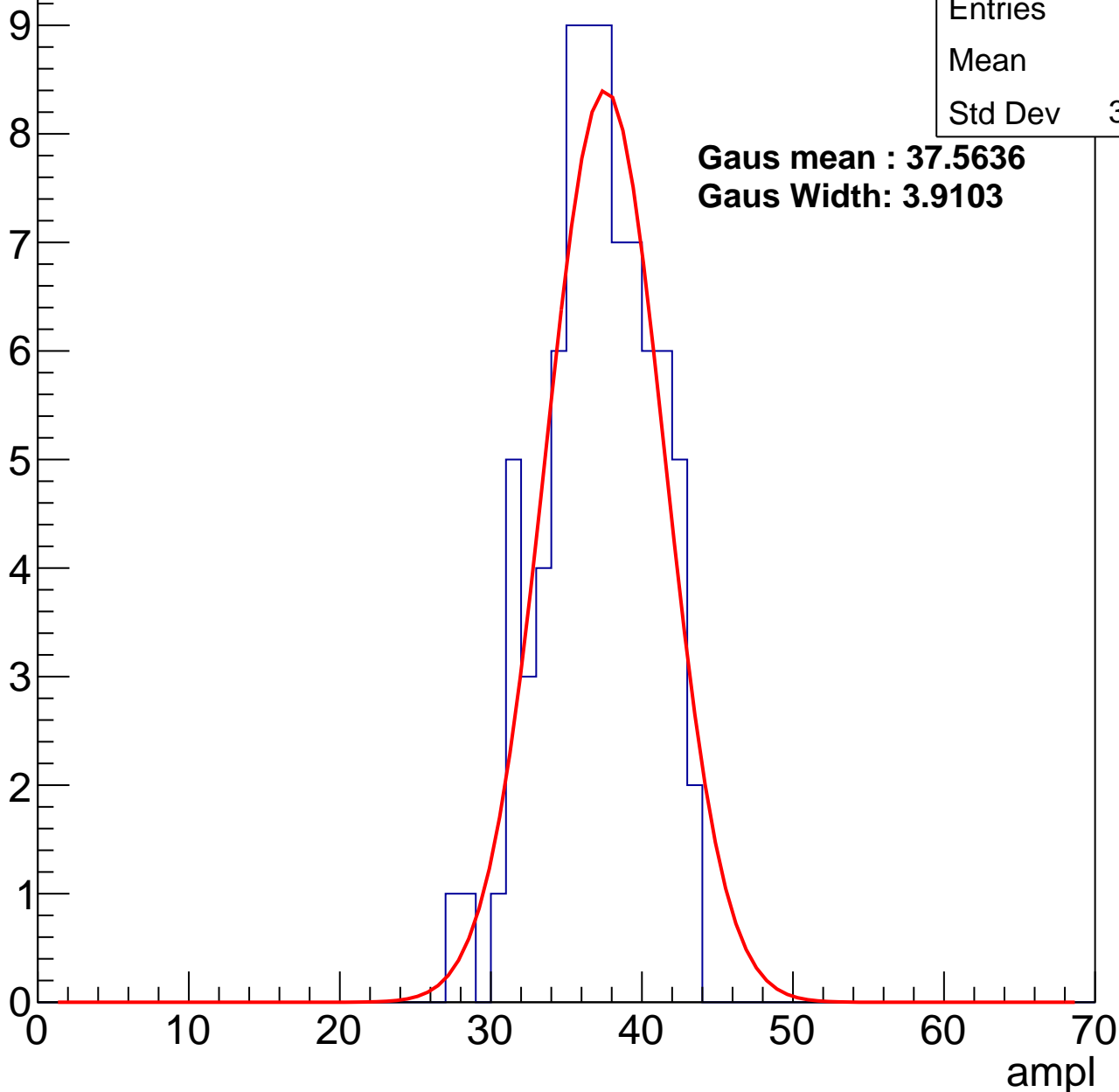
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 36.6  |
| Std Dev | 3.544 |

**Gaus mean : 37.5636**

**Gaus Width: 3.9103**



# B0L002S, U1-ch35, adc2

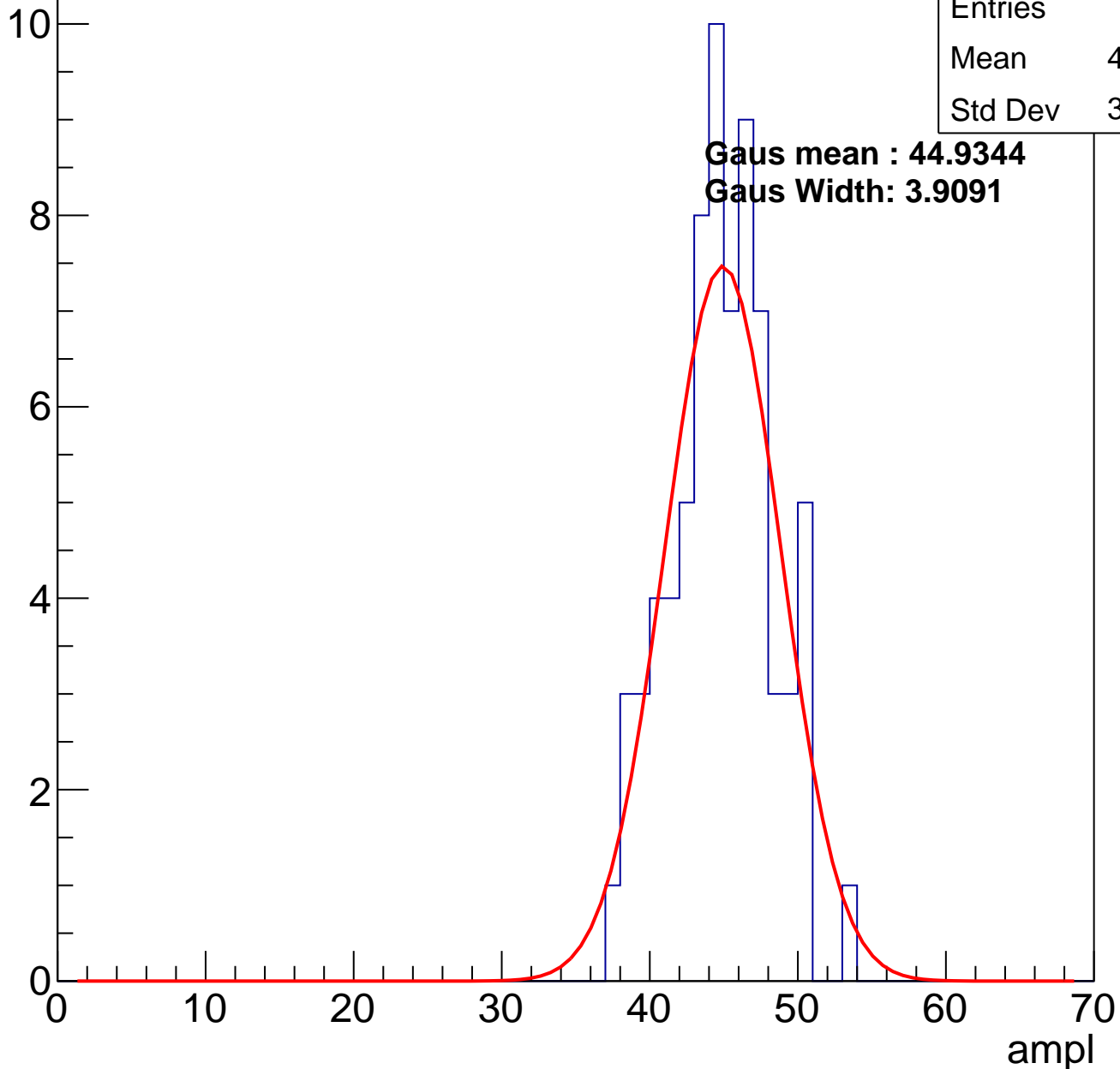
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 44.36 |
| Std Dev | 3.394 |

**Gaus mean : 44.9344**

**Gaus Width: 3.9091**

Entry

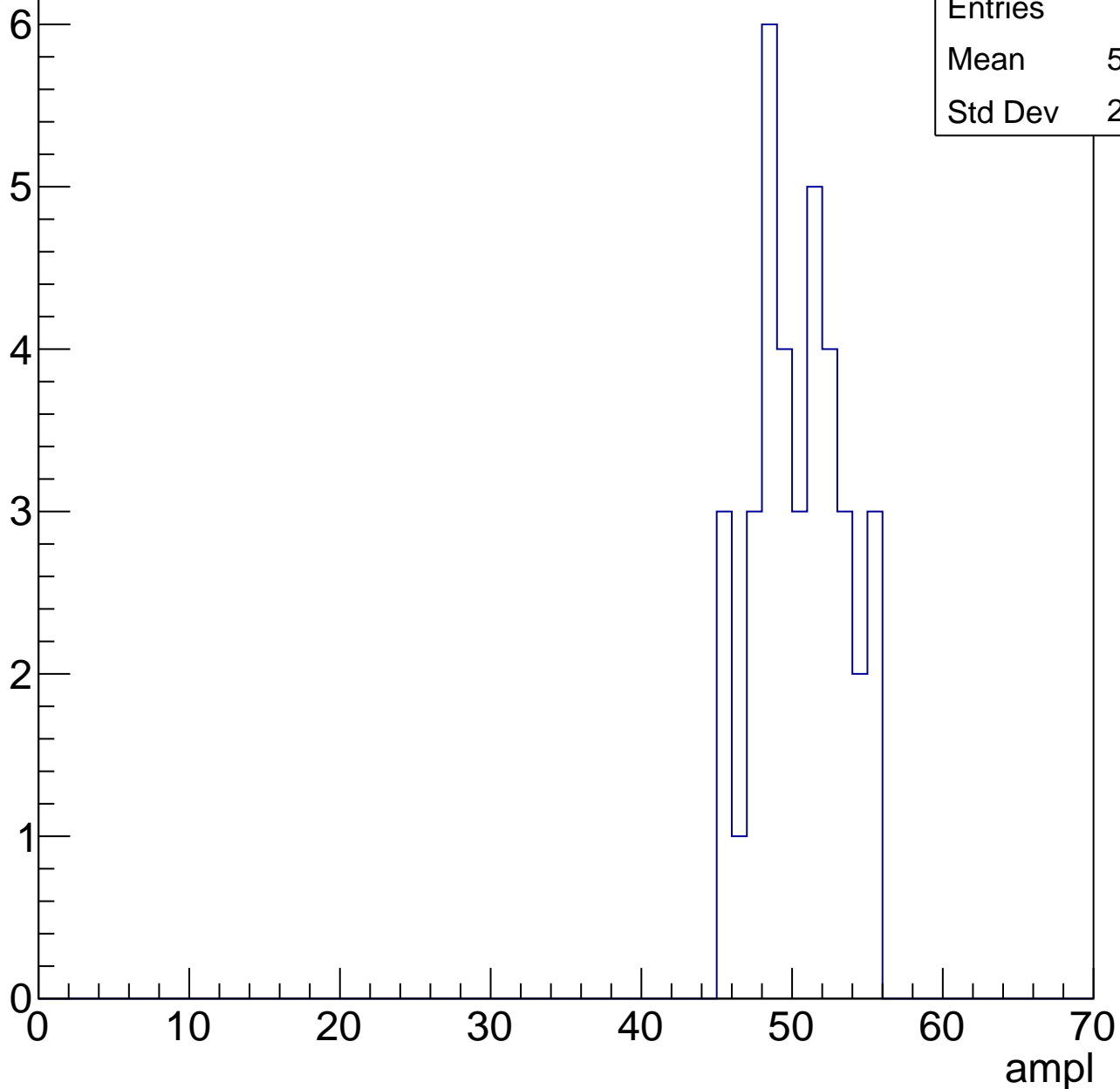


# B0L002S, U1-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

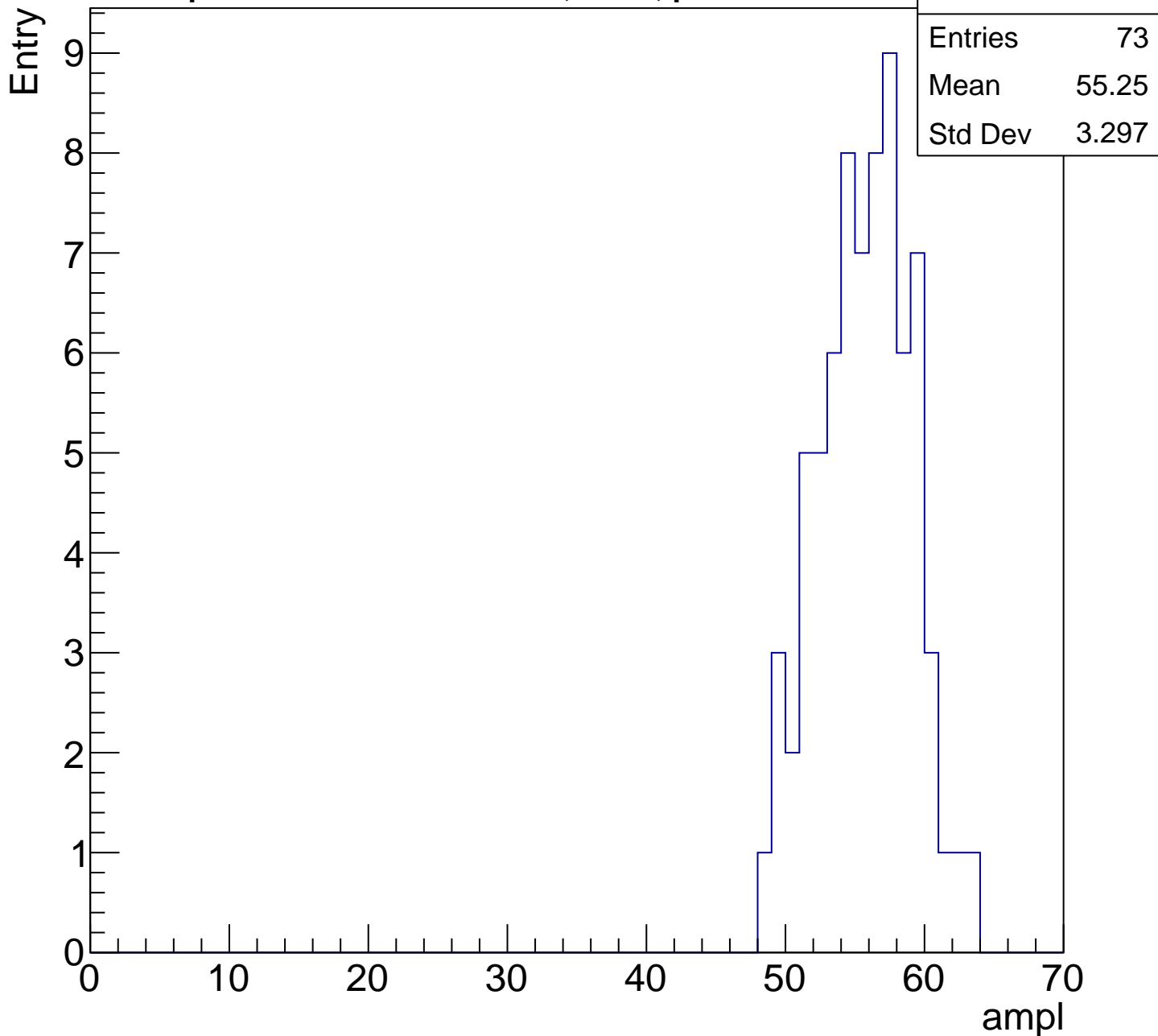
Entry

|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 50.03 |
| Std Dev | 2.852 |



# B0L002S, U1-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

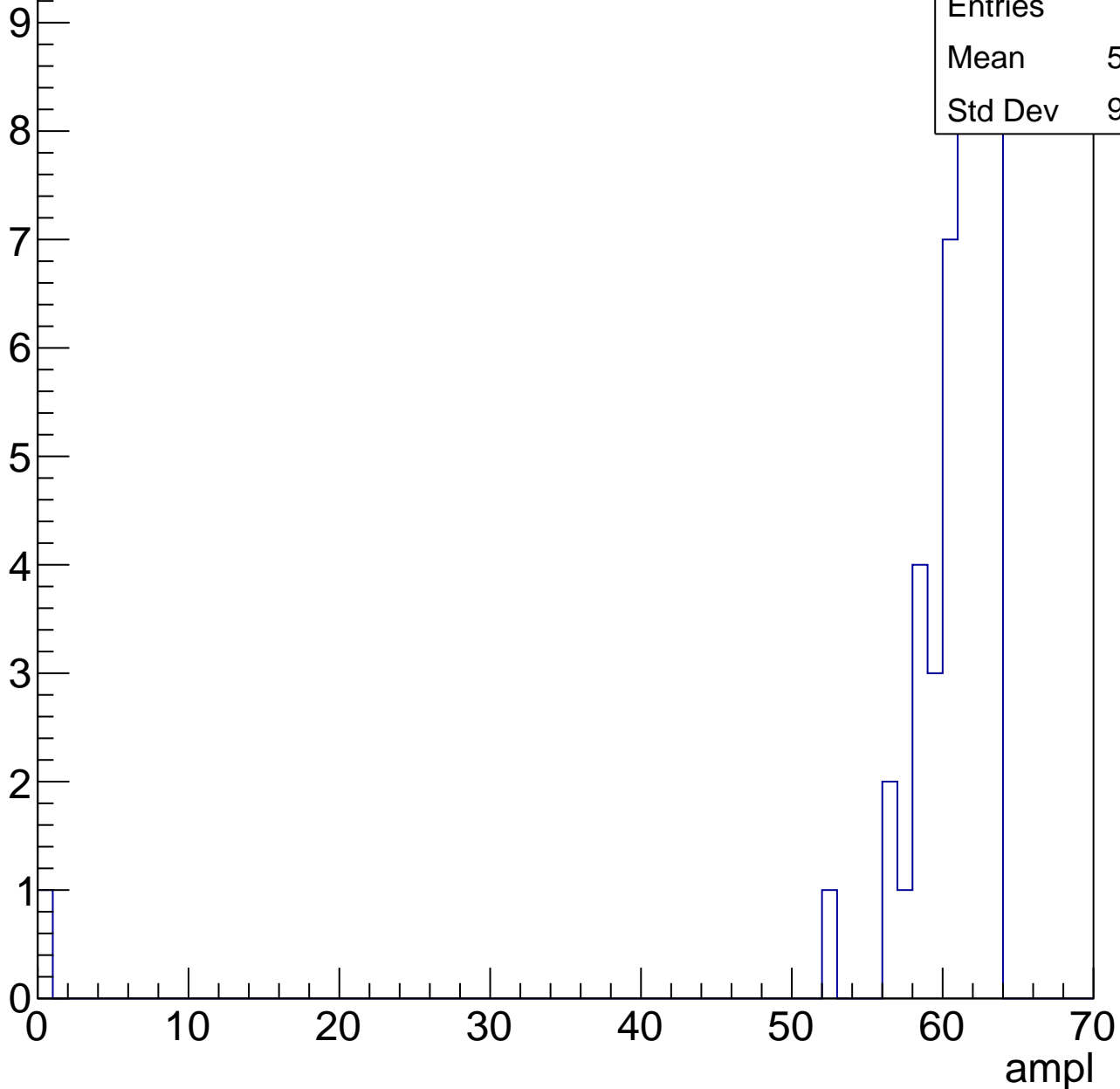


# B0L002S, U1-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 59.22 |
| Std Dev | 9.115 |



# B0L002S, U1-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch36, adc0

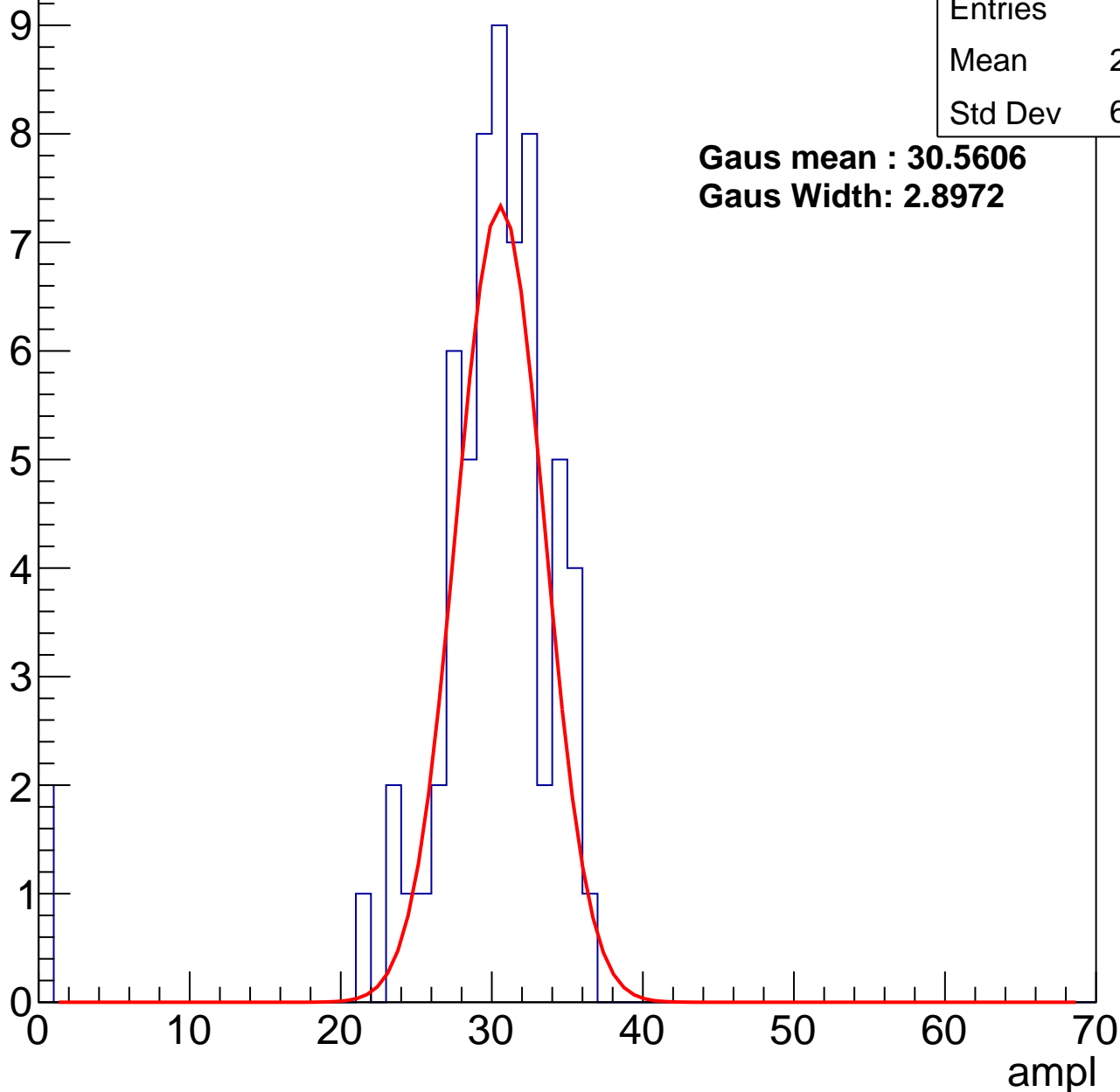
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 29.02 |
| Std Dev | 6.079 |

**Gaus mean : 30.5606**

**Gaus Width: 2.8972**

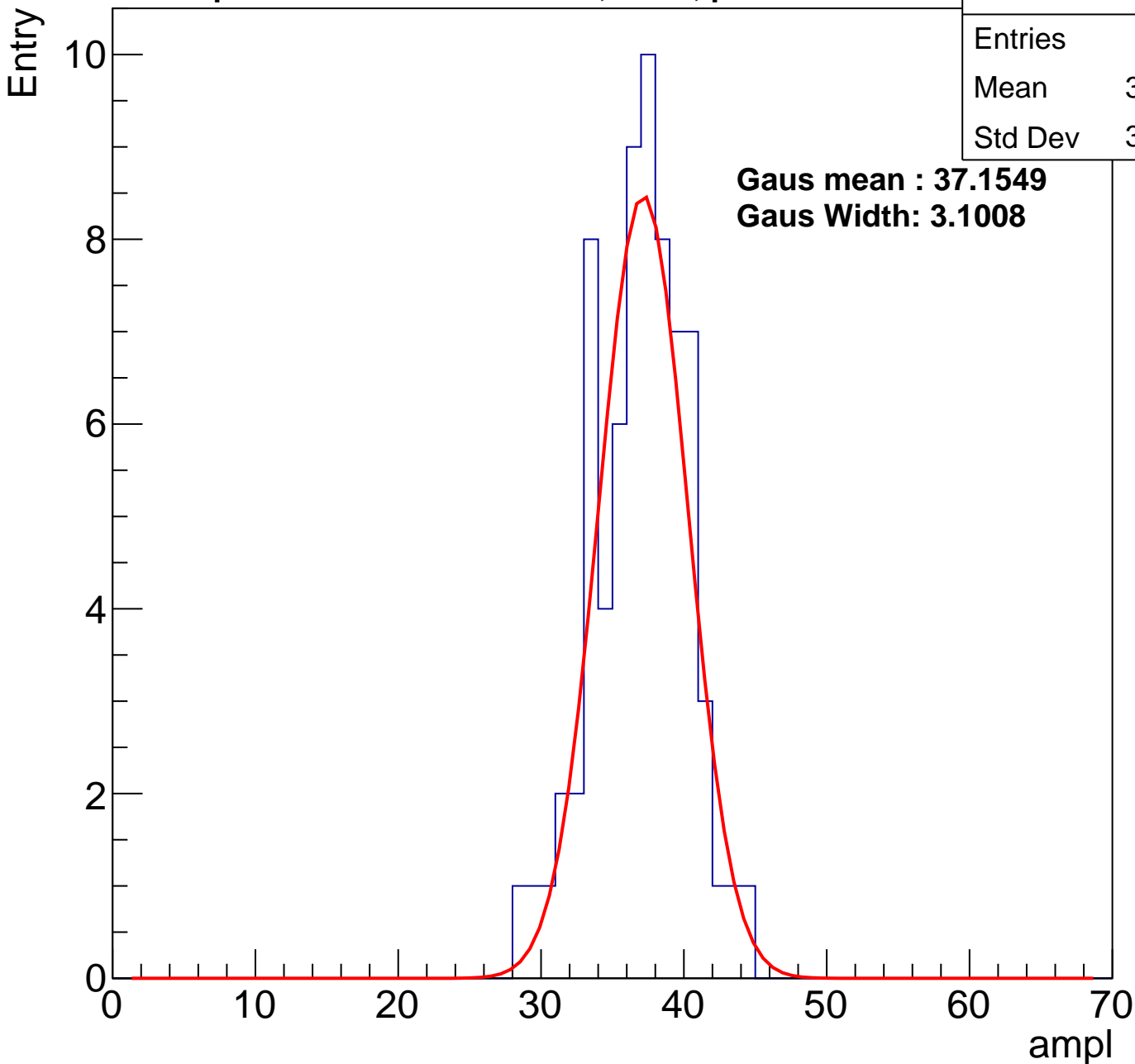


# B0L002S, U1-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 36.47 |
| Std Dev | 3.227 |

**Gaus mean : 37.1549**  
**Gaus Width: 3.1008**



# B0L002S, U1-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 43.05 |
| Std Dev | 2.696 |

**Gaus mean : 43.7846**

**Gaus Width: 2.8015**

Entry

10

8

6

4

2

0

0

10

20

30

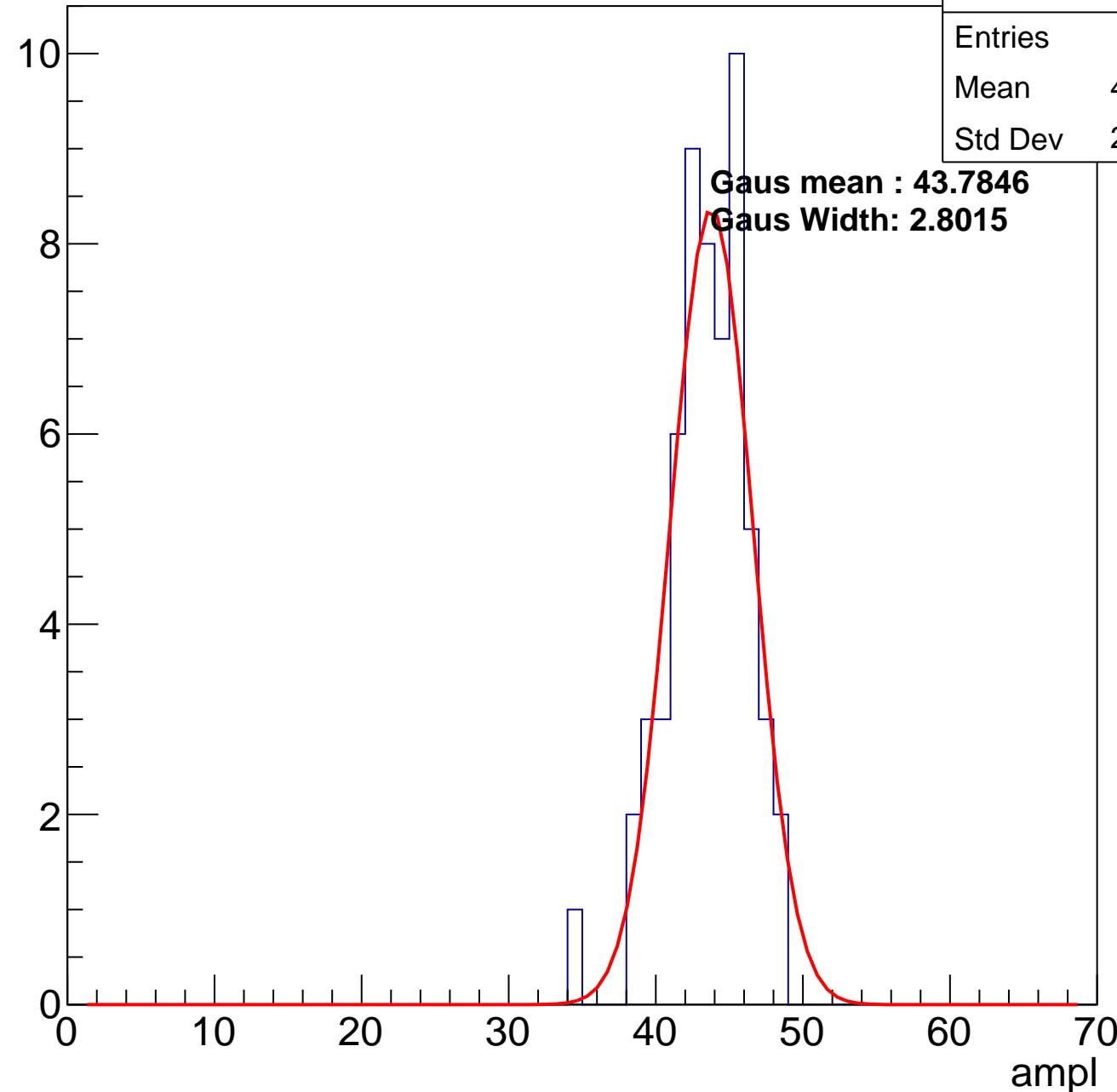
40

50

60

70

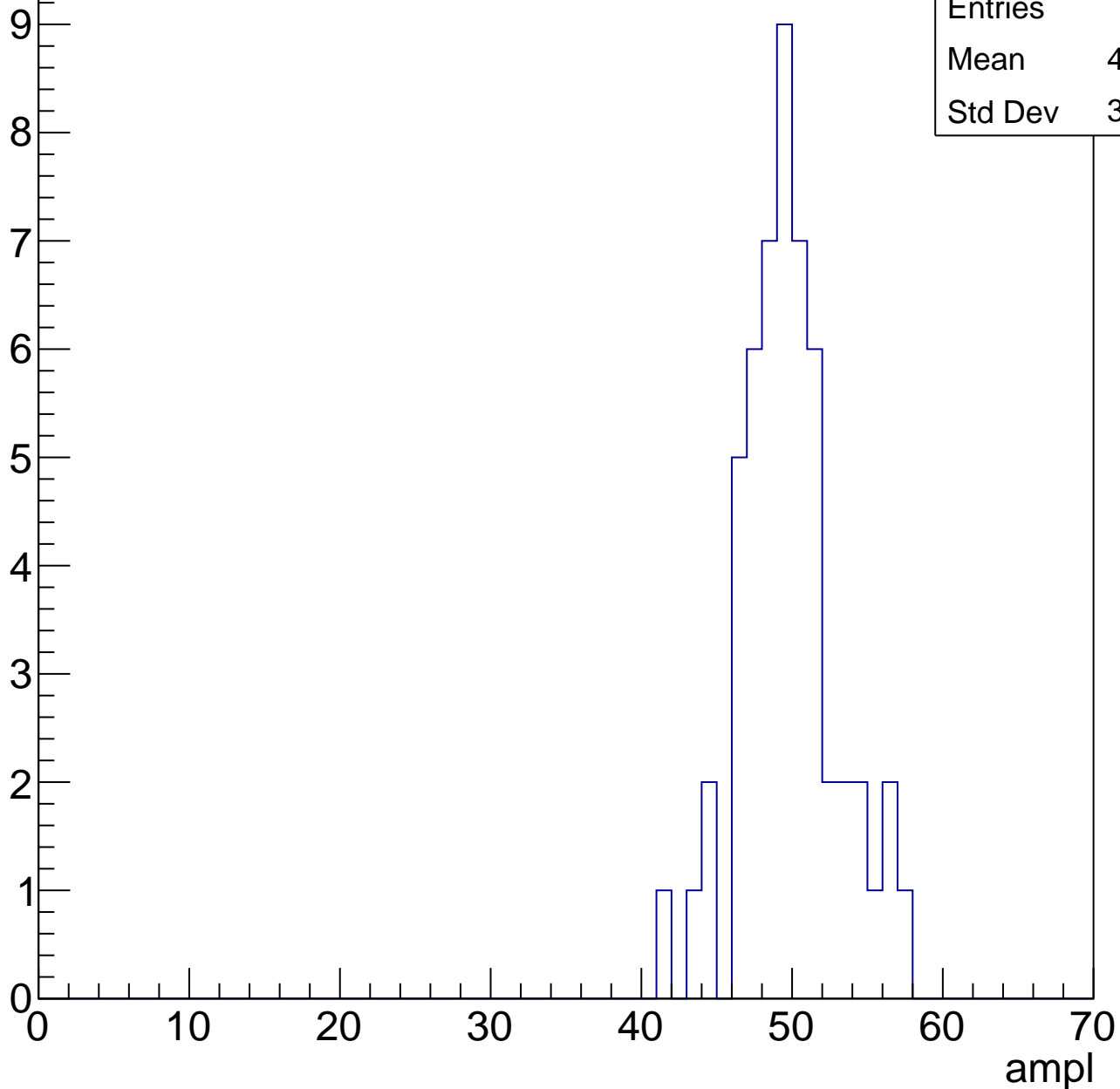
ampl



# B0L002S, U1-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

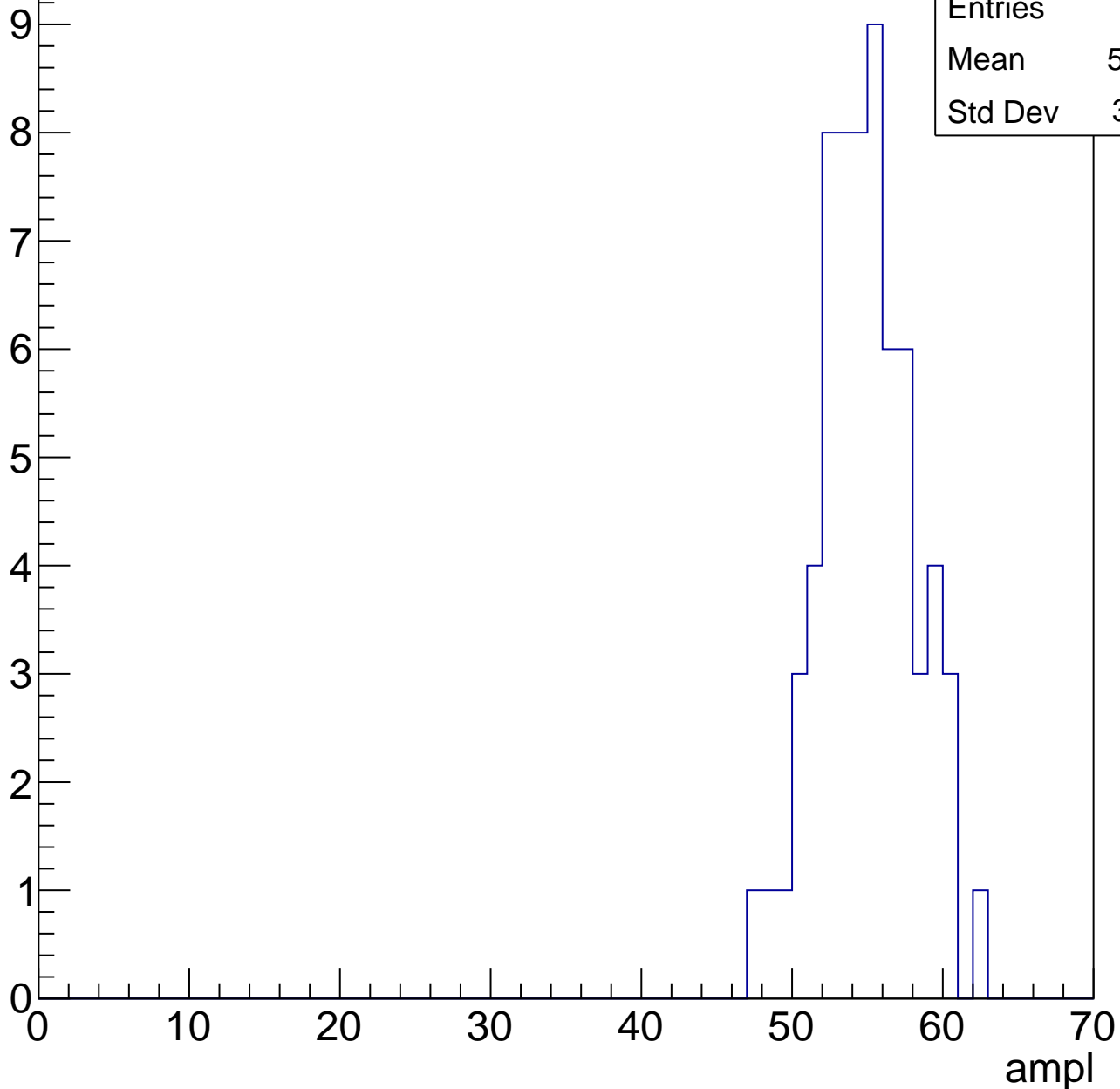


|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 49.24 |
| Std Dev | 3.197 |

# B0L002S, U1-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

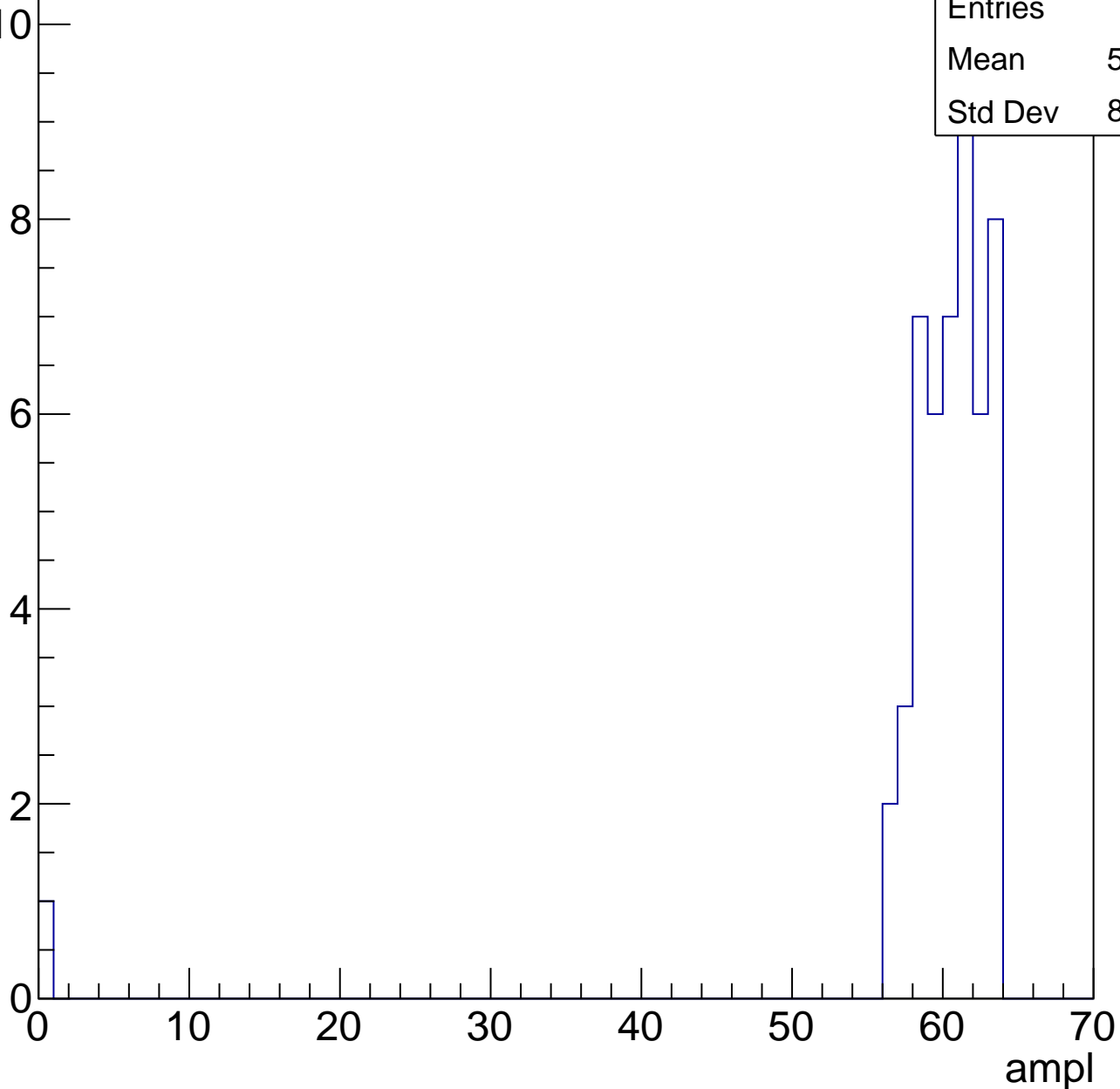


# B0L002S, U1-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 58.98 |
| Std Dev | 8.657 |



# B0L002S, U1-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch37, adc0

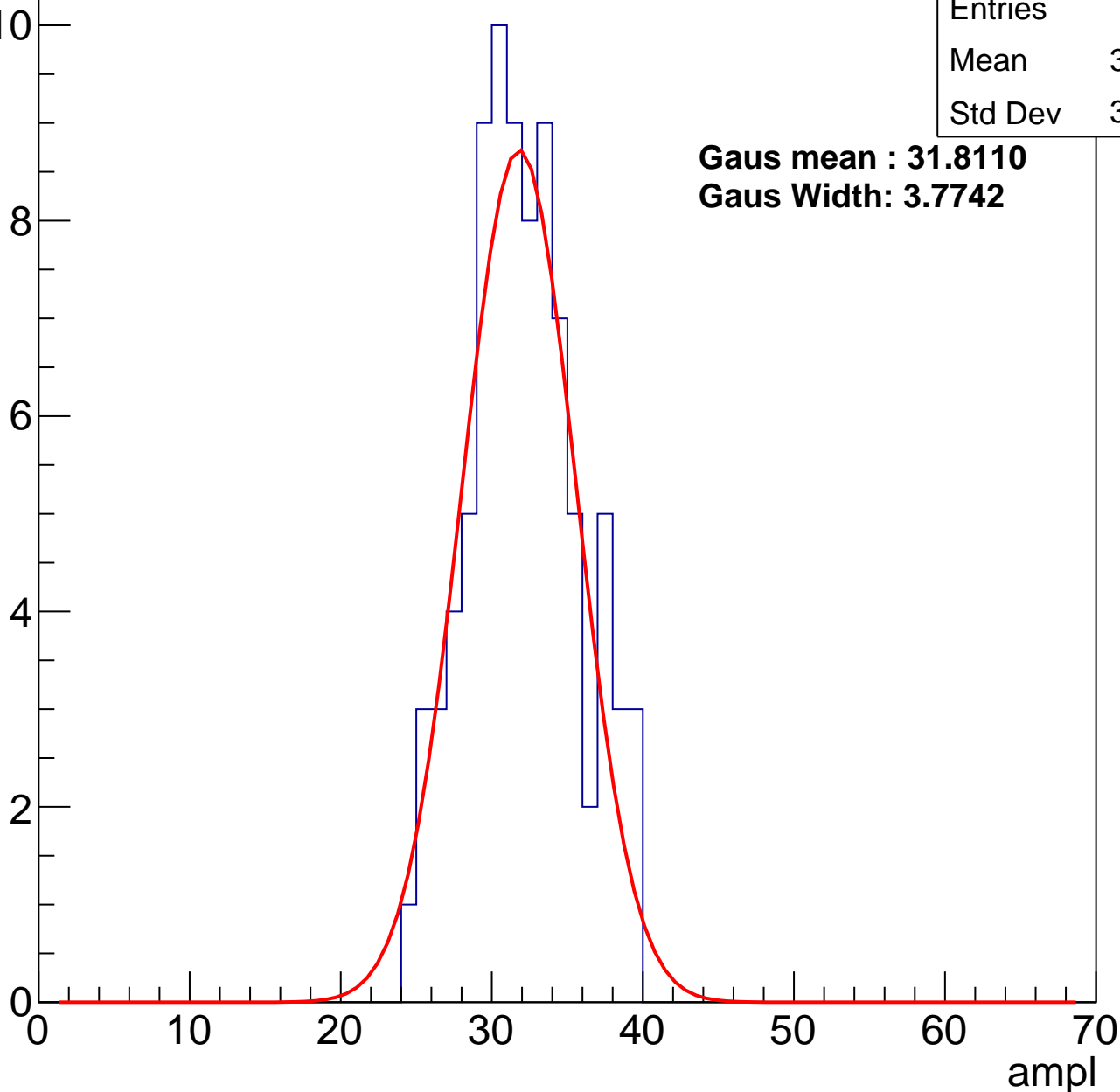
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 31.62 |
| Std Dev | 3.593 |

**Gaus mean : 31.8110**

**Gaus Width: 3.7742**



# B0L002S, U1-ch37, adc1

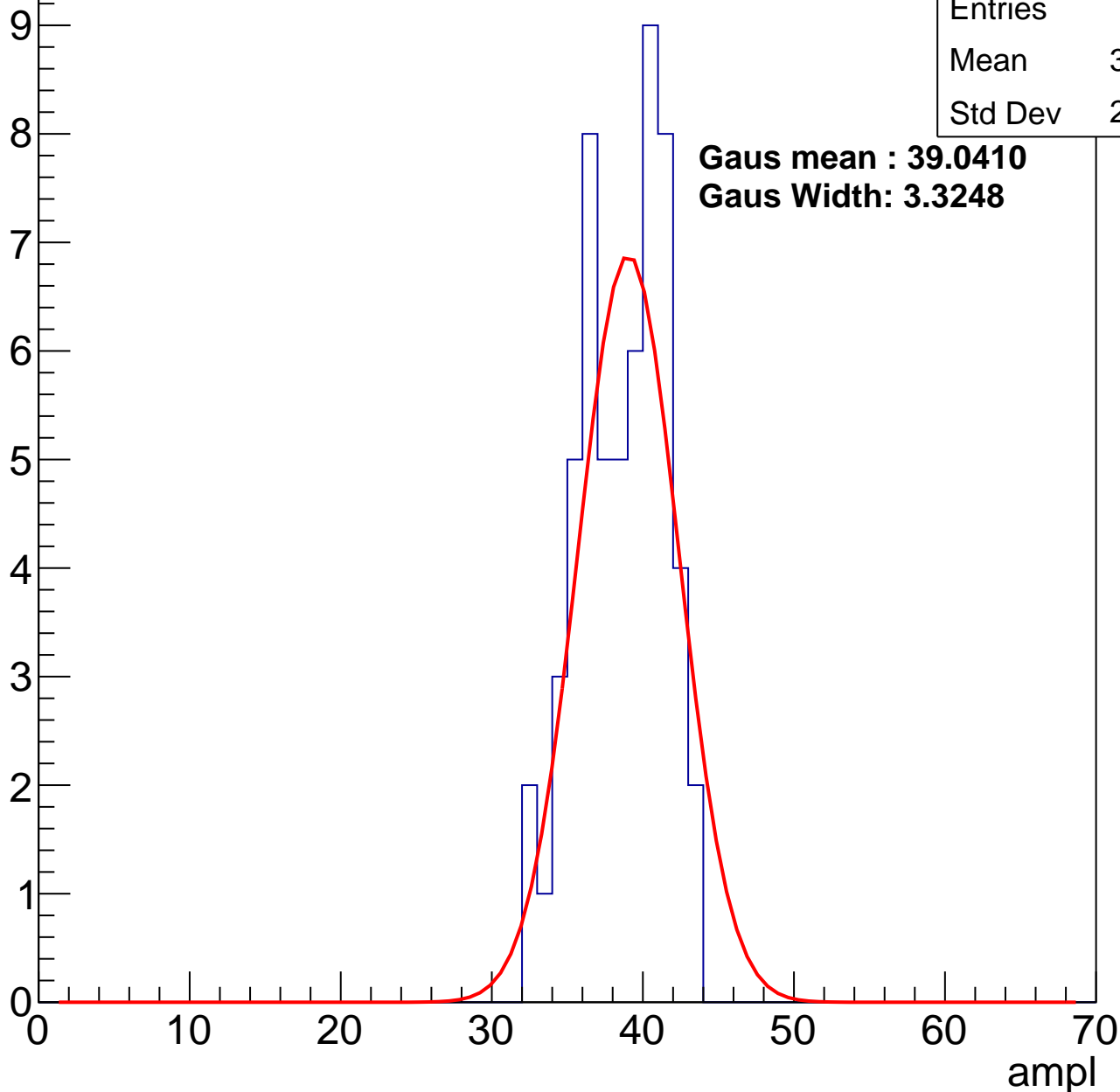
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 38.16 |
| Std Dev | 2.797 |

**Gaus mean : 39.0410**

**Gaus Width: 3.3248**



# B0L002S, U1-ch37, adc2

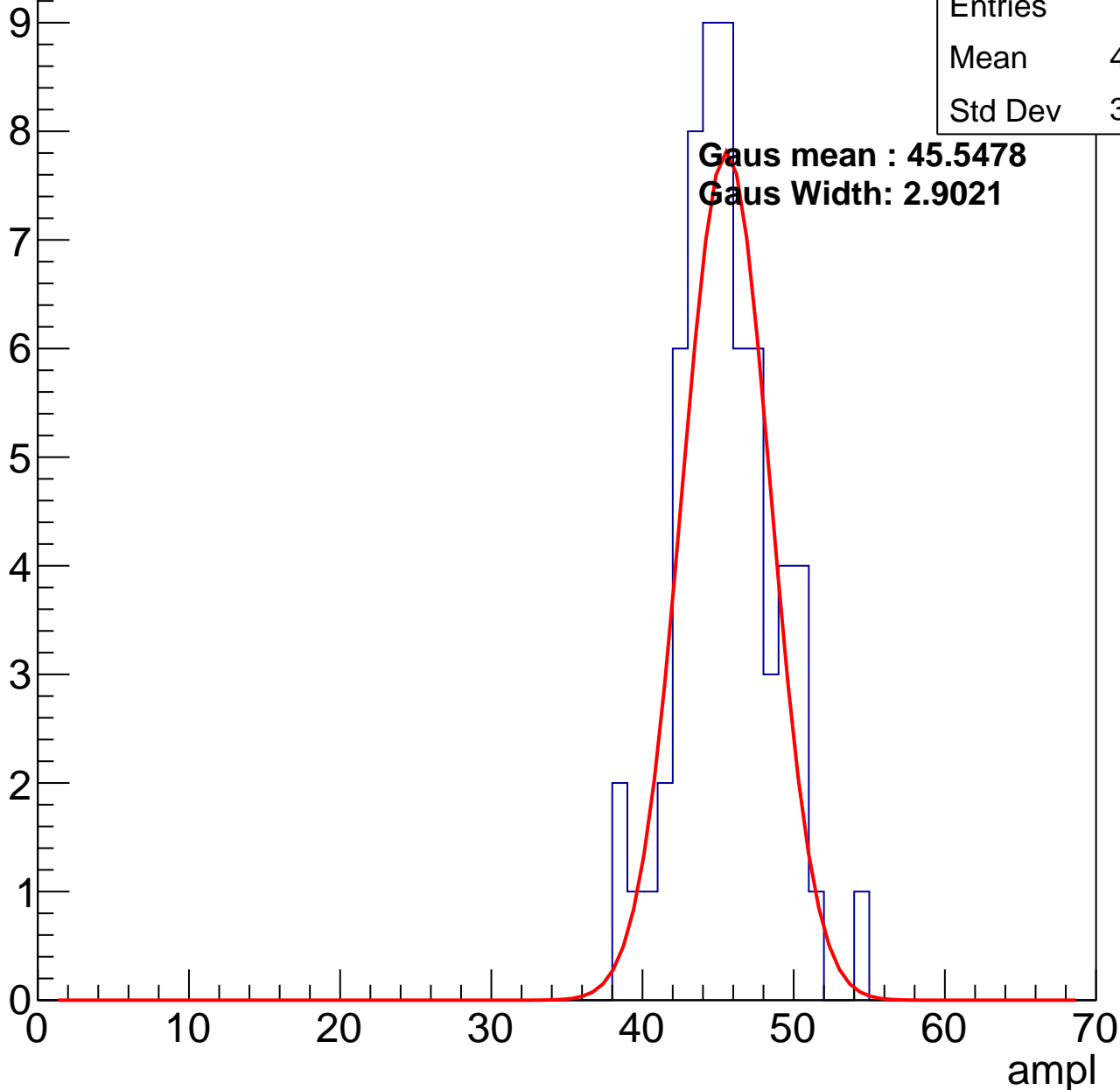
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 45.03 |
| Std Dev | 3.147 |

**Gaus mean : 45.5478**

**Gaus Width: 2.9021**

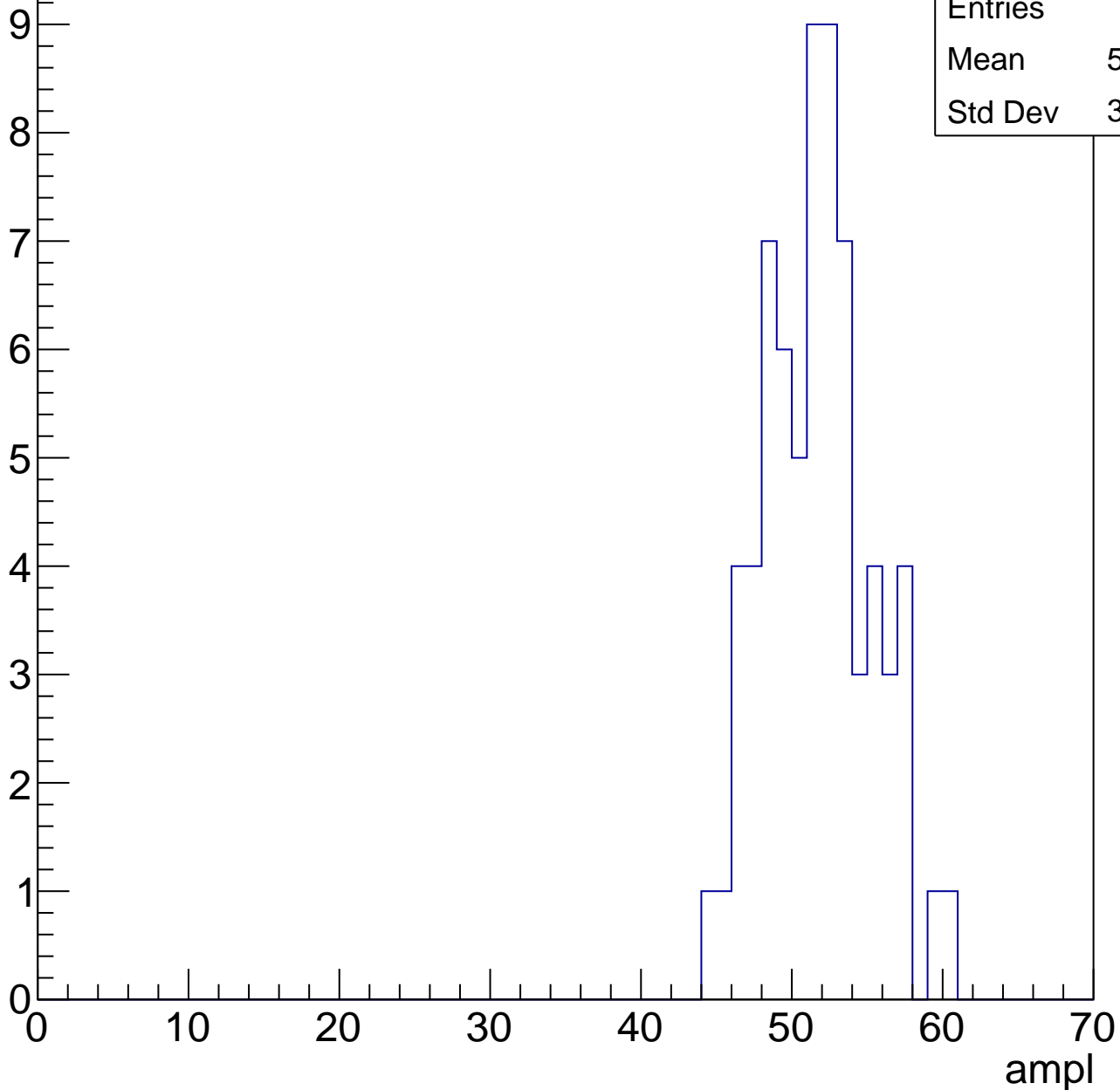


# B0L002S, U1-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 51.25 |
| Std Dev | 3.466 |

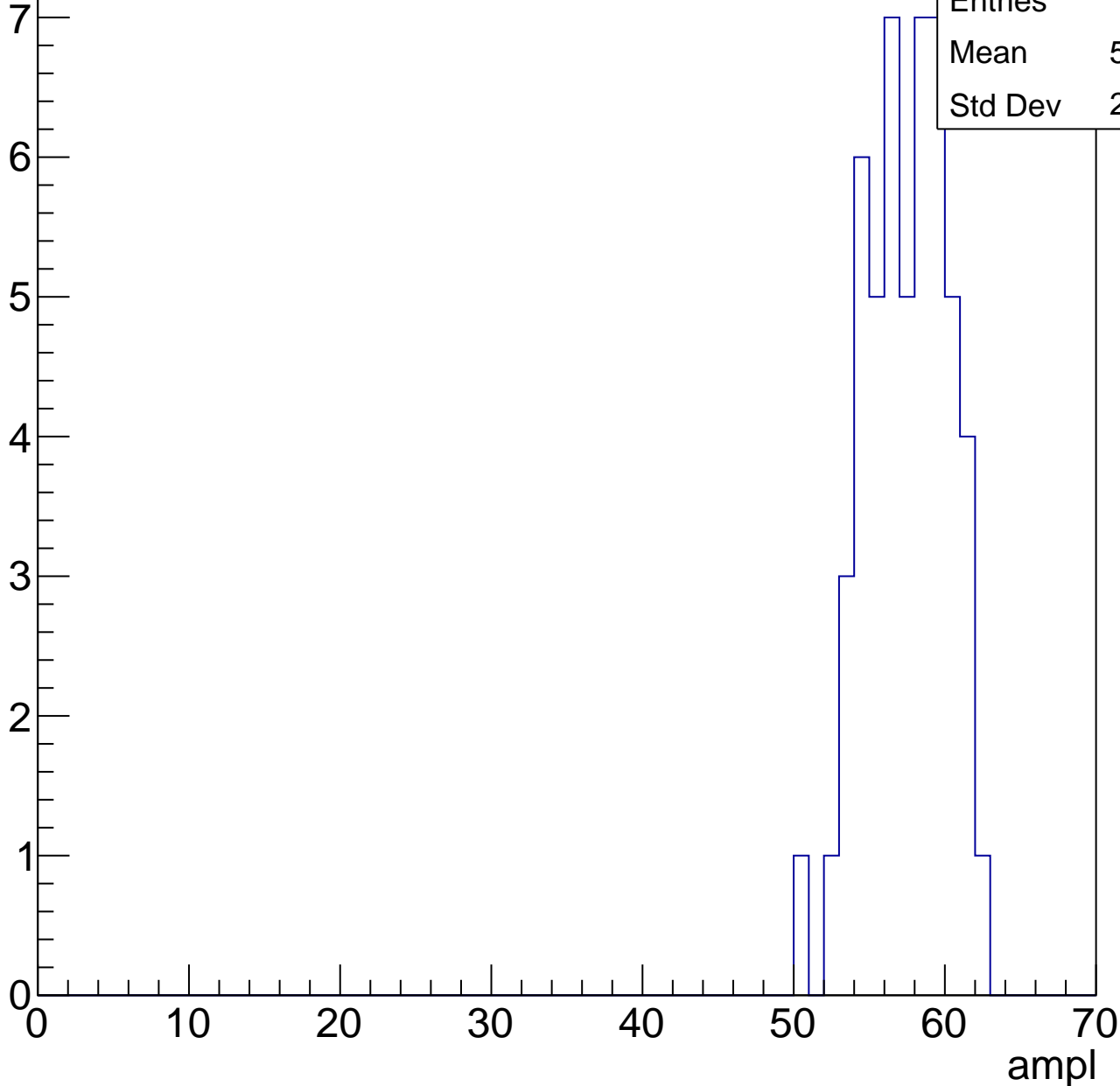


# B0L002S, U1-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 56.96 |
| Std Dev | 2.674 |

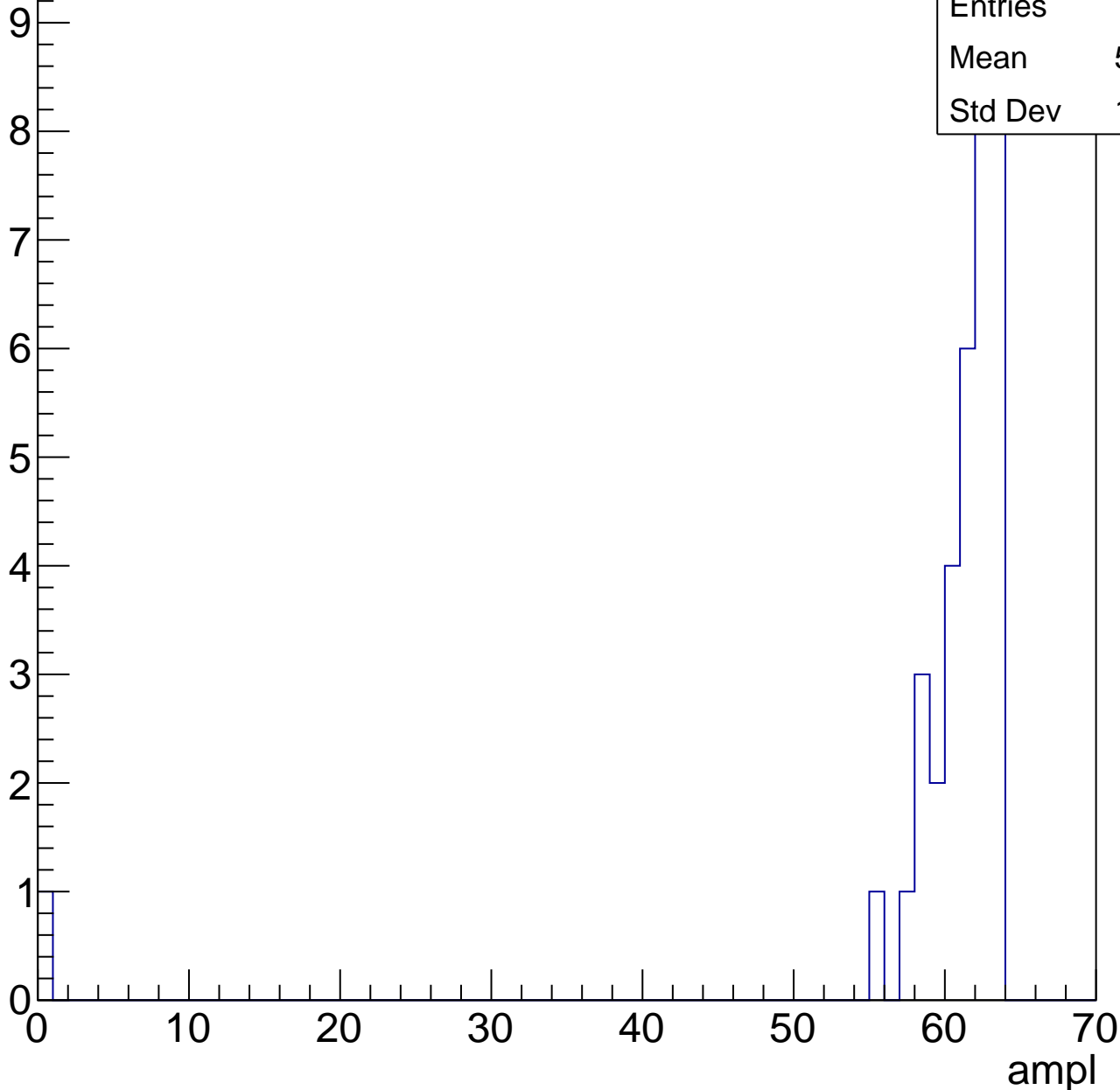


# B0L002S, U1-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 59.31 |
| Std Dev | 10.21 |



# B0L002S, U1-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch38, adc0

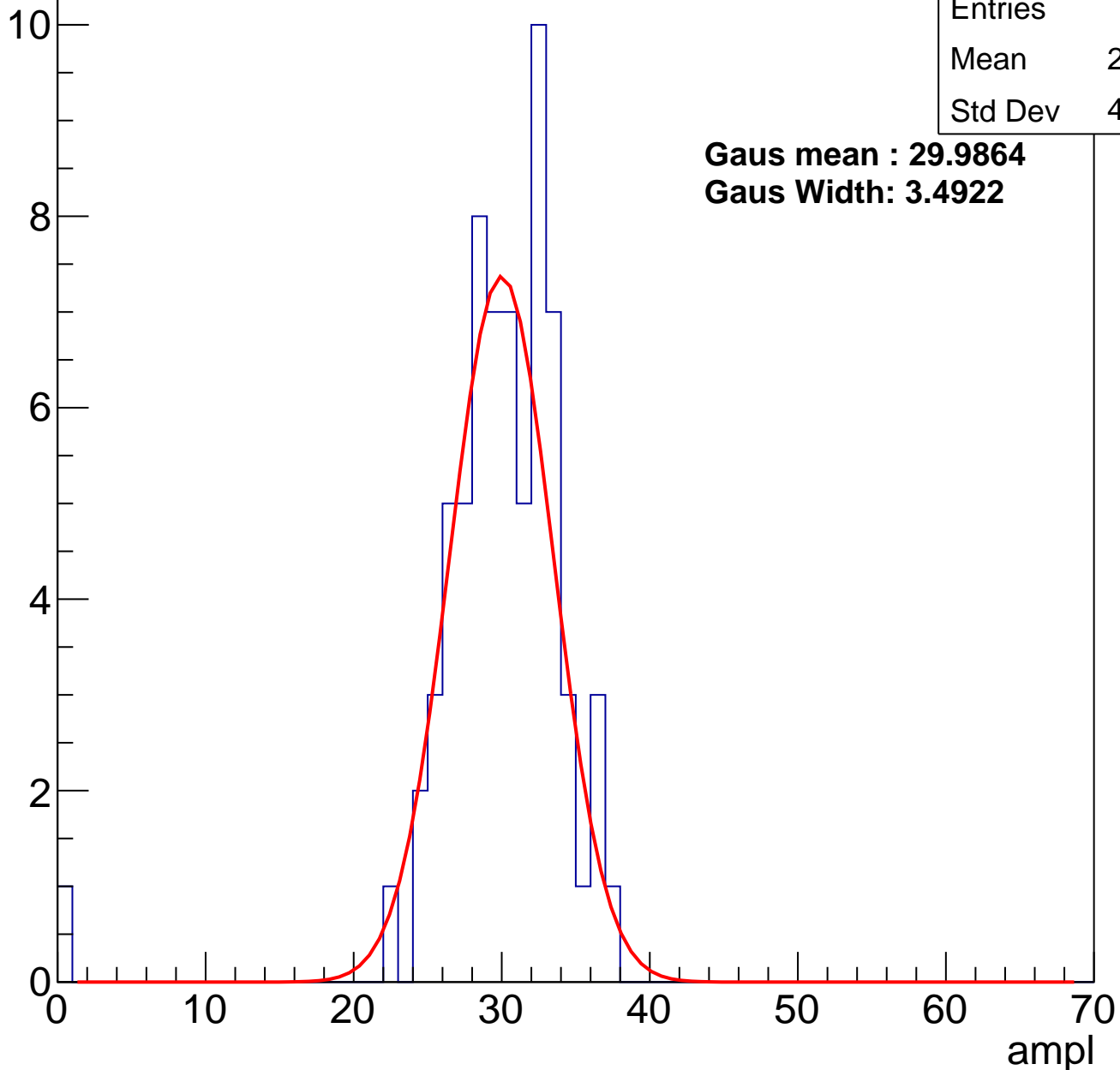
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 29.49 |
| Std Dev | 4.808 |

**Gaus mean : 29.9864**

**Gaus Width: 3.4922**

Entry



# B0L002S, U1-ch38, adc1

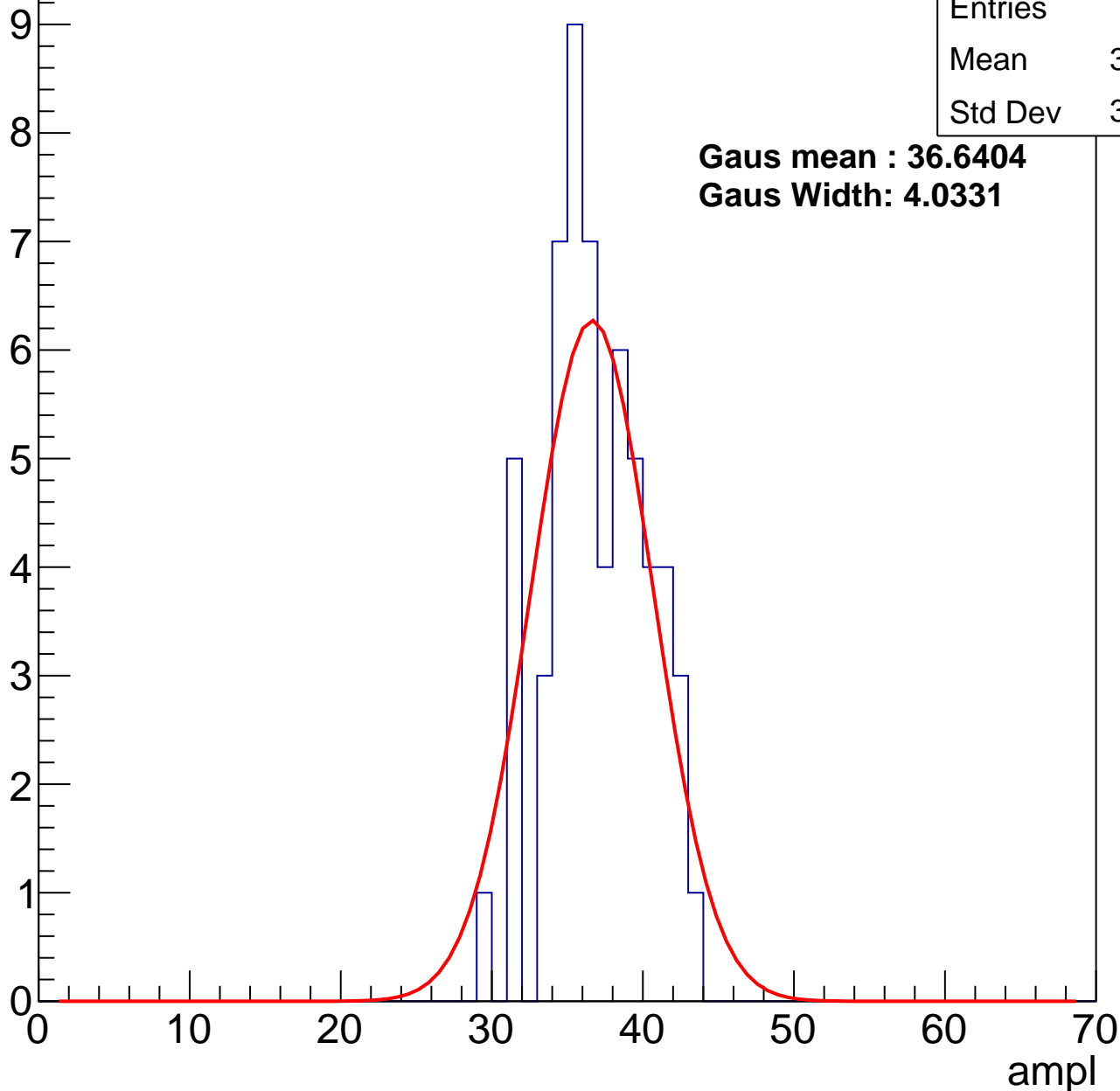
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 36.47 |
| Std Dev | 3.238 |

**Gaus mean : 36.6404**

**Gaus Width: 4.0331**



# B0L002S, U1-ch38, adc2

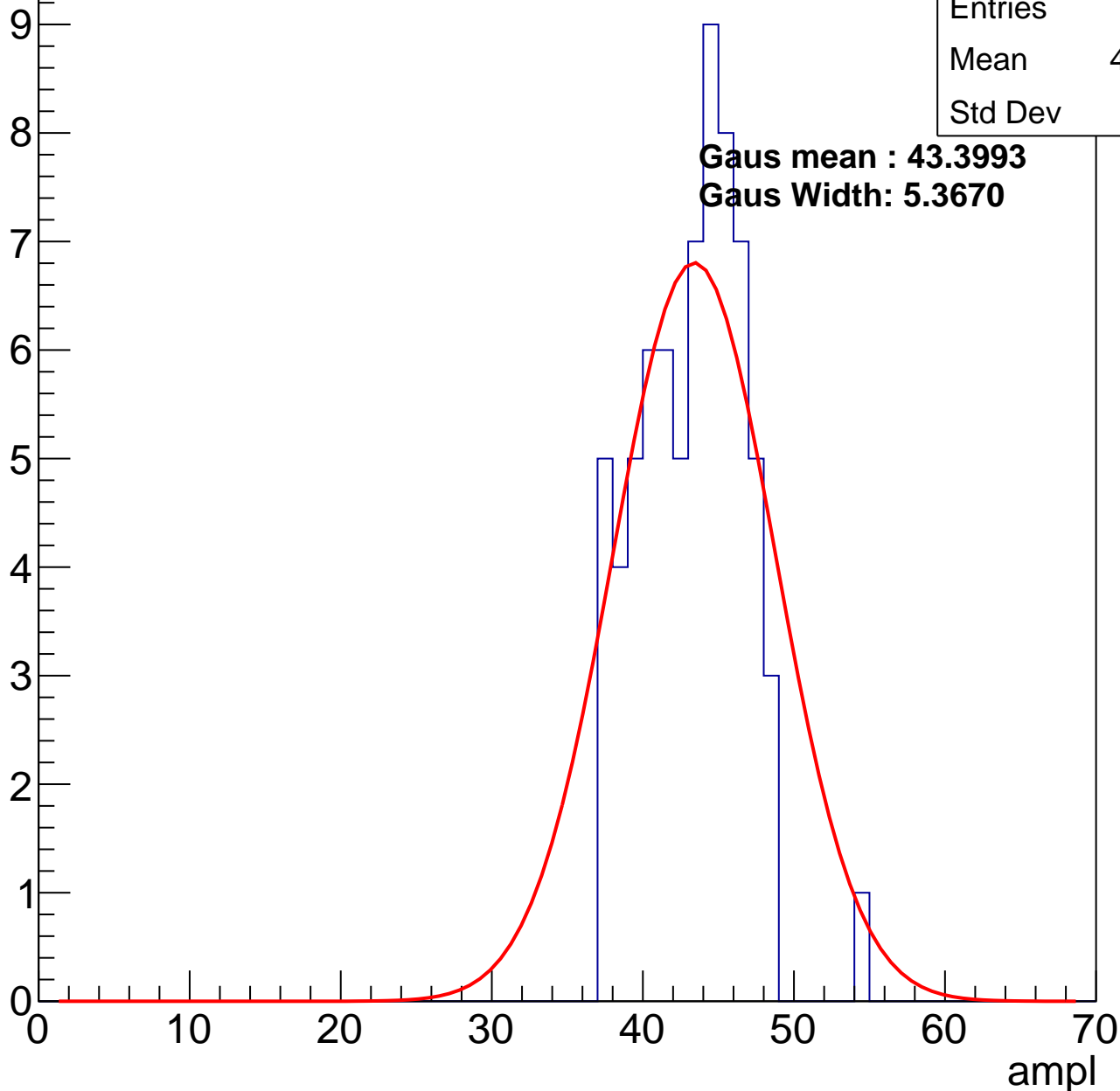
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 42.82 |
| Std Dev | 3.4   |

**Gaus mean : 43.3993**

**Gaus Width: 5.3670**



# B0L002S, U1-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

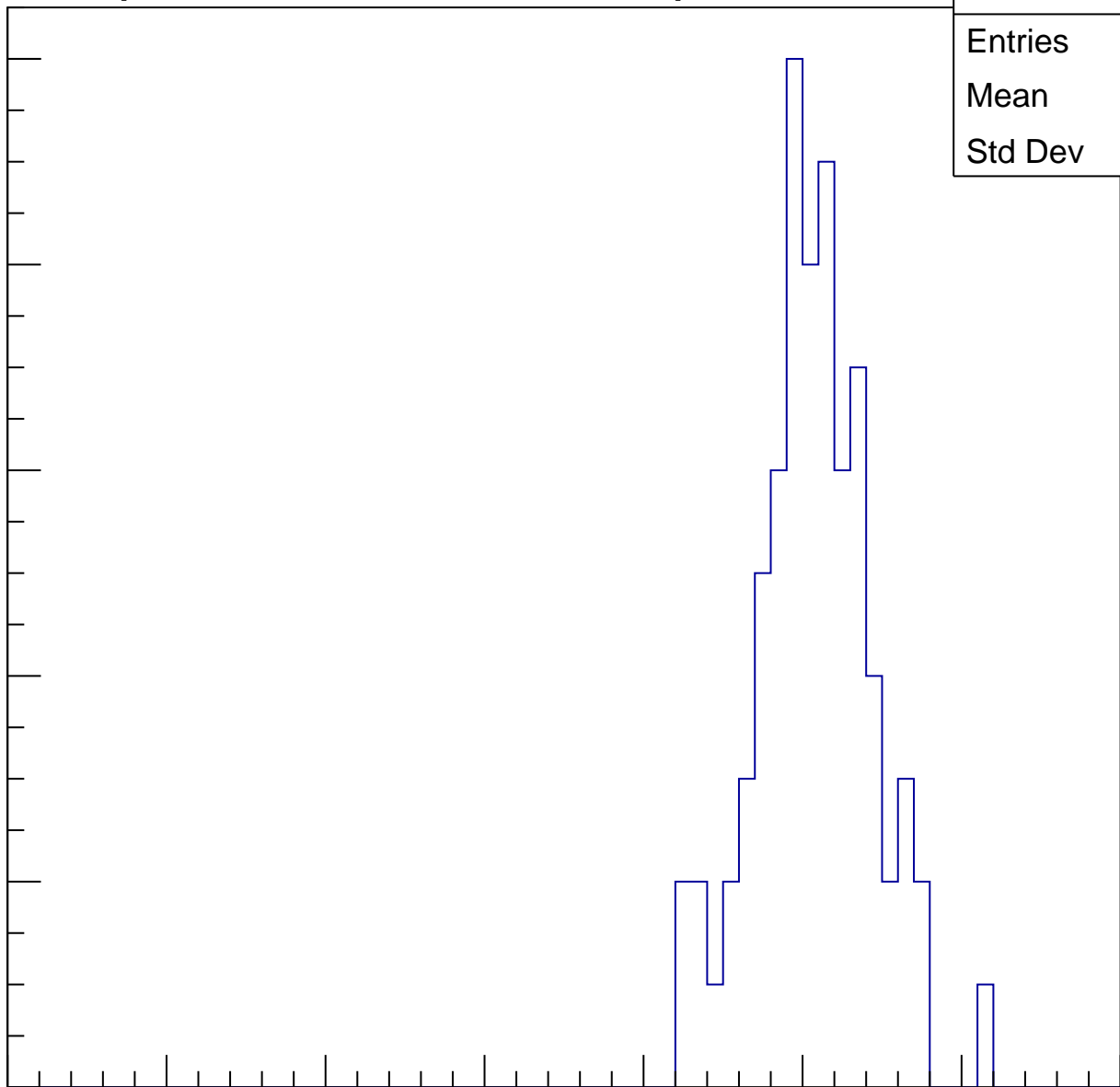
|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 50.22 |
| Std Dev | 3.65  |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

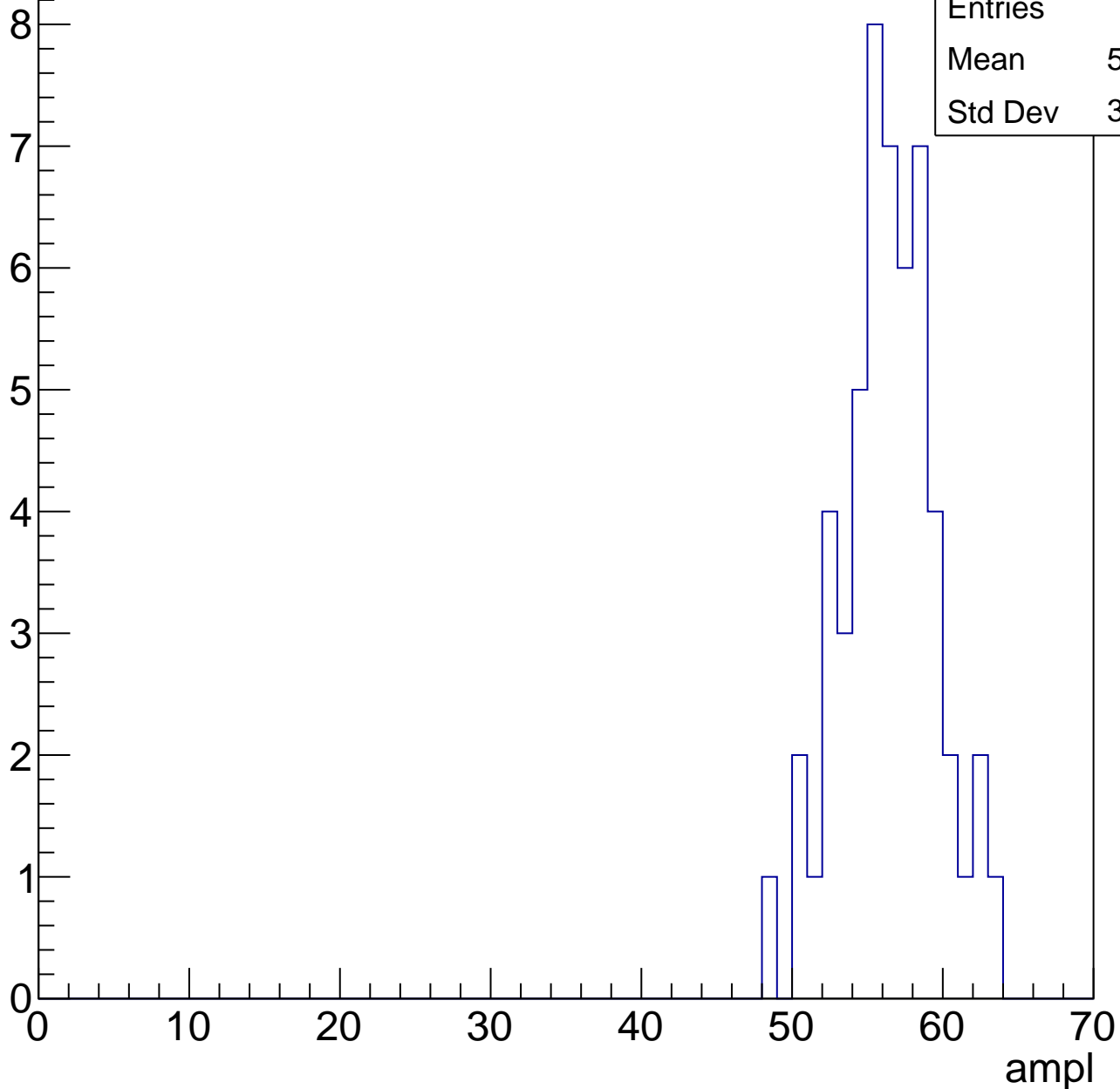
ampl



# B0L002S, U1-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

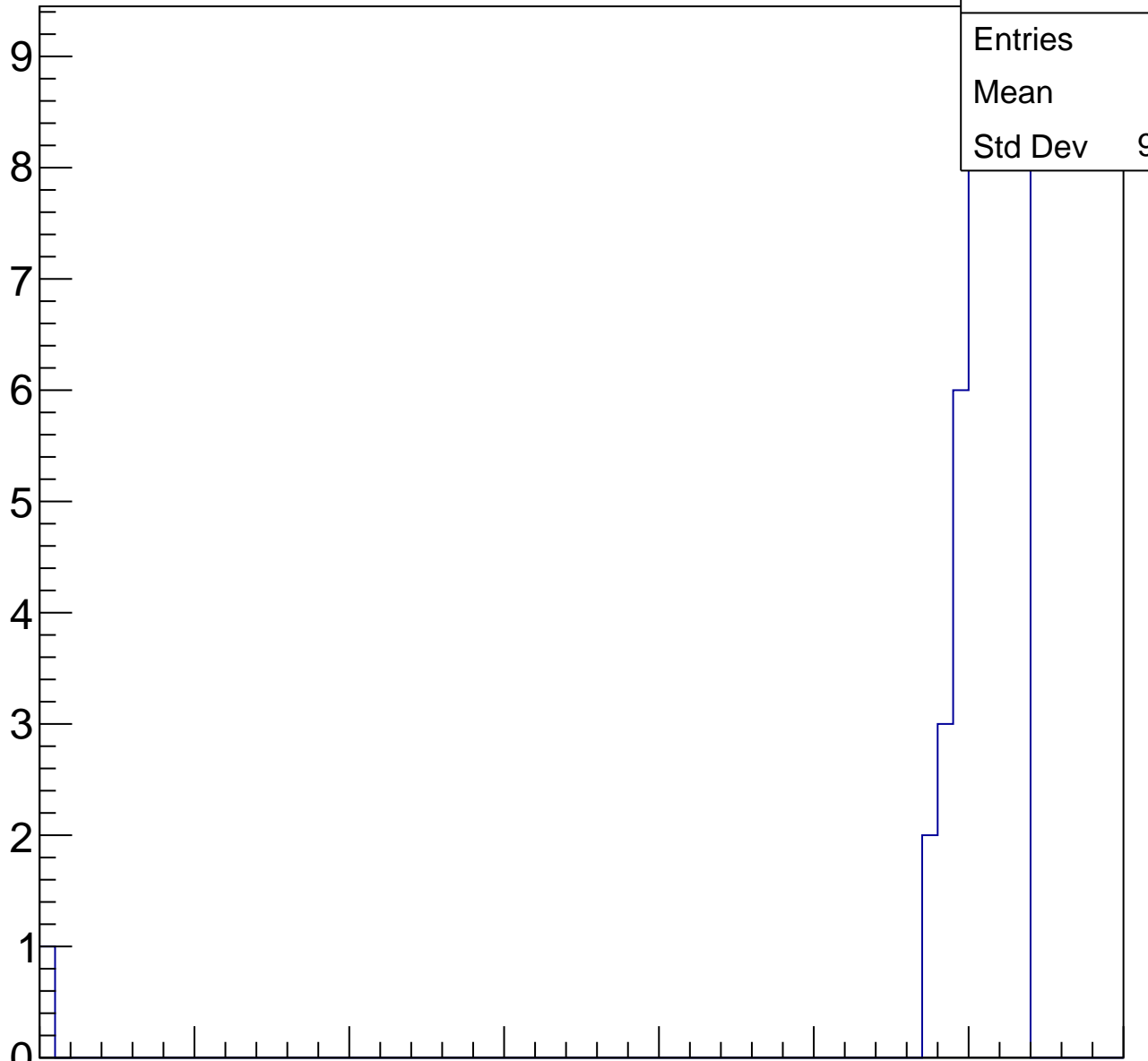
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 59.4  |
| Std Dev | 9.118 |

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch39, adc0

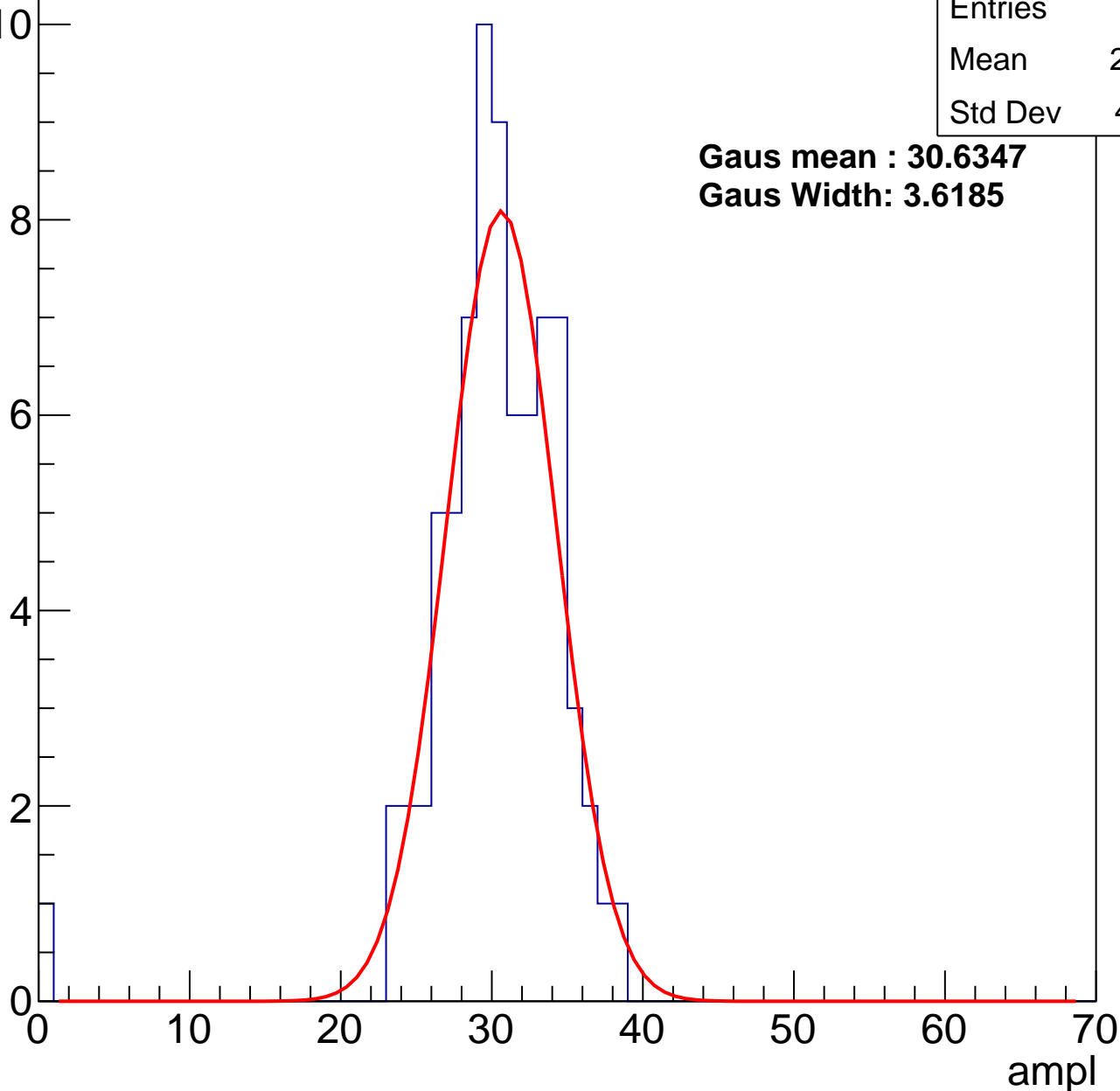
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 29.79 |
| Std Dev | 4.791 |

**Gaus mean : 30.6347**

**Gaus Width: 3.6185**



# B0L002S, U1-ch39, adc1

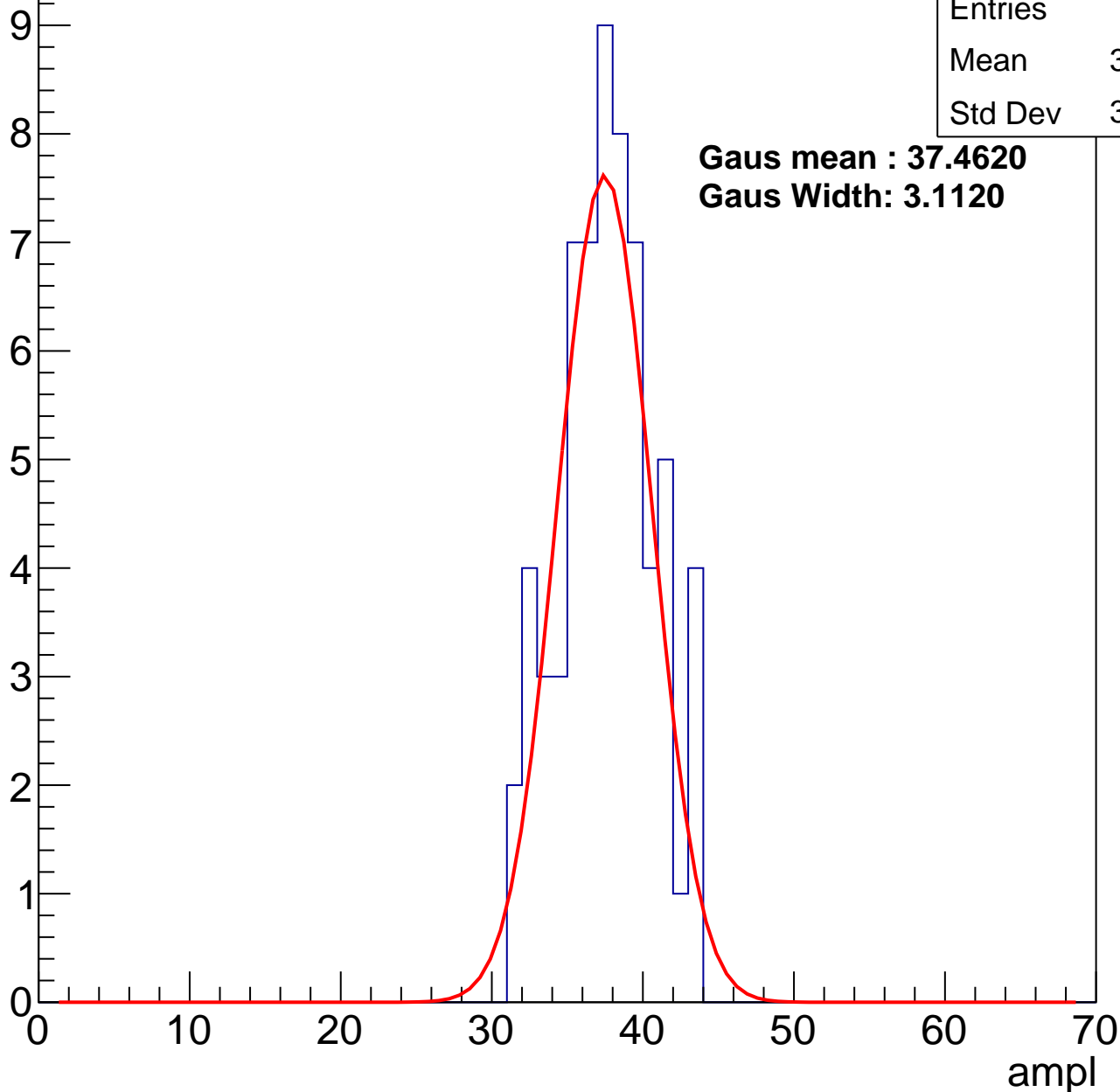
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 37.14 |
| Std Dev | 3.066 |

**Gaus mean : 37.4620**

**Gaus Width: 3.1120**



# B0L002S, U1-ch39, adc2

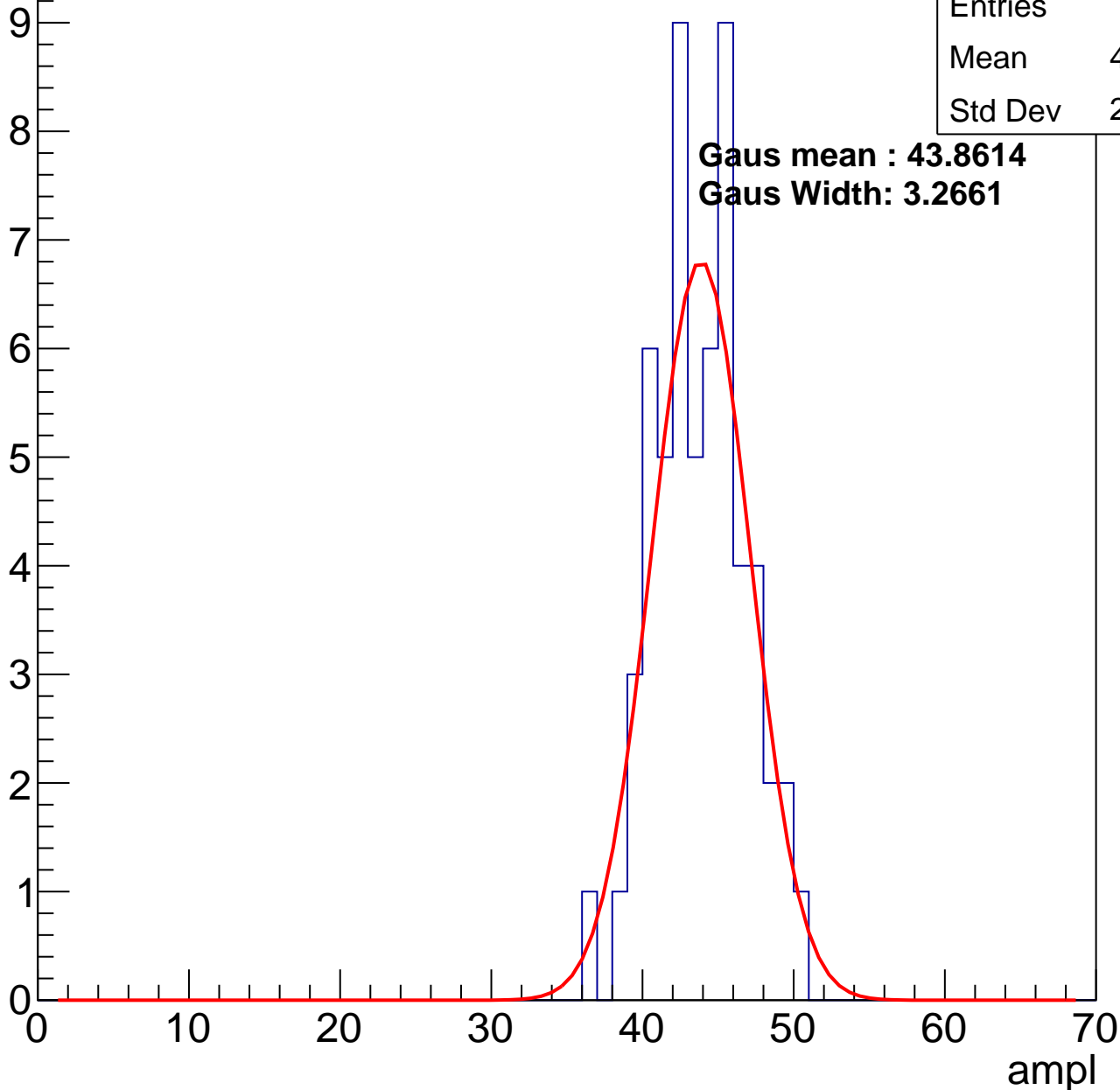
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 43.34 |
| Std Dev | 2.969 |

**Gaus mean : 43.8614**

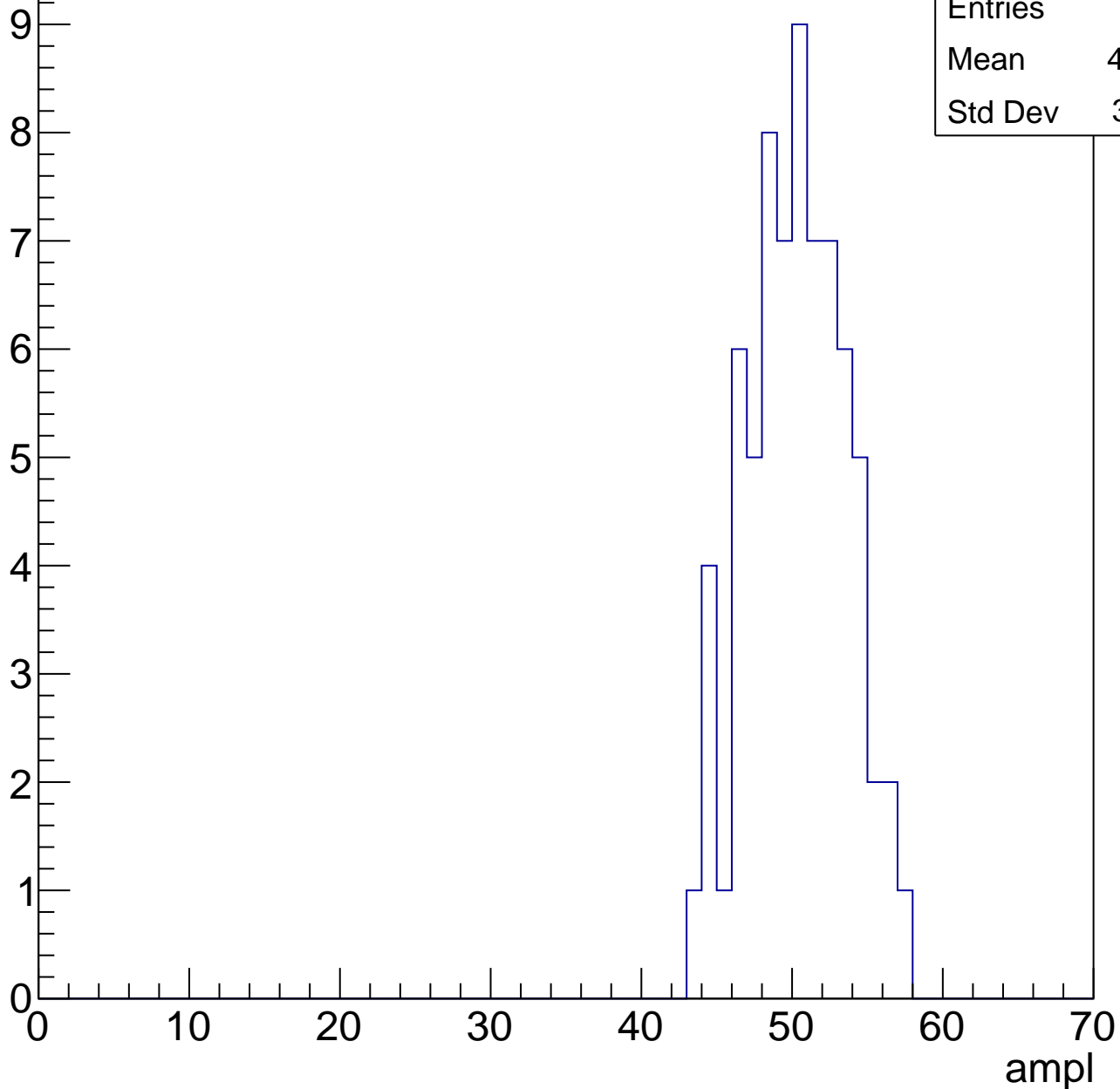
**Gaus Width: 3.2661**



# B0L002S, U1-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

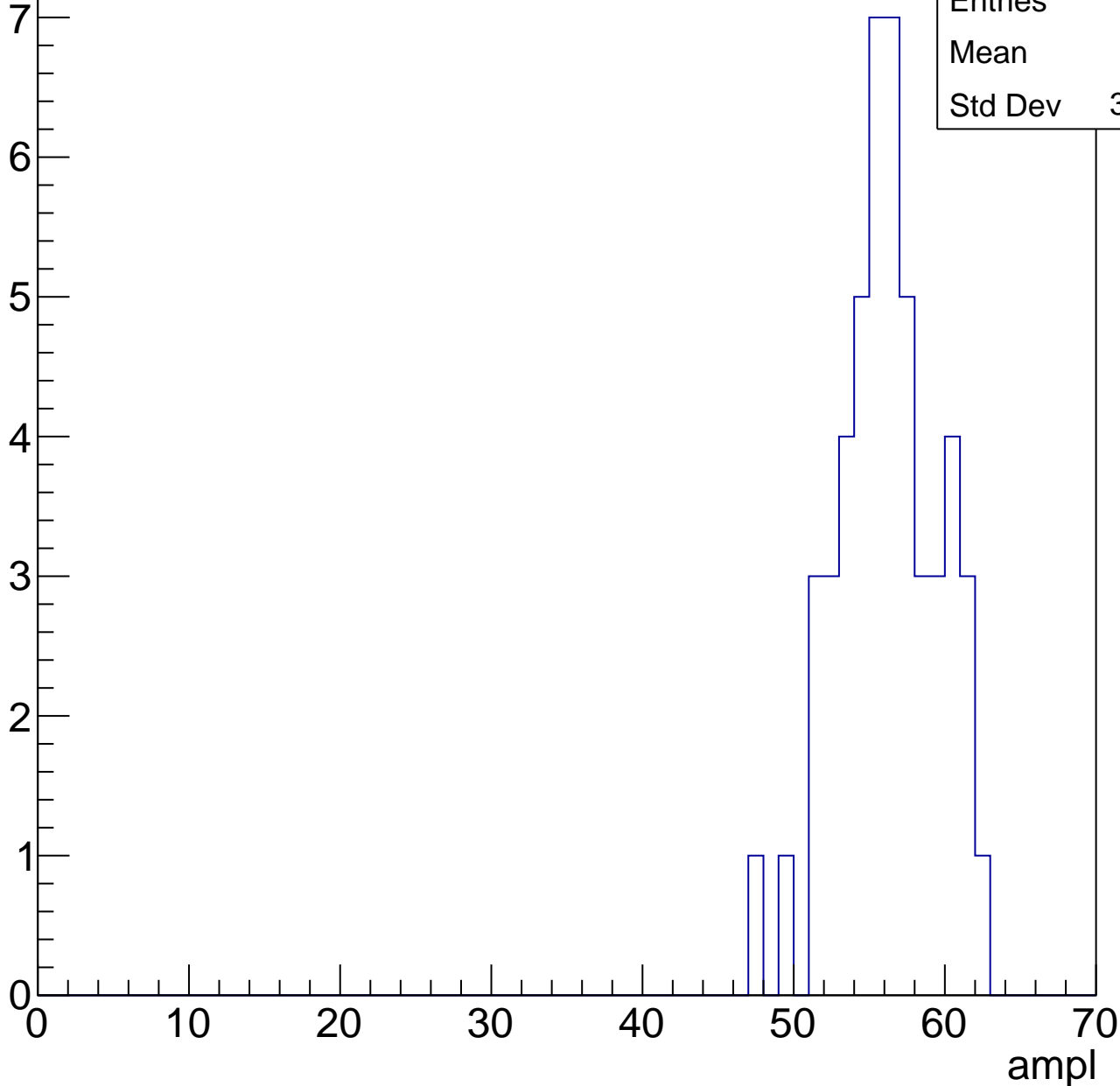


# B0L002S, U1-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 55.7  |
| Std Dev | 3.257 |

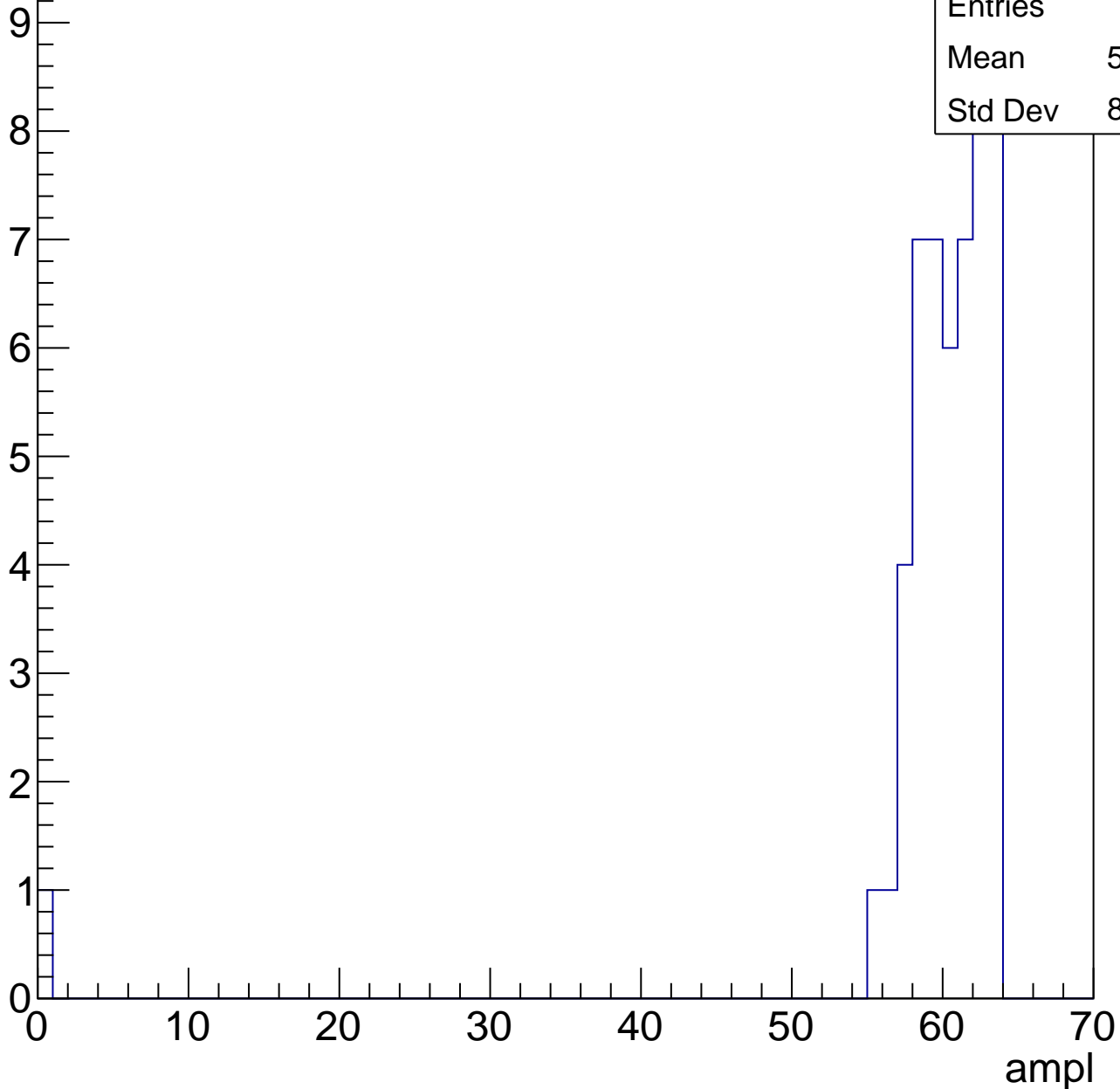


# B0L002S, U1-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 59.04 |
| Std Dev | 8.535 |



# B0L002S, U1-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 22 |
| Std Dev | 0  |

# B0L002S, U1-ch40, adc0

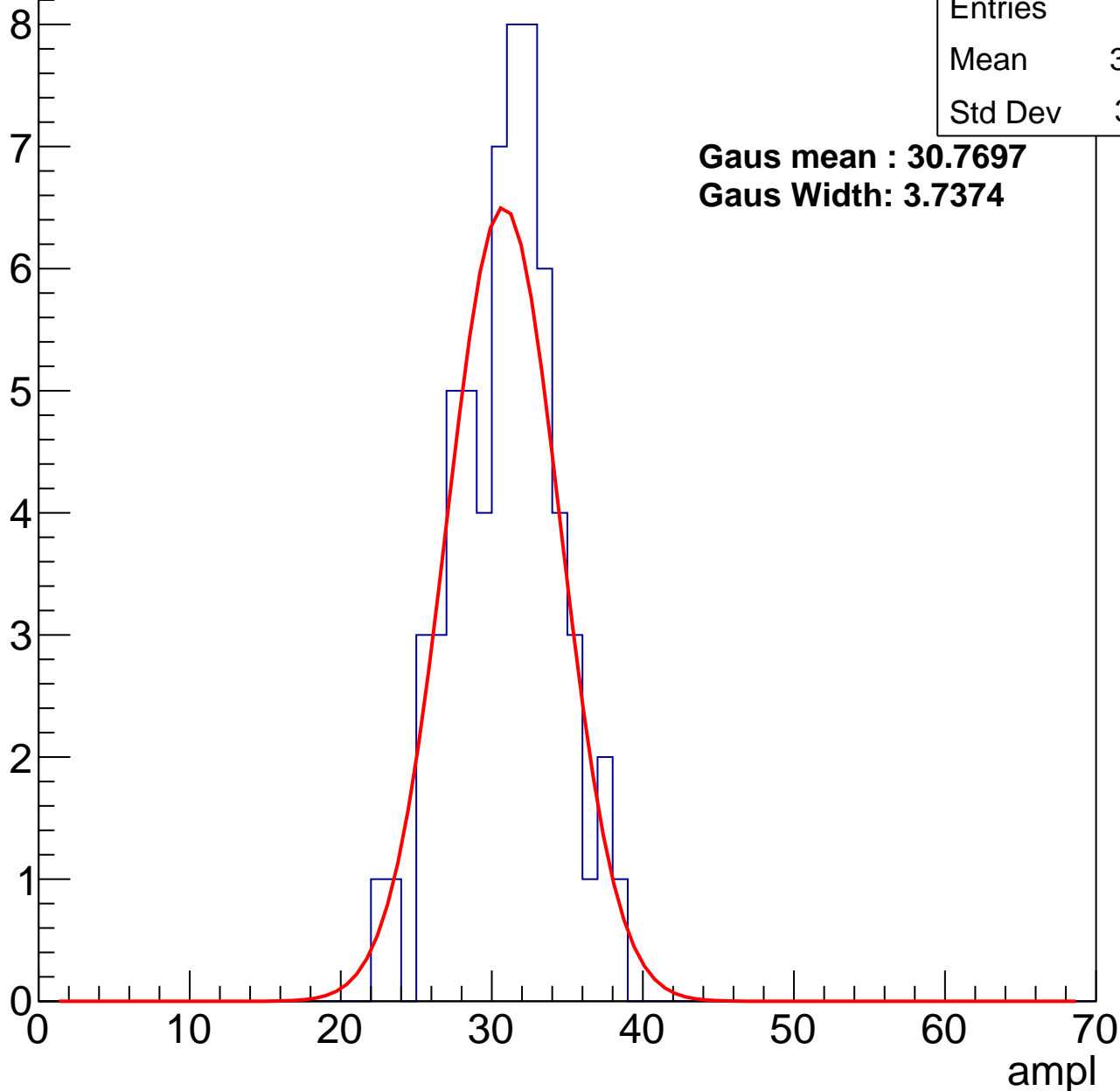
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 30.48 |
| Std Dev | 3.411 |

**Gaus mean : 30.7697**

**Gaus Width: 3.7374**



# B0L002S, U1-ch40, adc1

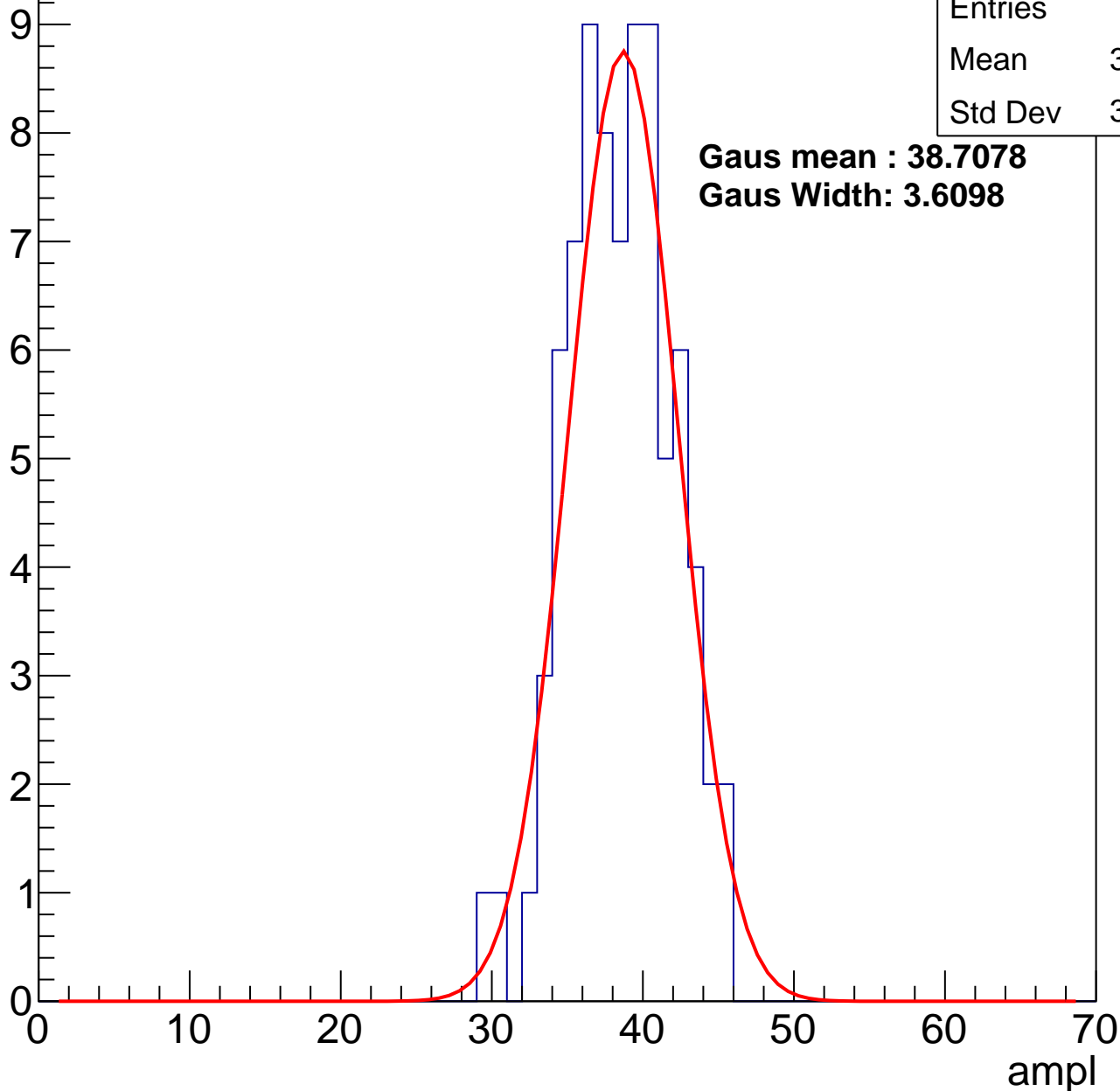
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 38.04 |
| Std Dev | 3.382 |

**Gaus mean : 38.7078**

**Gaus Width: 3.6098**

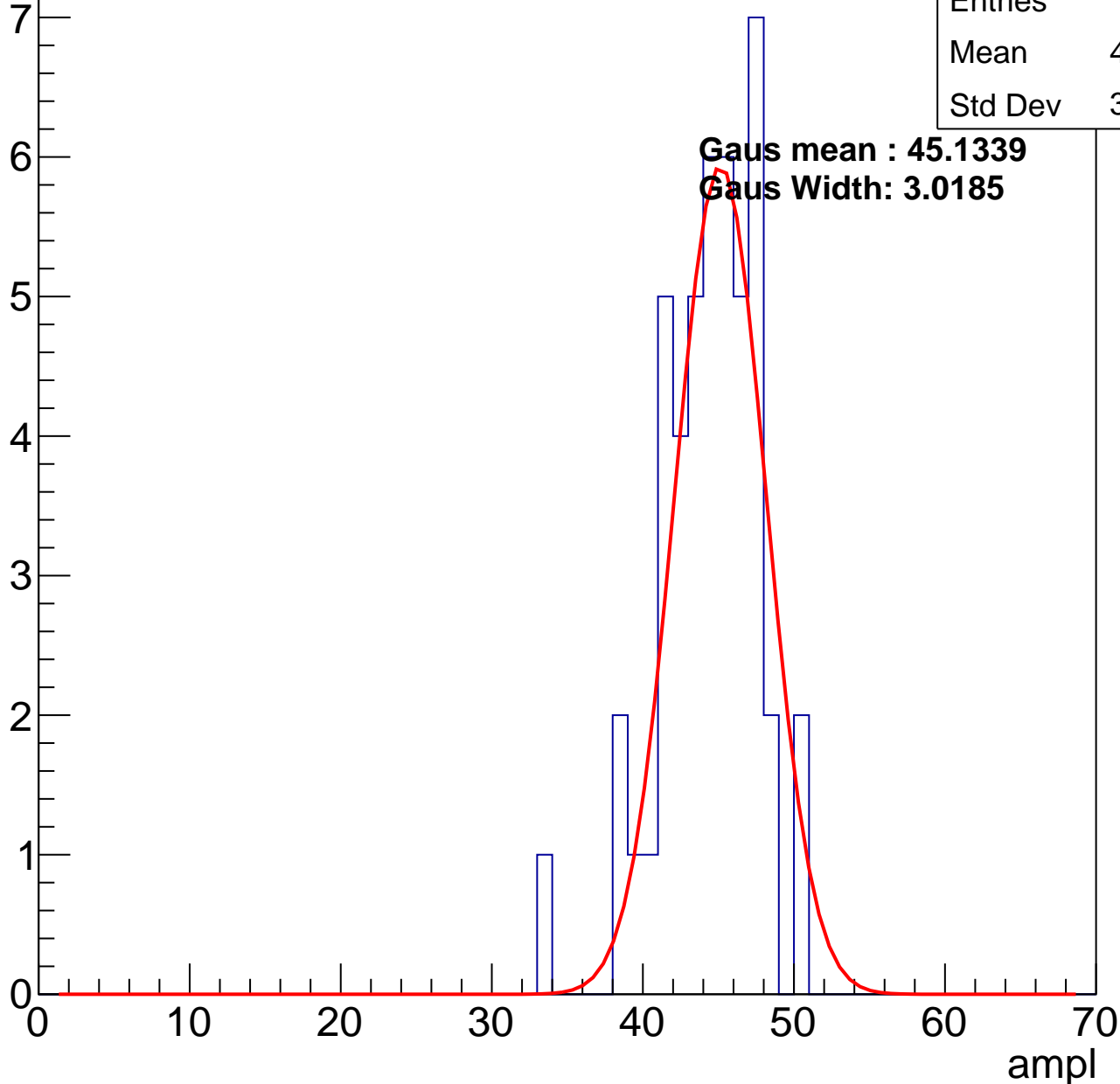


# B0L002S, U1-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 43.94 |
| Std Dev | 3.238 |

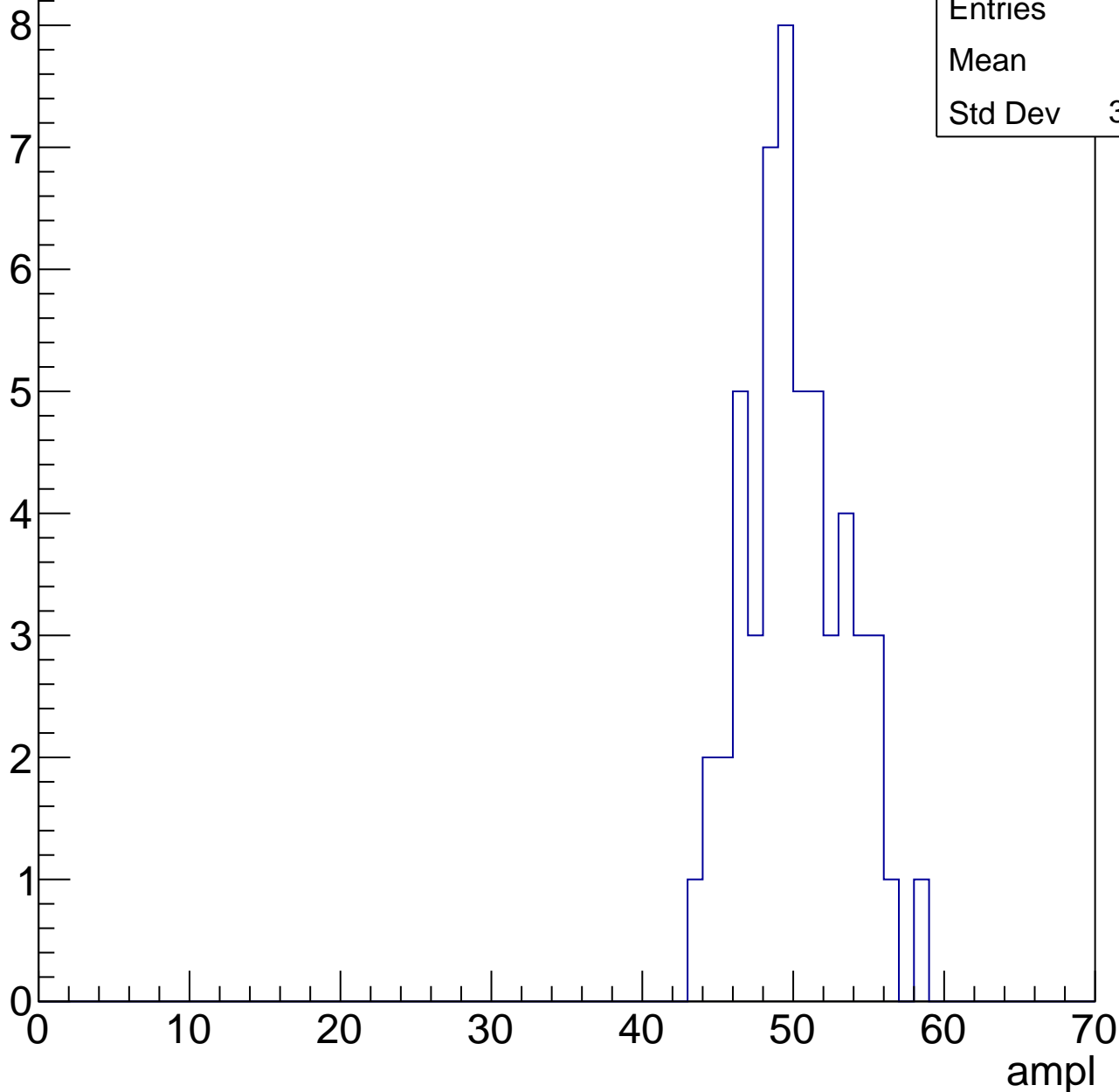


# B0L002S, U1-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 49.7  |
| Std Dev | 3.323 |

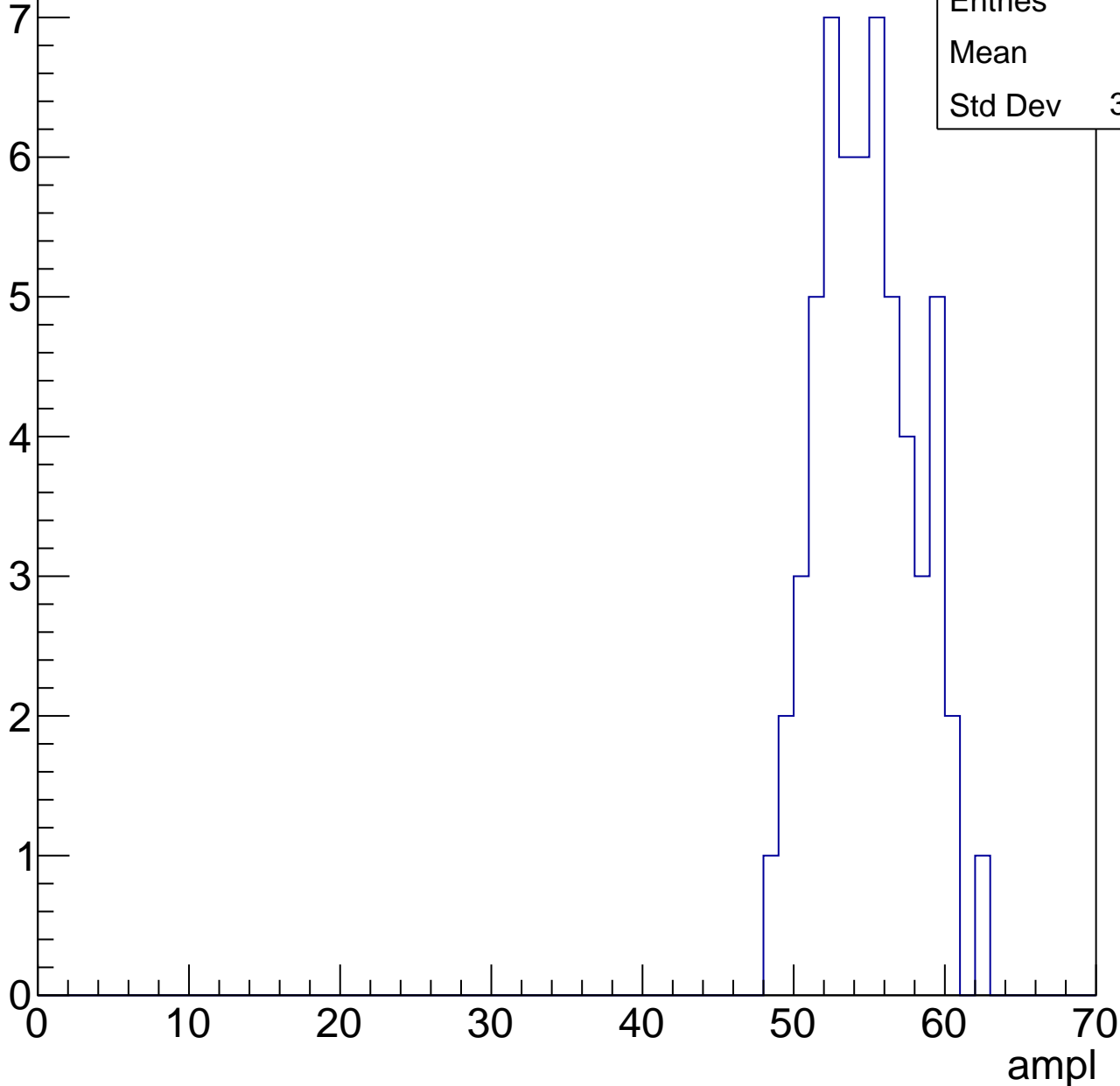


# B0L002S, U1-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 54.4  |
| Std Dev | 3.178 |

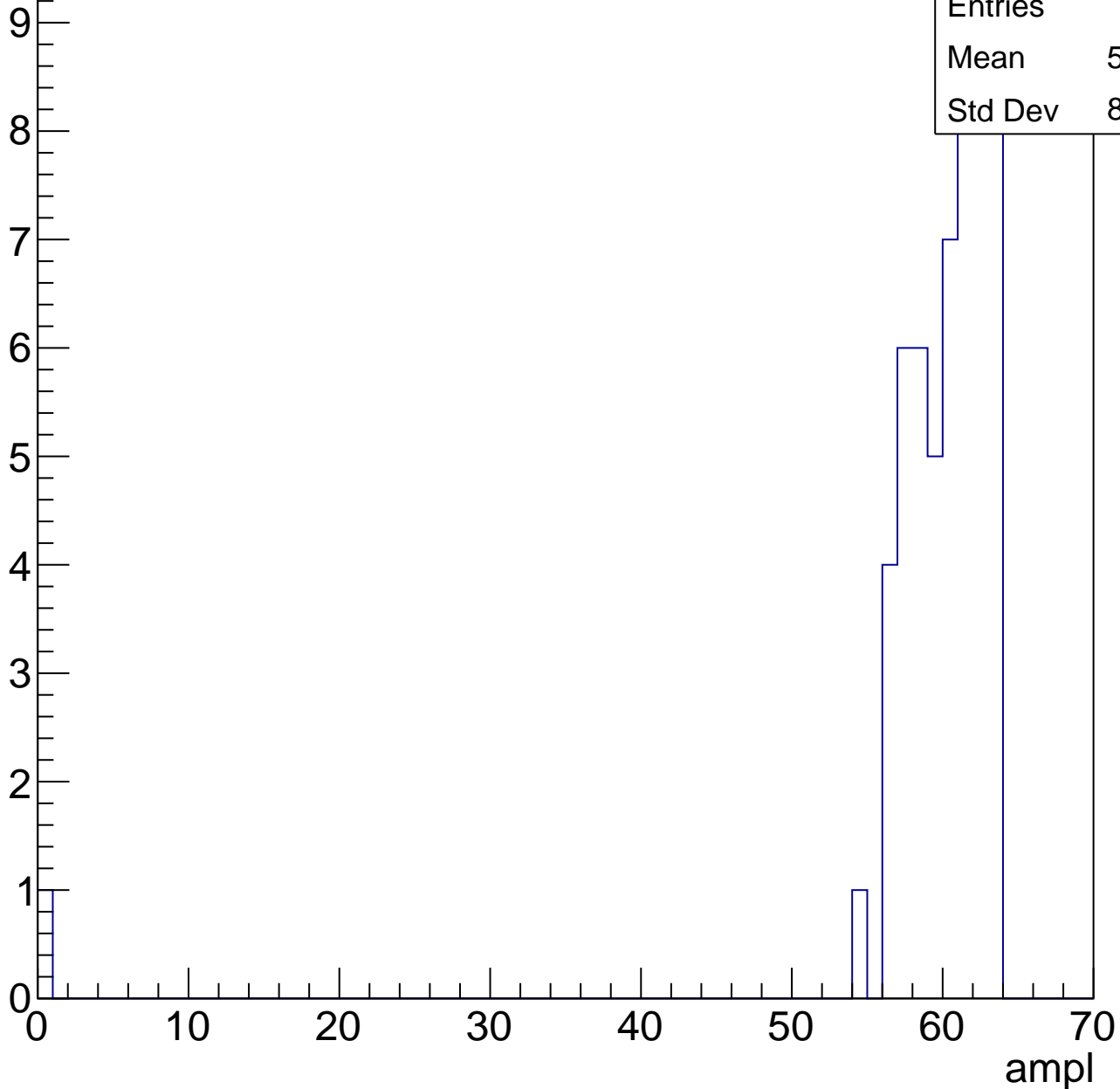


# B0L002S, U1-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 58.82 |
| Std Dev | 8.262 |



# B0L002S, U1-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch41, adc0

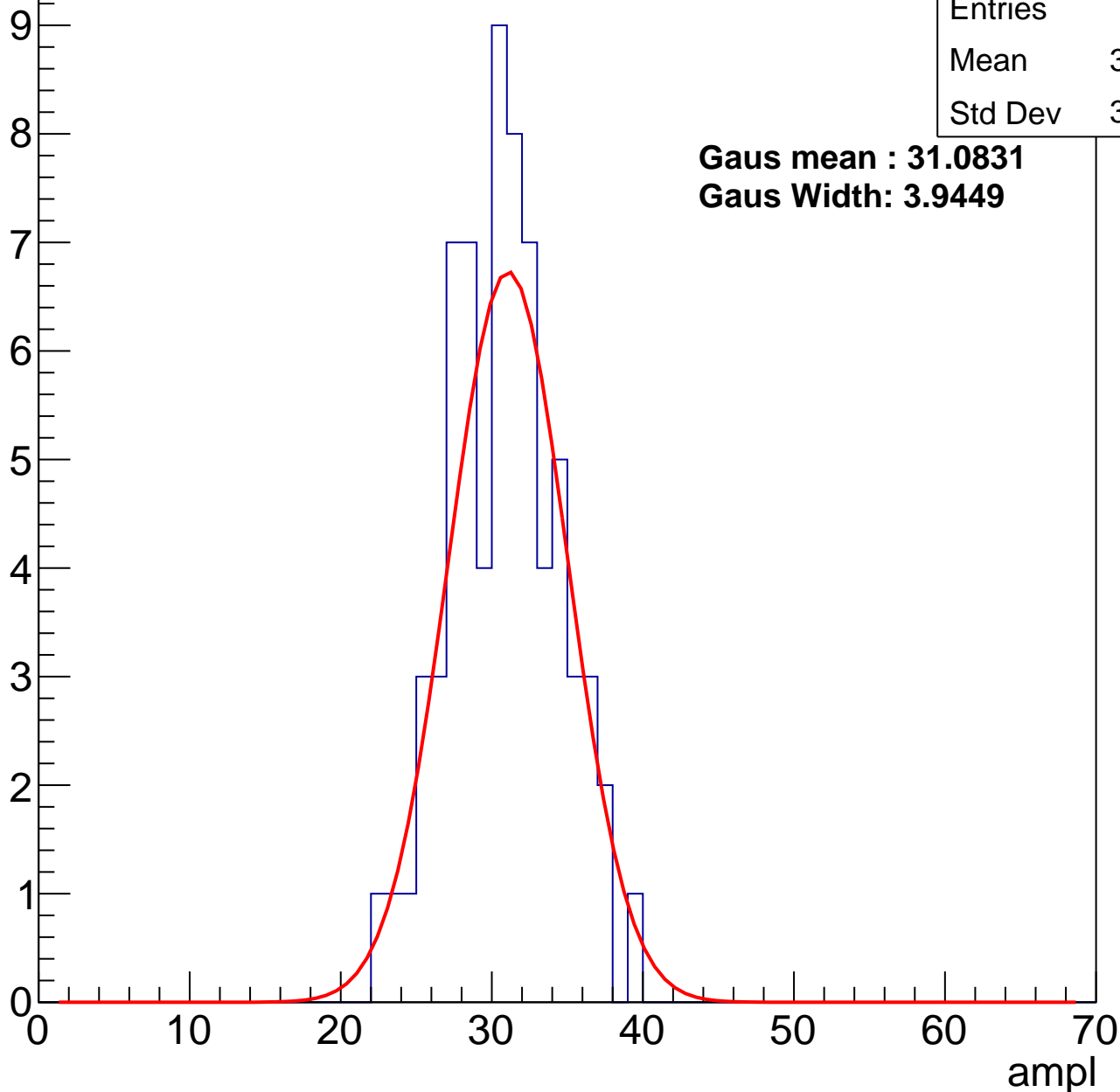
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 30.33 |
| Std Dev | 3.558 |

**Gaus mean : 31.0831**

**Gaus Width: 3.9449**



# B0L002S, U1-ch41, adc1

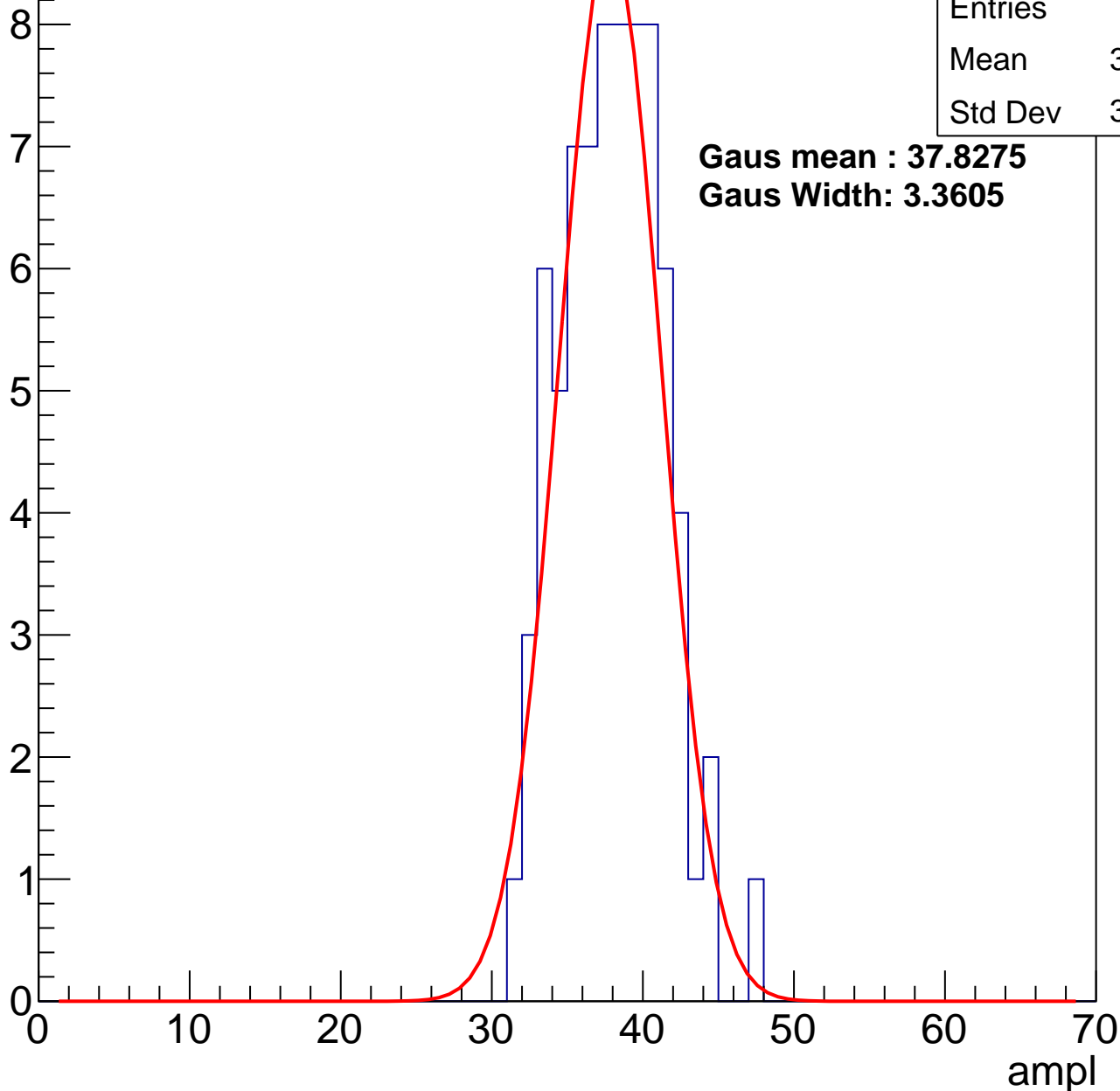
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 37.55 |
| Std Dev | 3.279 |

**Gaus mean : 37.8275**

**Gaus Width: 3.3605**



# B0L002S, U1-ch41, adc2

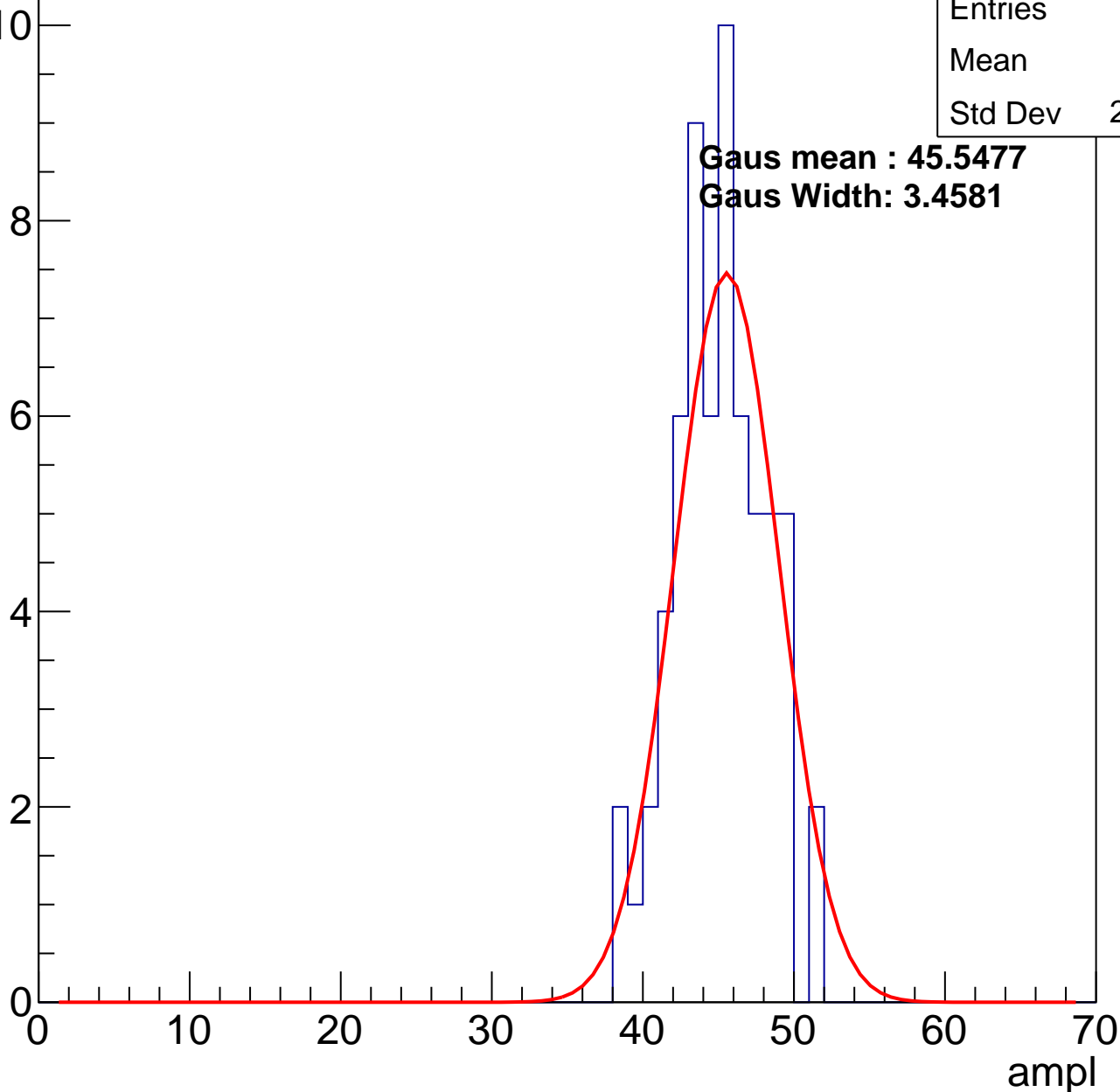
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 44.6  |
| Std Dev | 2.974 |

**Gaus mean : 45.5477**

**Gaus Width: 3.4581**

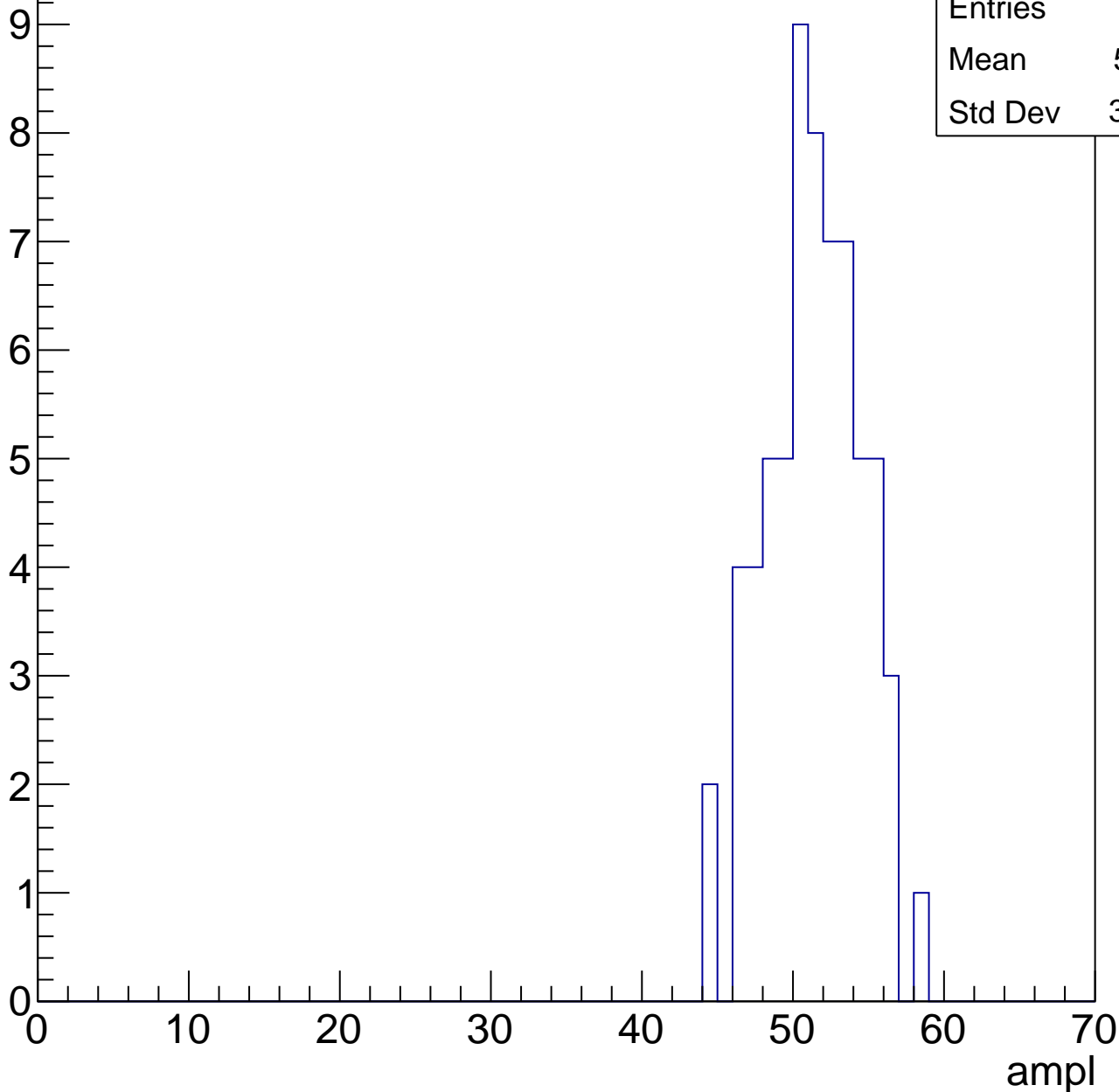


# B0L002S, U1-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 50.91 |
| Std Dev | 3.087 |



# B0L002S, U1-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

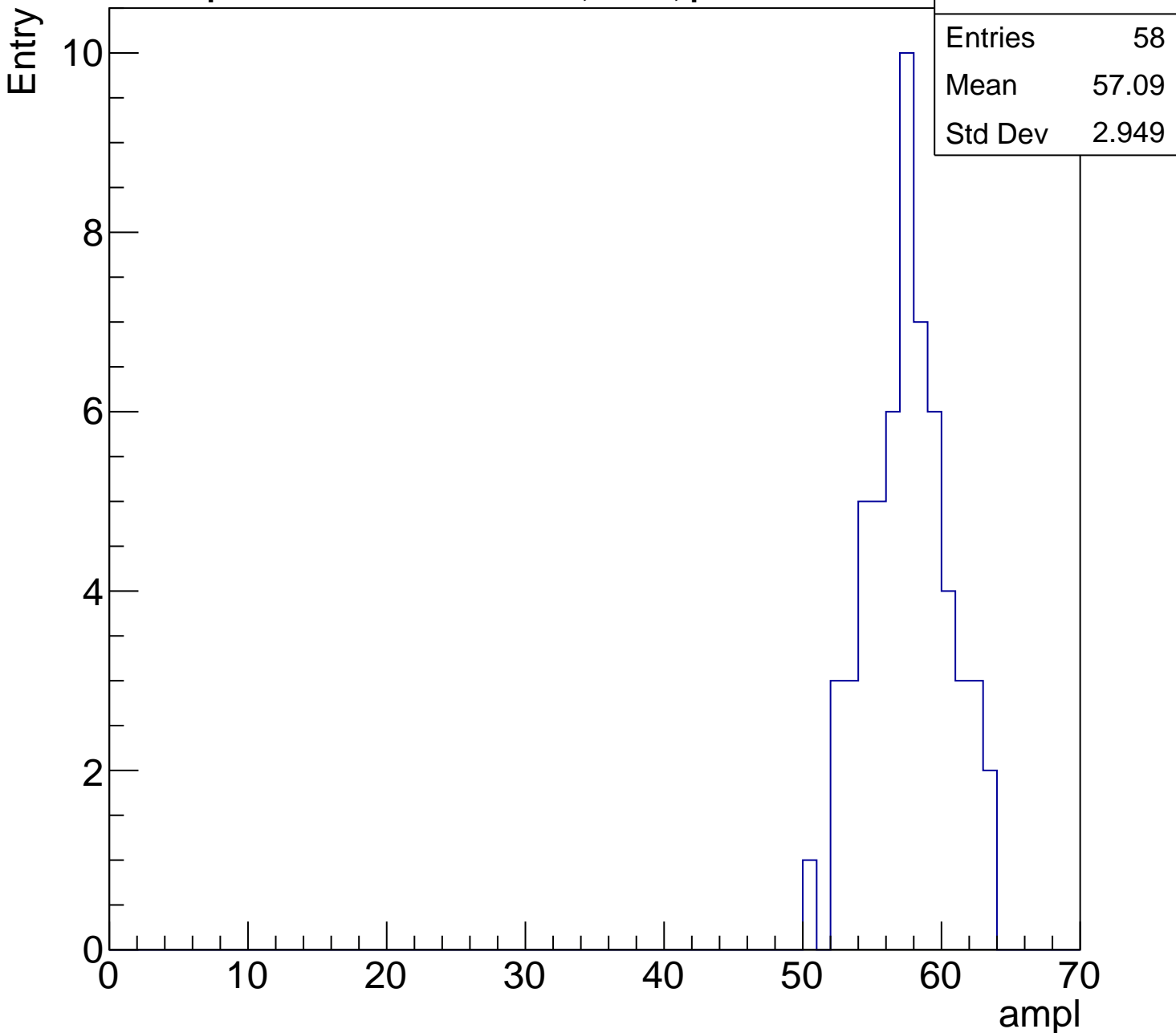
|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 57.09 |
| Std Dev | 2.949 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

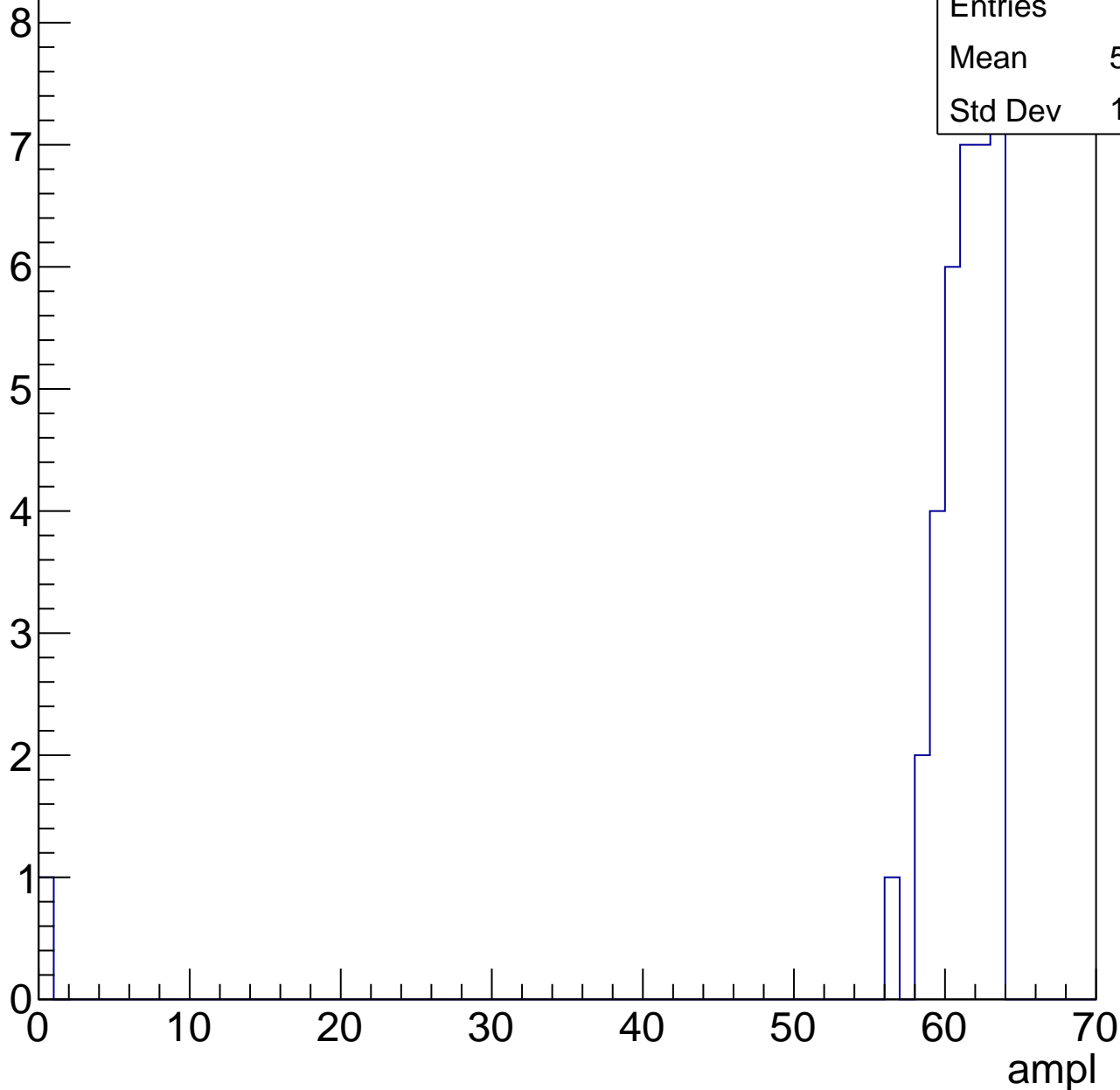


# B0L002S, U1-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

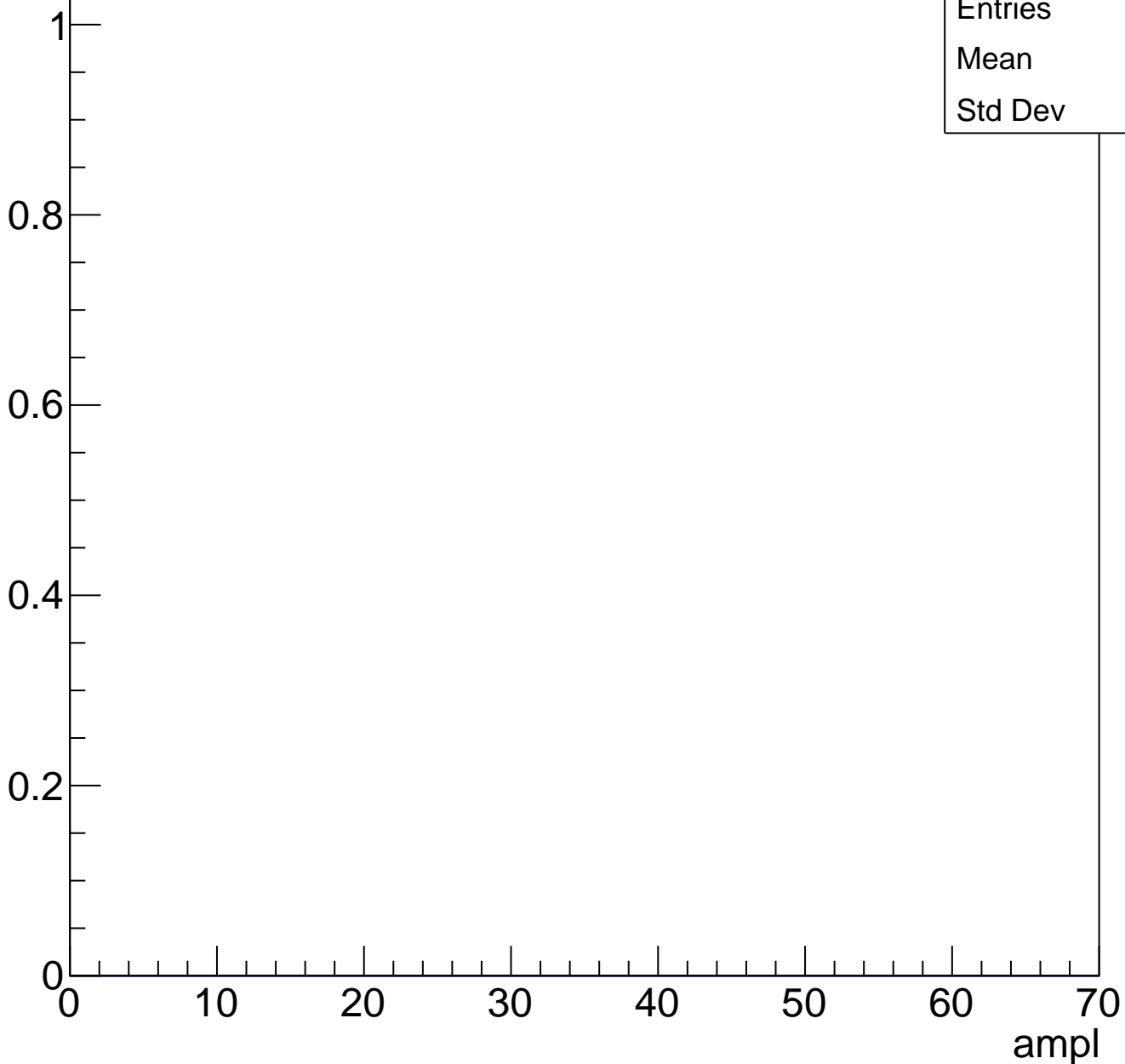
|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 59.25 |
| Std Dev | 10.16 |



# B0L002S, U1-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch42, adc0

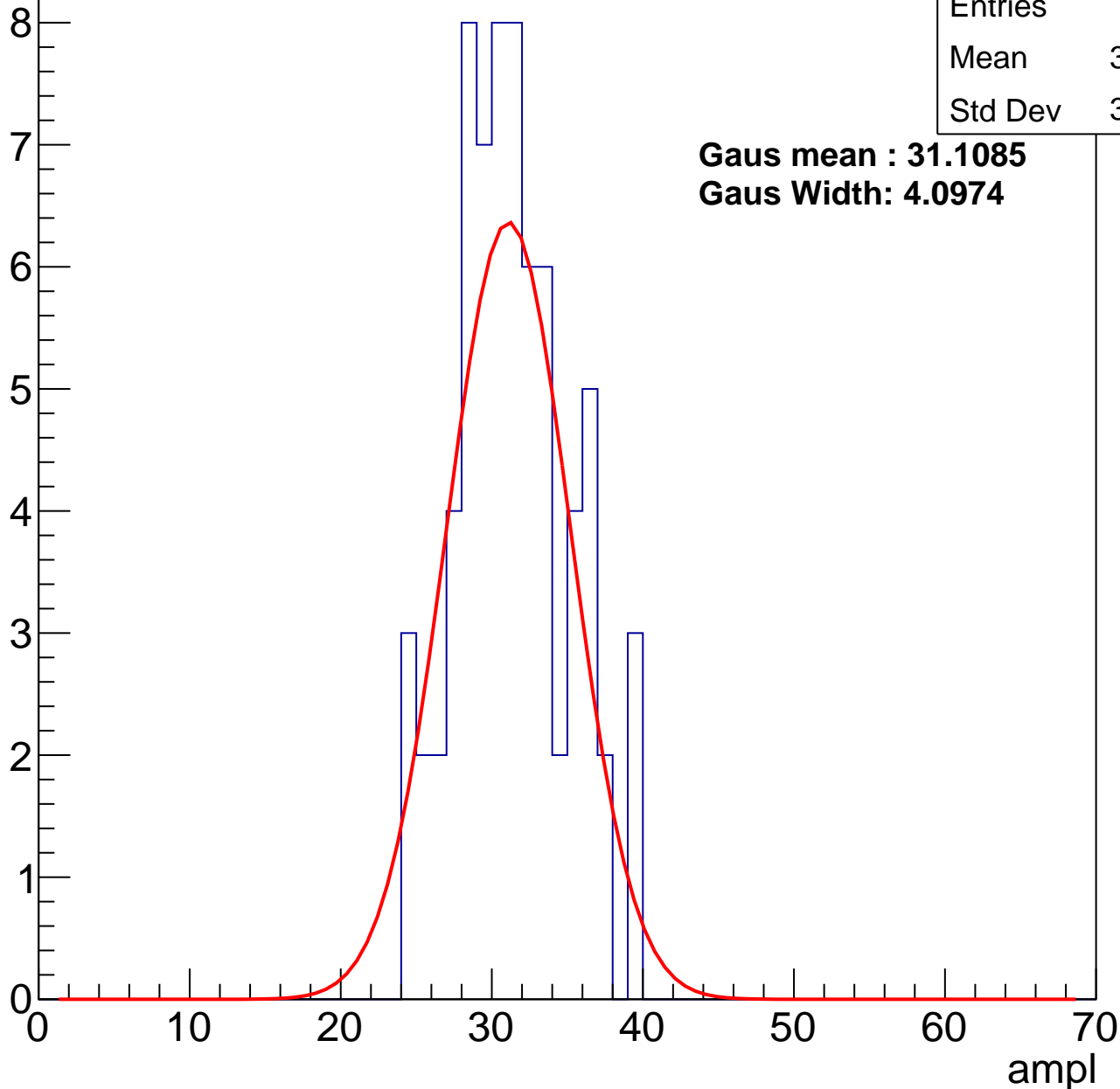
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 30.94 |
| Std Dev | 3.668 |

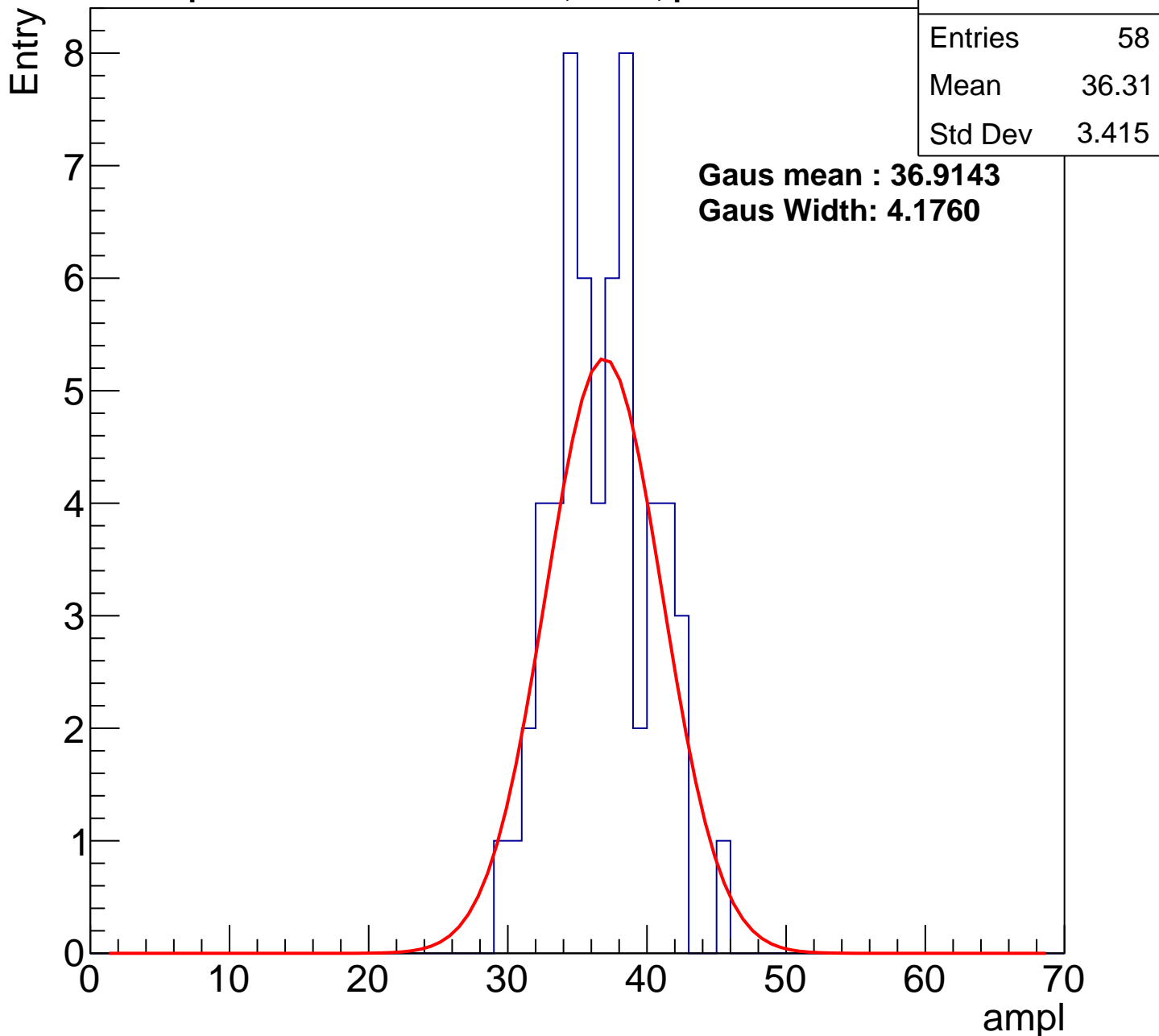
**Gaus mean : 31.1085**

**Gaus Width: 4.0974**



# B0L002S, U1-ch42, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch42, adc2

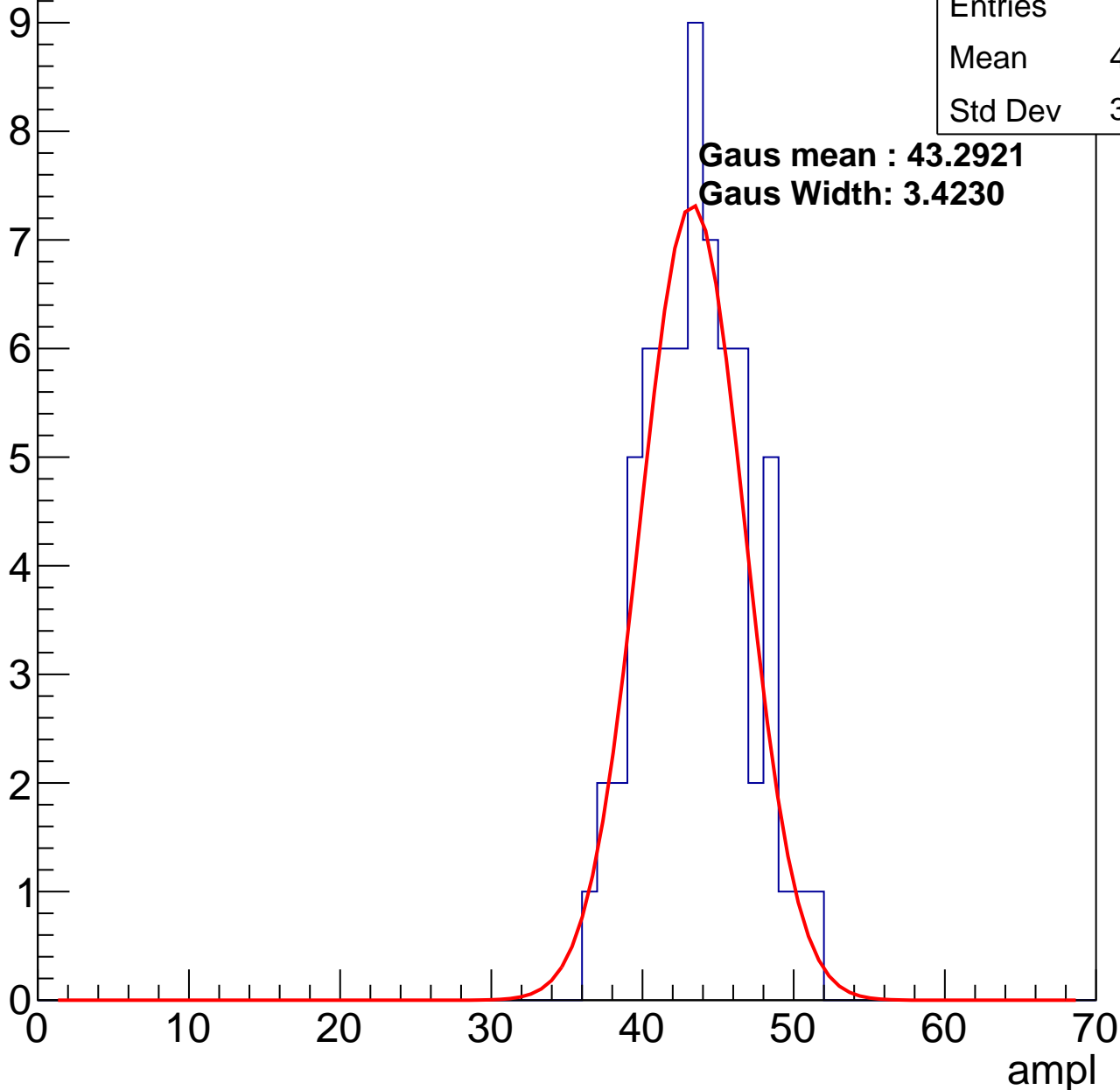
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 43.09 |
| Std Dev | 3.315 |

**Gaus mean : 43.2921**

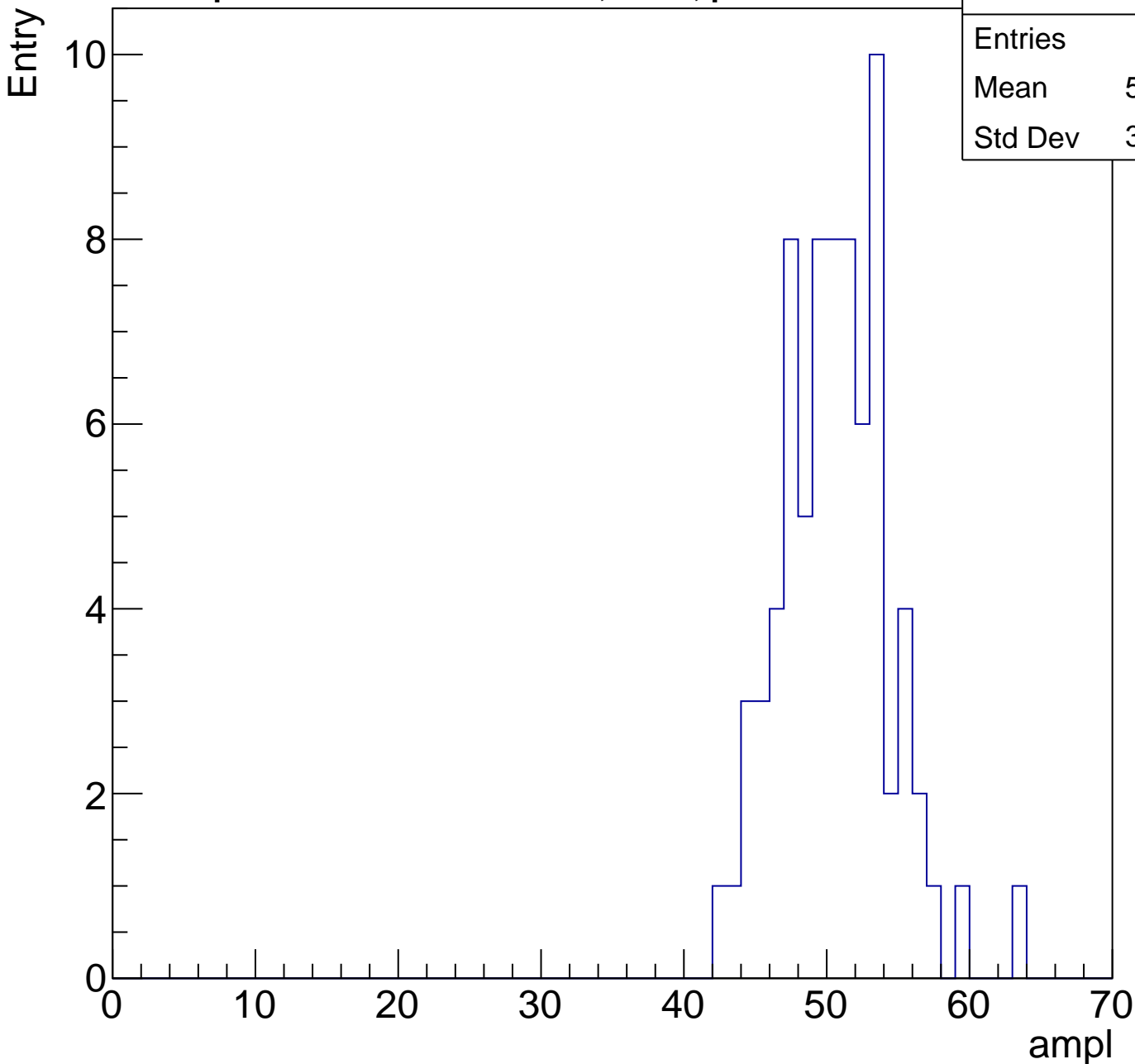
**Gaus Width: 3.4230**



# B0L002S, U1-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

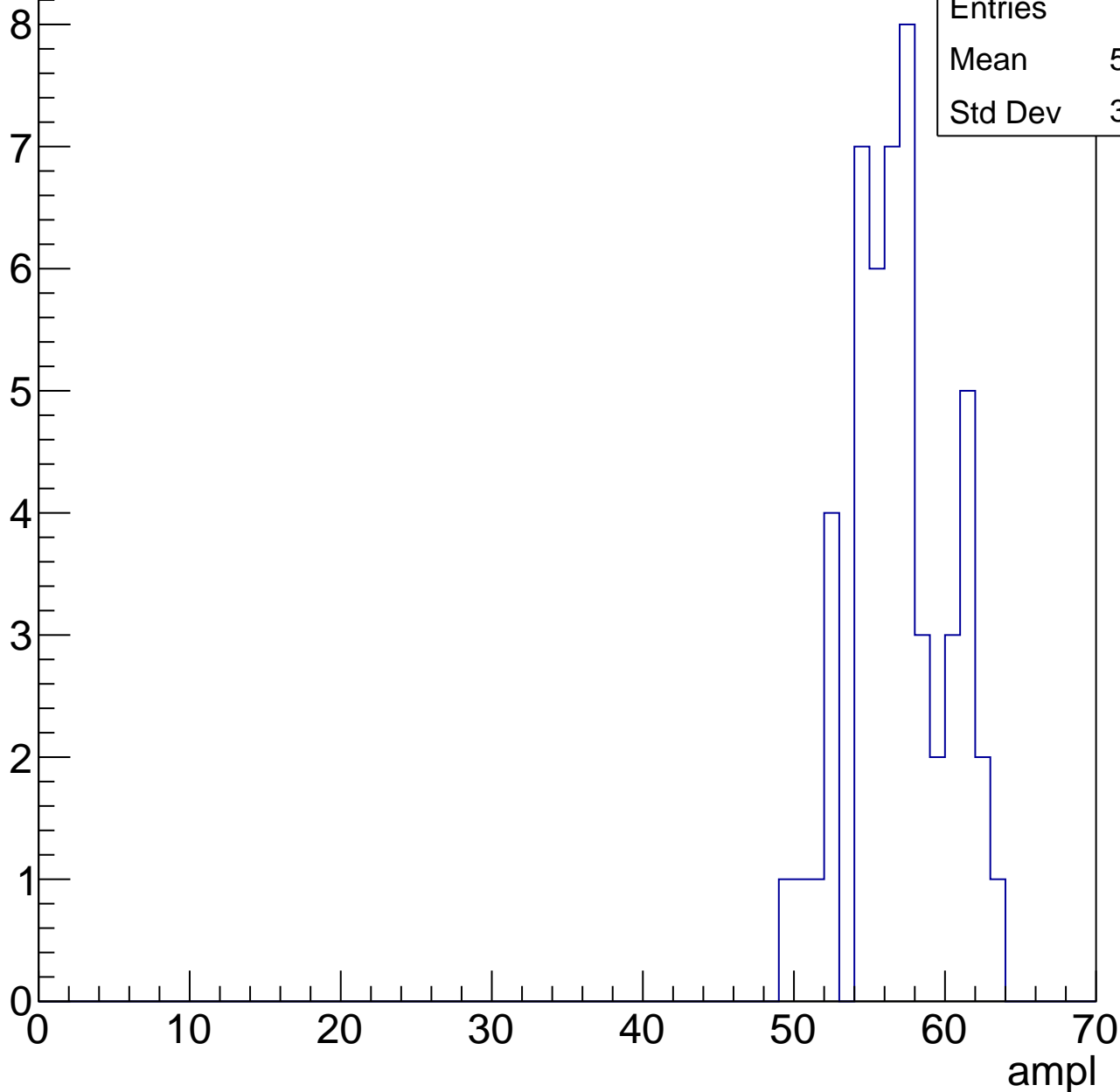
|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 50.17 |
| Std Dev | 3.775 |



# B0L002S, U1-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

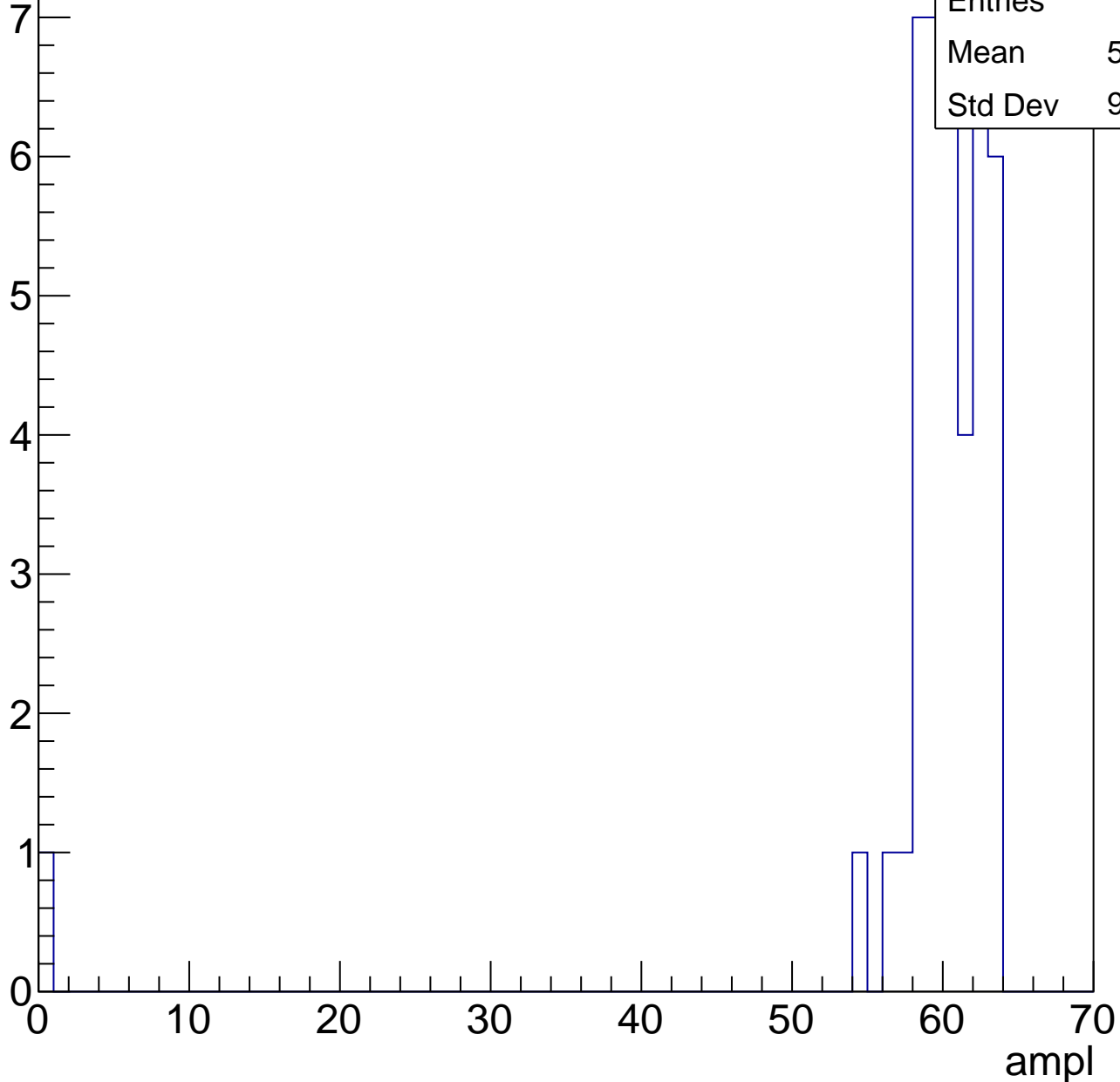
Entry



# B0L002S, U1-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch43, adc0

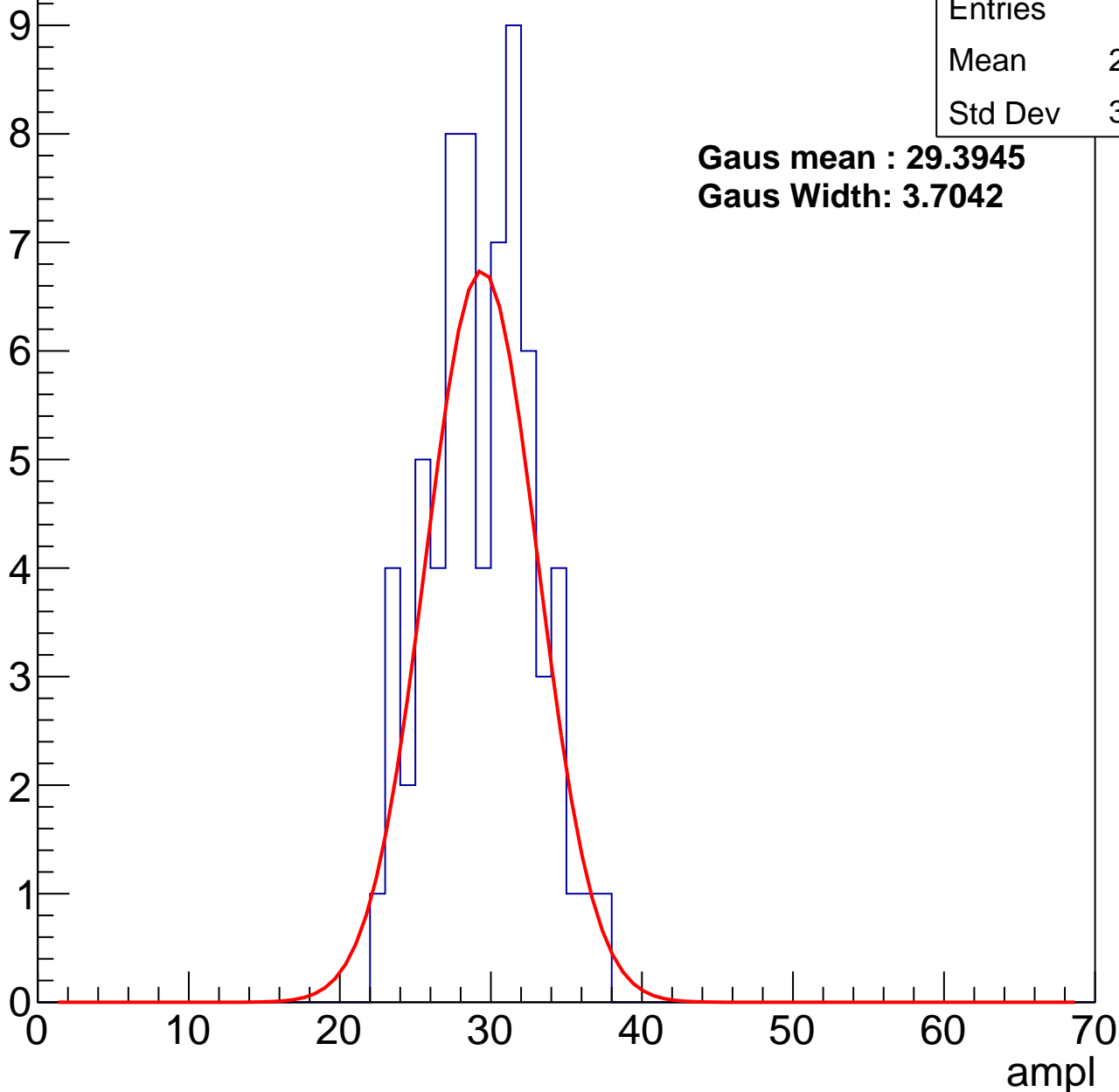
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 28.99 |
| Std Dev | 3.415 |

**Gaus mean : 29.3945**

**Gaus Width: 3.7042**



# B0L002S, U1-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 36.42 |
| Std Dev | 3.457 |

**Gaus mean : 36.8288**

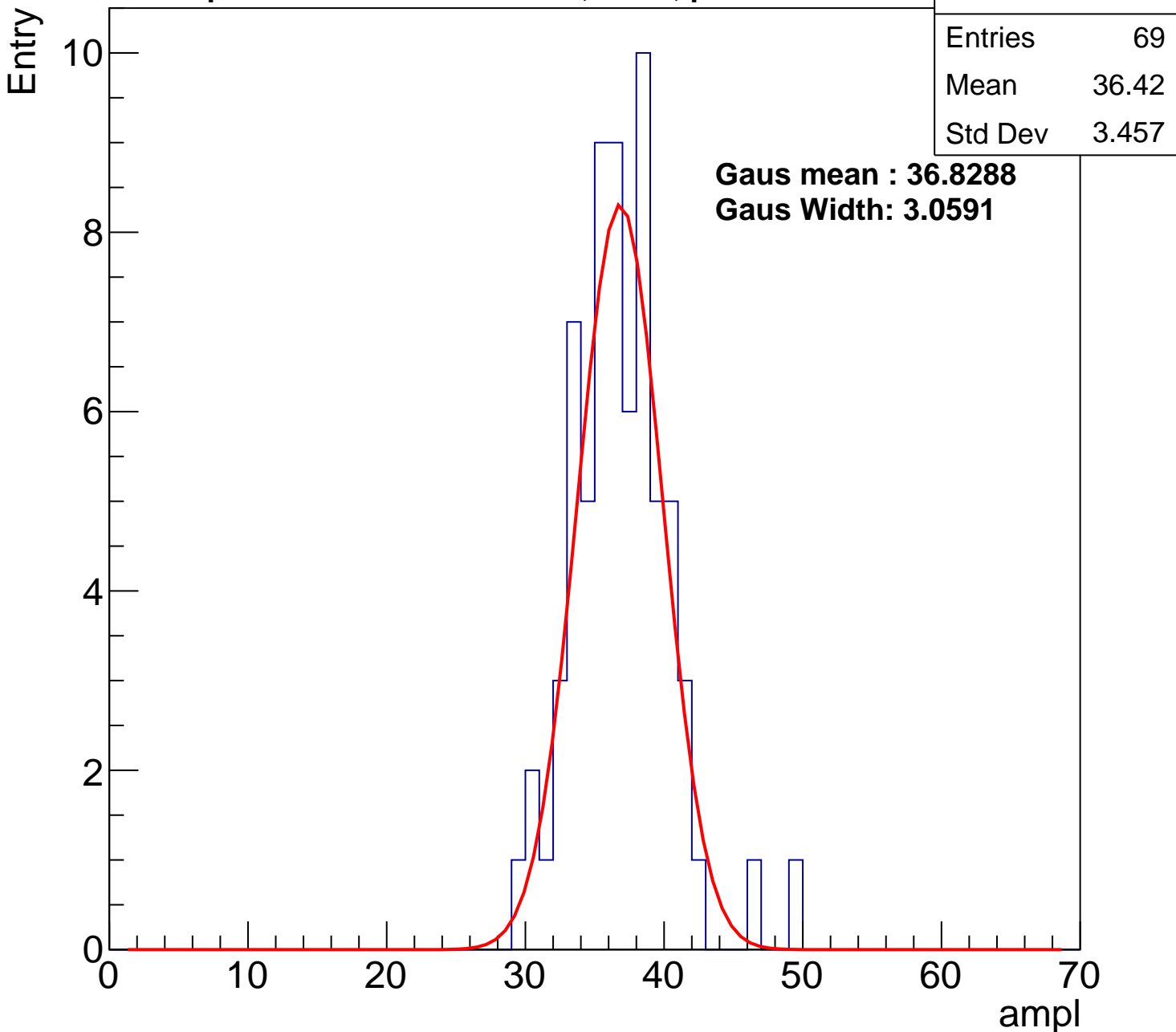
**Gaus Width: 3.0591**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch43, adc2

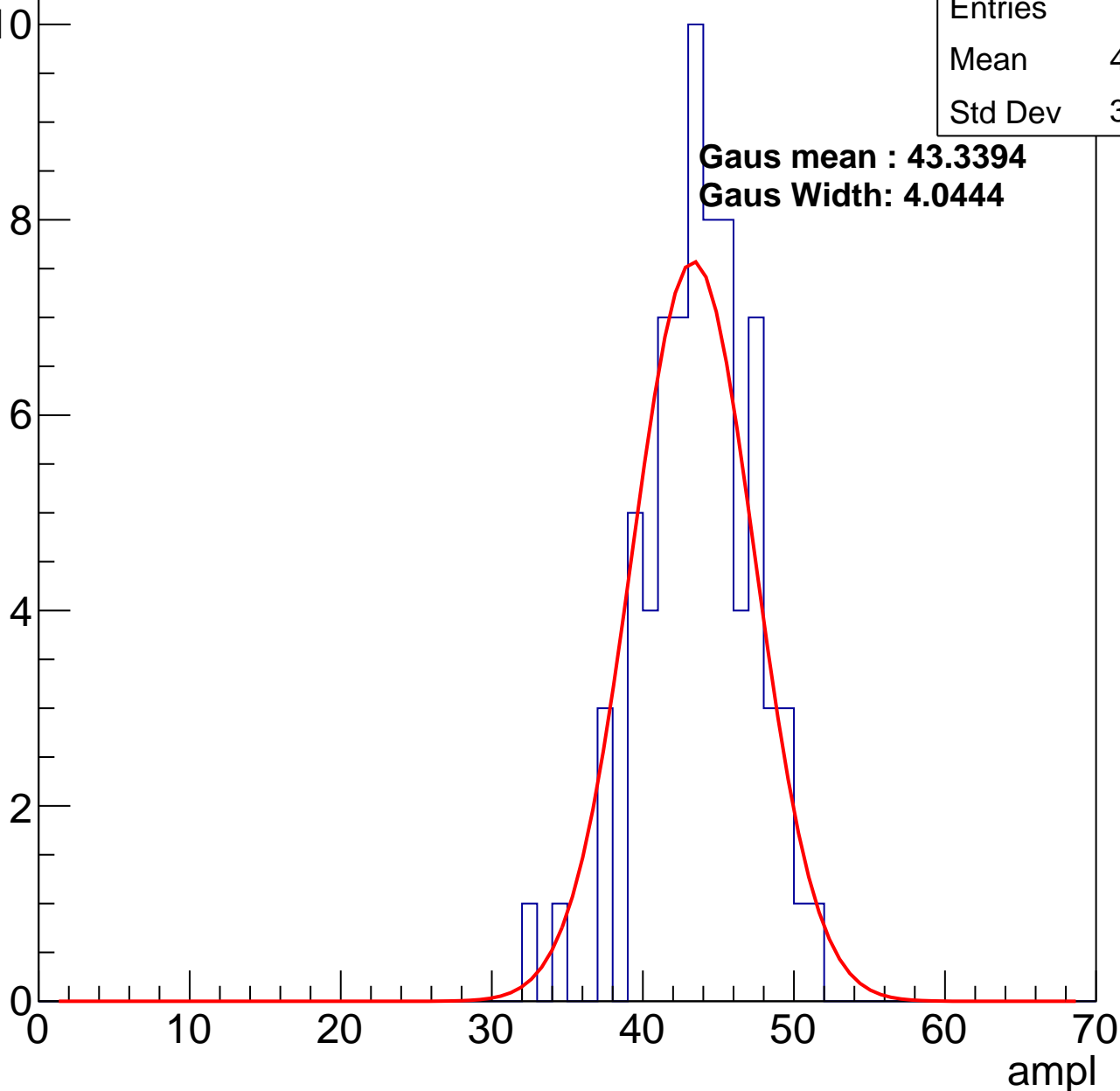
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 43.29 |
| Std Dev | 3.586 |

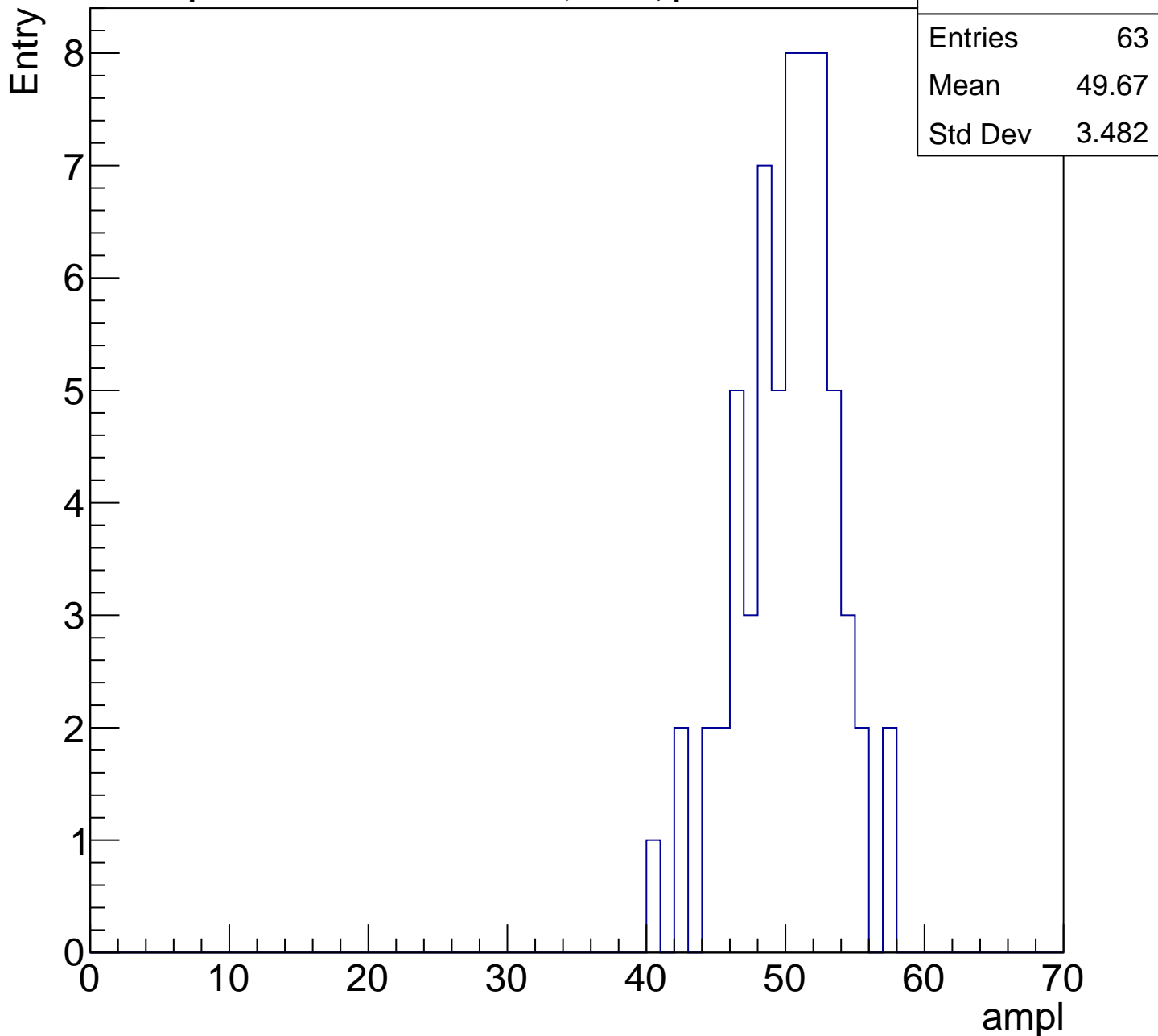
**Gaus mean : 43.3394**

**Gaus Width: 4.0444**



# B0L002S, U1-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

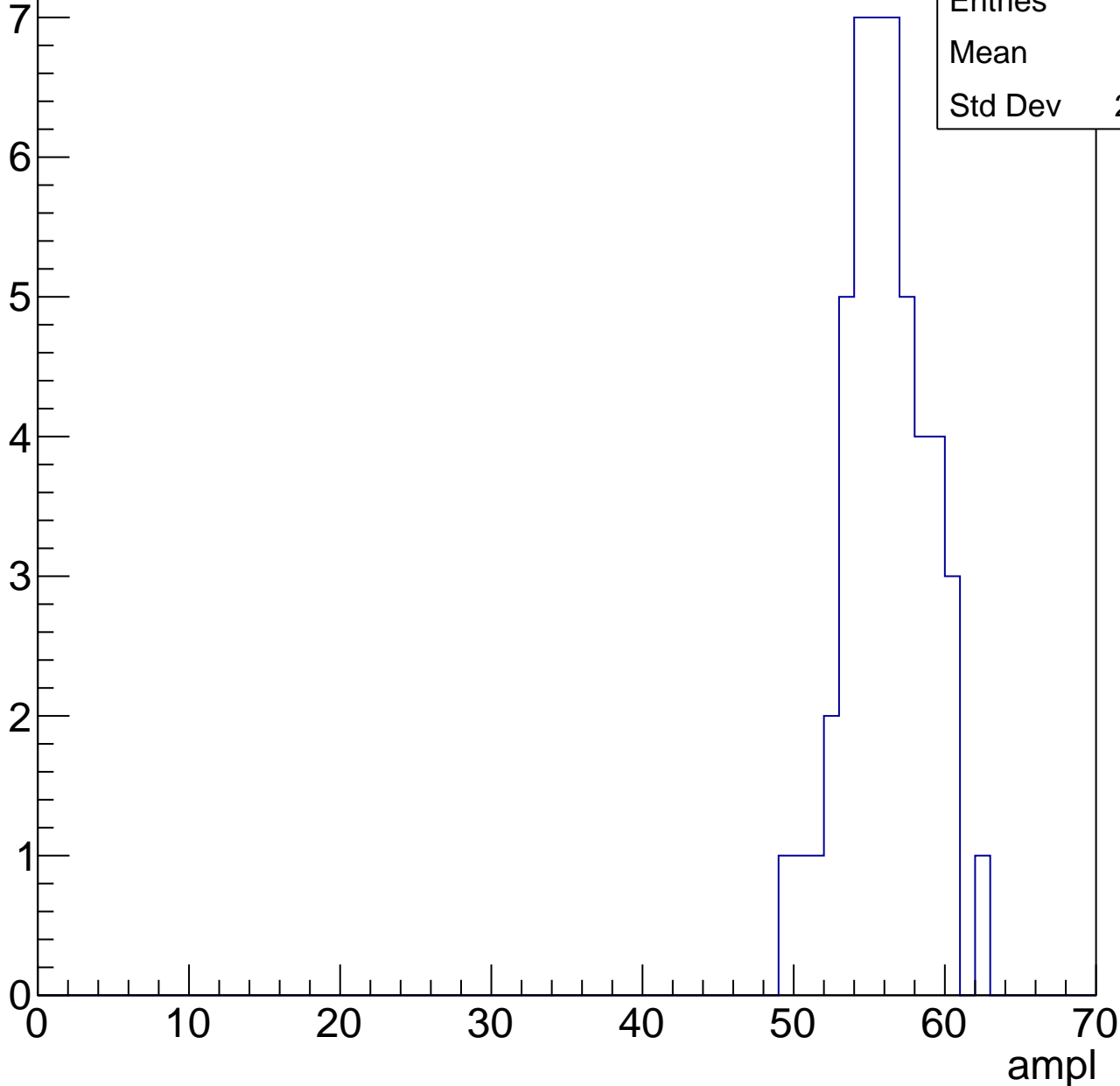


# B0L002S, U1-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 55.6  |
| Std Dev | 2.721 |

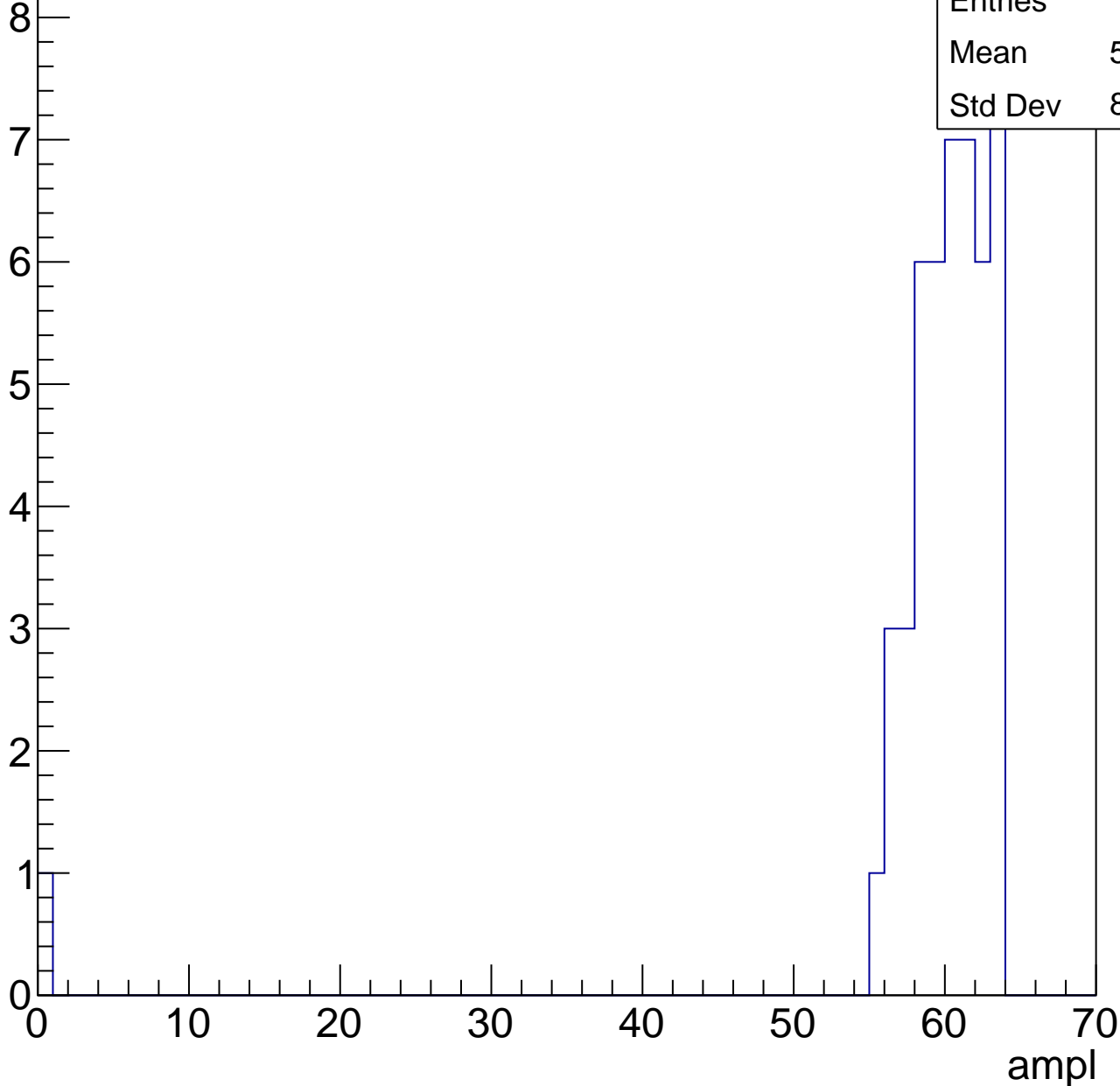


# B0L002S, U1-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

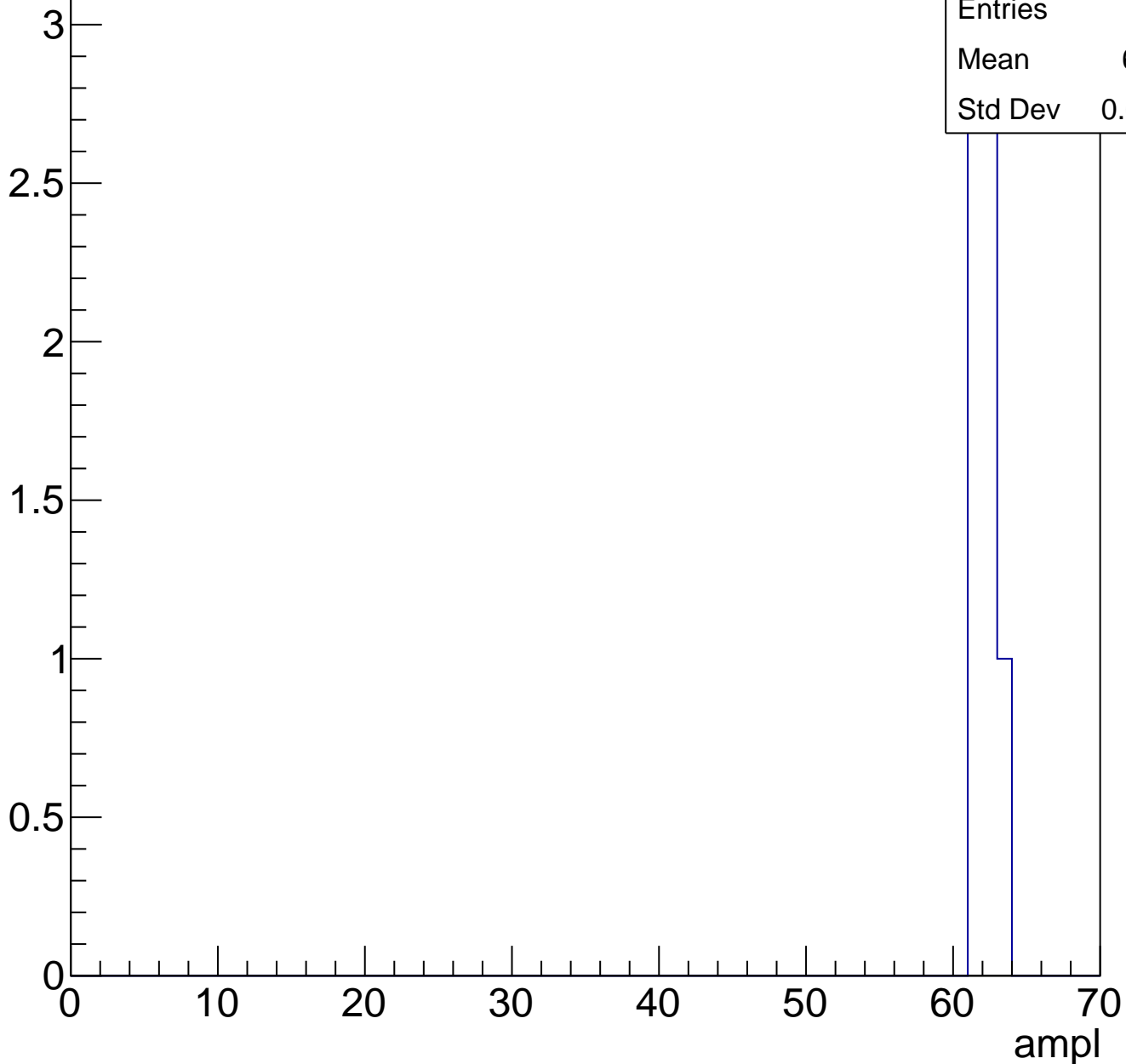
|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 58.73 |
| Std Dev | 8.845 |



# B0L002S, U1-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

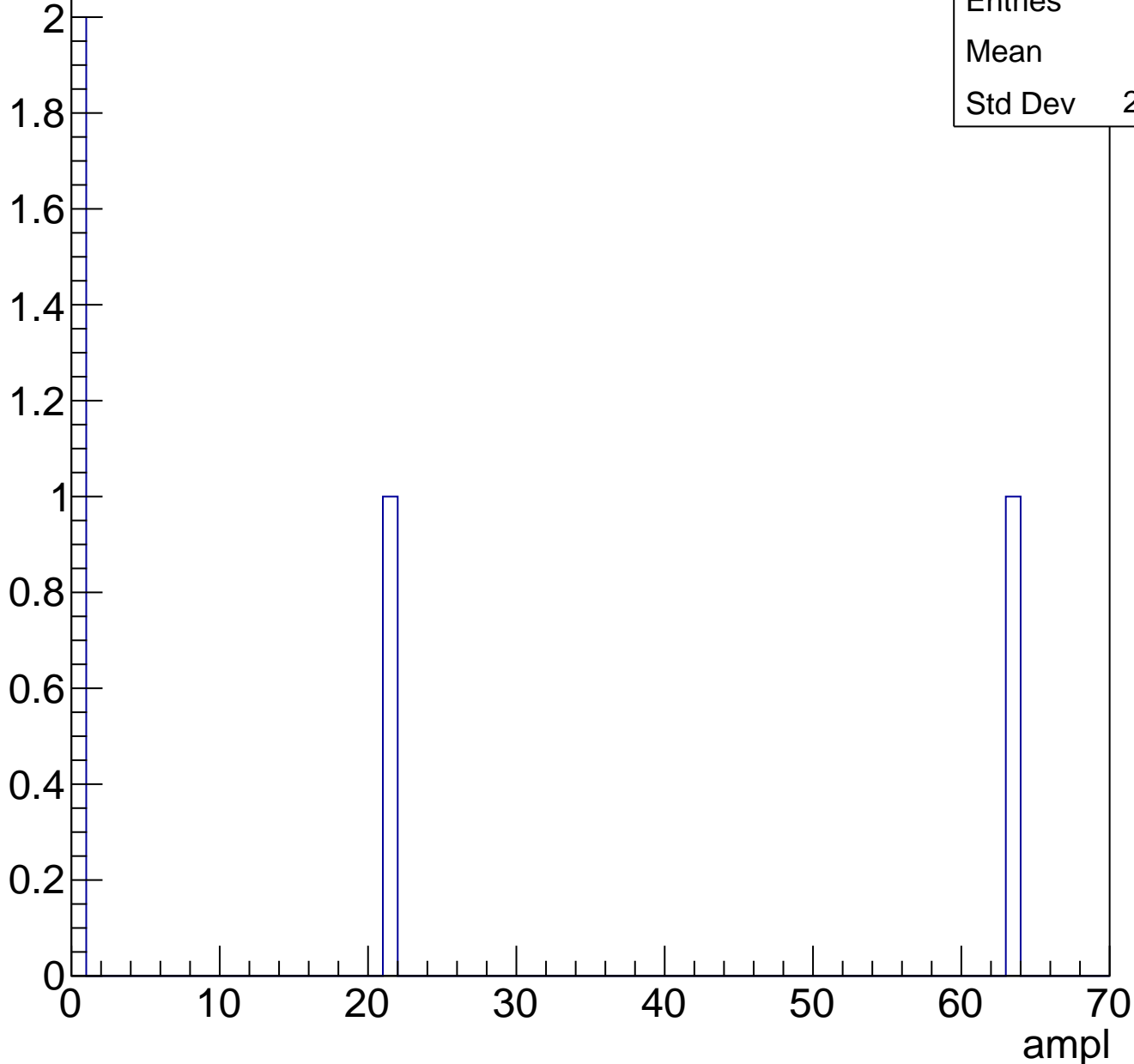




# B0L002S, U1-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 21    |
| Std Dev | 25.72 |

# B0L002S, U1-ch44, adc0

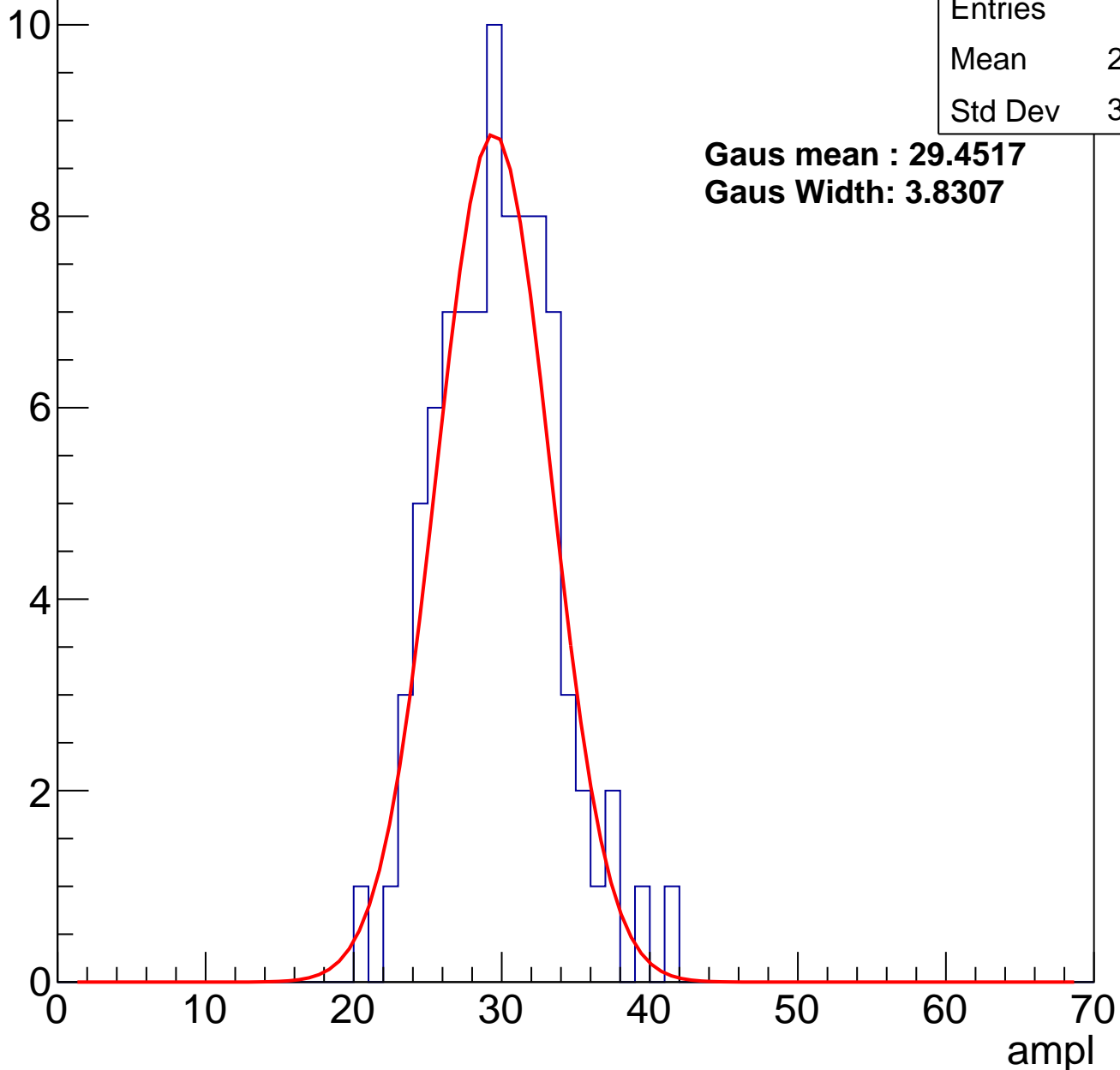
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 29.26 |
| Std Dev | 3.907 |

**Gaus mean : 29.4517**

**Gaus Width: 3.8307**

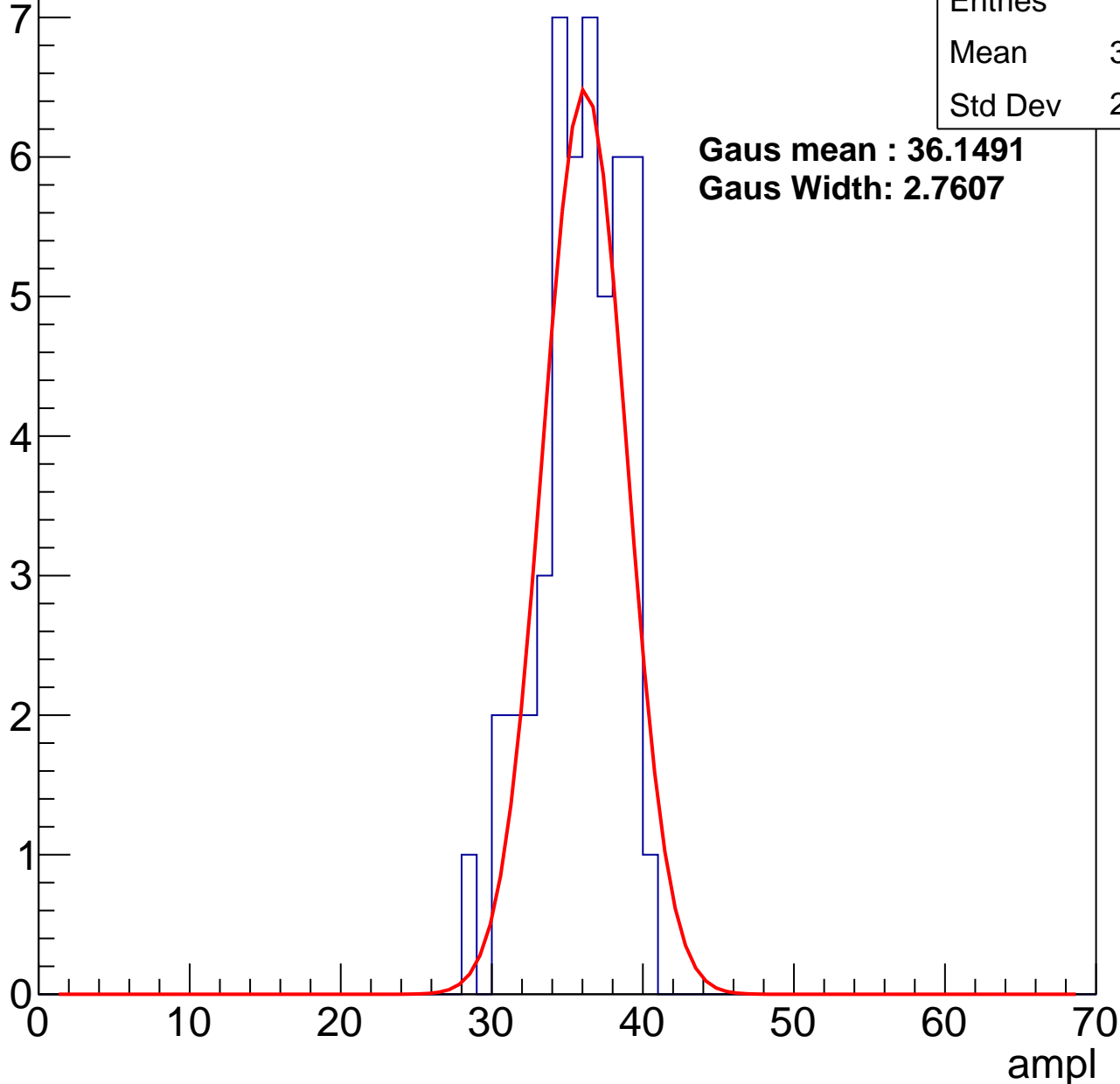
Entry



# B0L002S, U1-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch44, adc2

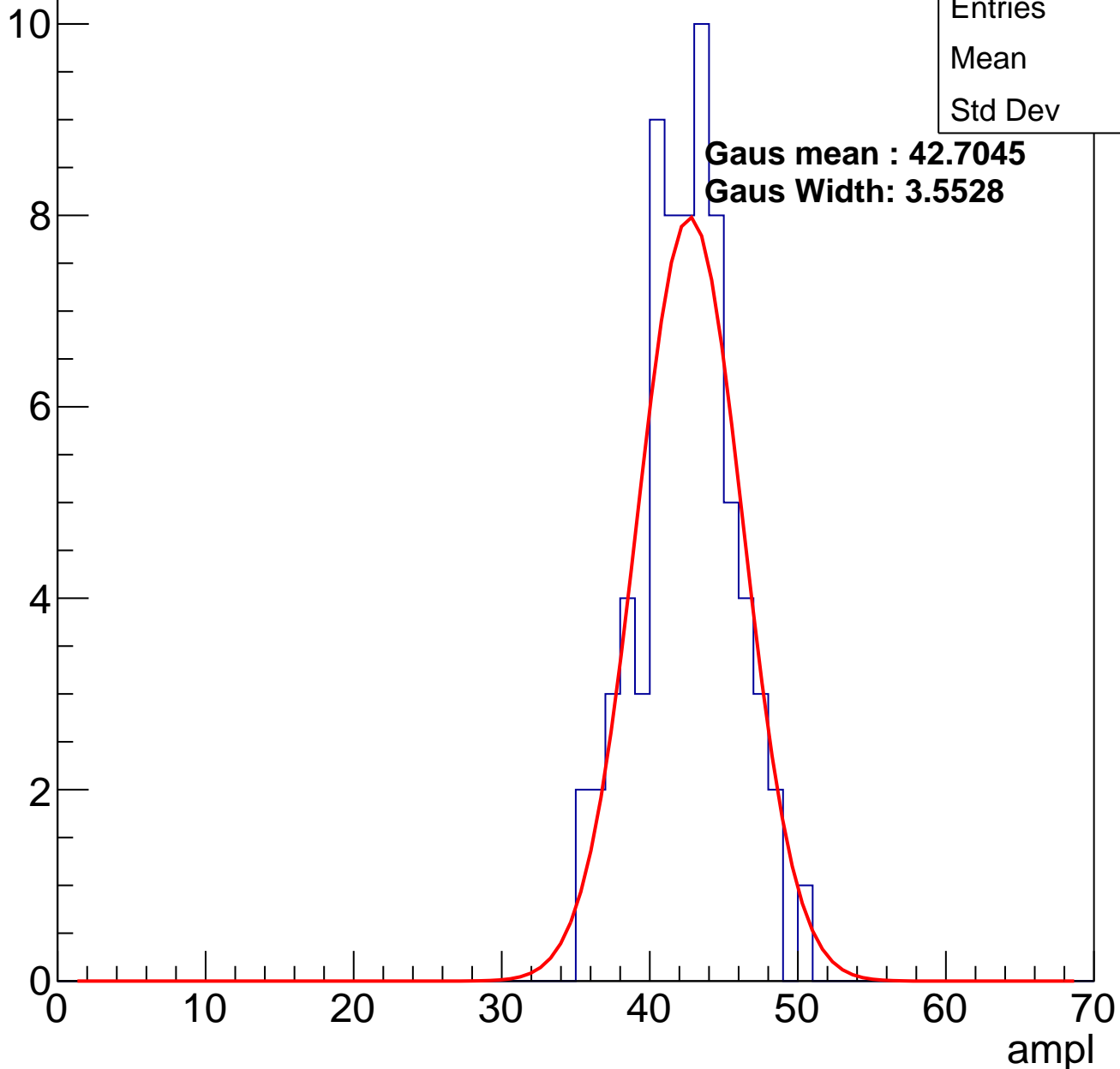
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |      |
|---------|------|
| Entries | 72   |
| Mean    | 42   |
| Std Dev | 3.21 |

**Gaus mean : 42.7045**

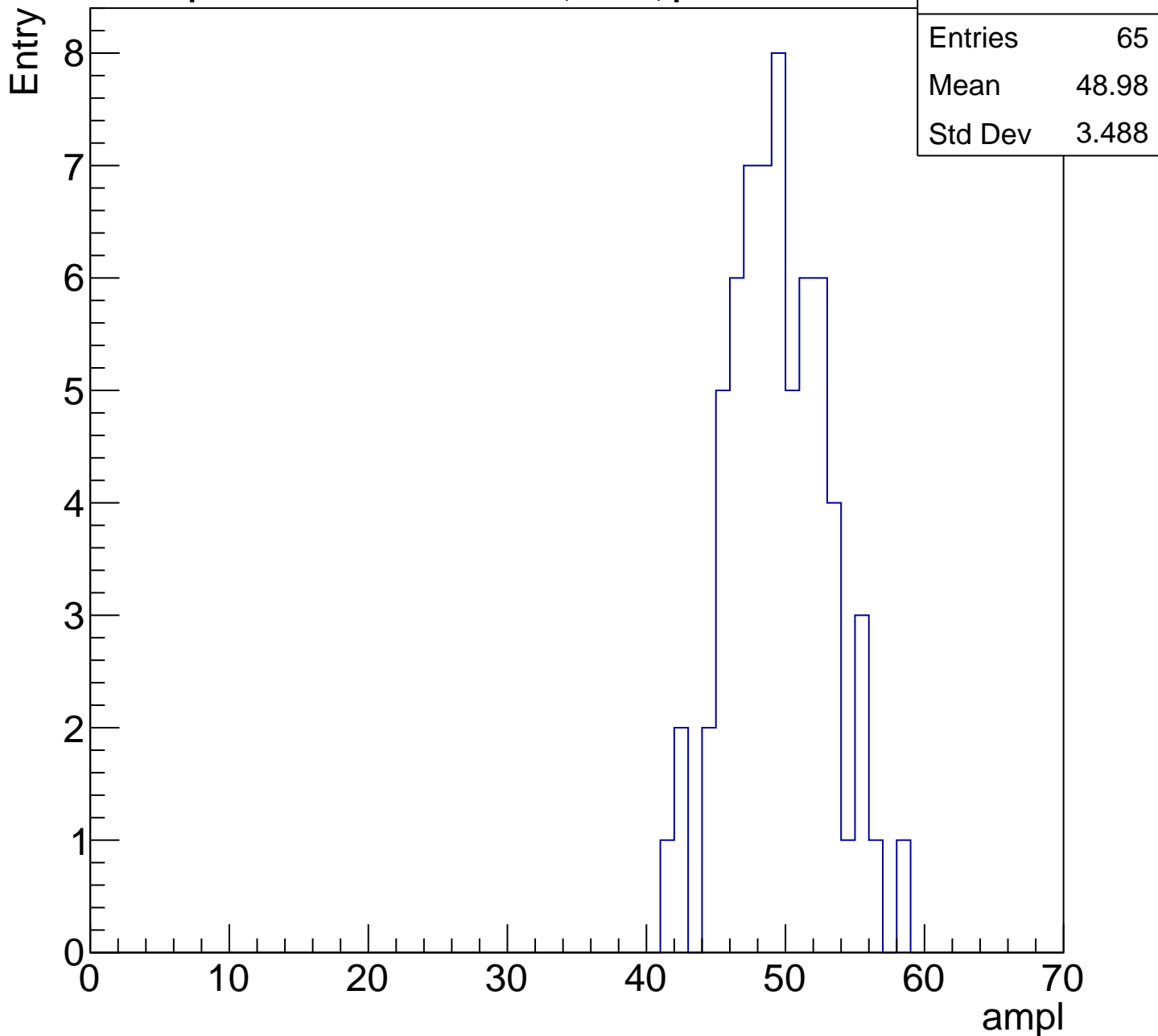
**Gaus Width: 3.5528**

Entry



# B0L002S, U1-ch44, adc3

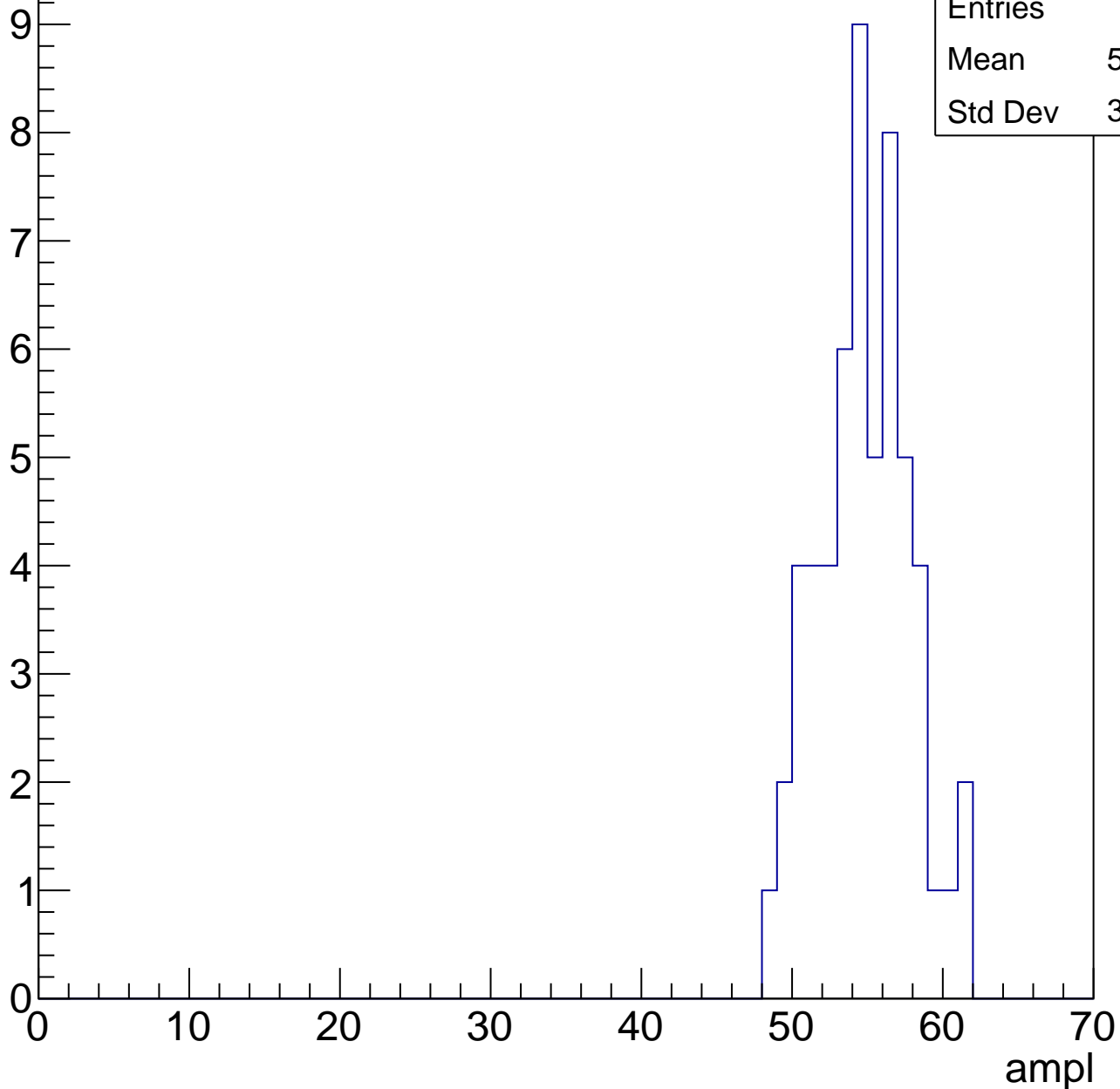
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

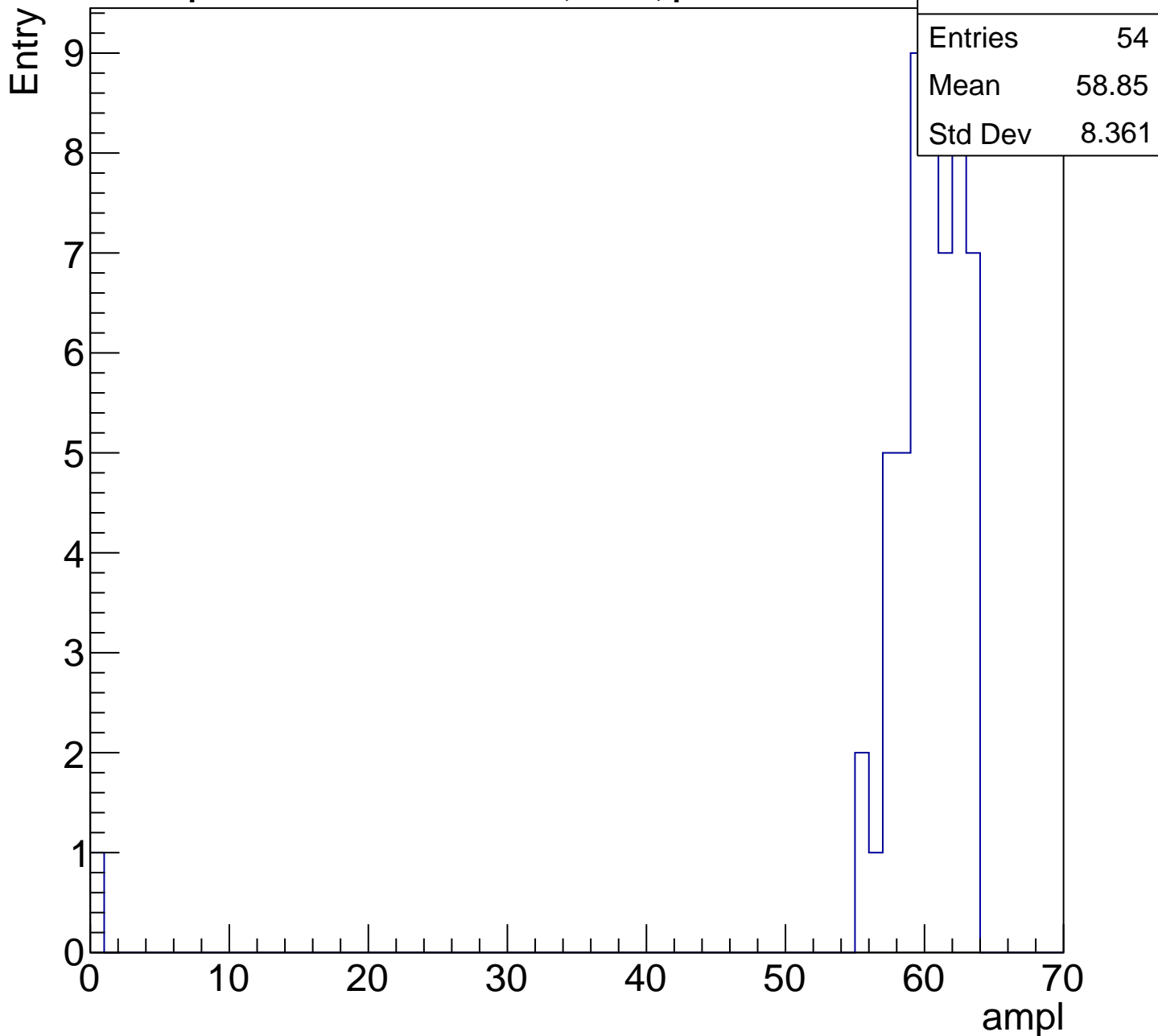
Entry



|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 54.34 |
| Std Dev | 3.008 |

# B0L002S, U1-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch45, adc0

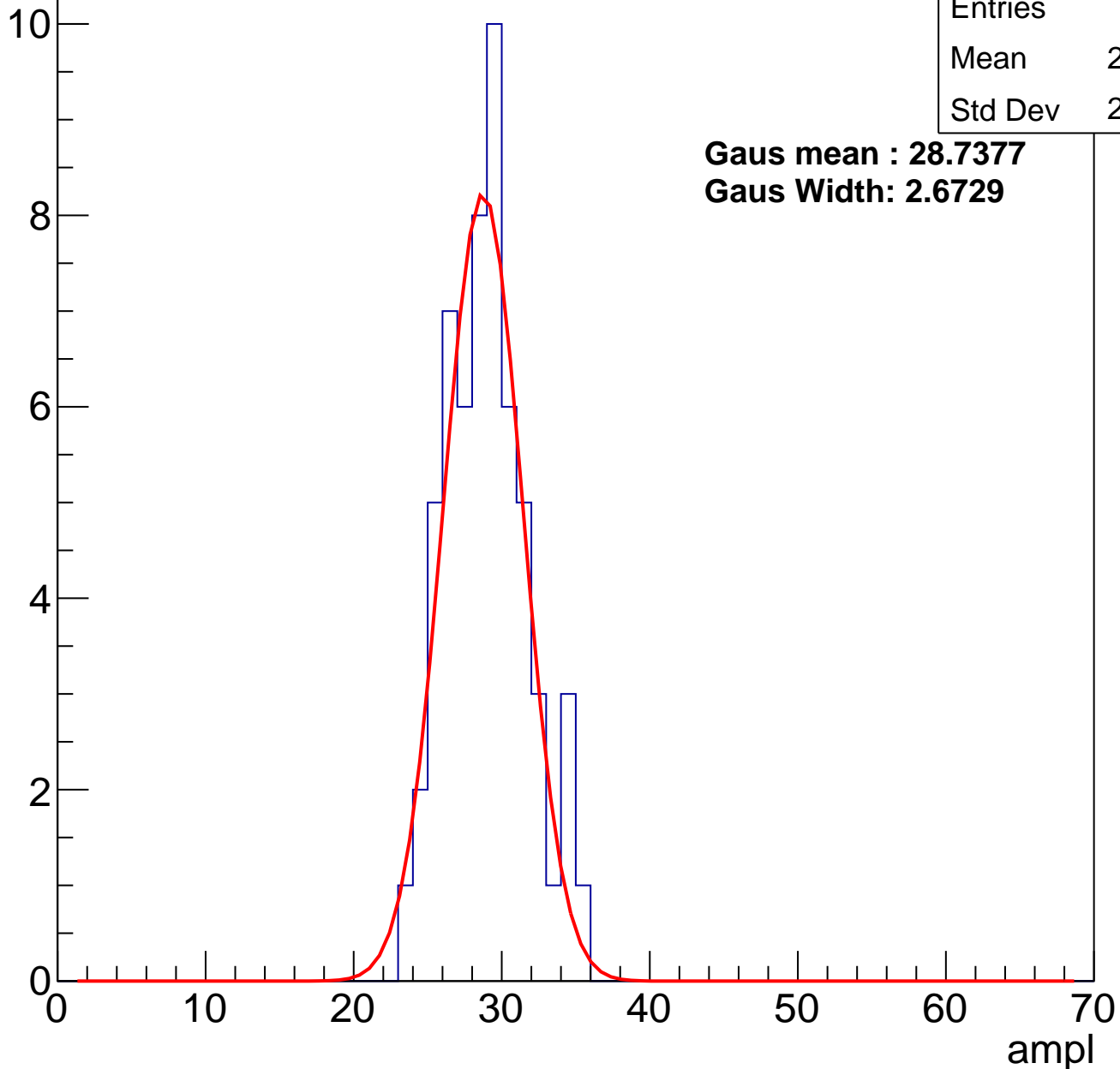
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 28.53 |
| Std Dev | 2.718 |

**Gaus mean : 28.7377**

**Gaus Width: 2.6729**

Entry



# B0L002S, U1-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 86    |
| Mean    | 35.55 |
| Std Dev | 3.811 |

**Gaus mean : 35.6632**

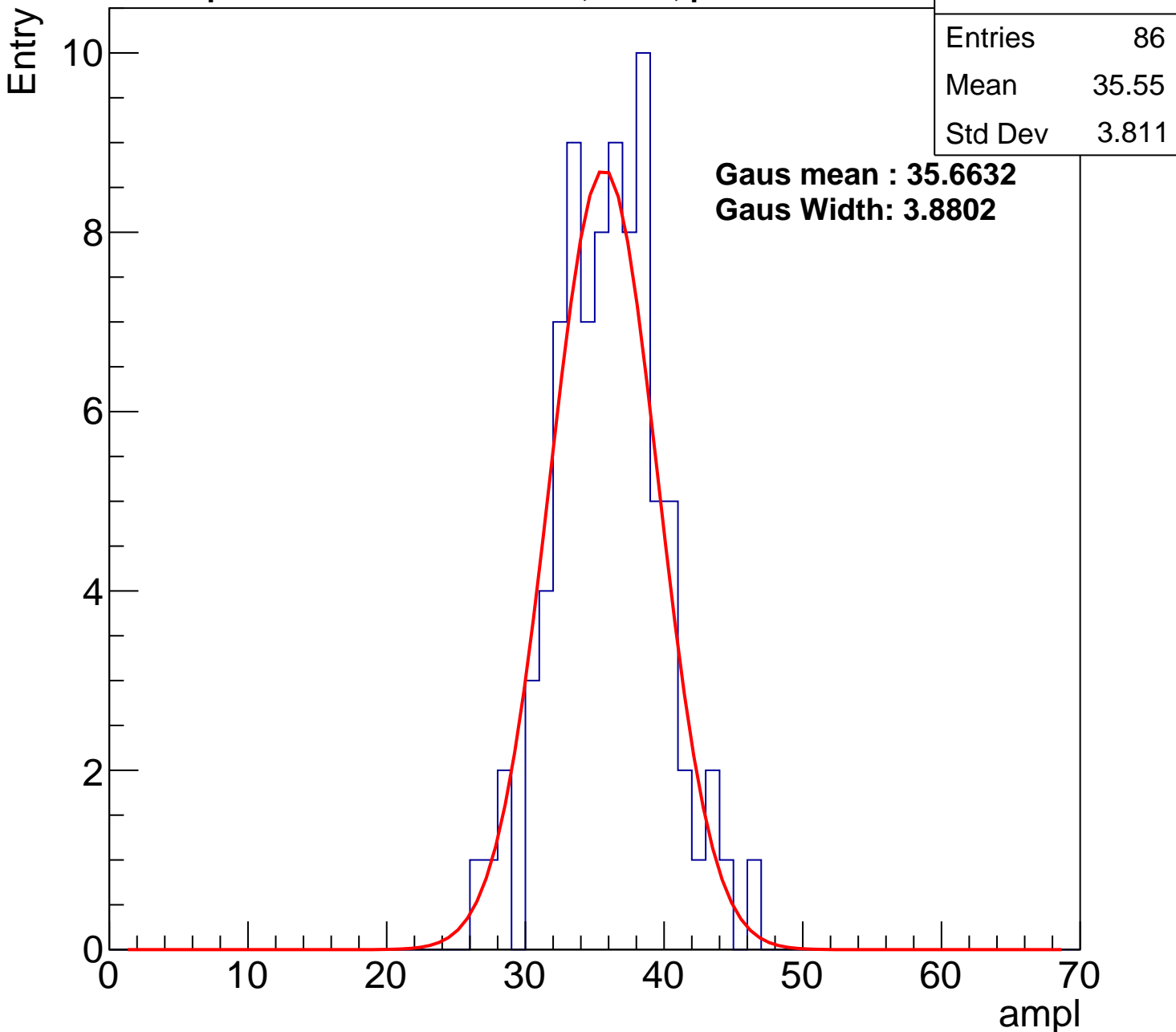
**Gaus Width: 3.8802**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch45, adc2

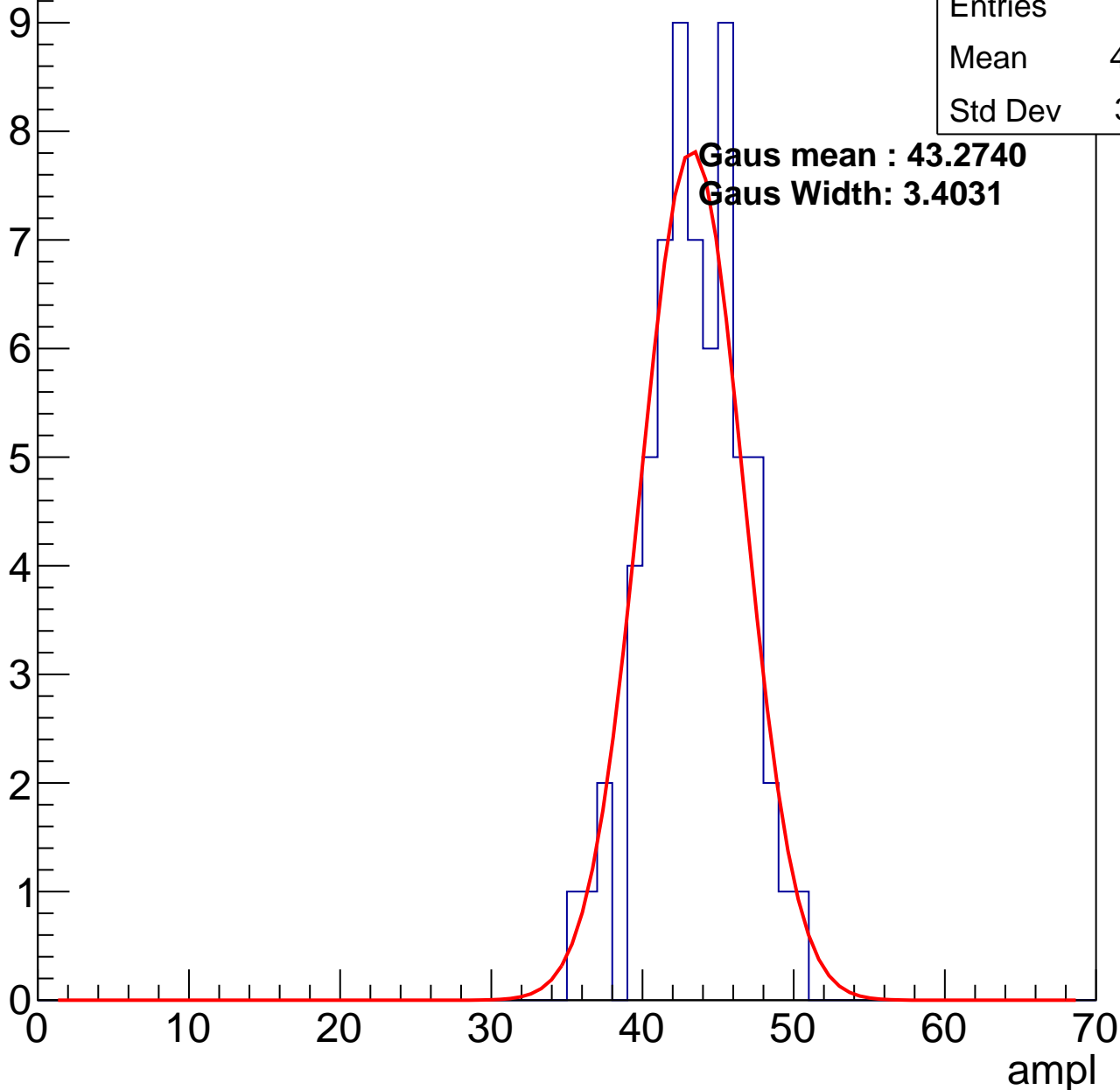
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 43.02 |
| Std Dev | 3.121 |

**Gaus mean : 43.2740**

**Gaus Width: 3.4031**

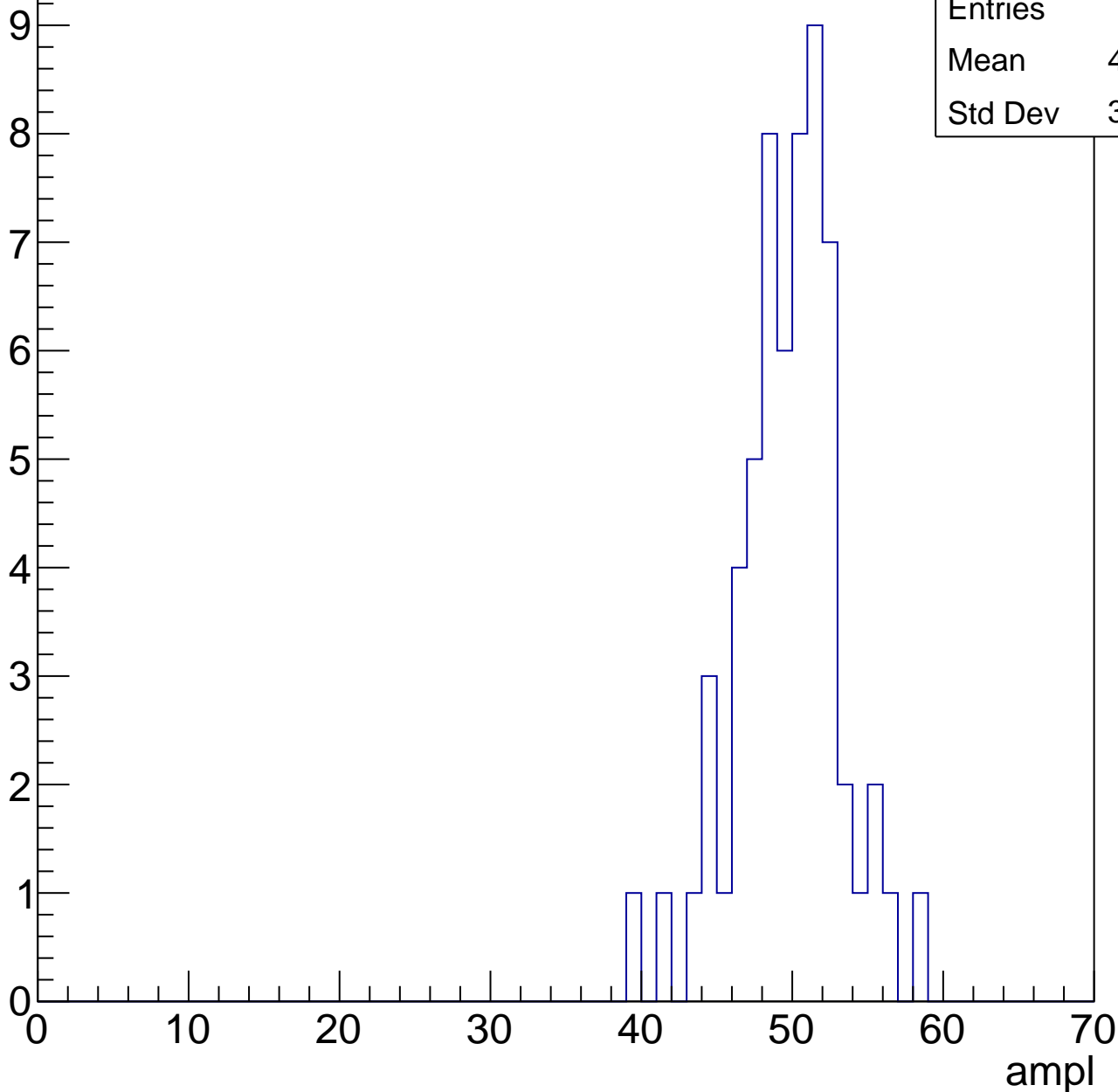


# B0L002S, U1-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

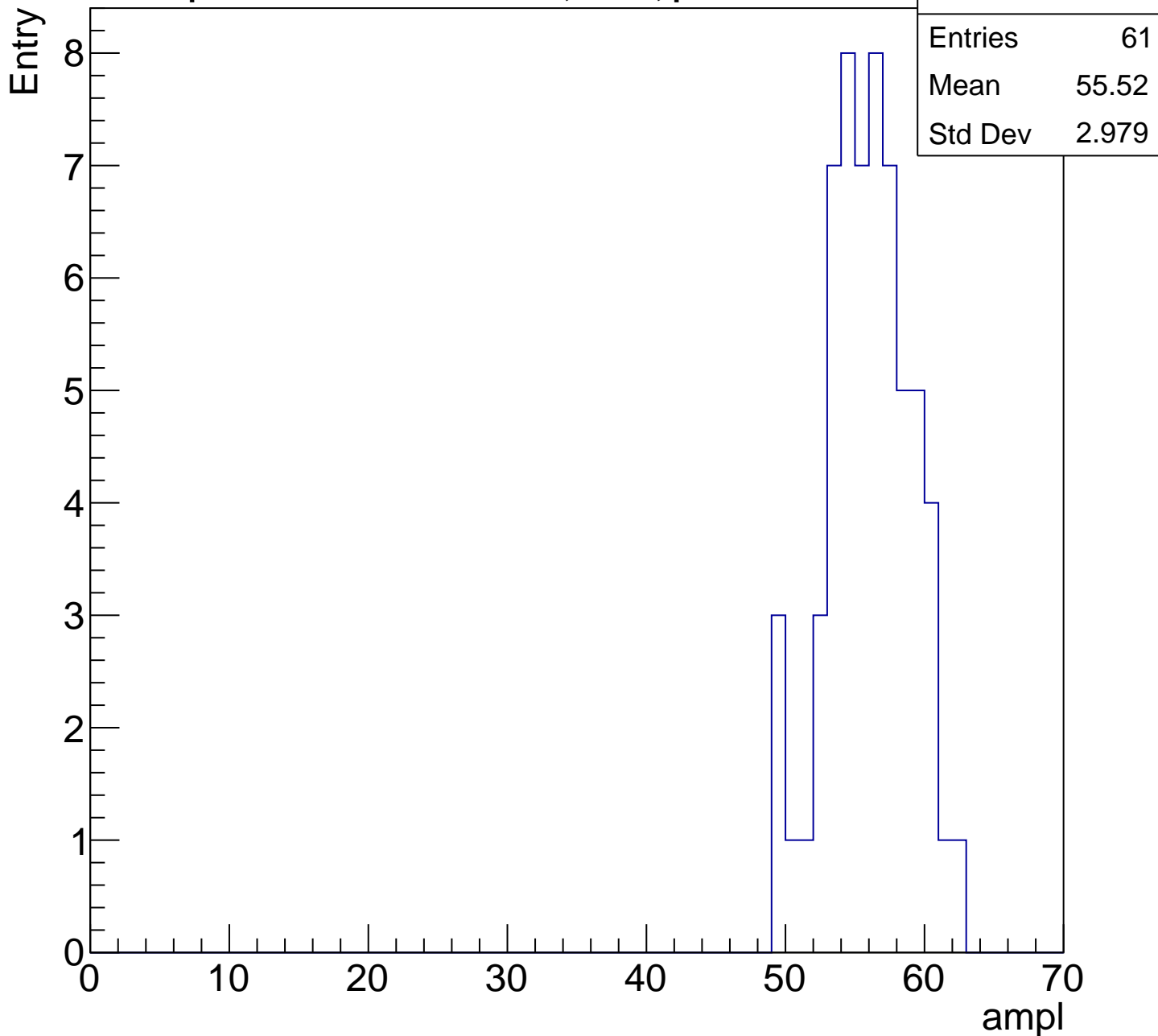
Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 49.25 |
| Std Dev | 3.429 |



# B0L002S, U1-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

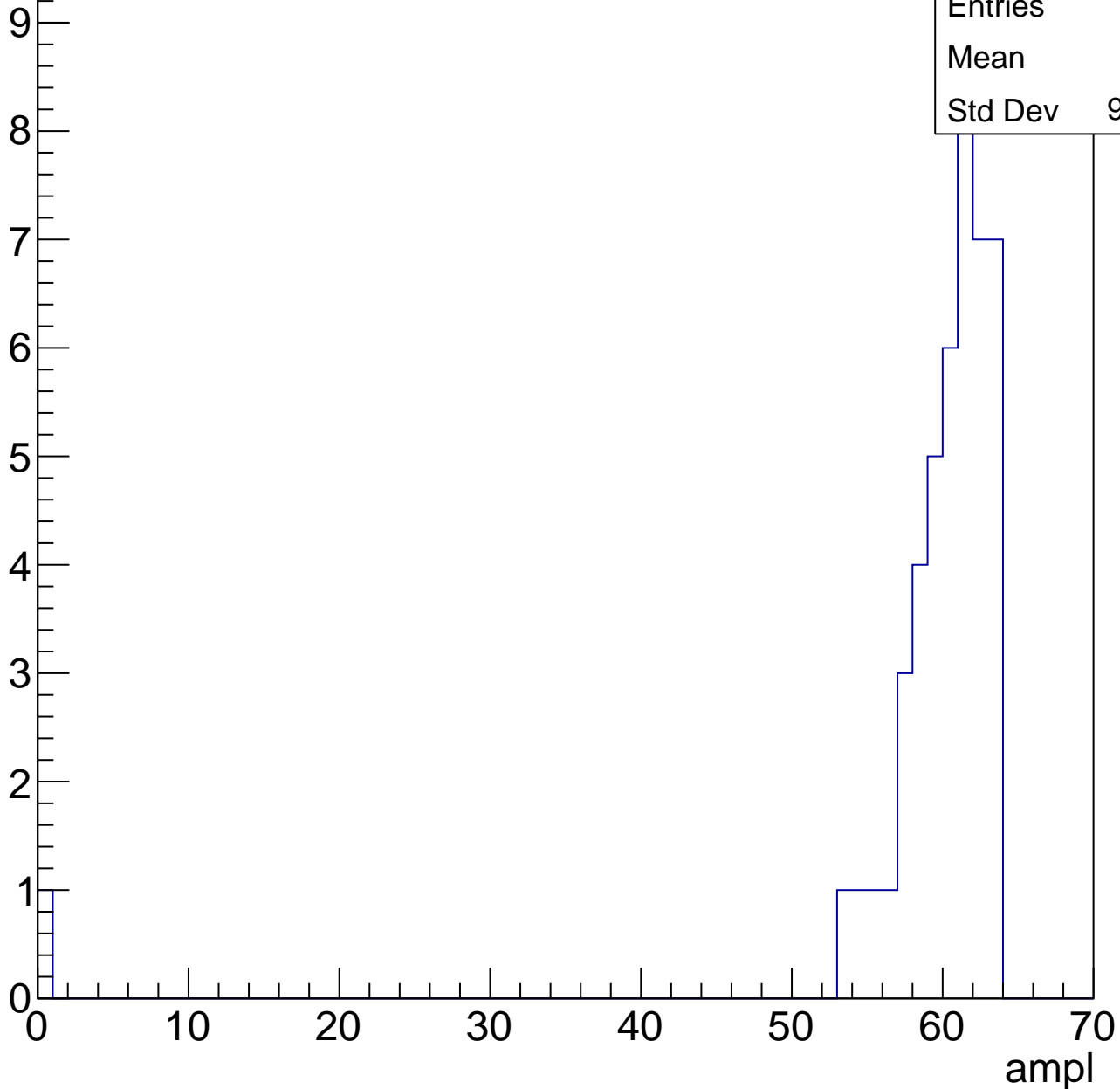


# B0L002S, U1-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 58.7  |
| Std Dev | 9.084 |



# B0L002S, U1-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch46, adc0

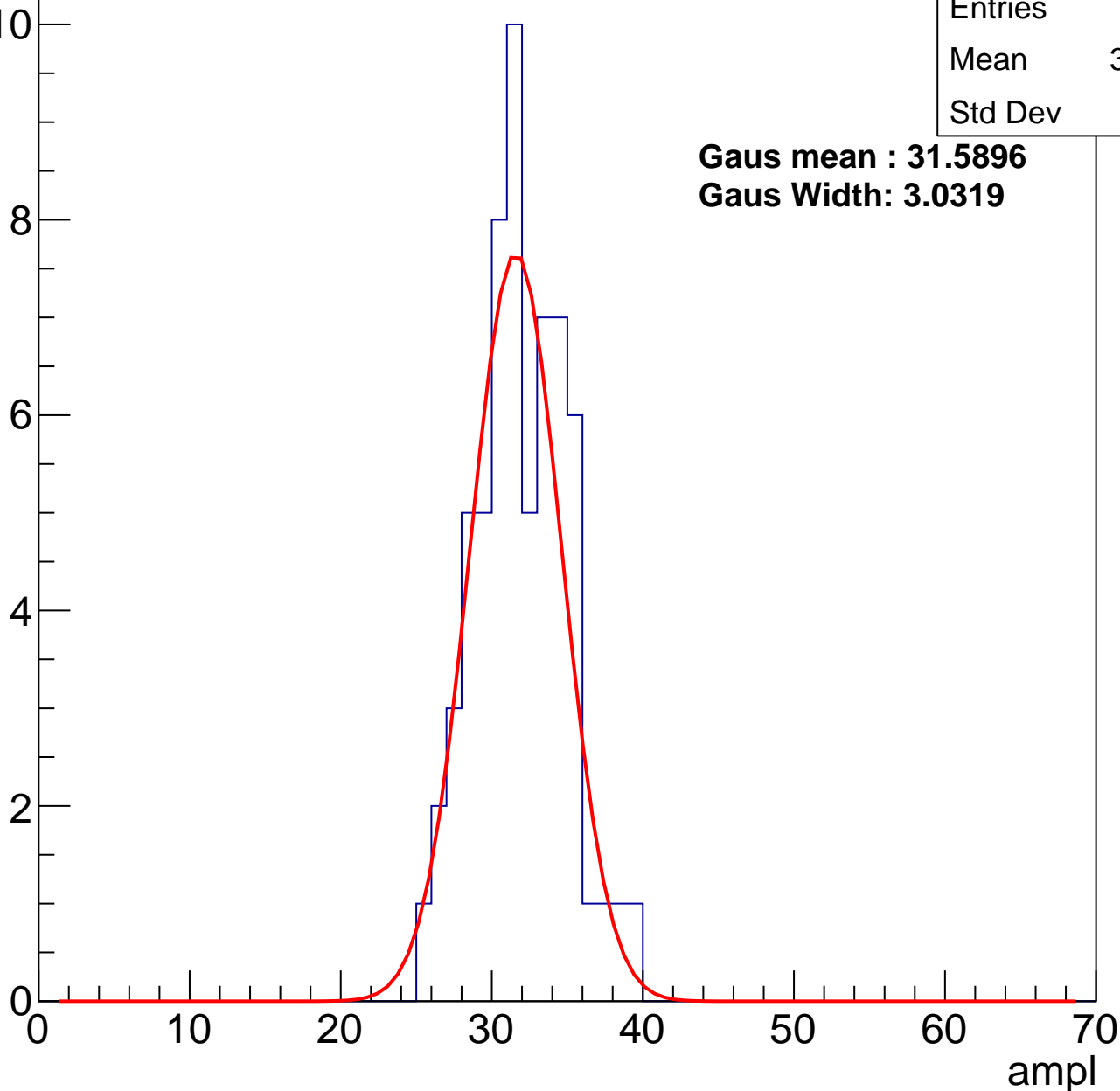
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 31.46 |
| Std Dev | 2.98  |

**Gaus mean : 31.5896**

**Gaus Width: 3.0319**



# B0L002S, U1-ch46, adc1

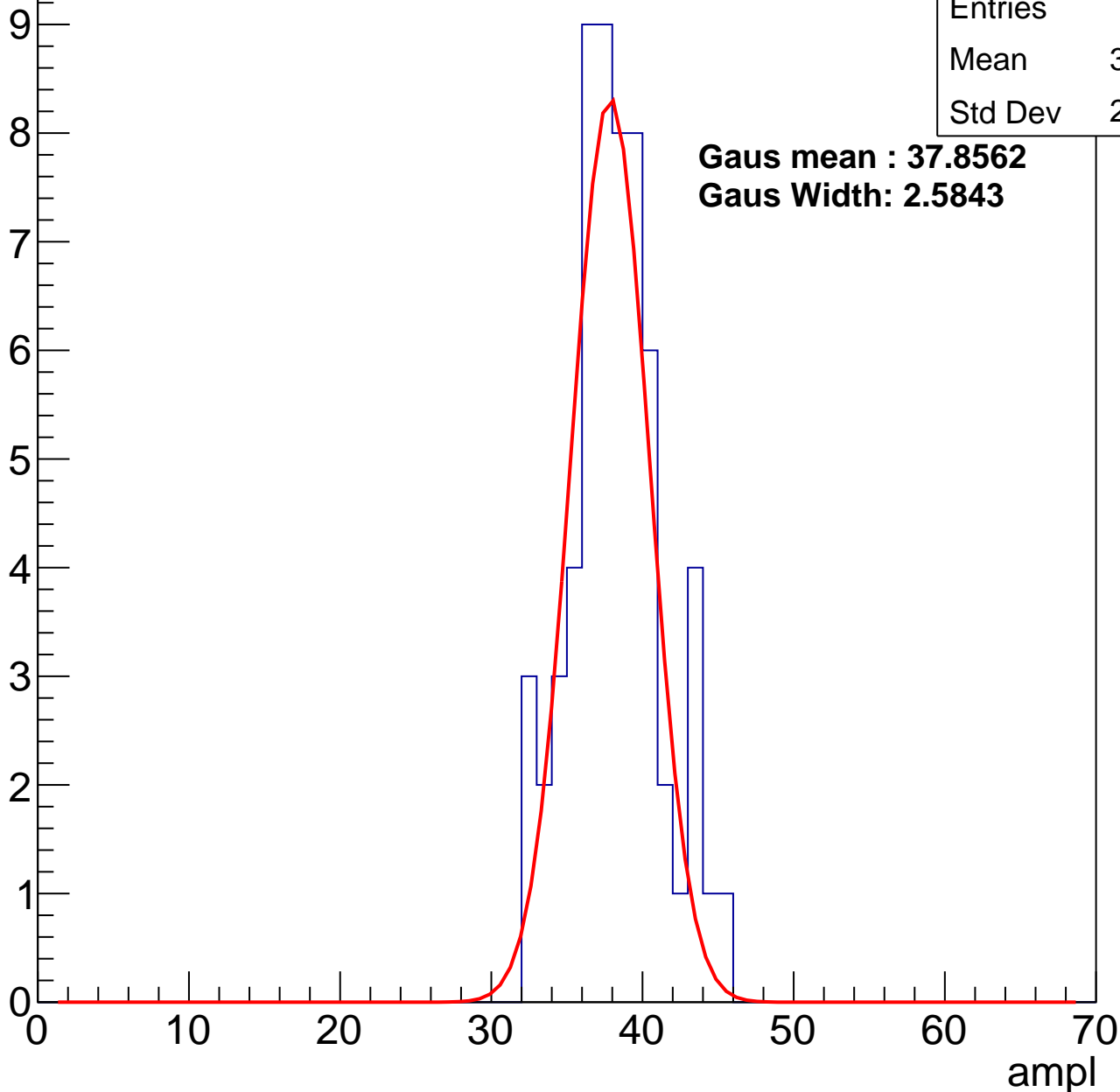
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 37.74 |
| Std Dev | 2.958 |

**Gaus mean : 37.8562**

**Gaus Width: 2.5843**



# B0L002S, U1-ch46, adc2

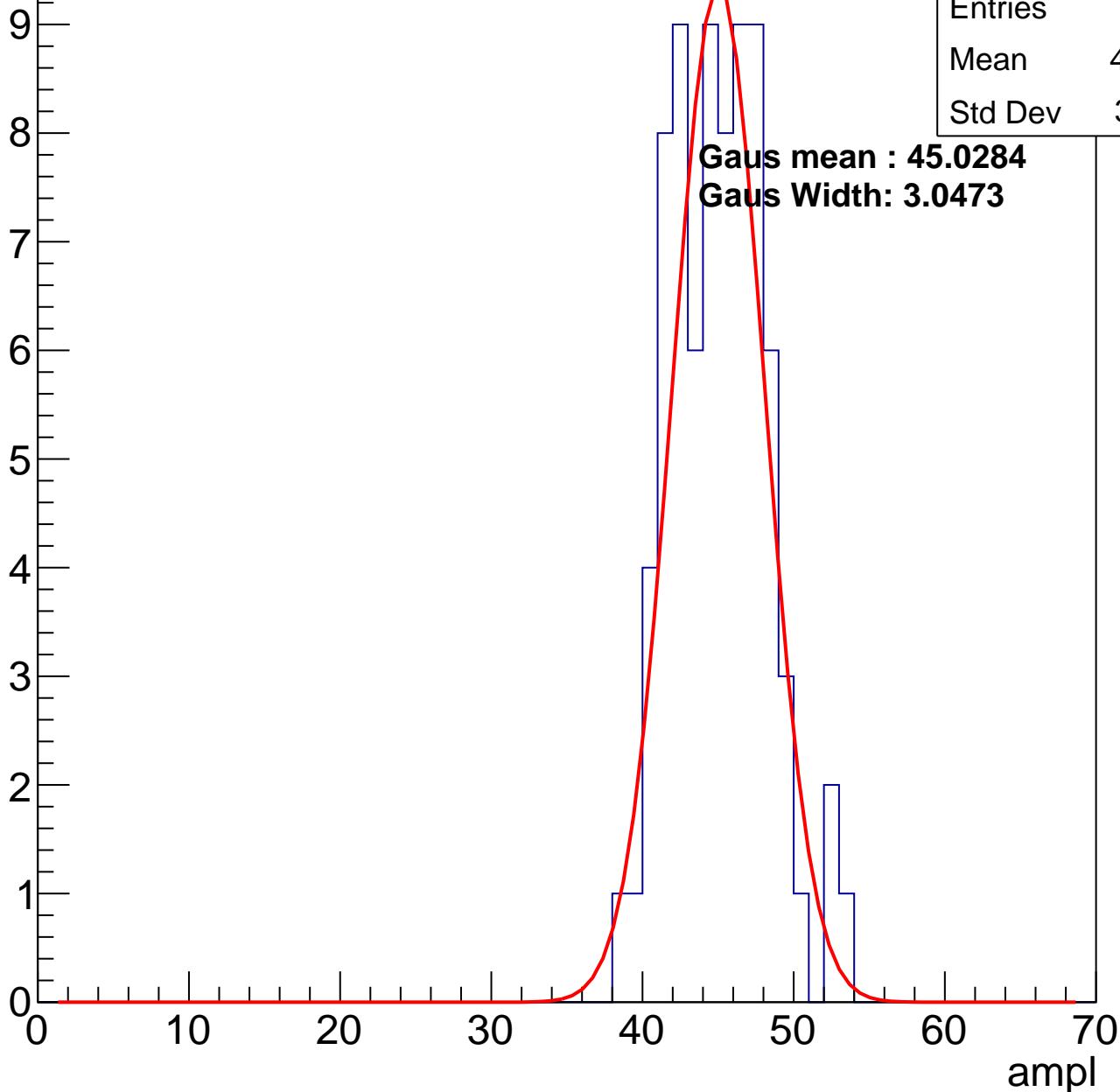
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 44.62 |
| Std Dev | 3.121 |

**Gaus mean : 45.0284**

**Gaus Width: 3.0473**

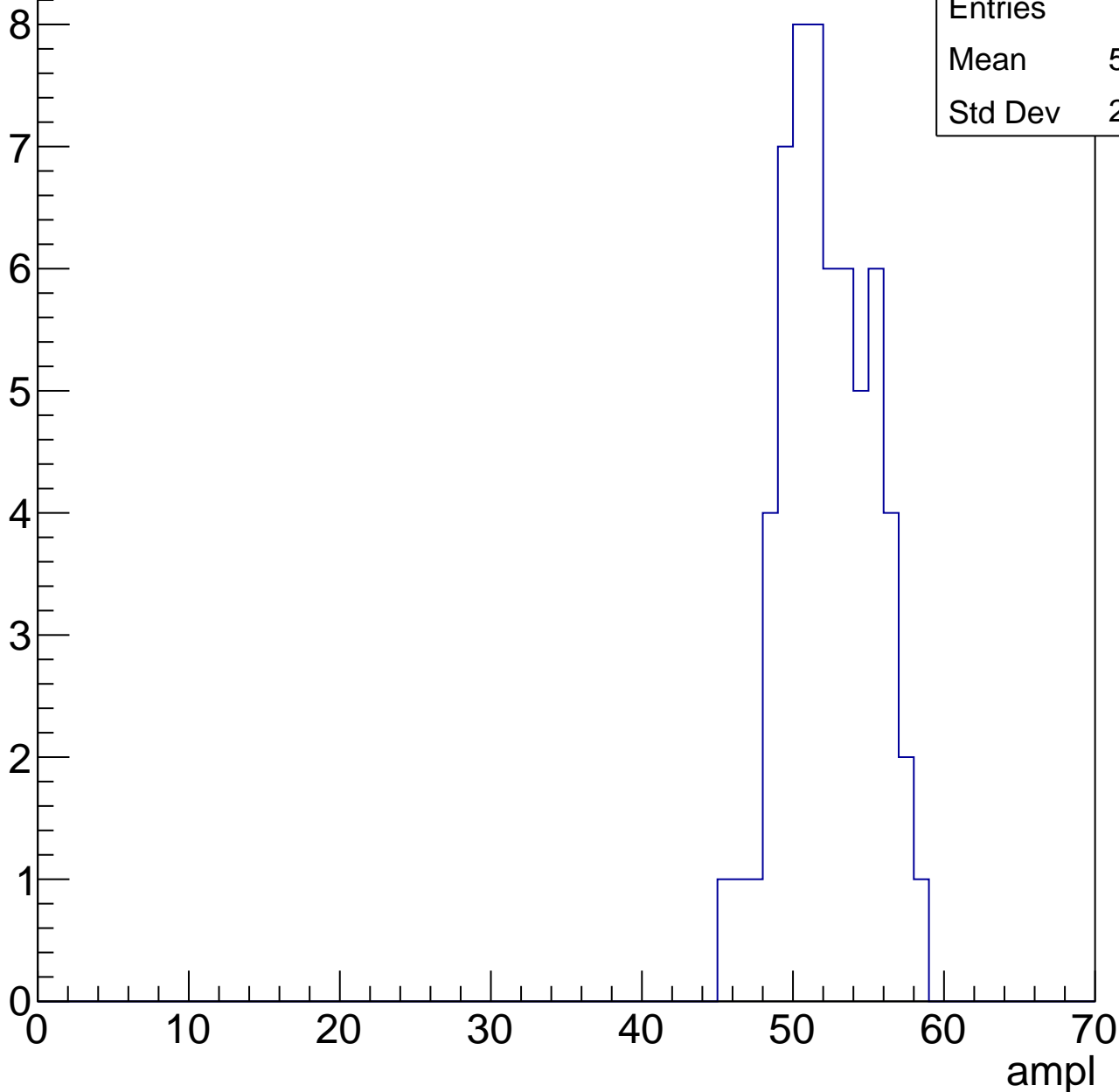


# B0L002S, U1-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

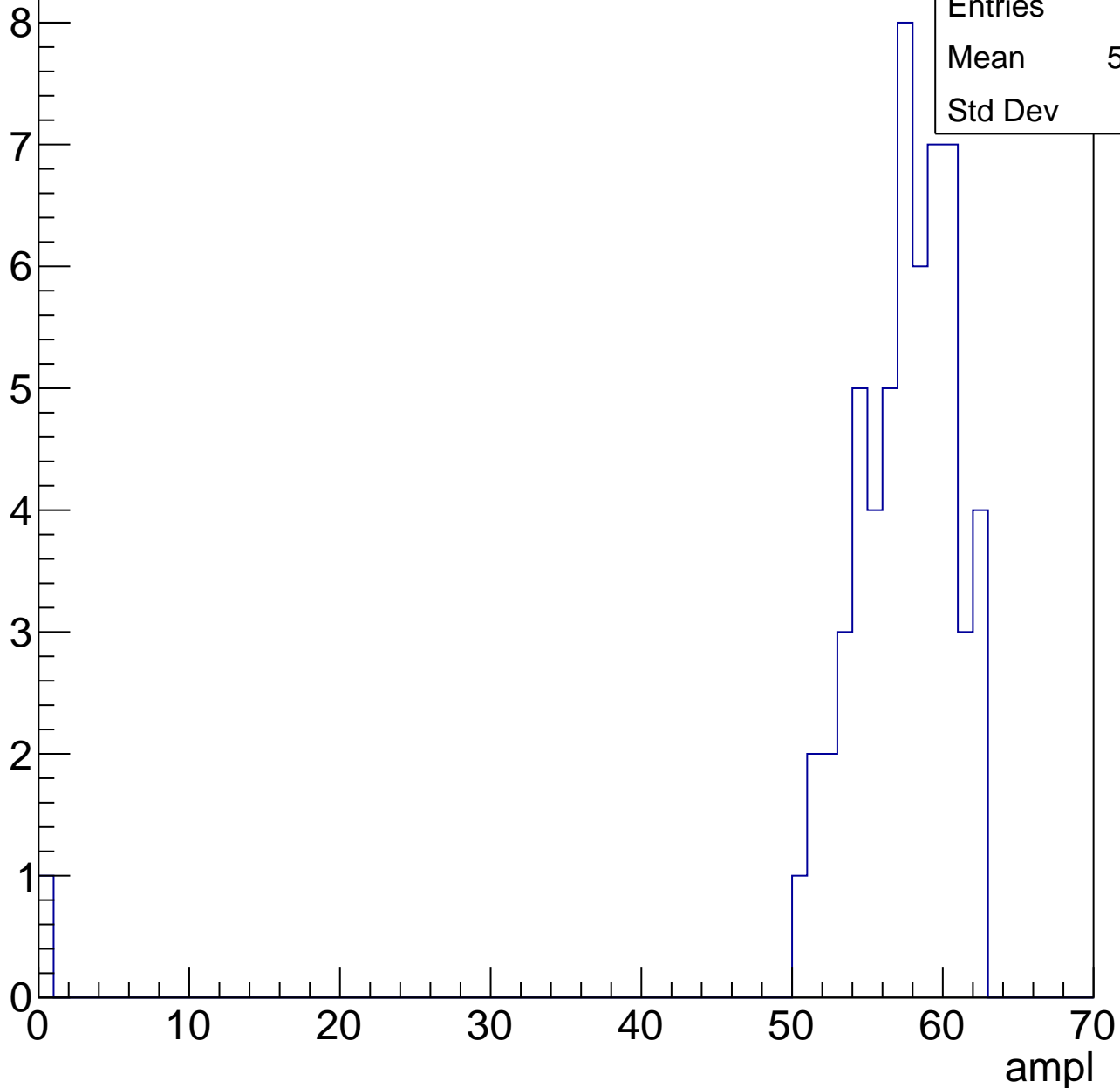
|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 51.78 |
| Std Dev | 2.899 |



# B0L002S, U1-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



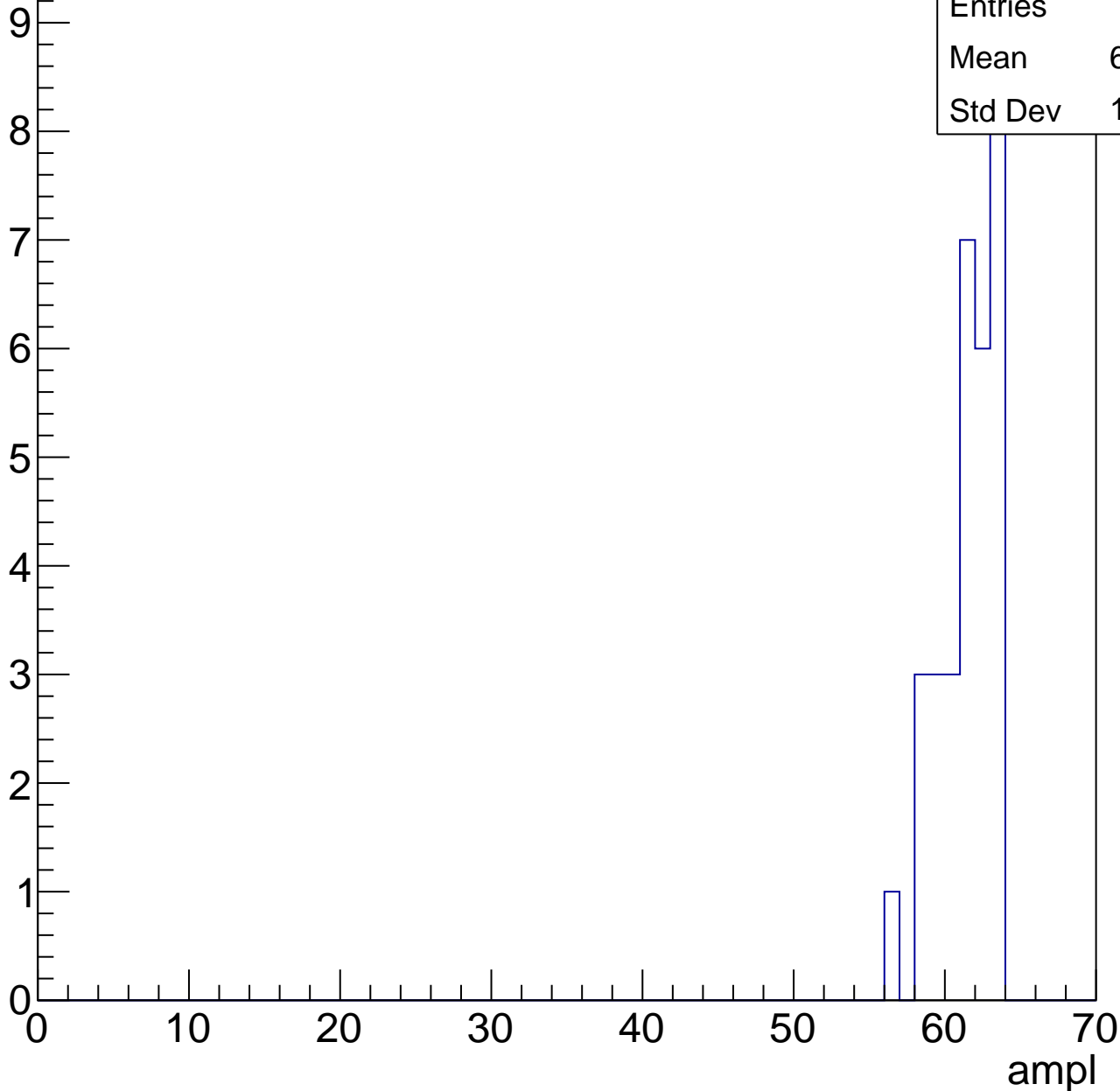
|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 56.09 |
| Std Dev | 8.02  |

# B0L002S, U1-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 32    |
| Mean    | 61.03 |
| Std Dev | 1.845 |



# B0L002S, U1-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch47, adc0

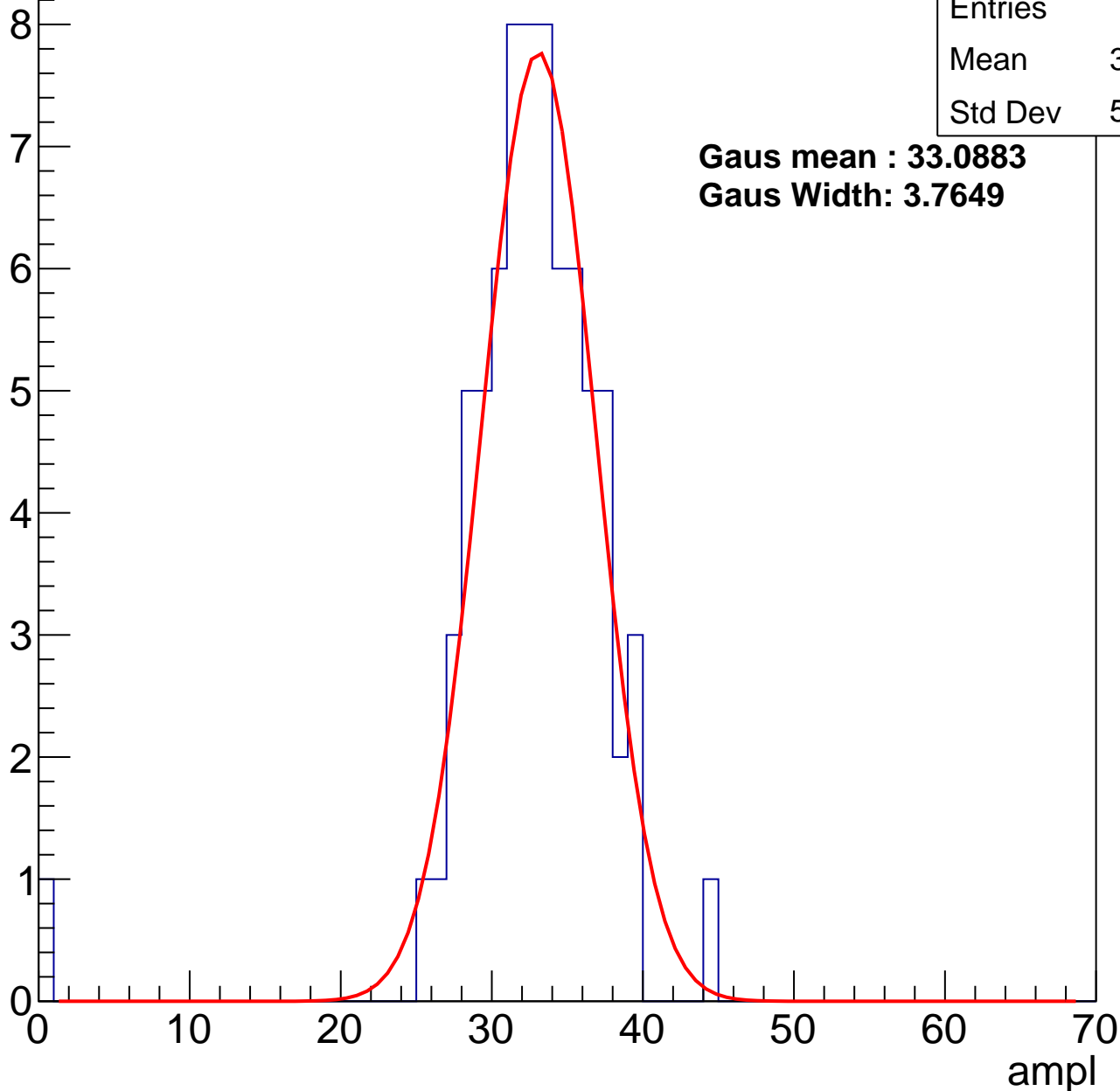
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 32.18 |
| Std Dev | 5.189 |

**Gaus mean : 33.0883**

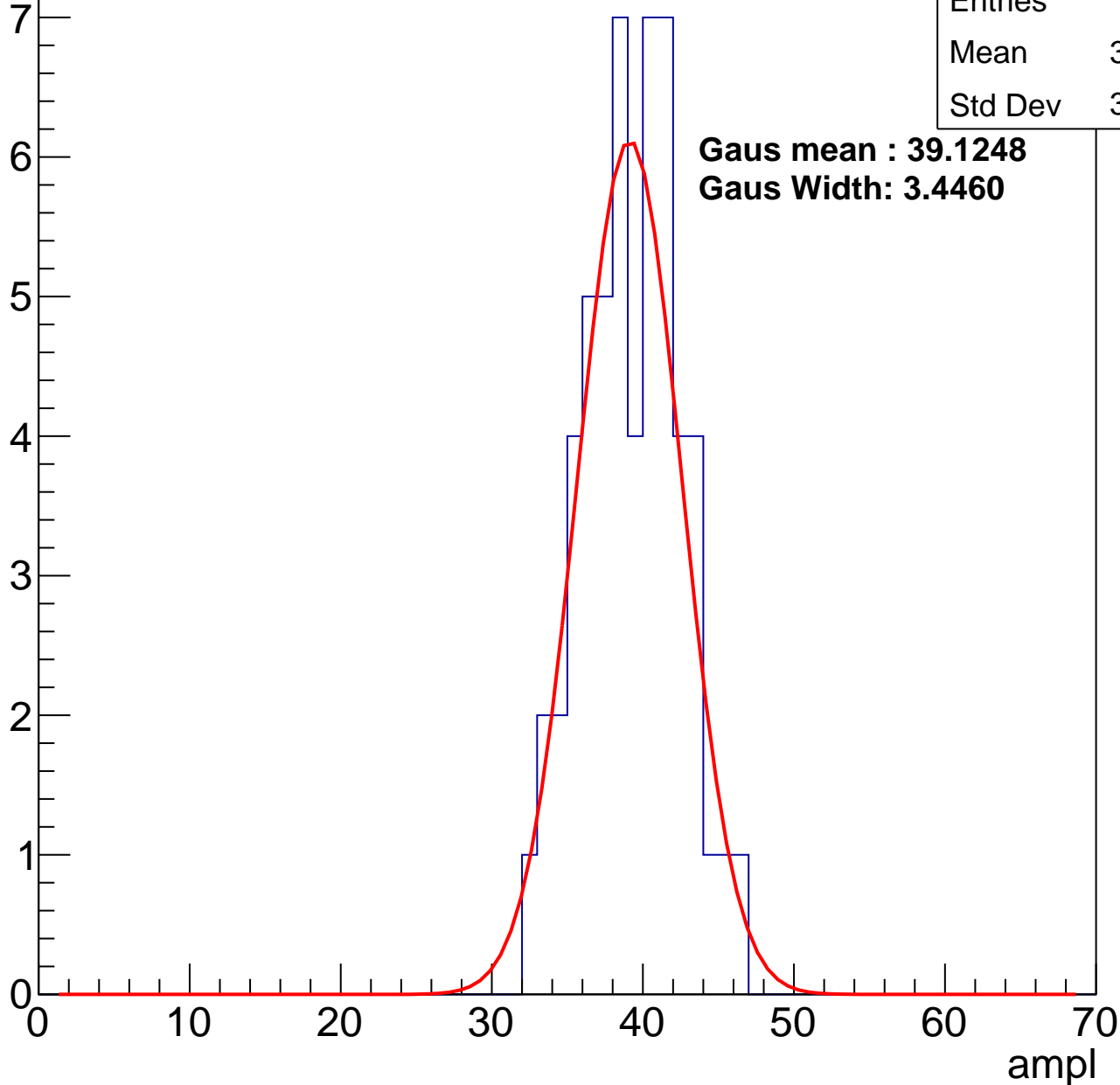
**Gaus Width: 3.7649**



# B0L002S, U1-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch47, adc2

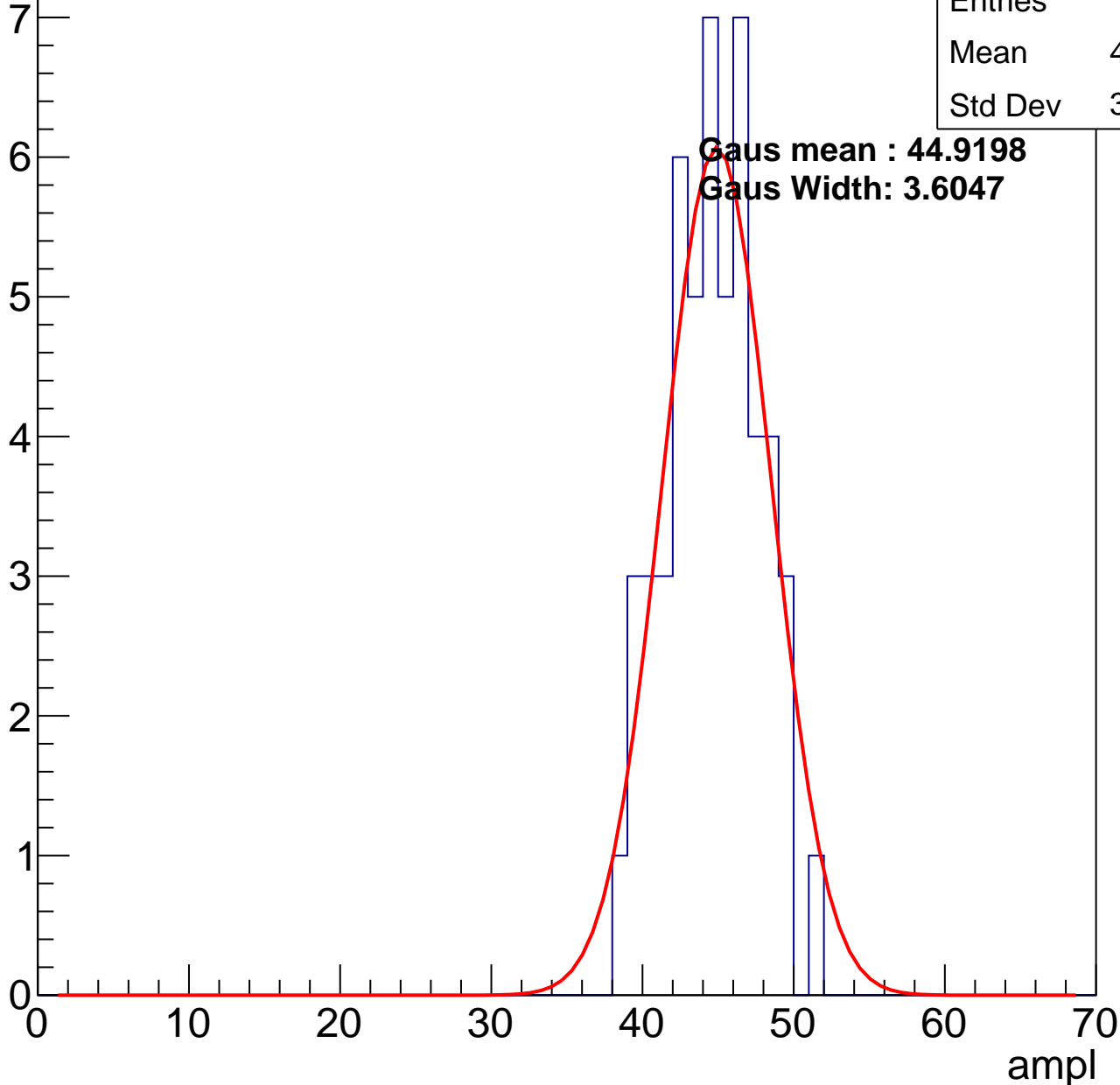
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 44.19 |
| Std Dev | 3.007 |

**Gaus mean : 44.9198**

**Gaus Width: 3.6047**

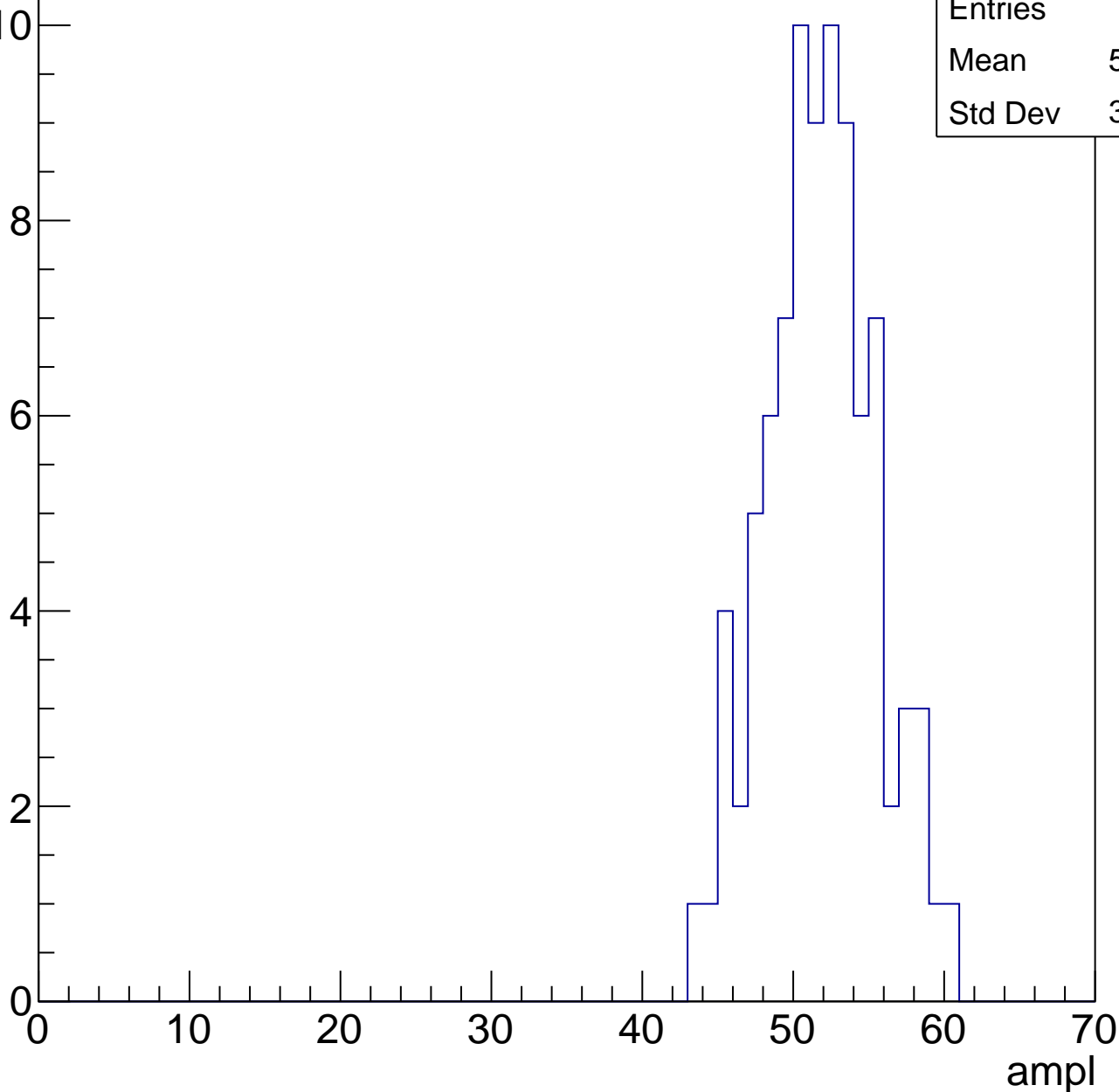


# B0L002S, U1-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

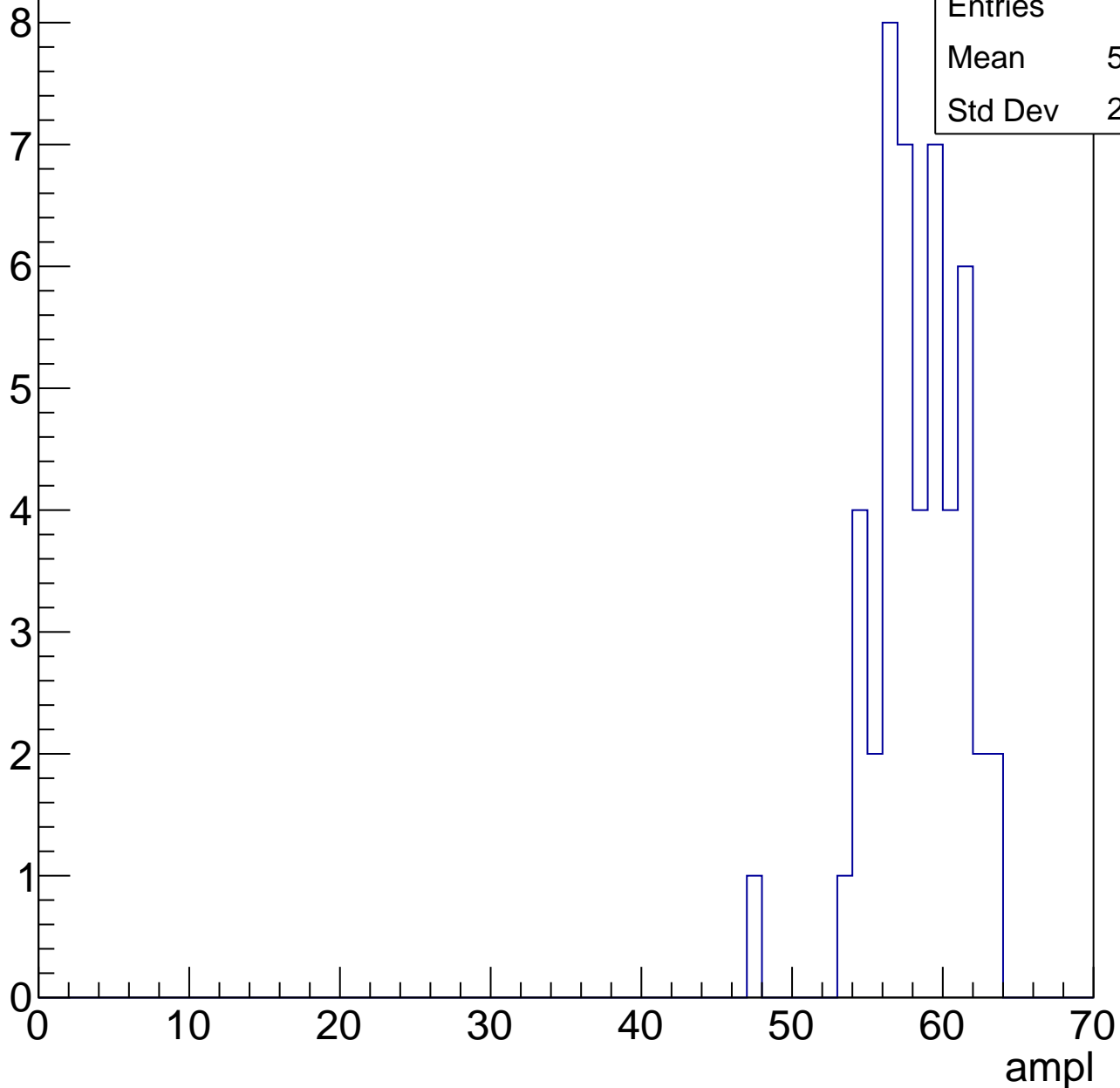
|         |       |
|---------|-------|
| Entries | 87    |
| Mean    | 51.33 |
| Std Dev | 3.606 |



# B0L002S, U1-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

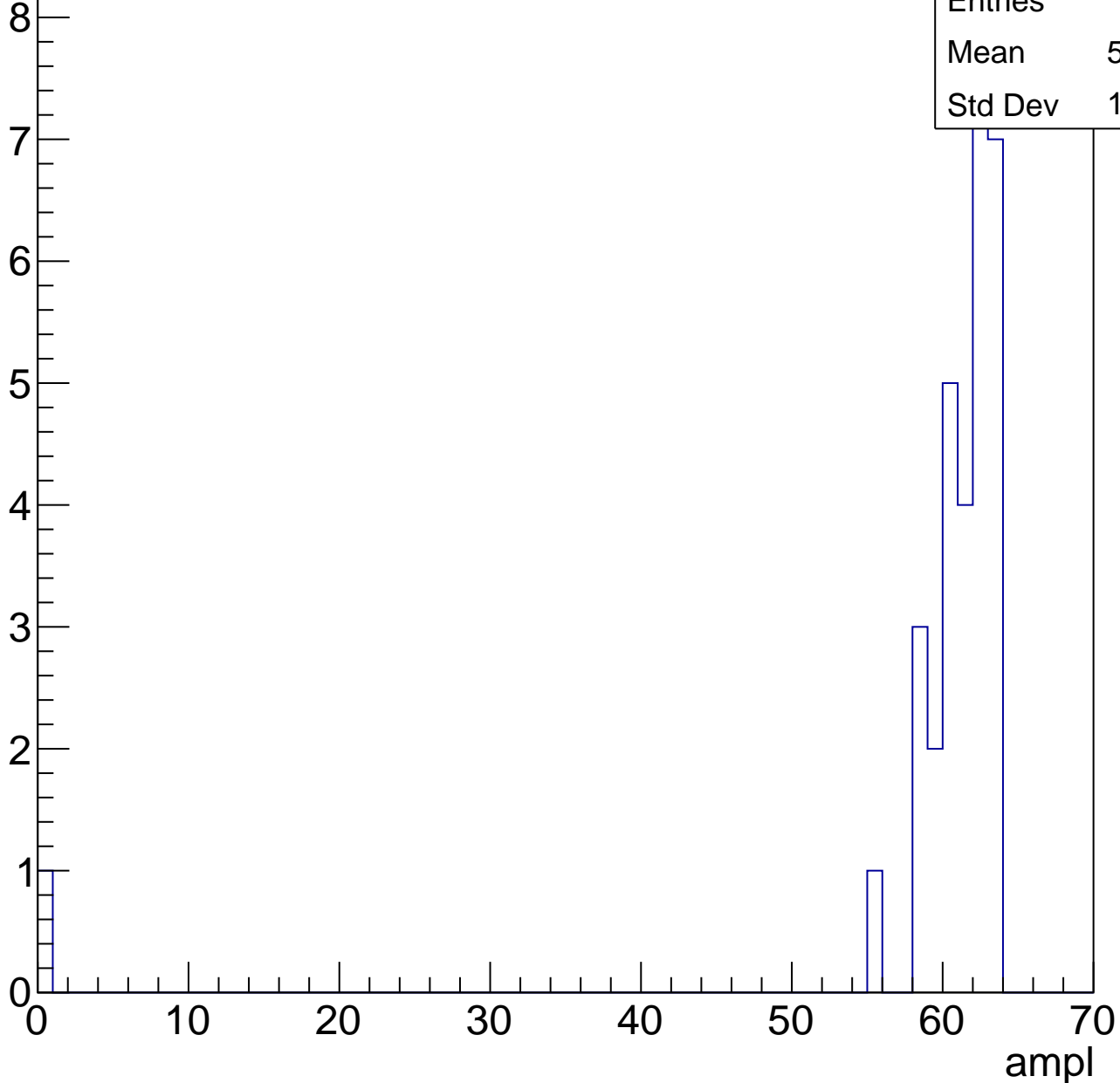


# B0L002S, U1-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 31    |
| Mean    | 58.97 |
| Std Dev | 10.93 |



# B0L002S, U1-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch48, adc0

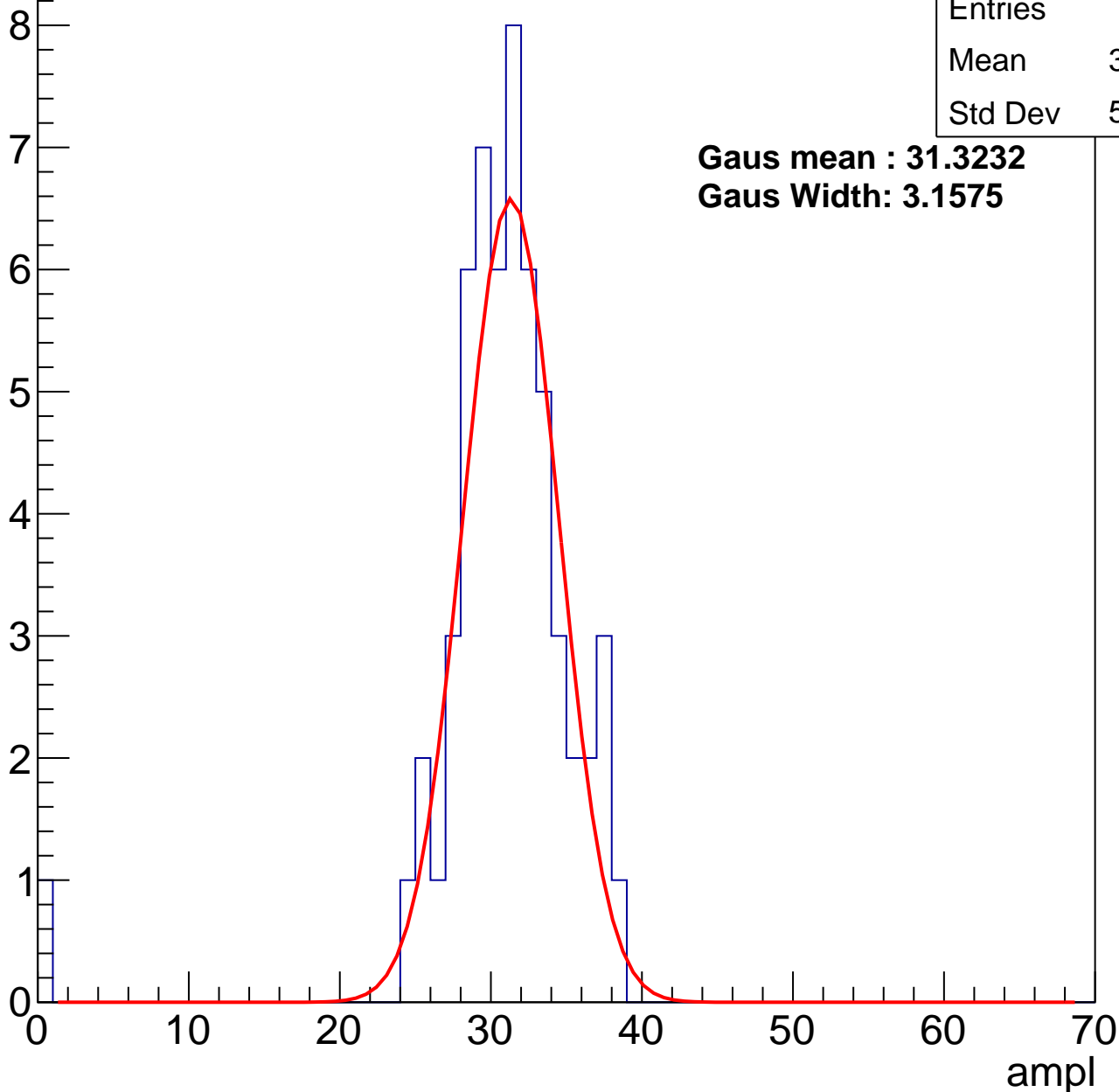
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 30.35 |
| Std Dev | 5.149 |

**Gaus mean : 31.3232**

**Gaus Width: 3.1575**



# B0L002S, U1-ch48, adc1

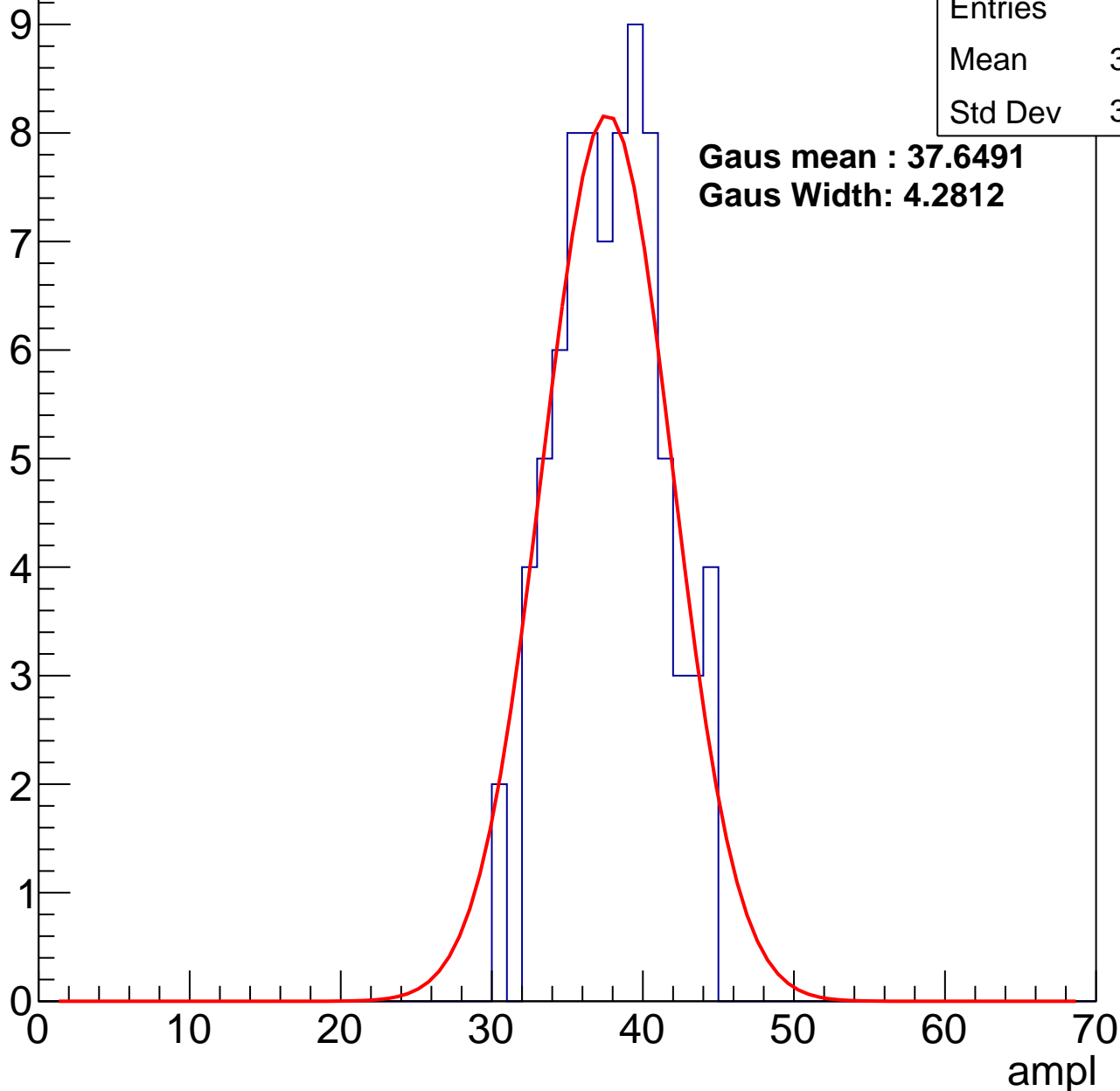
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 37.44 |
| Std Dev | 3.413 |

**Gaus mean : 37.6491**

**Gaus Width: 4.2812**



# B0L002S, U1-ch48, adc2

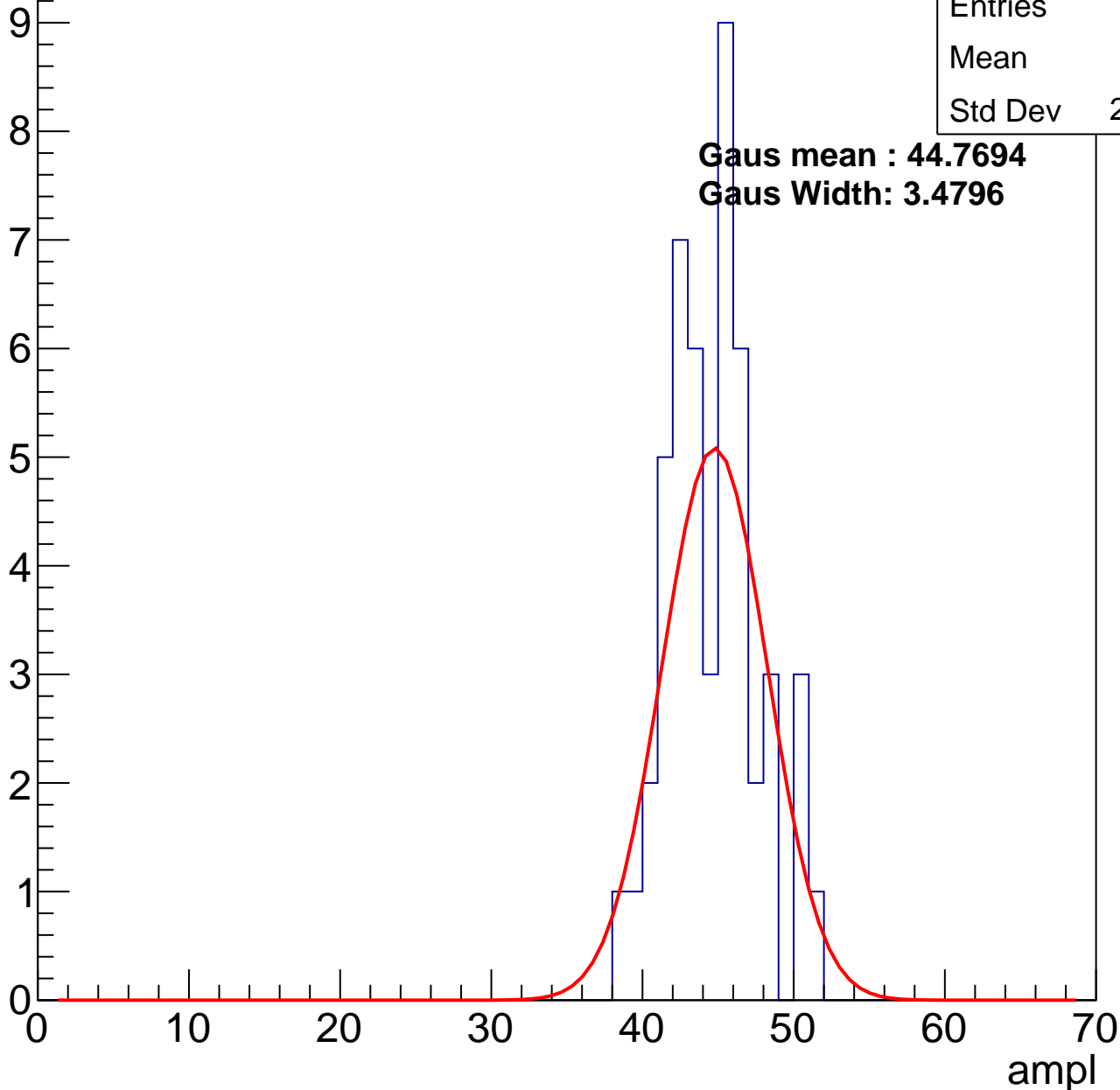
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 44.2  |
| Std Dev | 2.948 |

**Gaus mean : 44.7694**

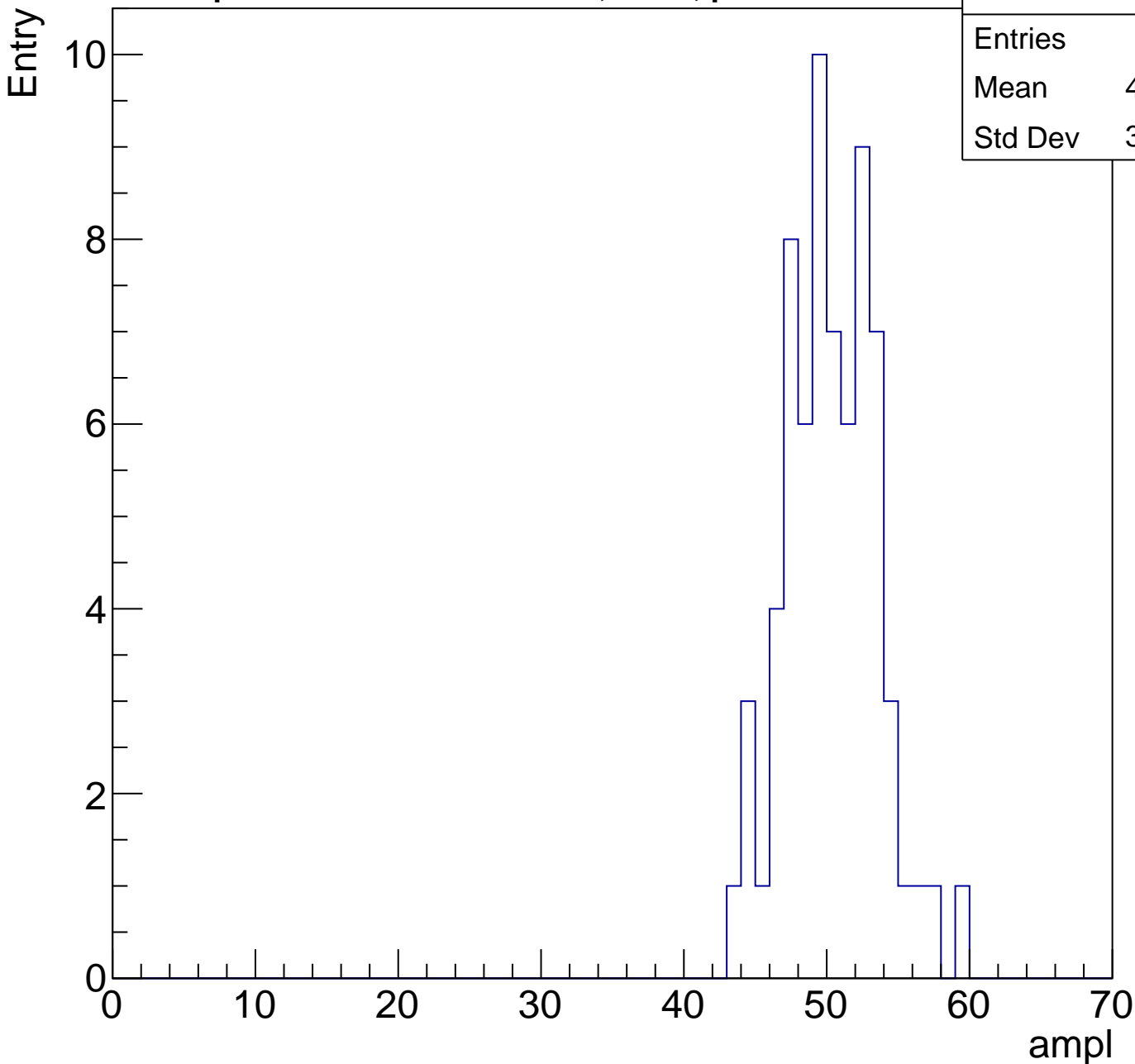
**Gaus Width: 3.4796**



# B0L002S, U1-ch48, adc3

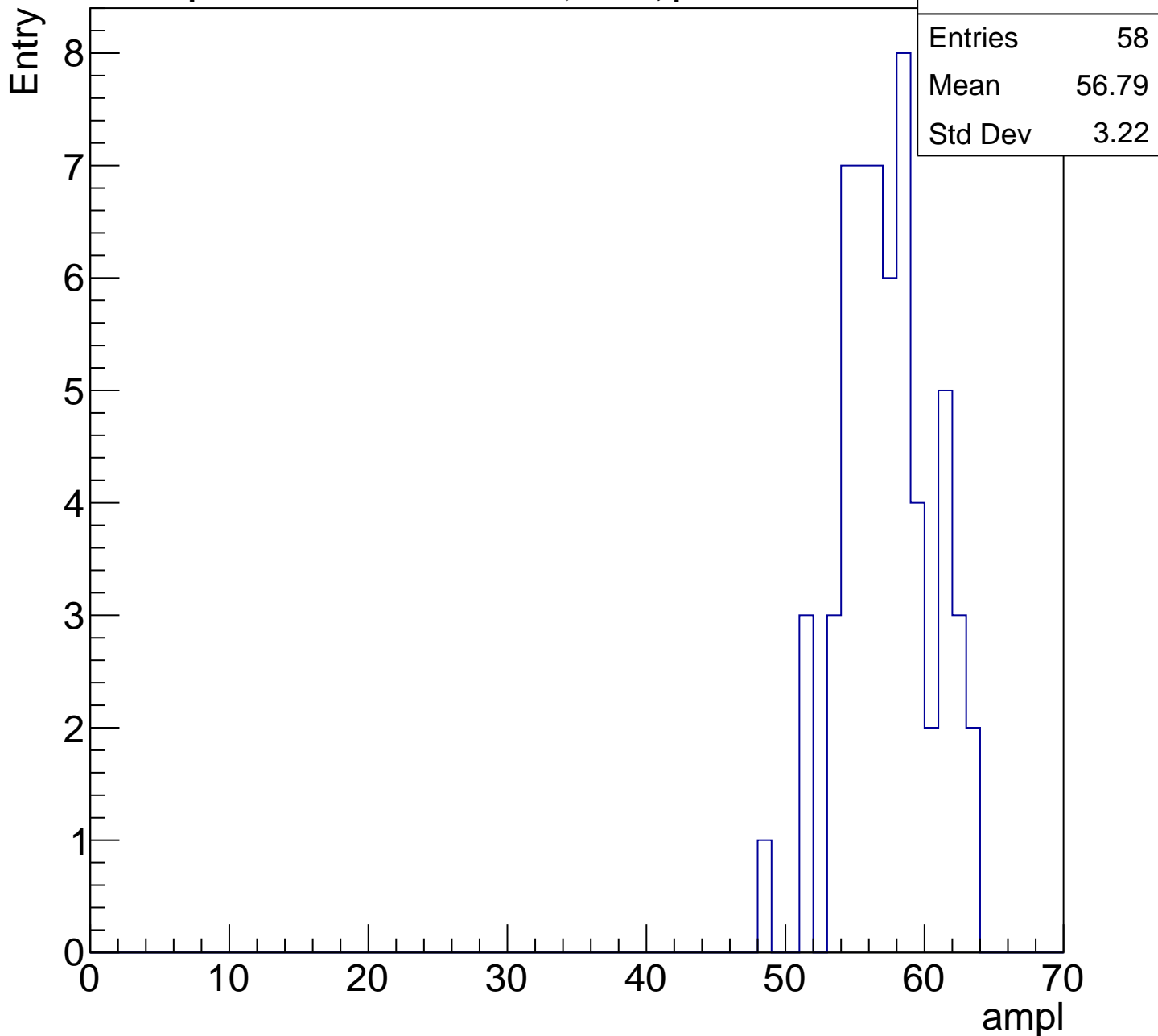
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 49.88 |
| Std Dev | 3.174 |



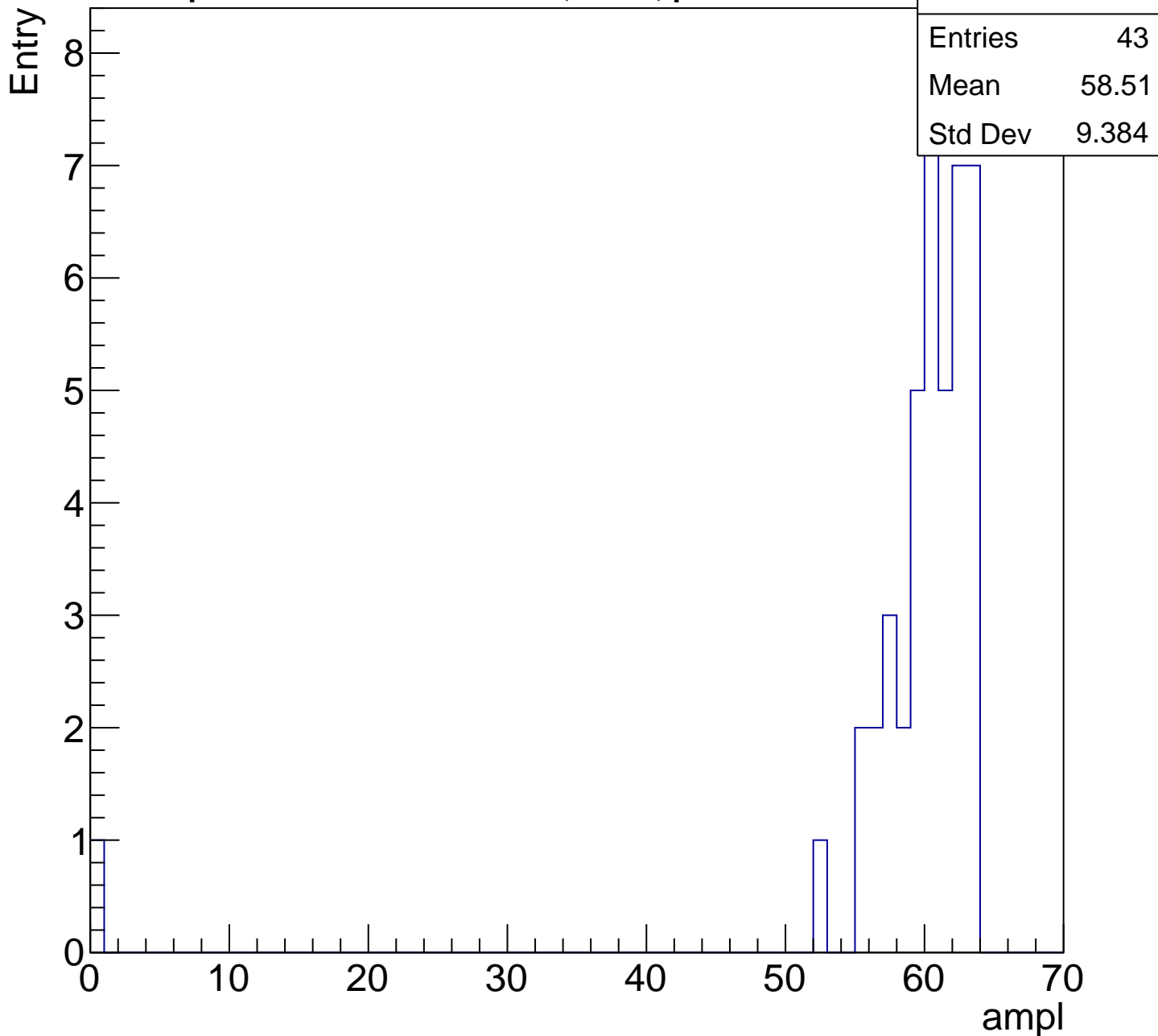
# B0L002S, U1-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 63 |
| Std Dev | 0  |

ampl



# B0L002S, U1-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch49, adc0

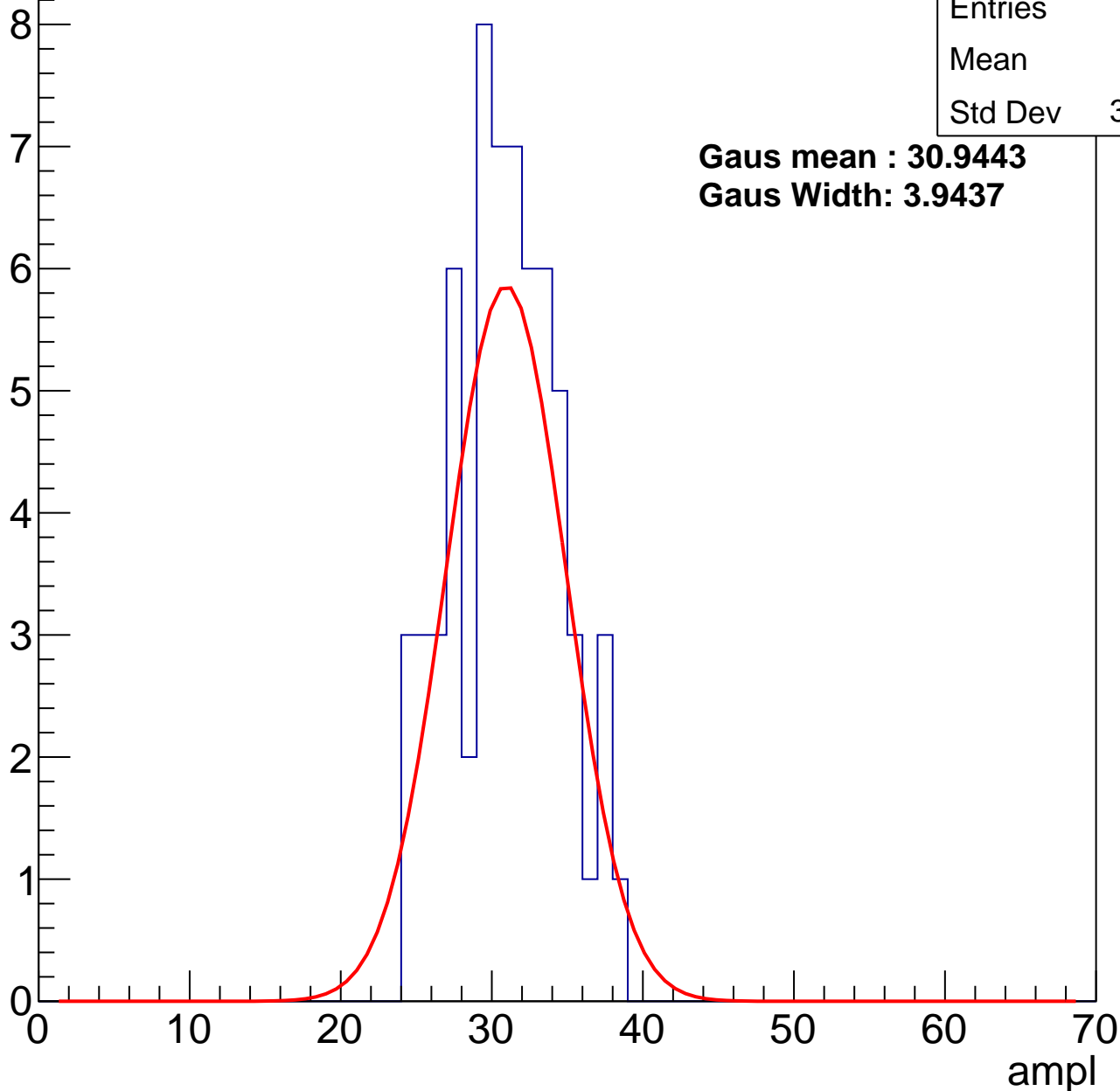
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 30.5  |
| Std Dev | 3.473 |

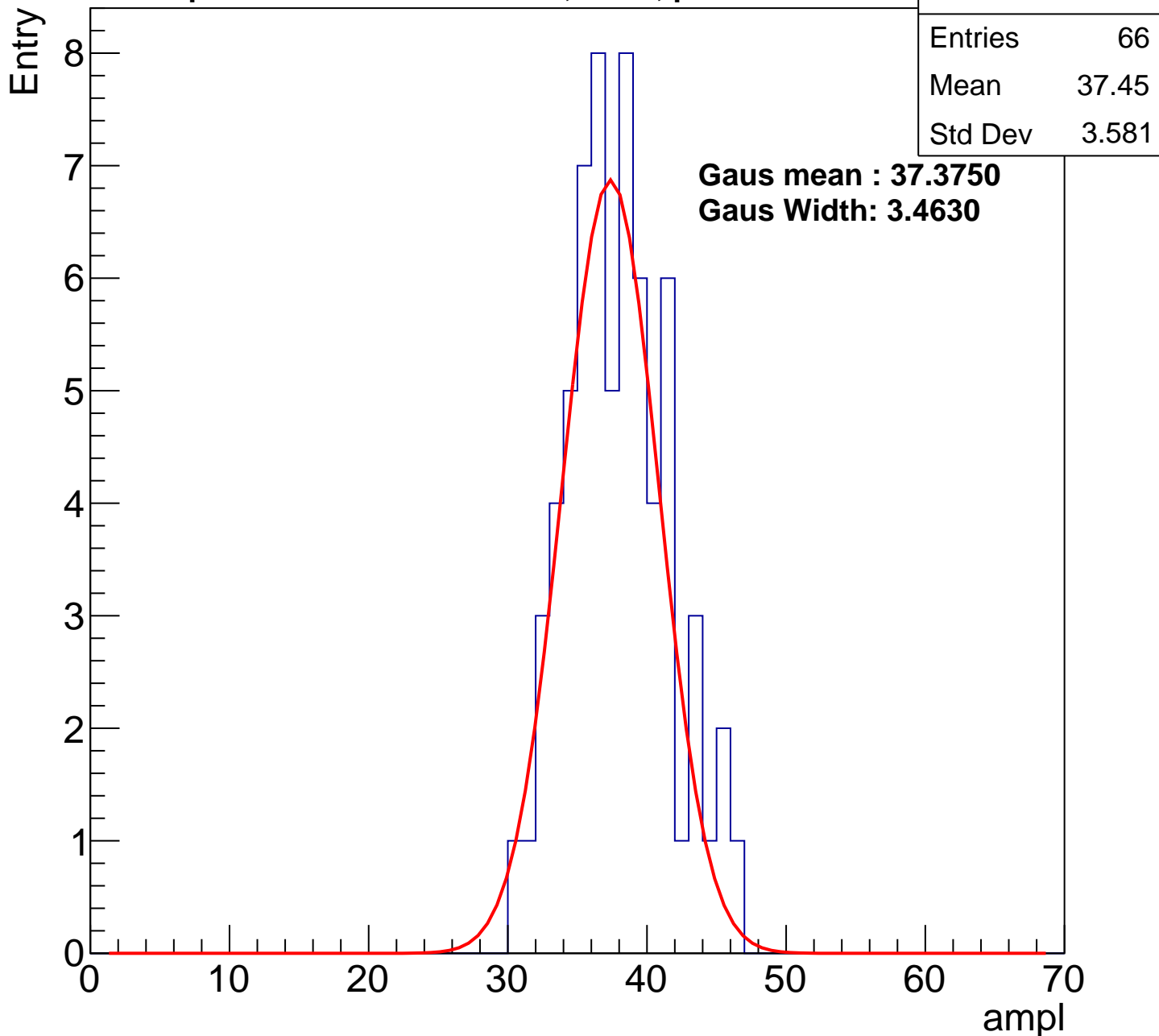
**Gaus mean : 30.9443**

**Gaus Width: 3.9437**



# B0L002S, U1-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch49, adc2

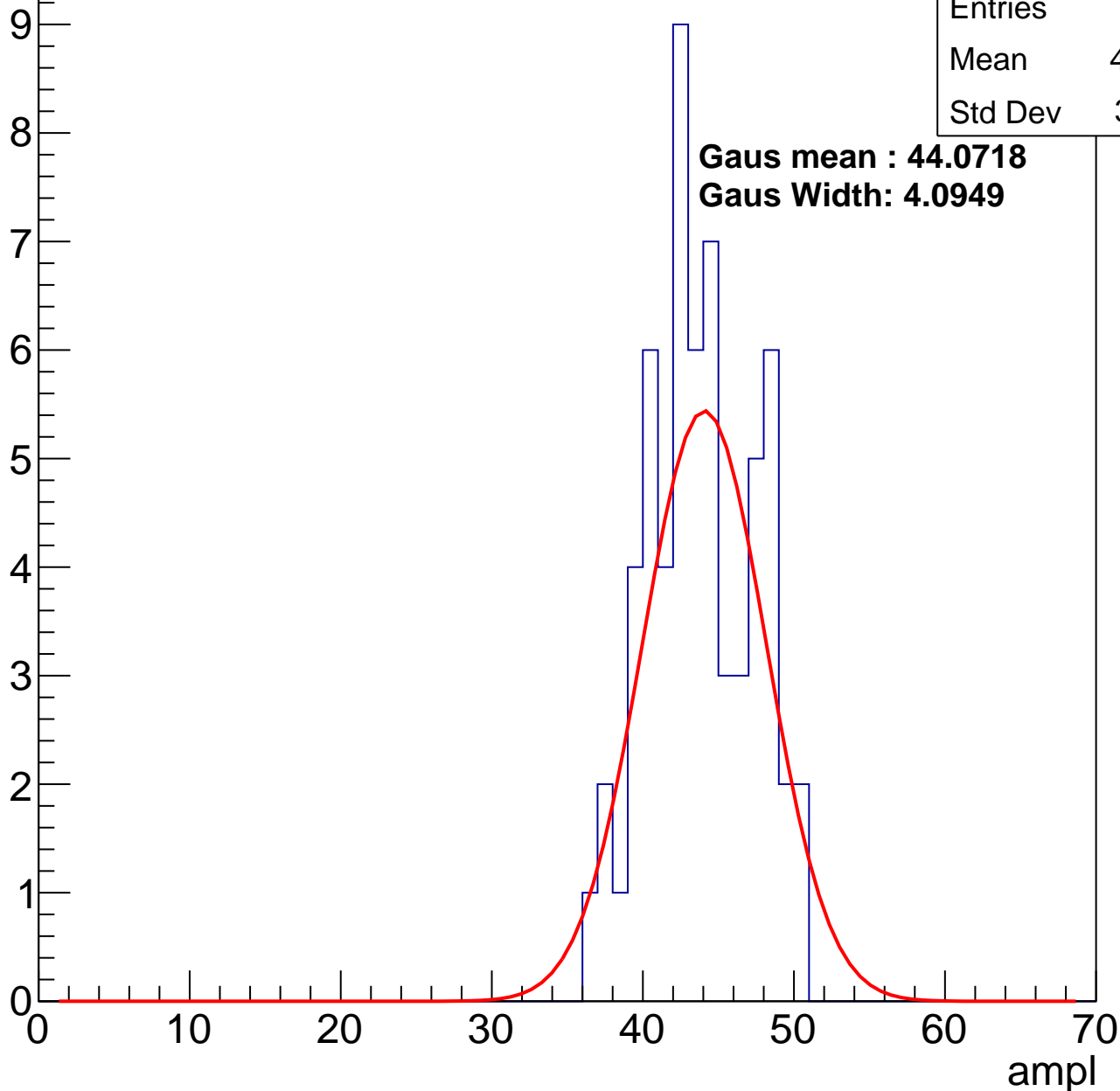
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 43.38 |
| Std Dev | 3.451 |

**Gaus mean : 44.0718**

**Gaus Width: 4.0949**

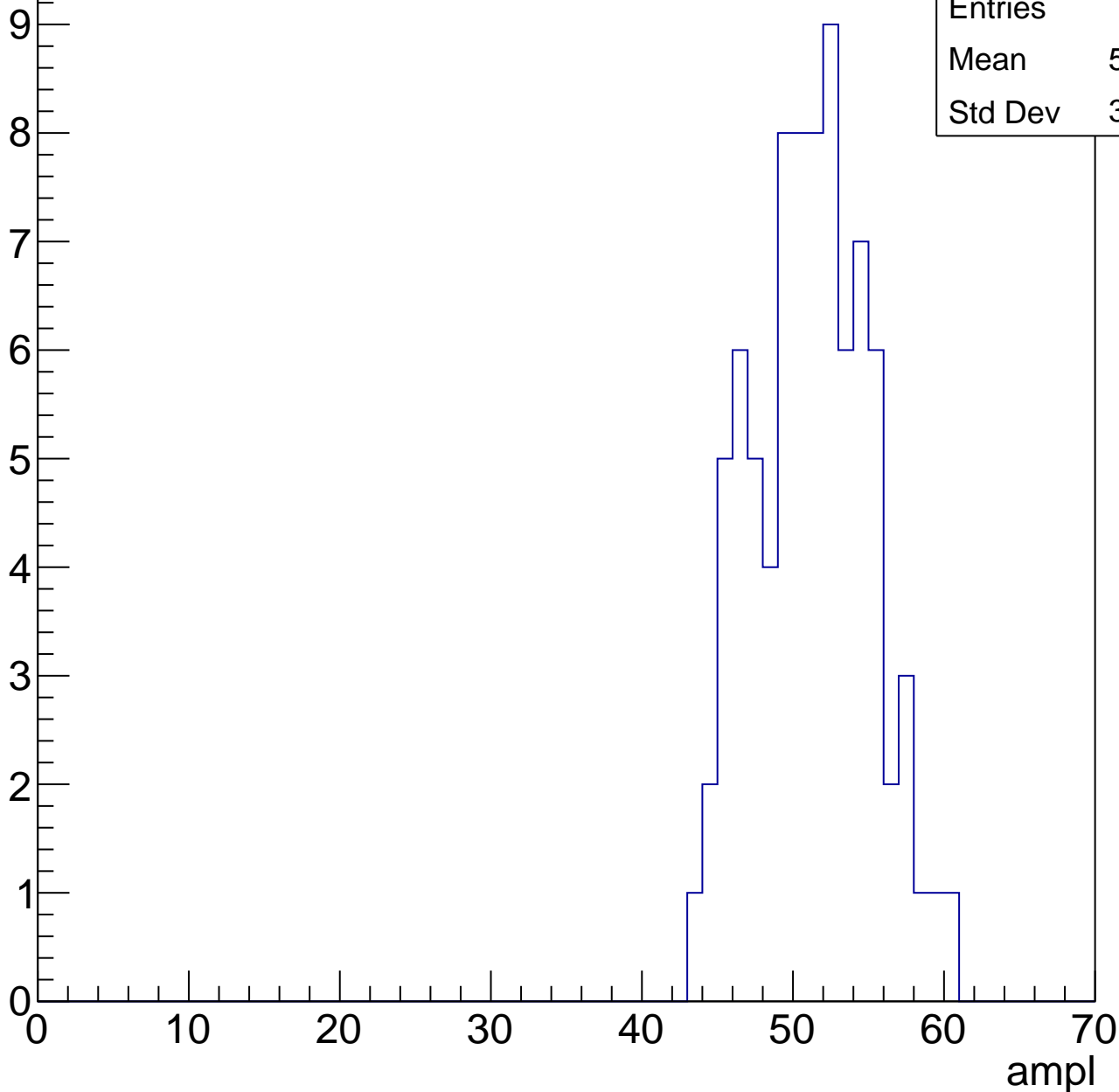


# B0L002S, U1-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 50.76 |
| Std Dev | 3.782 |

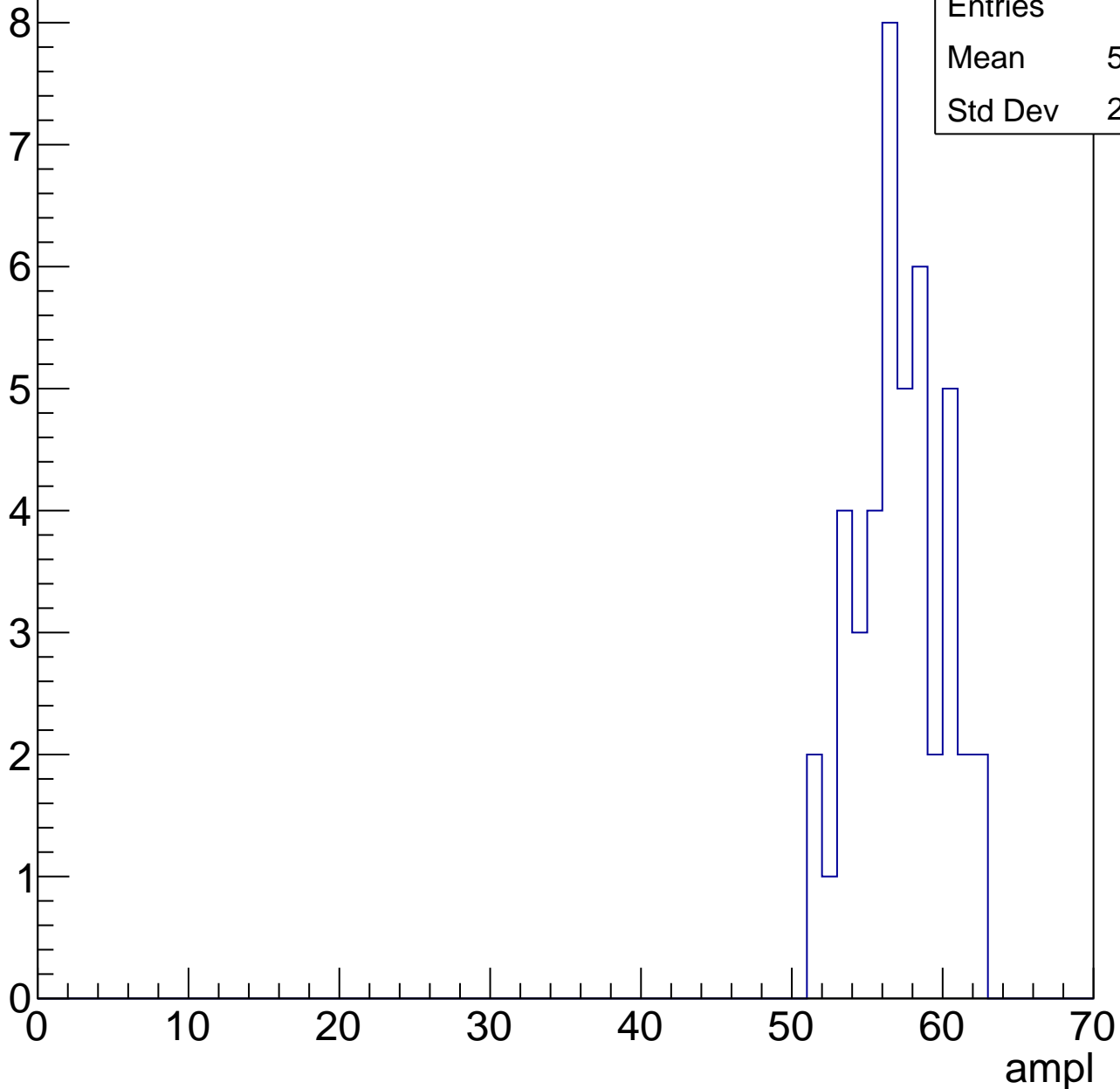


# B0L002S, U1-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 56.66 |
| Std Dev | 2.812 |



# B0L002S, U1-ch49, adc5

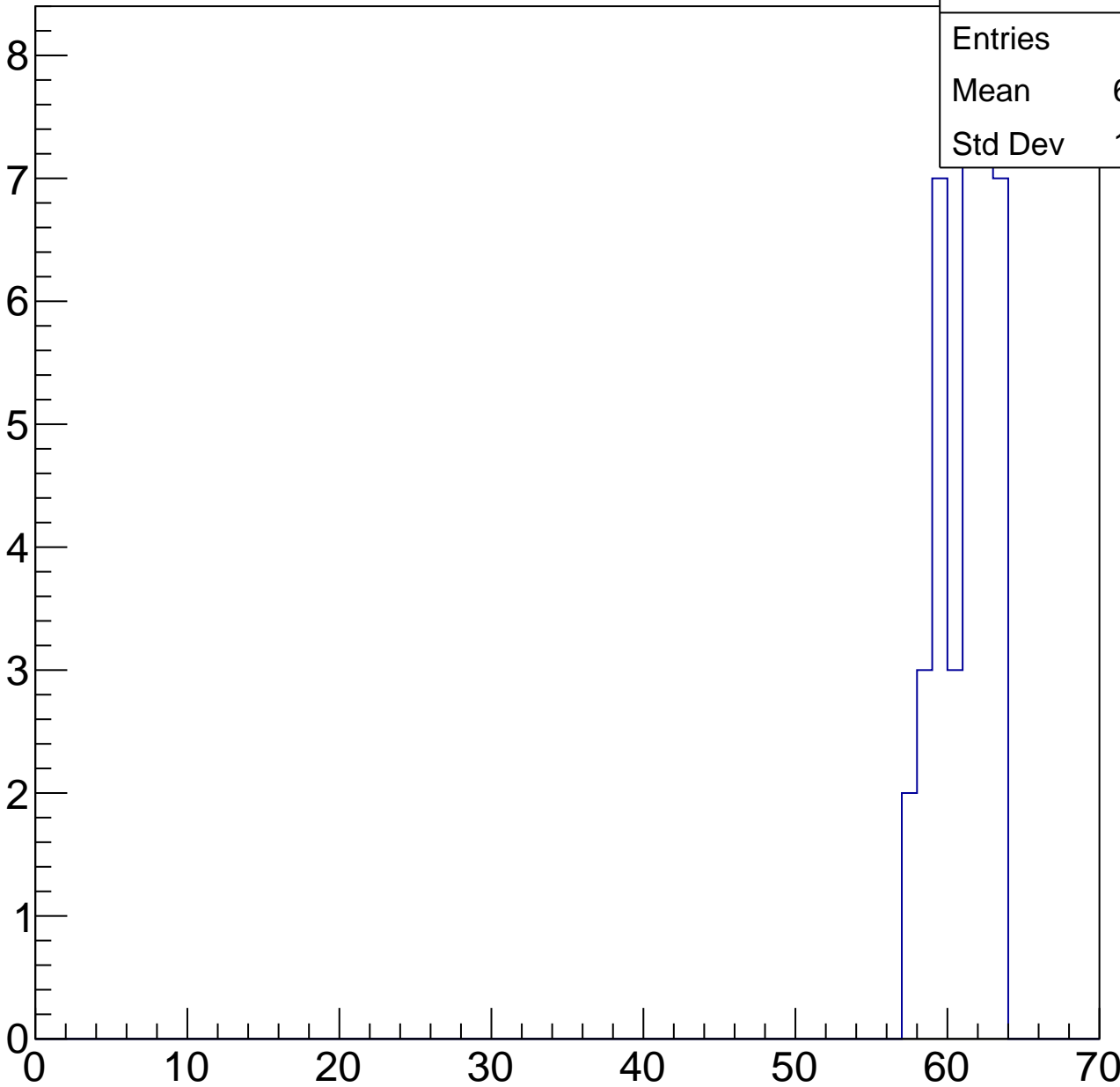
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 60.68 |
| Std Dev | 1.793 |

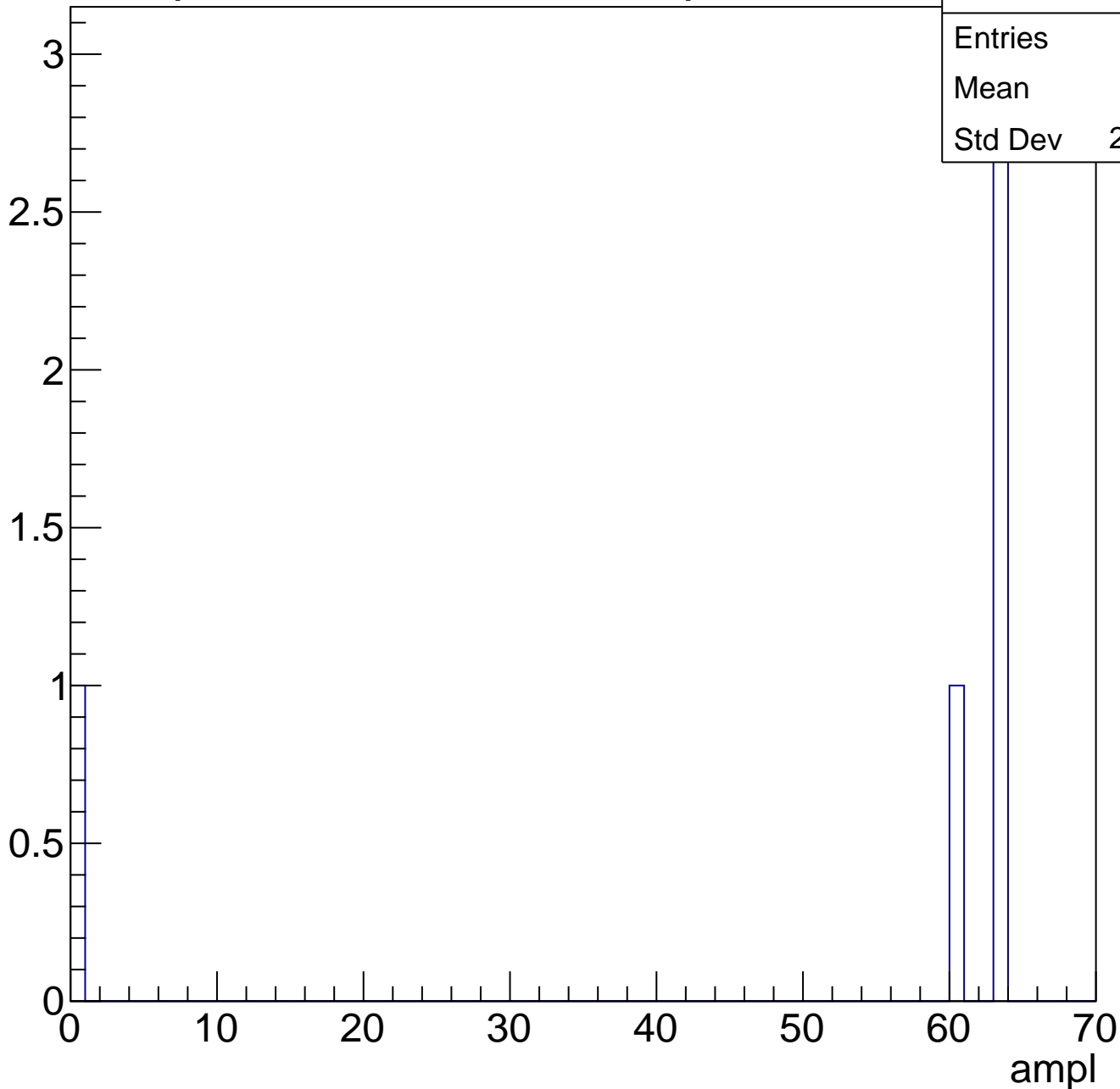
ampl



# B0L002S, U1-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch50, adc0

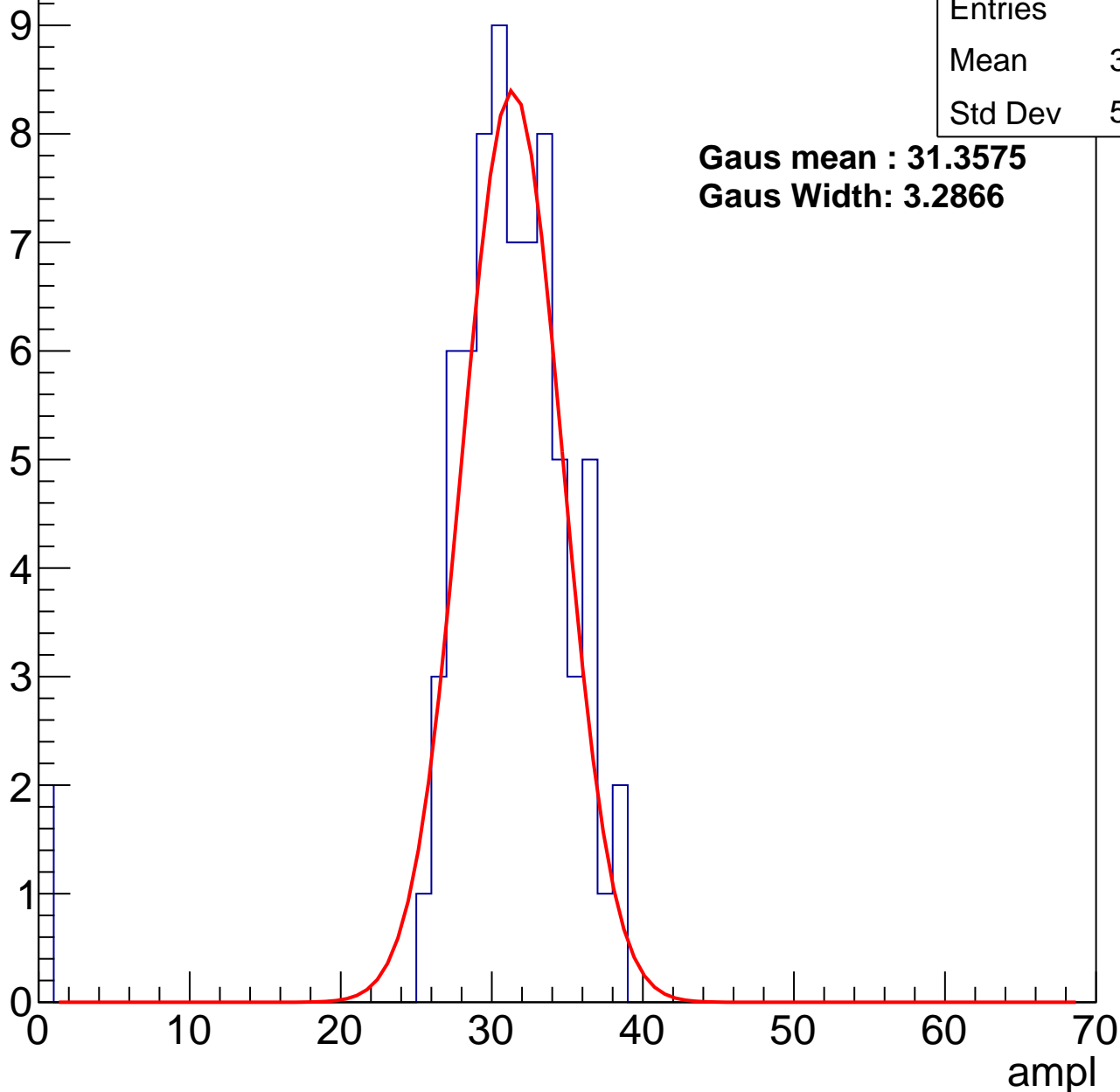
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 30.25 |
| Std Dev | 5.938 |

**Gaus mean : 31.3575**

**Gaus Width: 3.2866**



# B0L002S, U1-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 37.95 |
| Std Dev | 3.229 |

**Gaus mean : 38.8174**

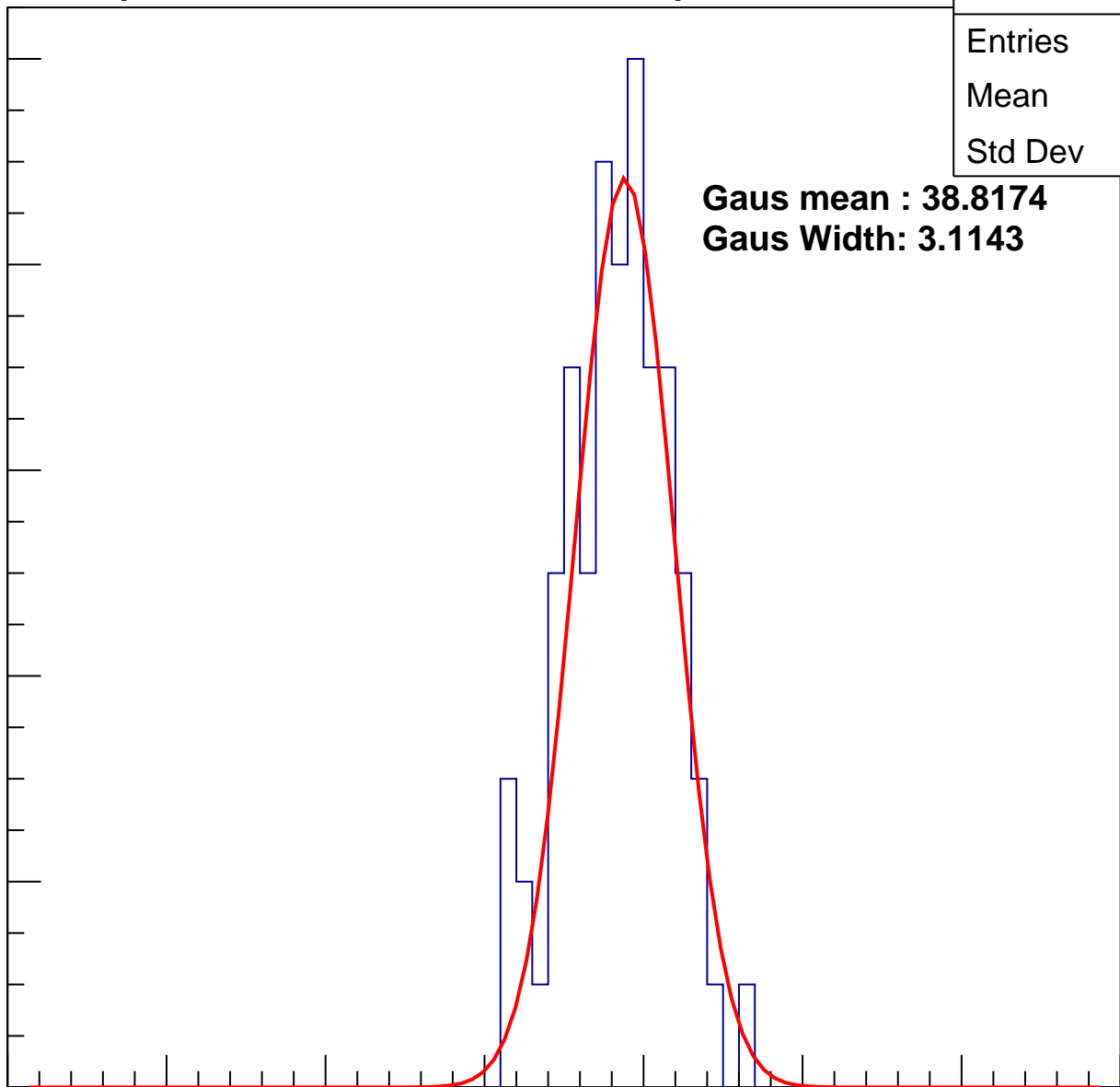
**Gaus Width: 3.1143**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch50, adc2

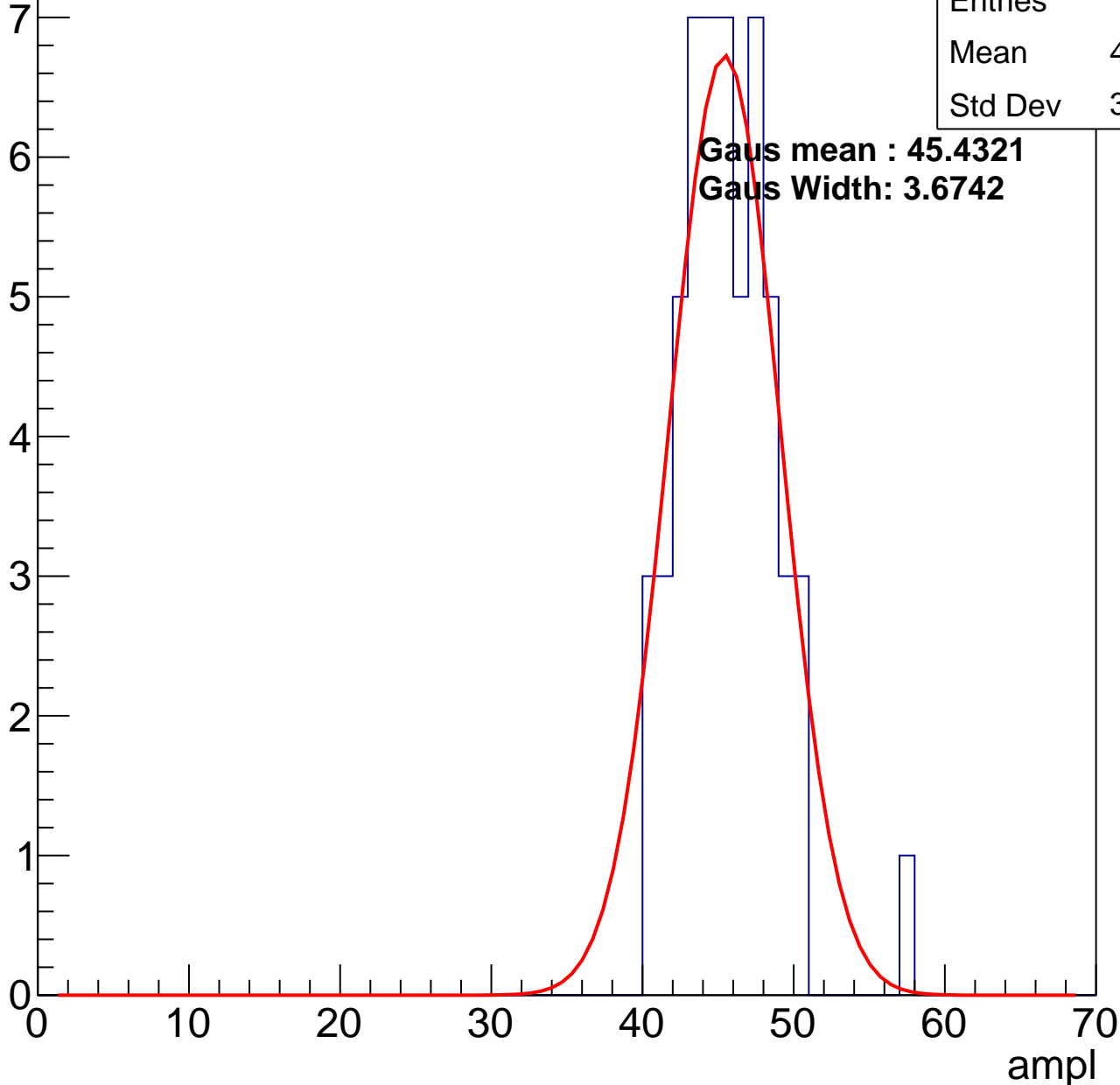
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 45.18 |
| Std Dev | 3.123 |

**Gaus mean : 45.4321**

**Gaus Width: 3.6742**

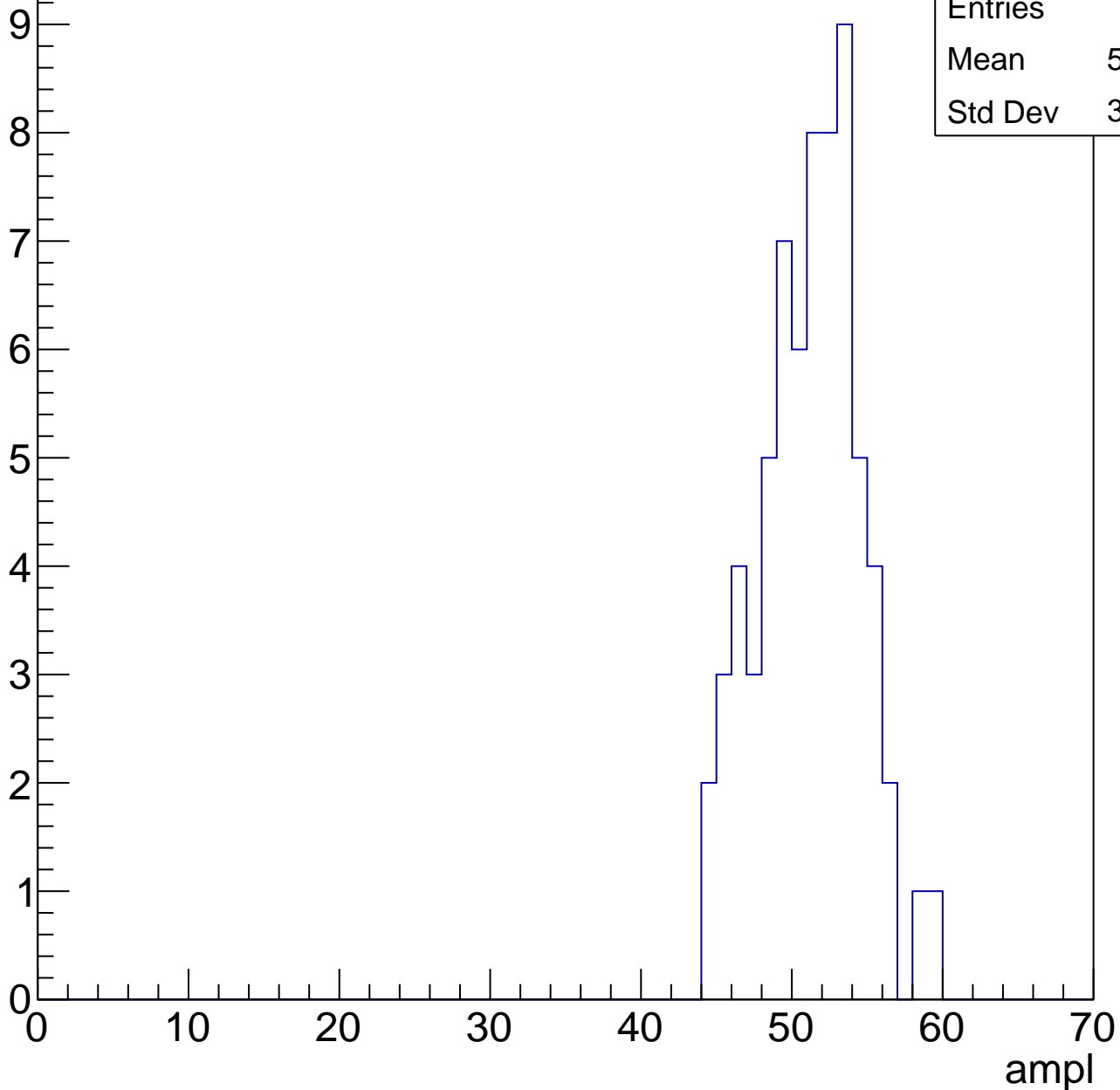


# B0L002S, U1-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 50.75 |
| Std Dev | 3.309 |

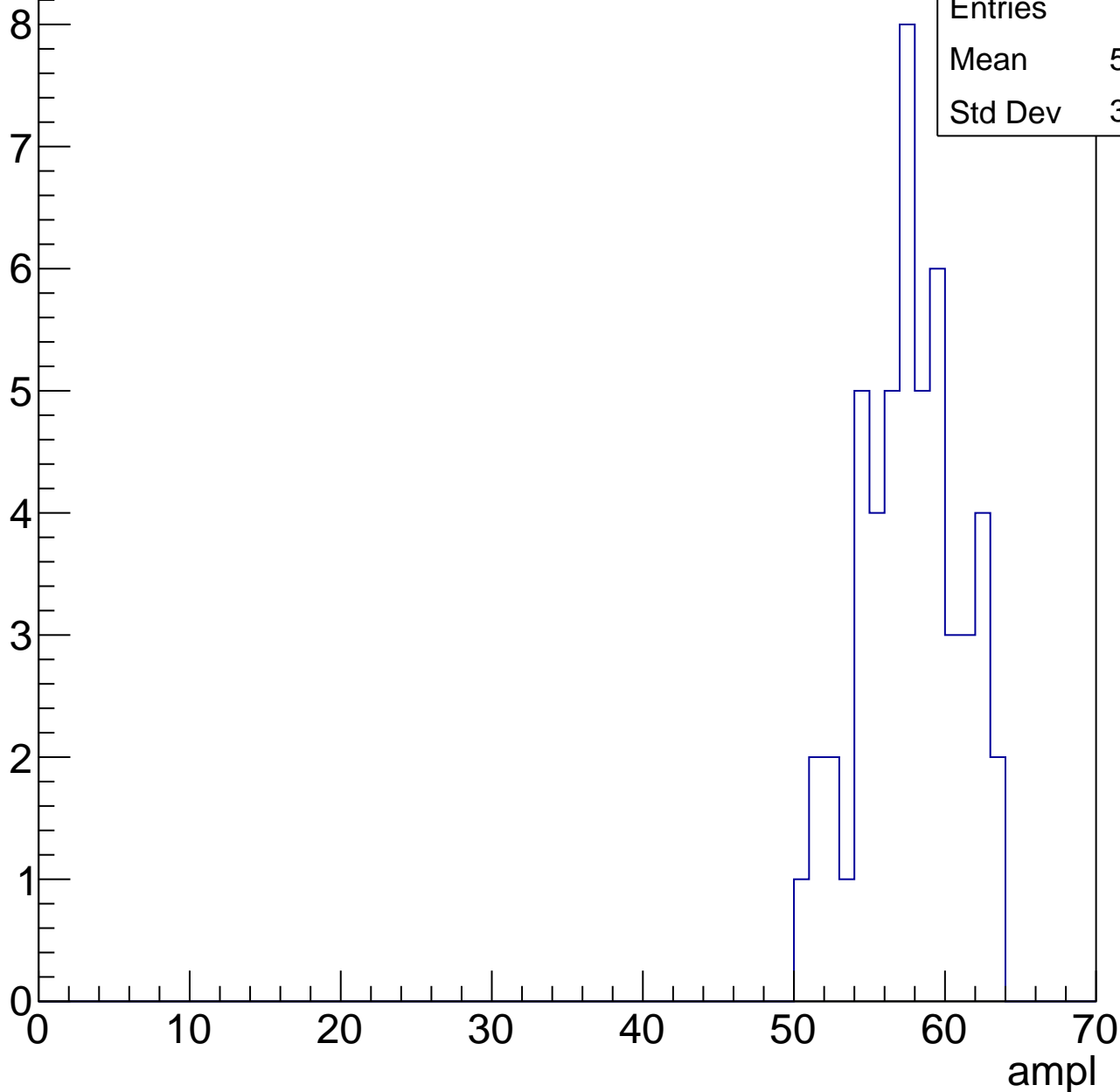


# B0L002S, U1-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 57.18 |
| Std Dev | 3.216 |

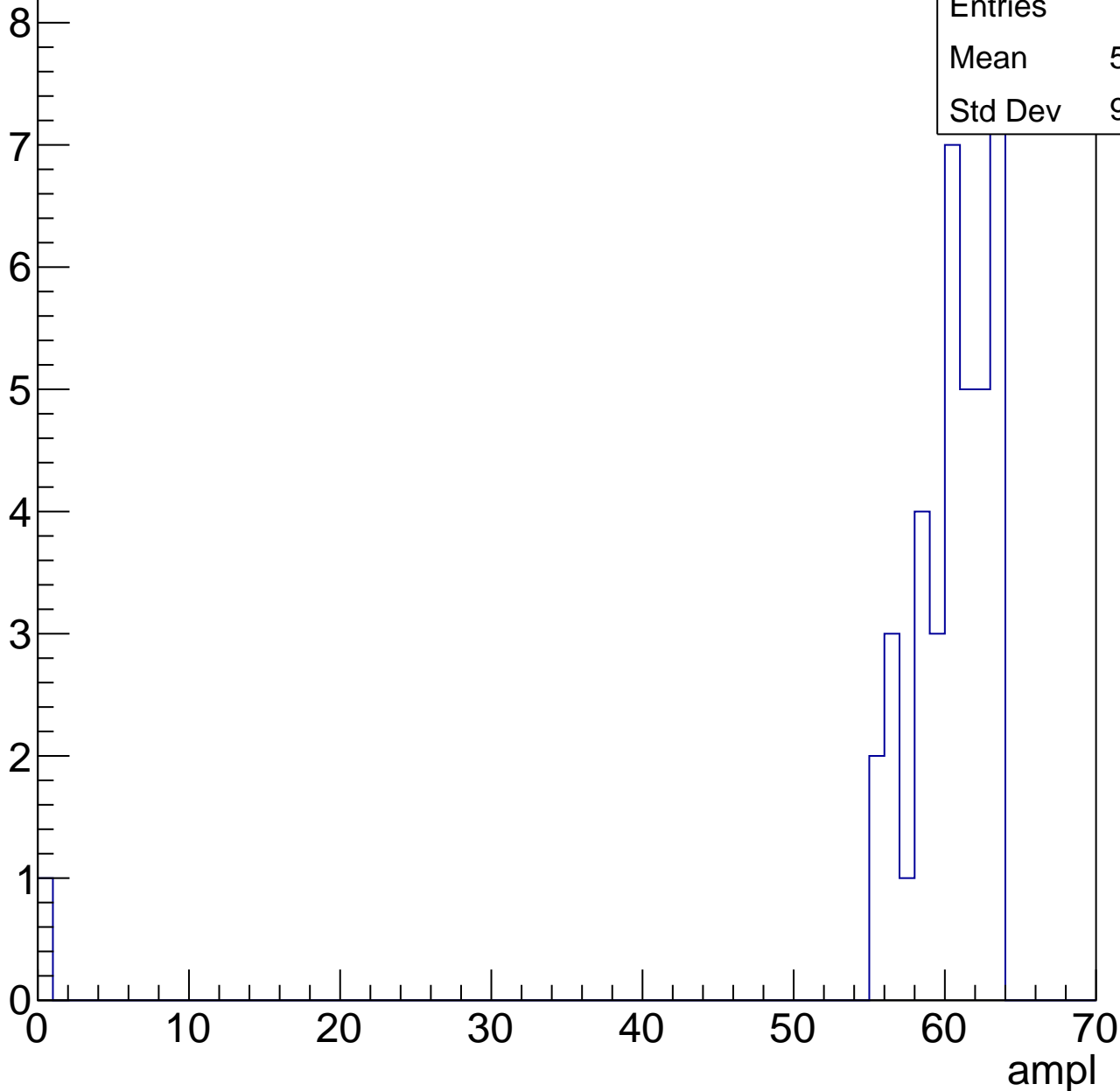


# B0L002S, U1-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

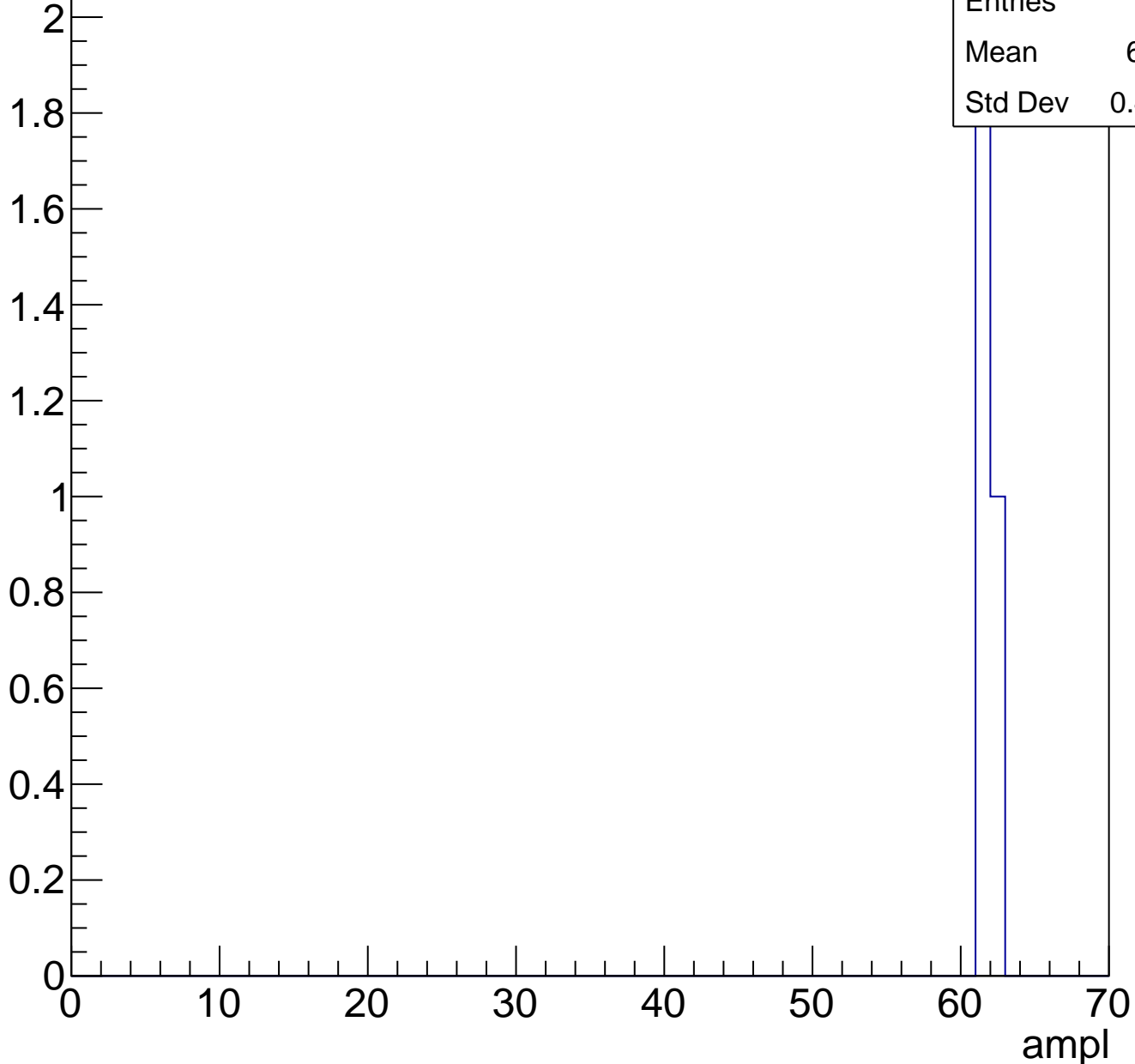
|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 58.54 |
| Std Dev | 9.792 |



# B0L002S, U1-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



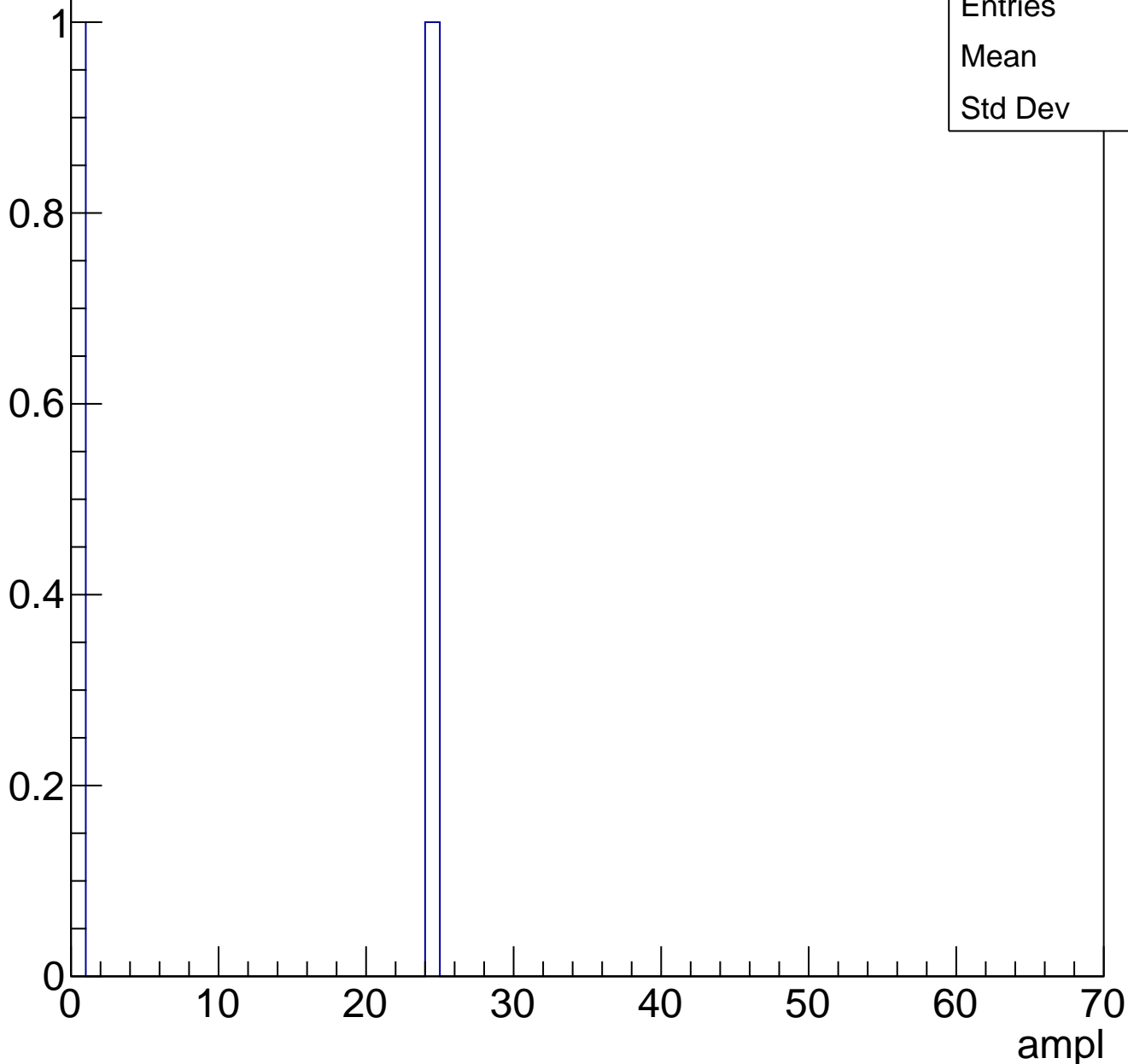
|         |        |
|---------|--------|
| Entries | 3      |
| Mean    | 61.33  |
| Std Dev | 0.4714 |



# B0L002S, U1-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch51, adc0

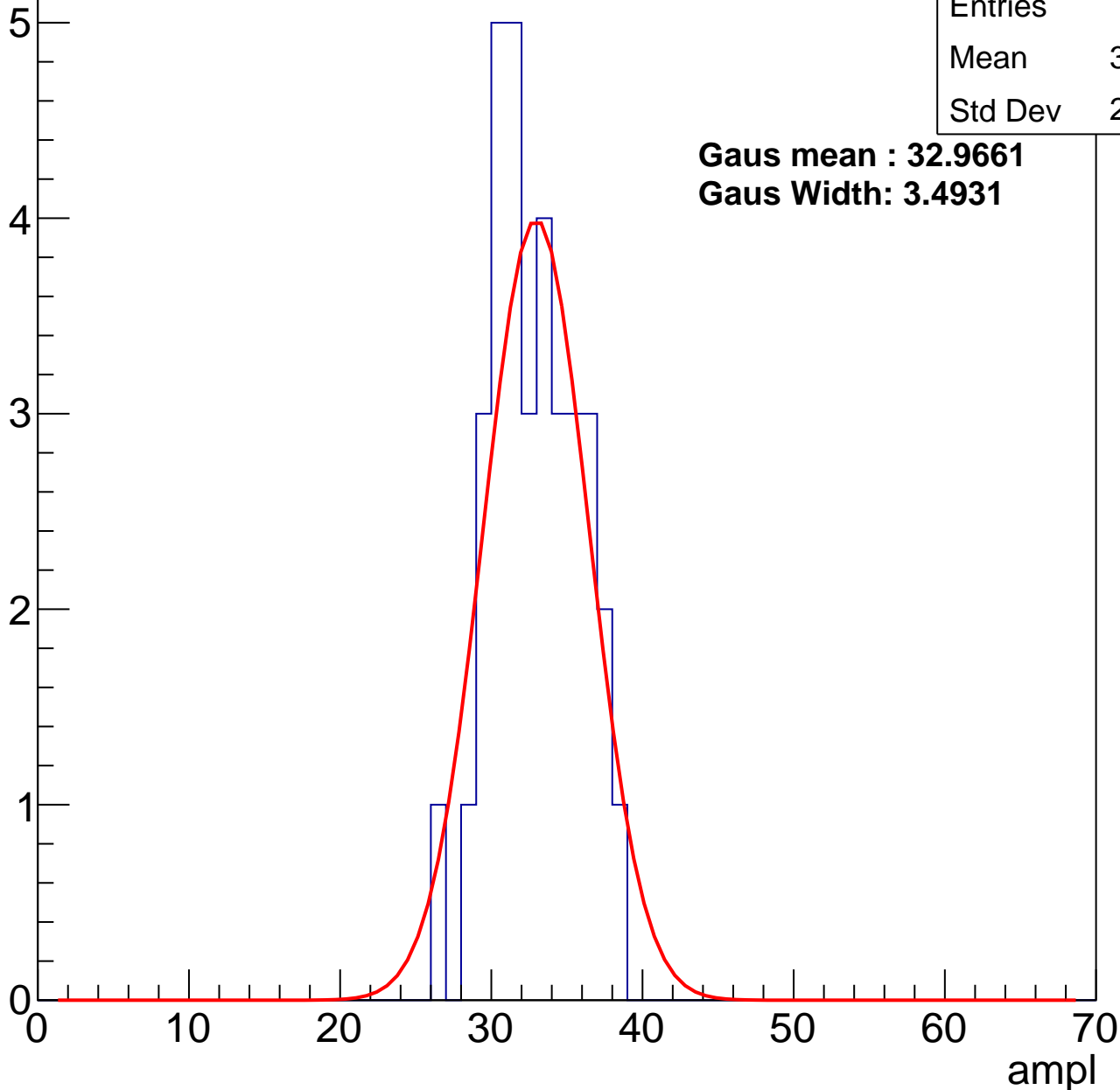
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 32.38 |
| Std Dev | 2.849 |

**Gaus mean : 32.9661**

**Gaus Width: 3.4931**



# B0L002S, U1-ch51, adc1

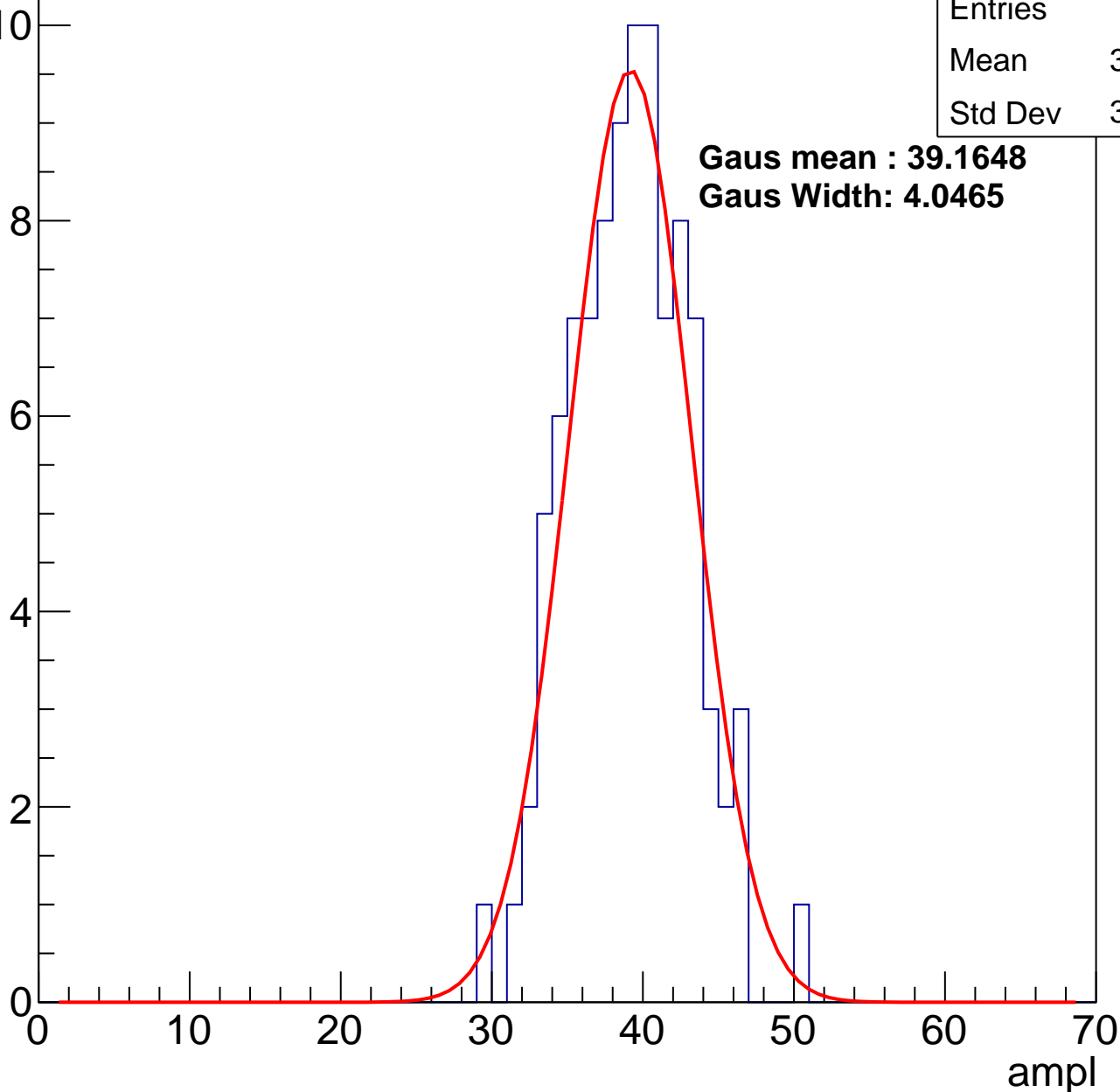
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 97    |
| Mean    | 38.68 |
| Std Dev | 3.846 |

**Gaus mean : 39.1648**

**Gaus Width: 4.0465**



# B0L002S, U1-ch51, adc2

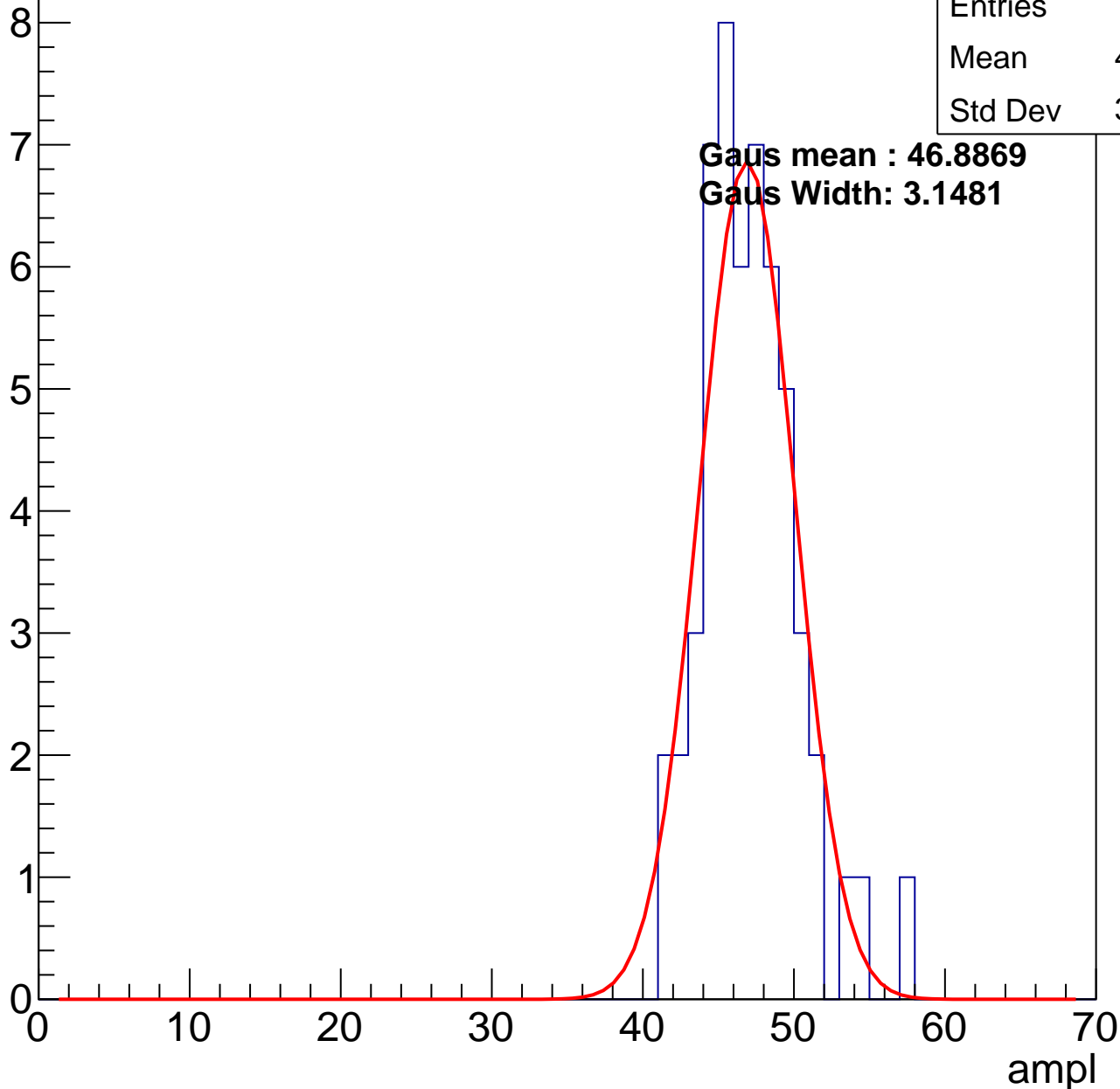
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 46.61 |
| Std Dev | 3.141 |

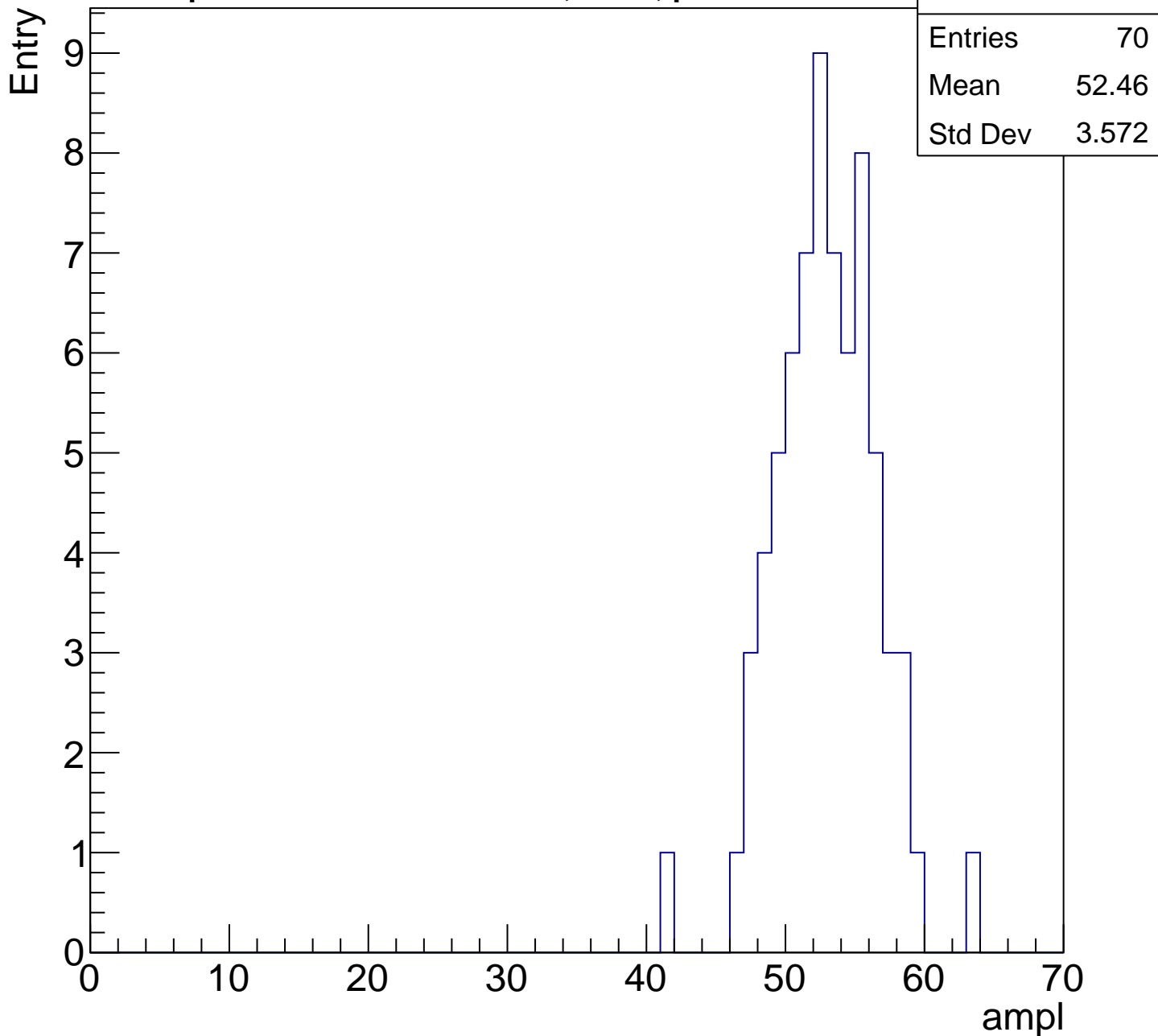
Gaus mean : 46.8869

Gaus Width: 3.1481



# B0L002S, U1-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

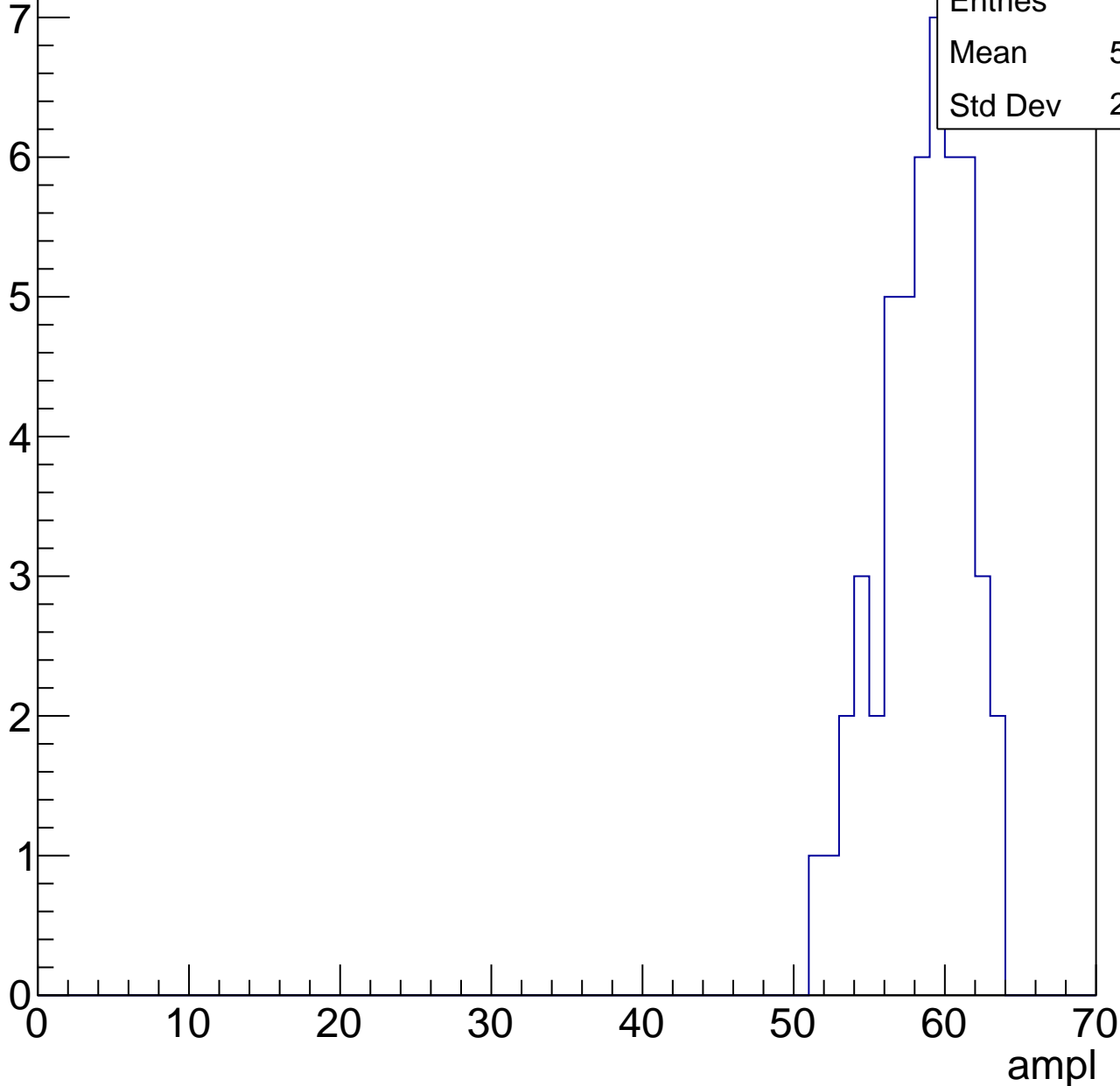


# B0L002S, U1-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 58.06 |
| Std Dev | 2.888 |

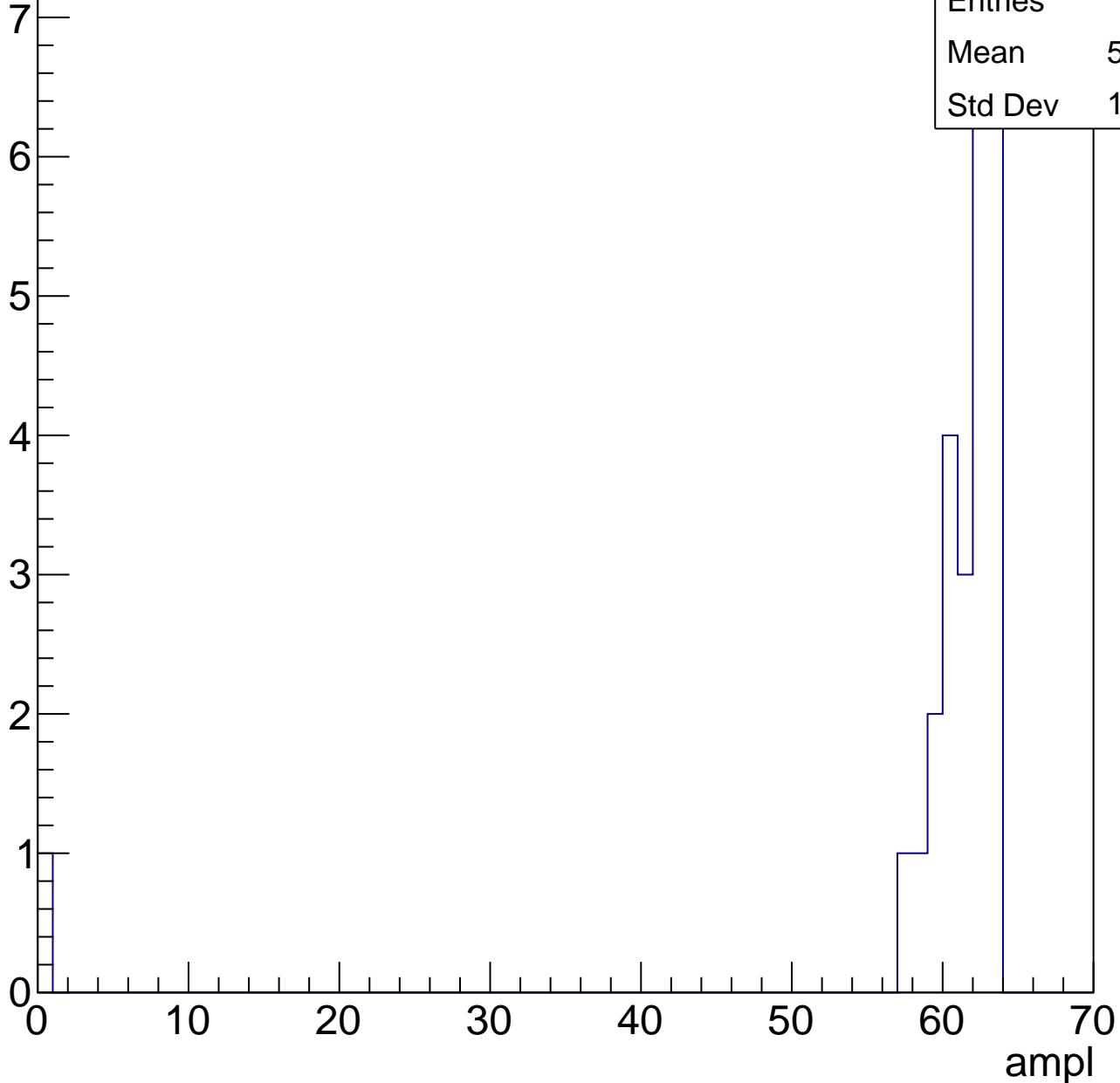


# B0L002S, U1-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

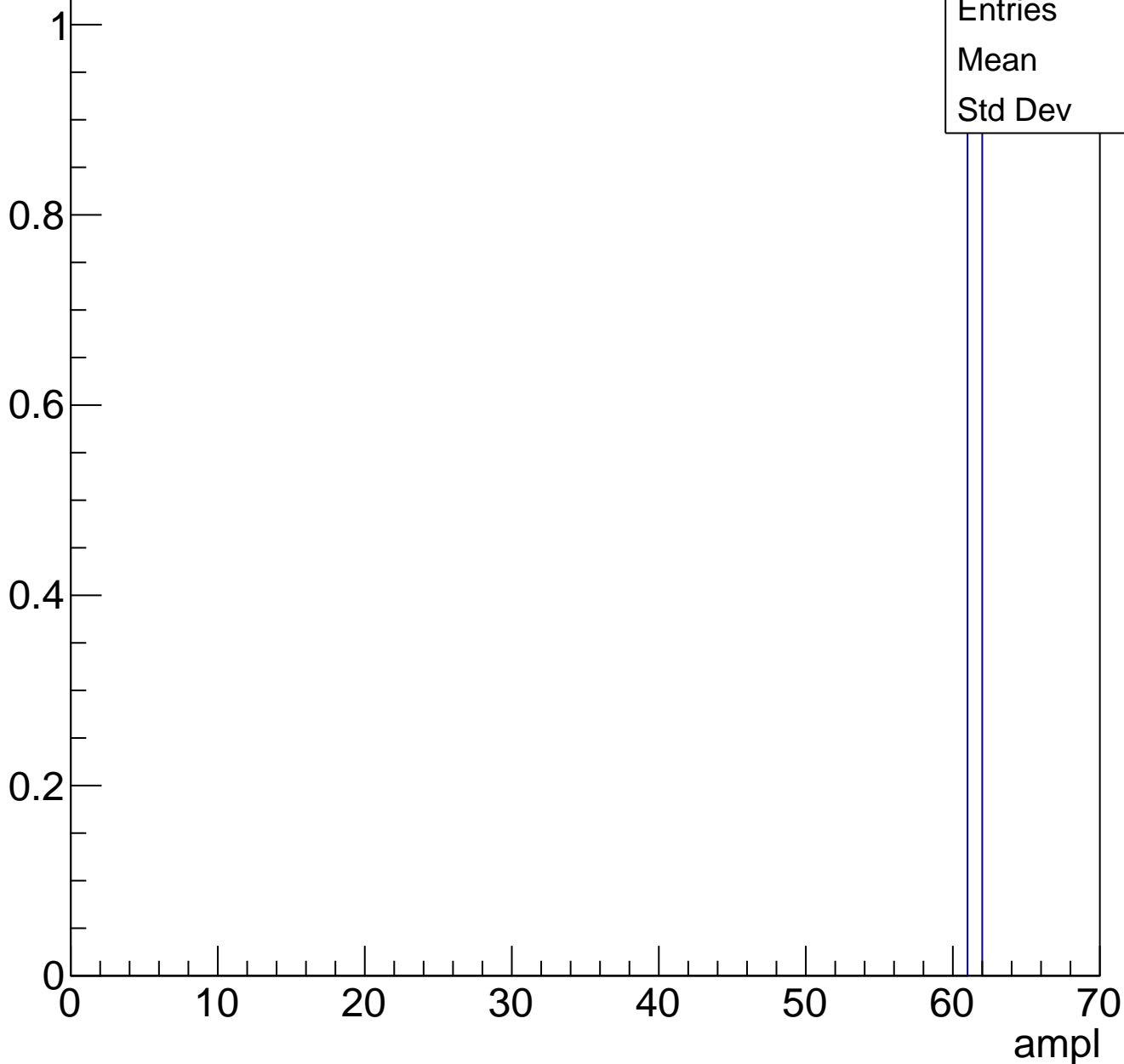
|         |       |
|---------|-------|
| Entries | 26    |
| Mean    | 58.88 |
| Std Dev | 11.89 |



# B0L002S, U1-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch52, adc0

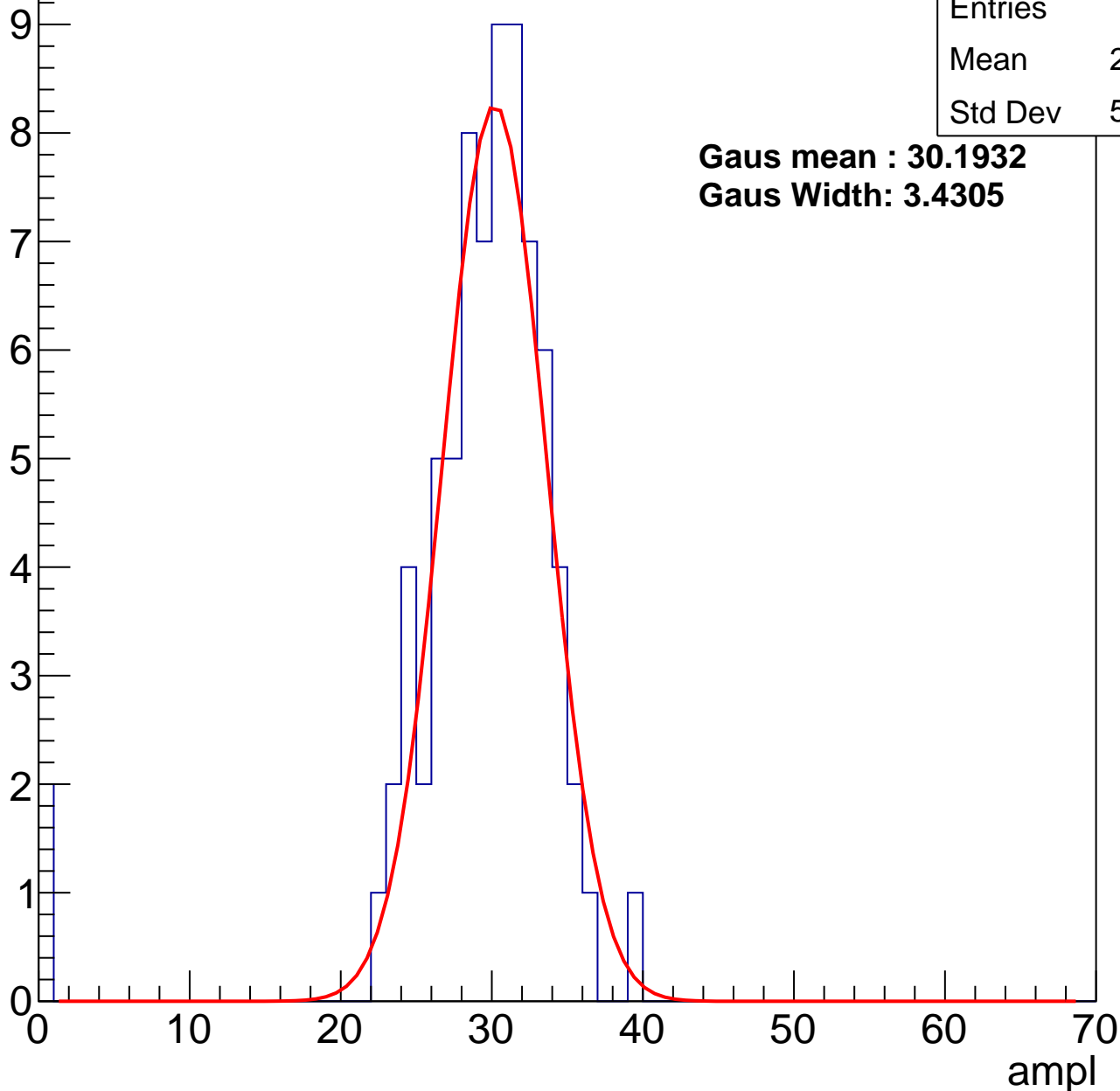
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 28.77 |
| Std Dev | 5.807 |

**Gaus mean : 30.1932**

**Gaus Width: 3.4305**



# B0L002S, U1-ch52, adc1

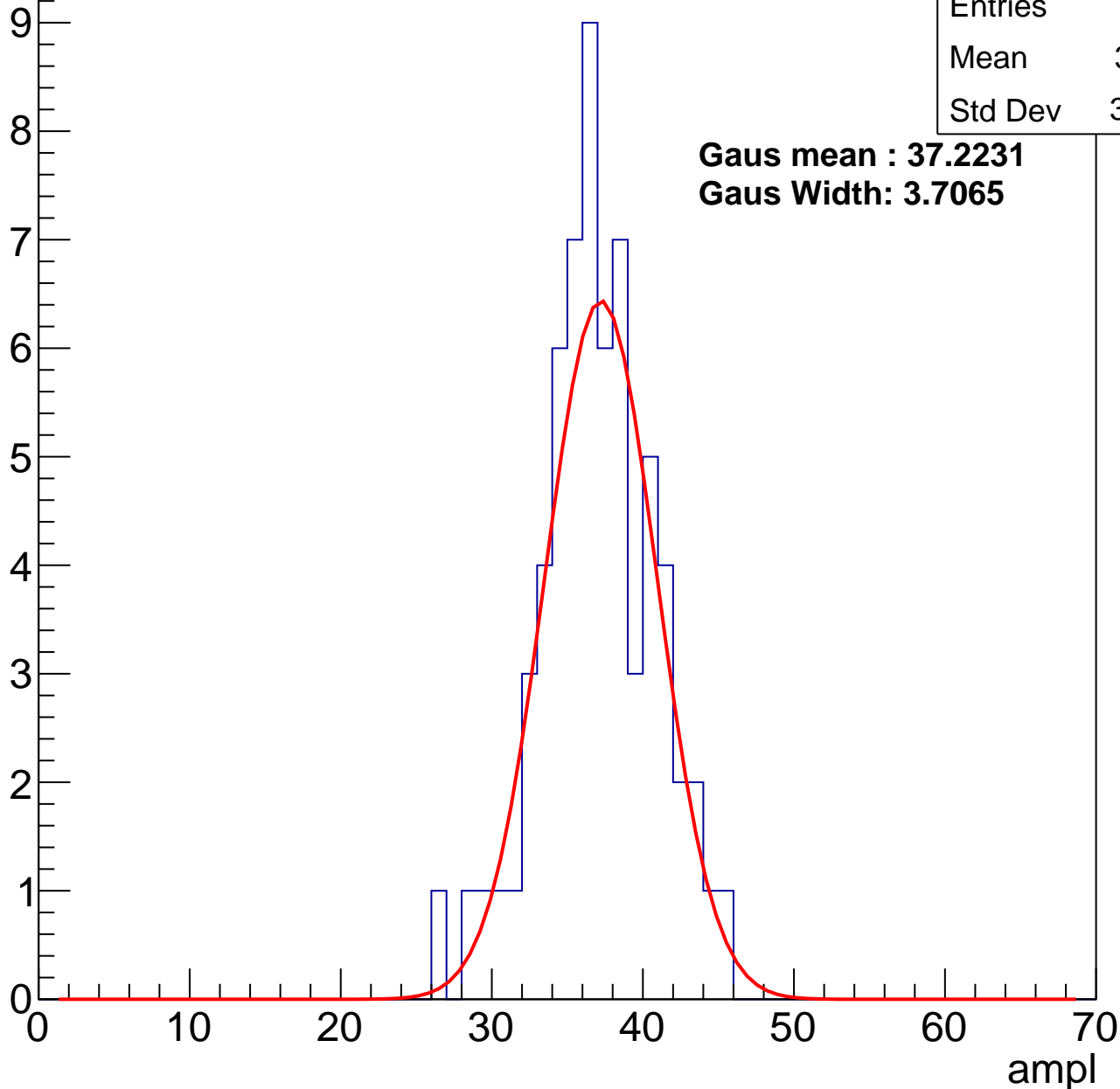
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 36.51 |
| Std Dev | 3.779 |

**Gaus mean : 37.2231**

**Gaus Width: 3.7065**



# B0L002S, U1-ch52, adc2

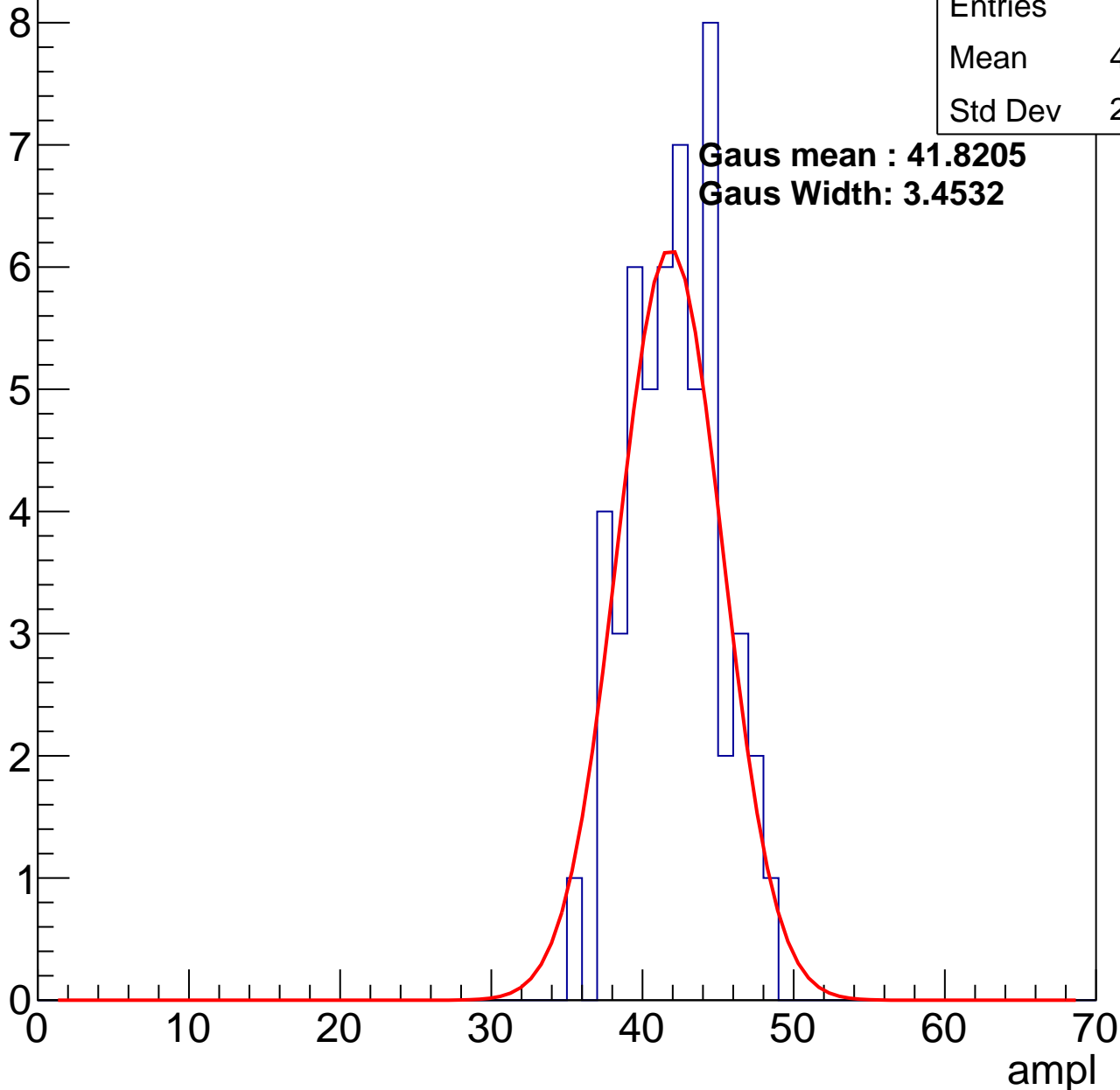
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 41.66 |
| Std Dev | 2.946 |

**Gaus mean : 41.8205**

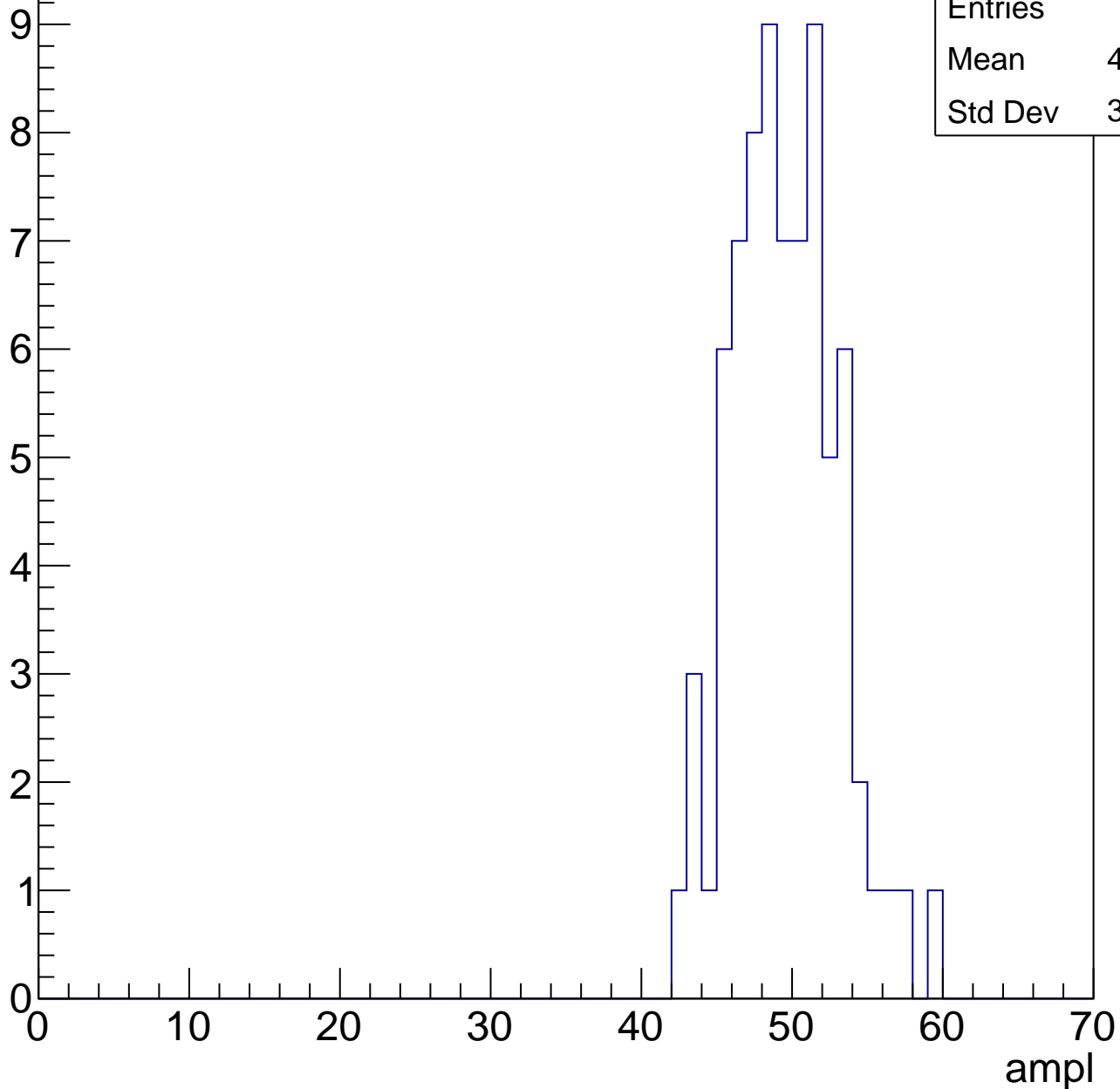
**Gaus Width: 3.4532**



# B0L002S, U1-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 49.07 |
| Std Dev | 3.395 |

# B0L002S, U1-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

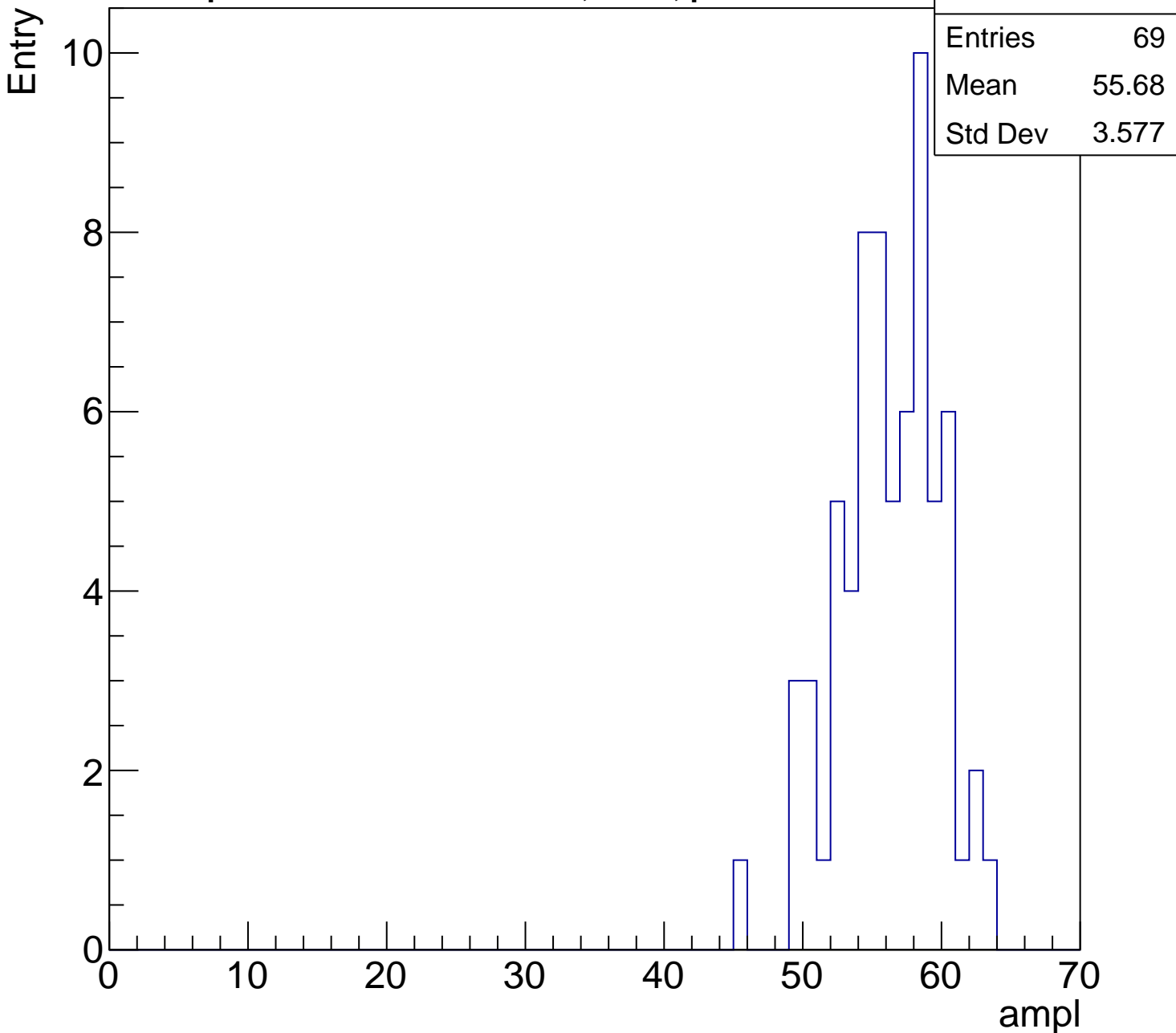
|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 55.68 |
| Std Dev | 3.577 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

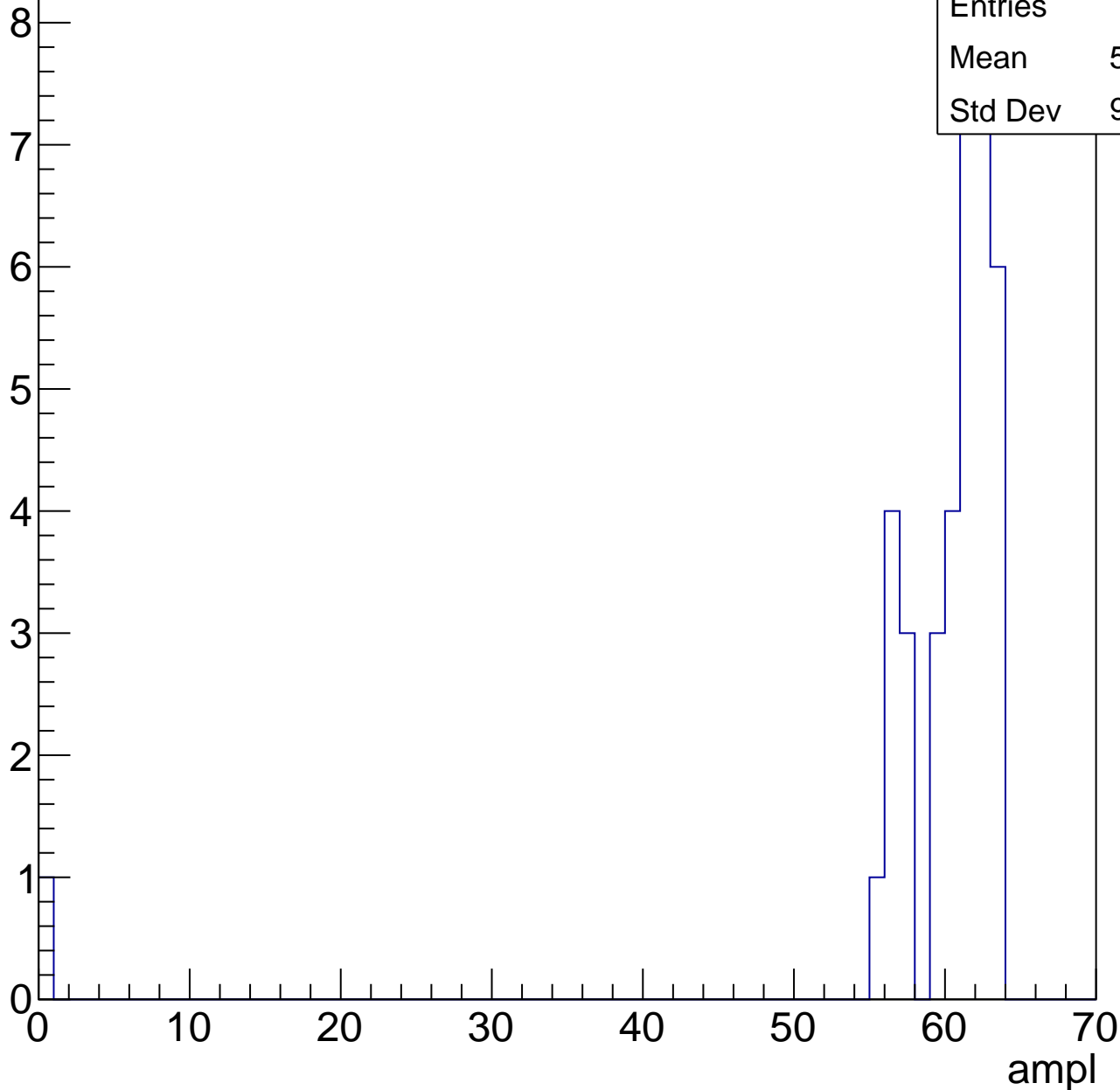


# B0L002S, U1-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

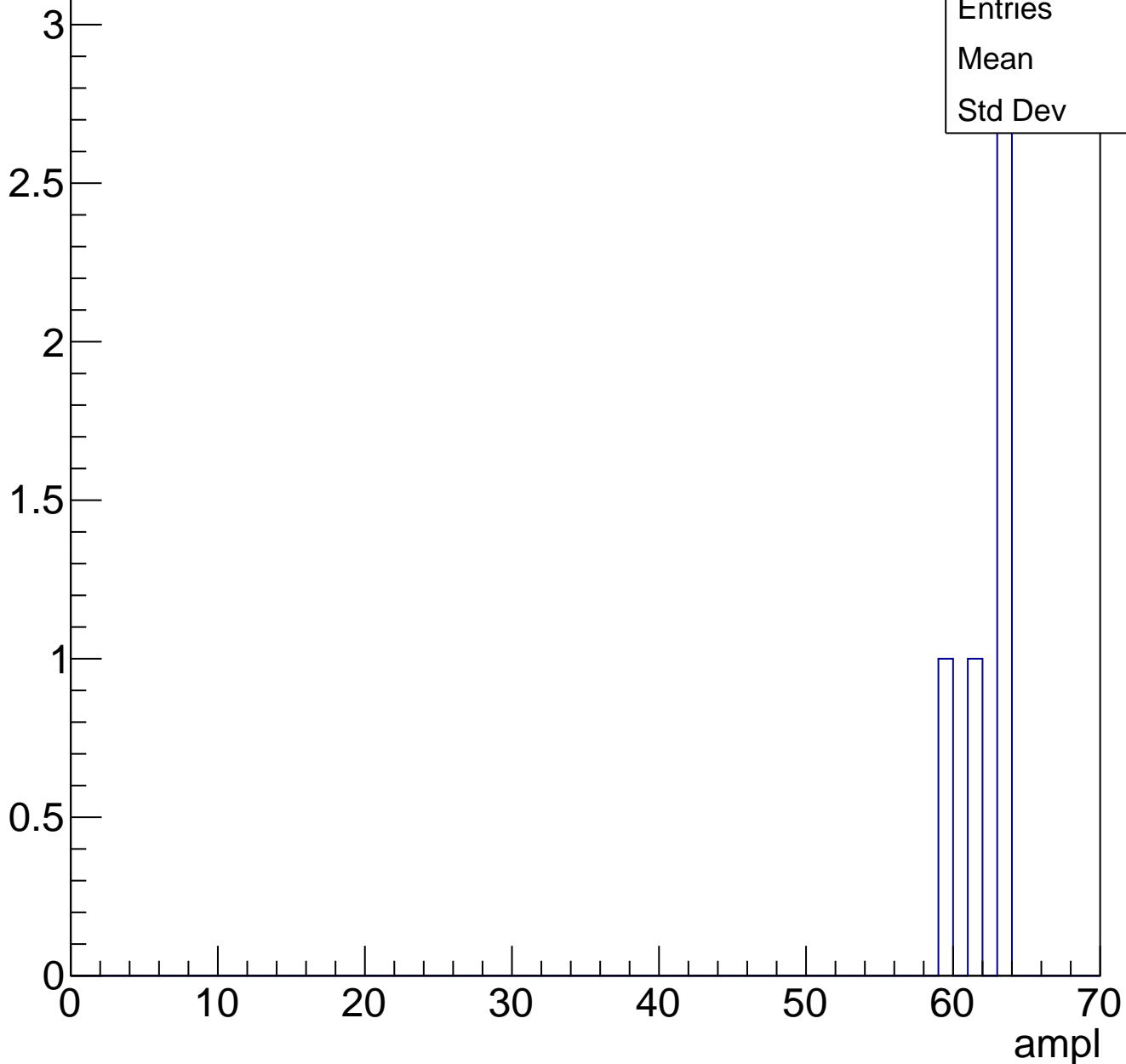
|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 58.66 |
| Std Dev | 9.927 |



# B0L002S, U1-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch53, adc0

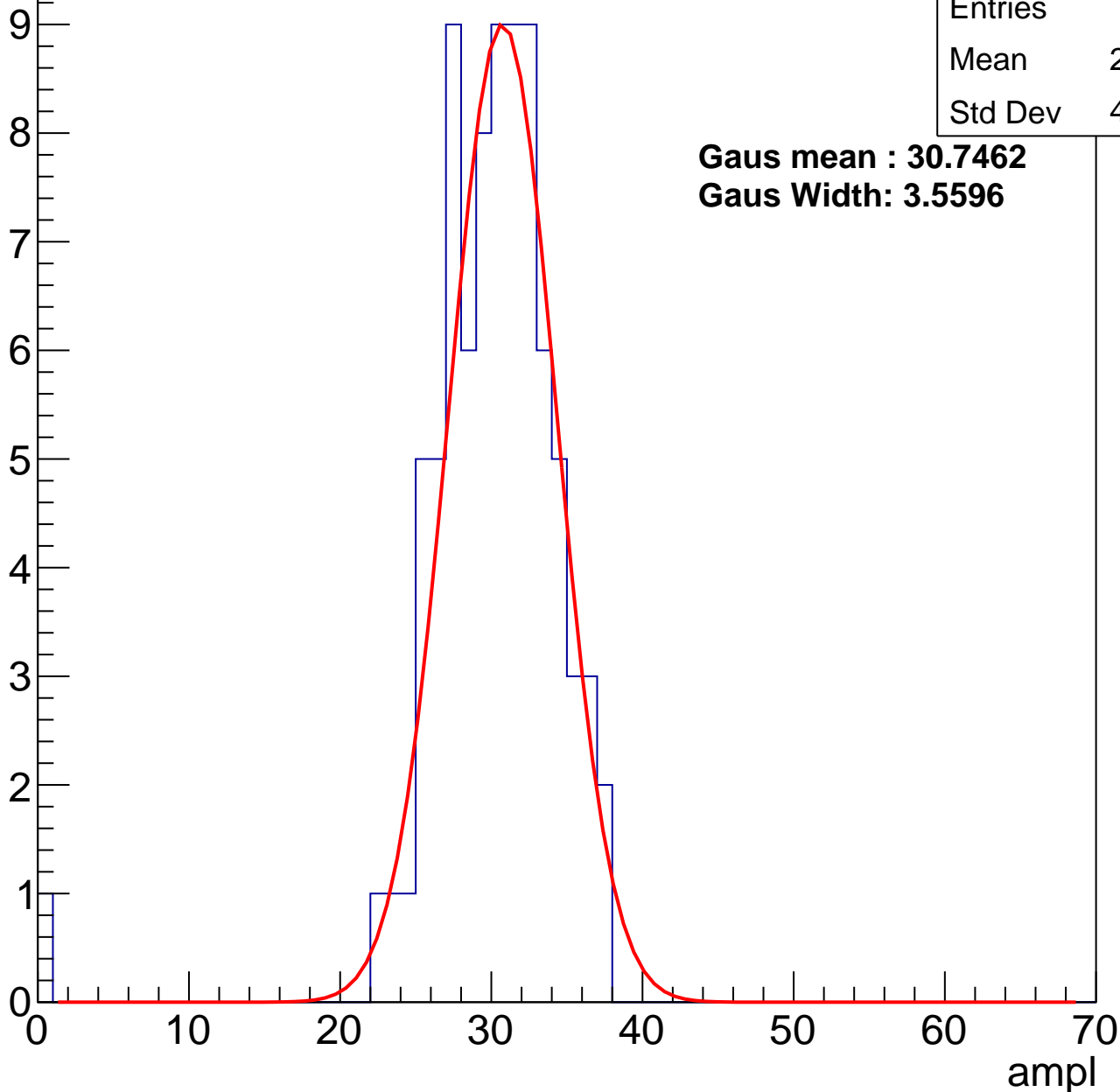
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 29.63 |
| Std Dev | 4.677 |

**Gaus mean : 30.7462**

**Gaus Width: 3.5596**



# B0L002S, U1-ch53, adc1

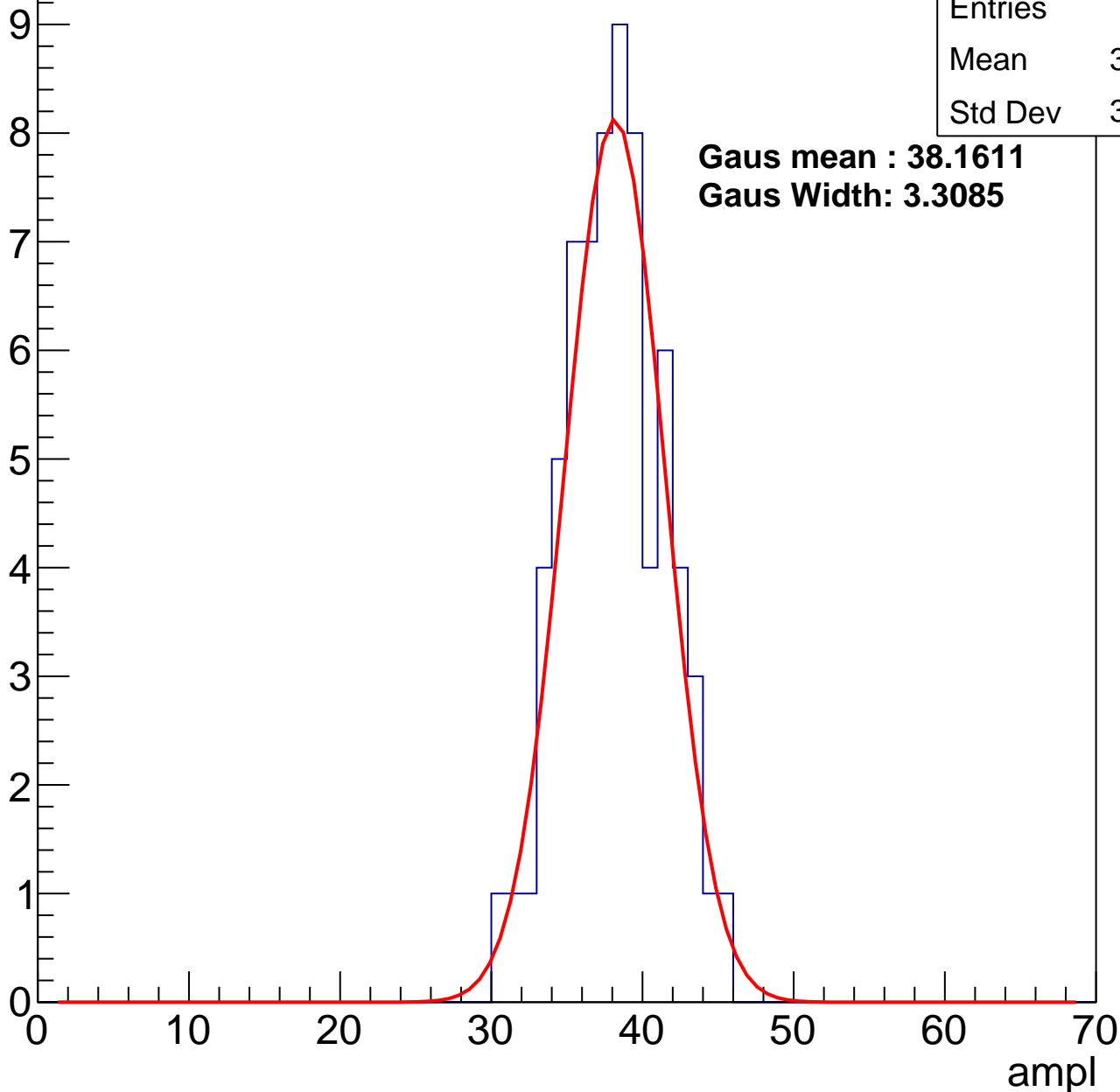
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 37.63 |
| Std Dev | 3.208 |

**Gaus mean : 38.1611**

**Gaus Width: 3.3085**

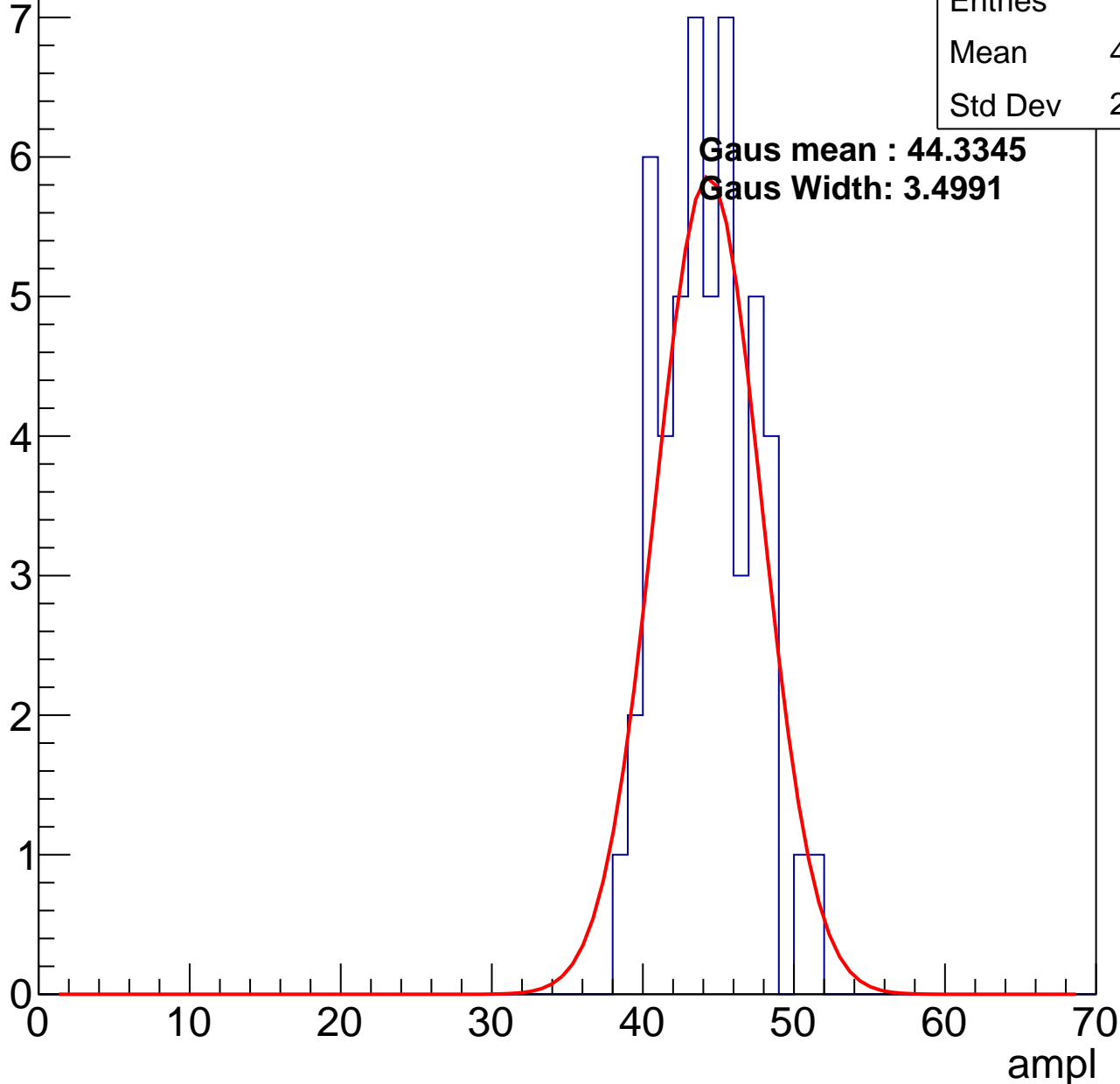


# B0L002S, U1-ch53, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

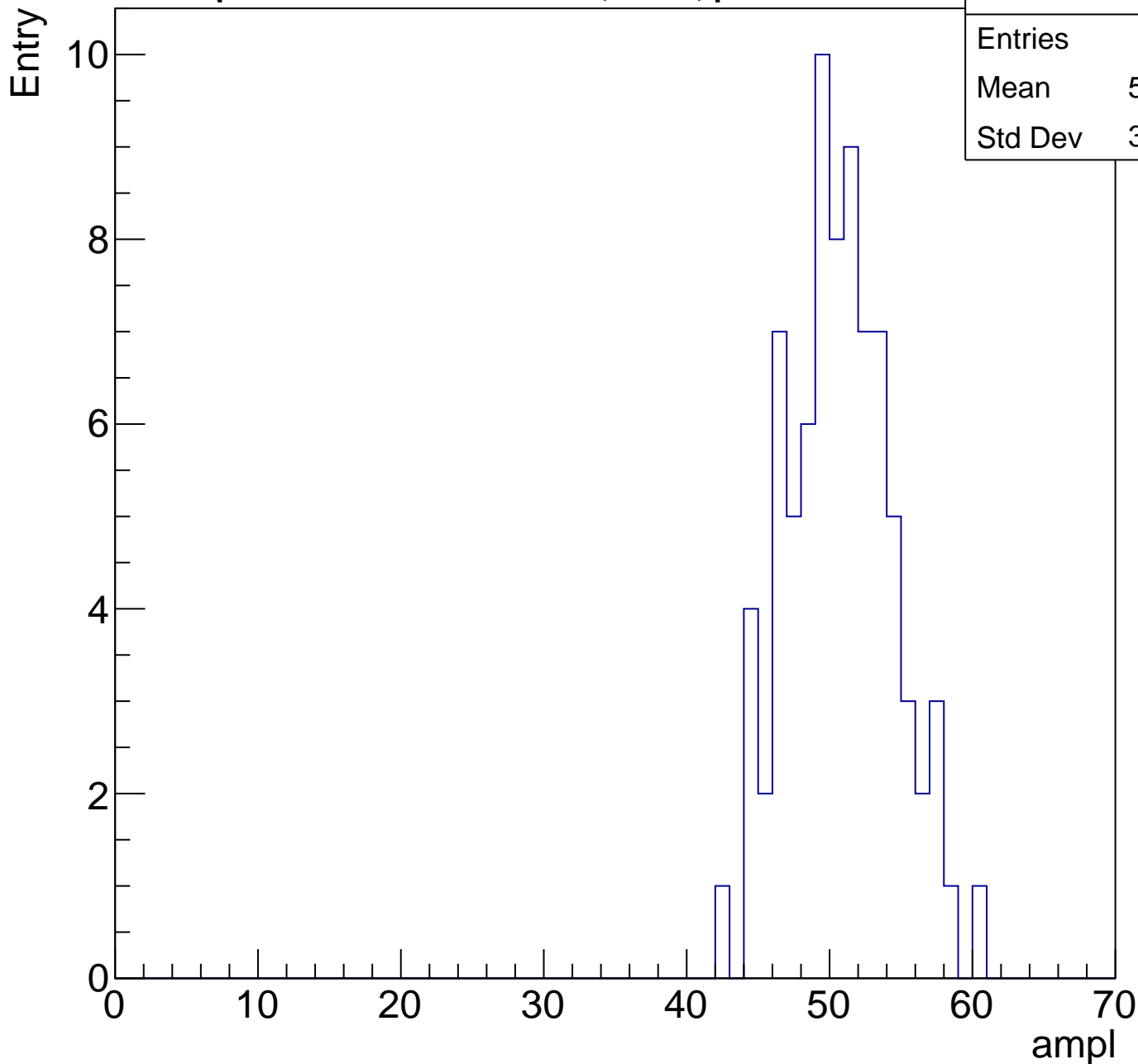
|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 43.76 |
| Std Dev | 2.987 |



# B0L002S, U1-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 50.28 |
| Std Dev | 3.652 |

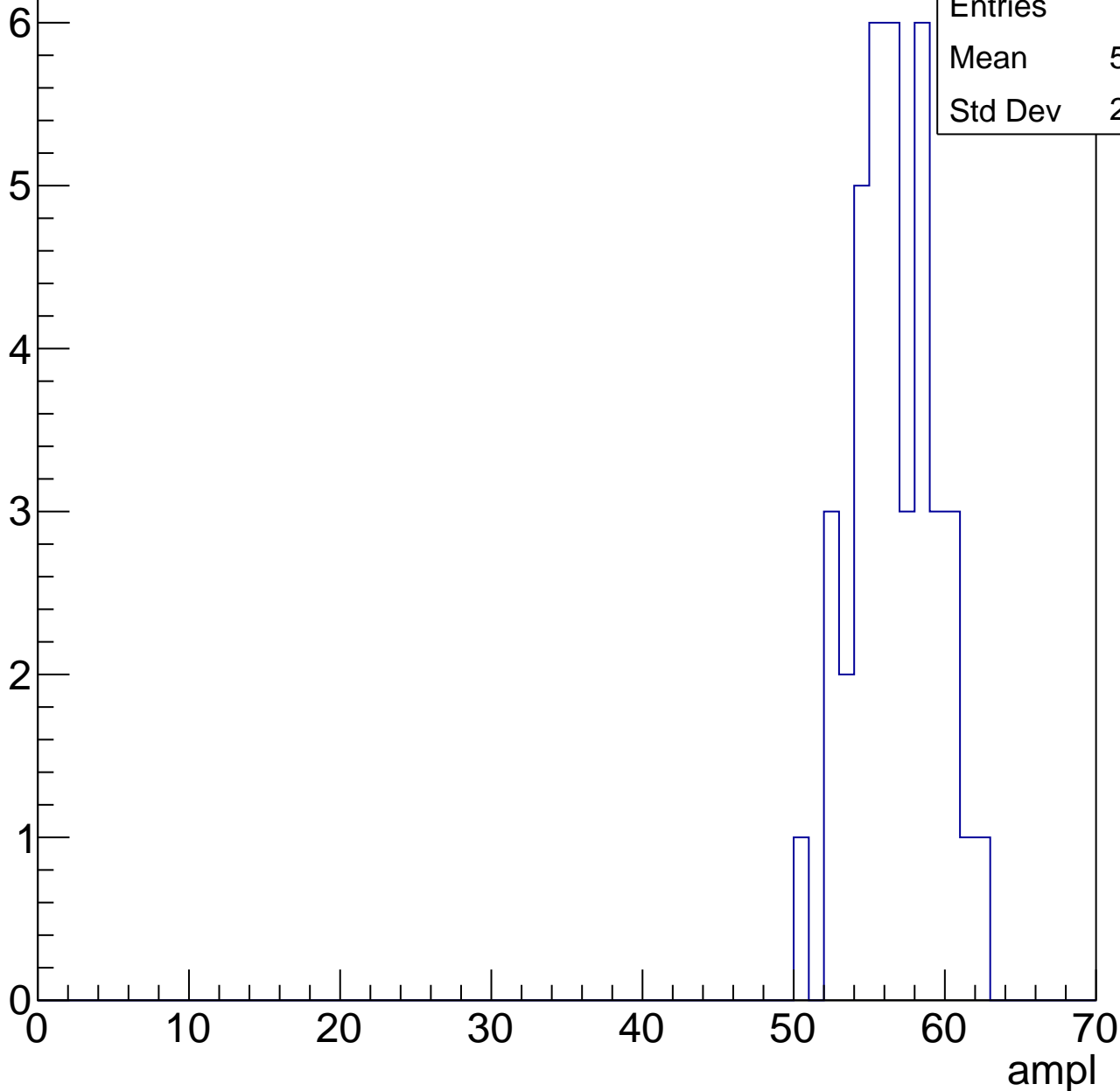


# B0L002S, U1-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 40    |
| Mean    | 56.17 |
| Std Dev | 2.692 |

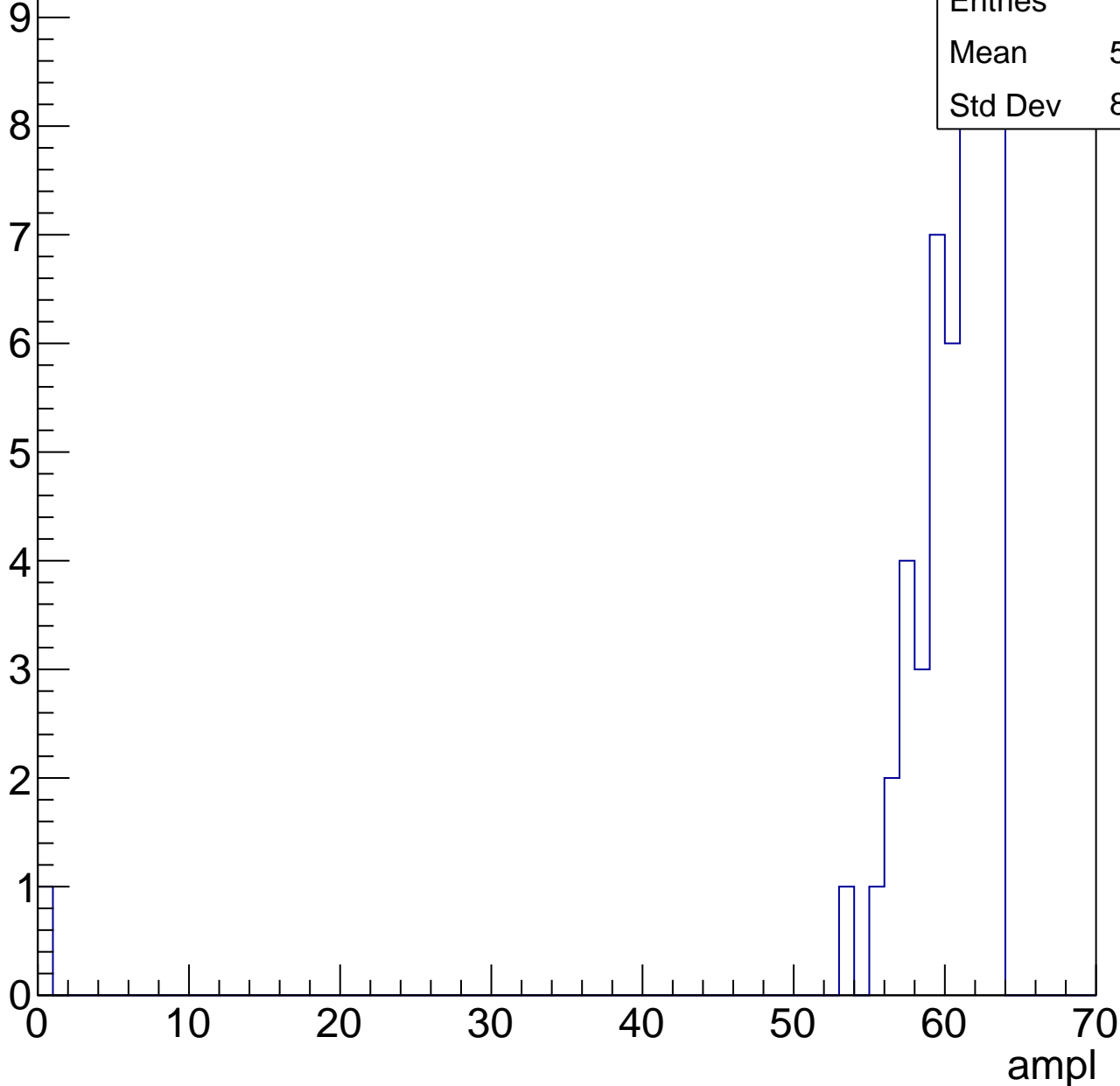


# B0L002S, U1-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

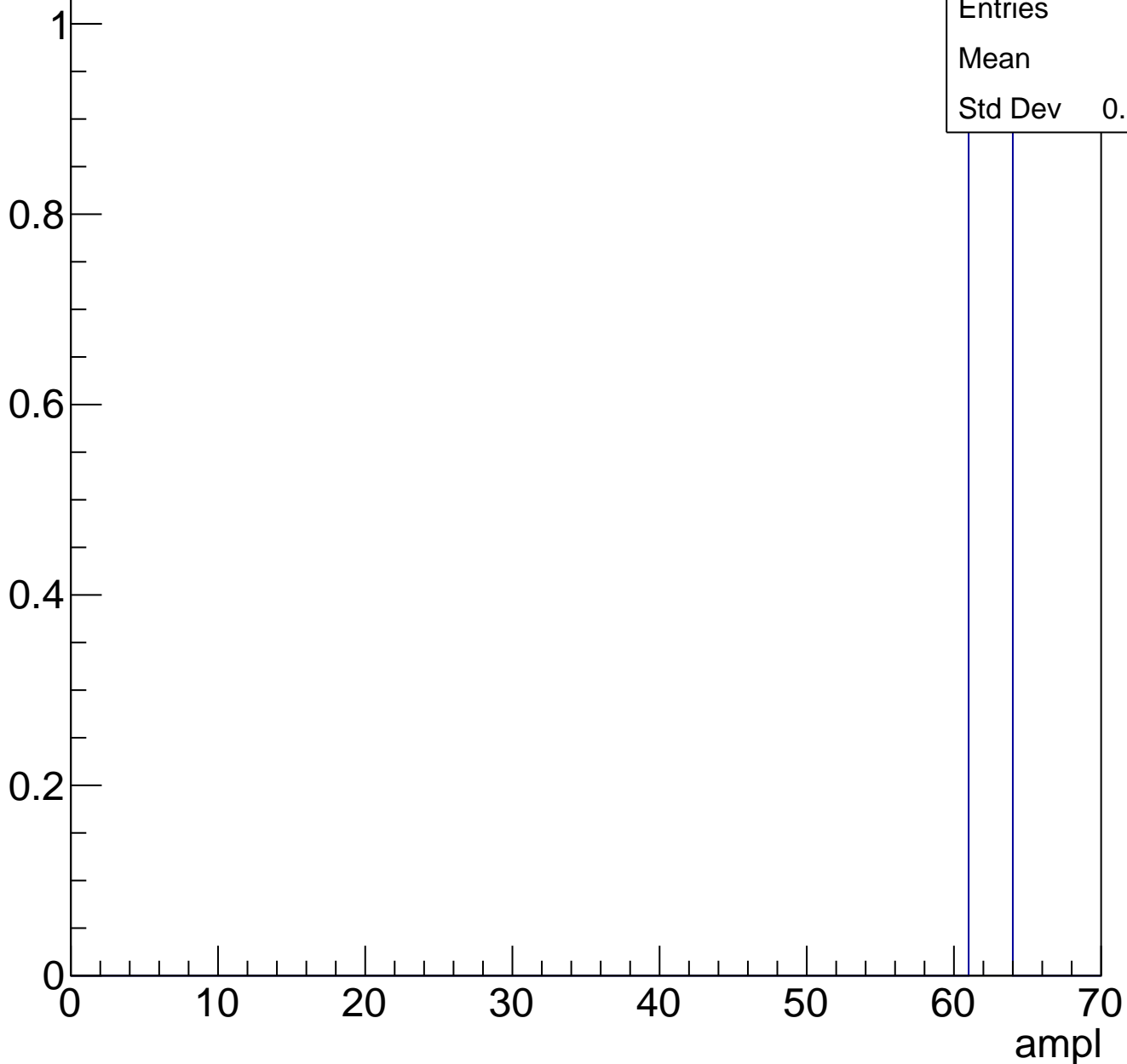
|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 58.92 |
| Std Dev | 8.745 |



# B0L002S, U1-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

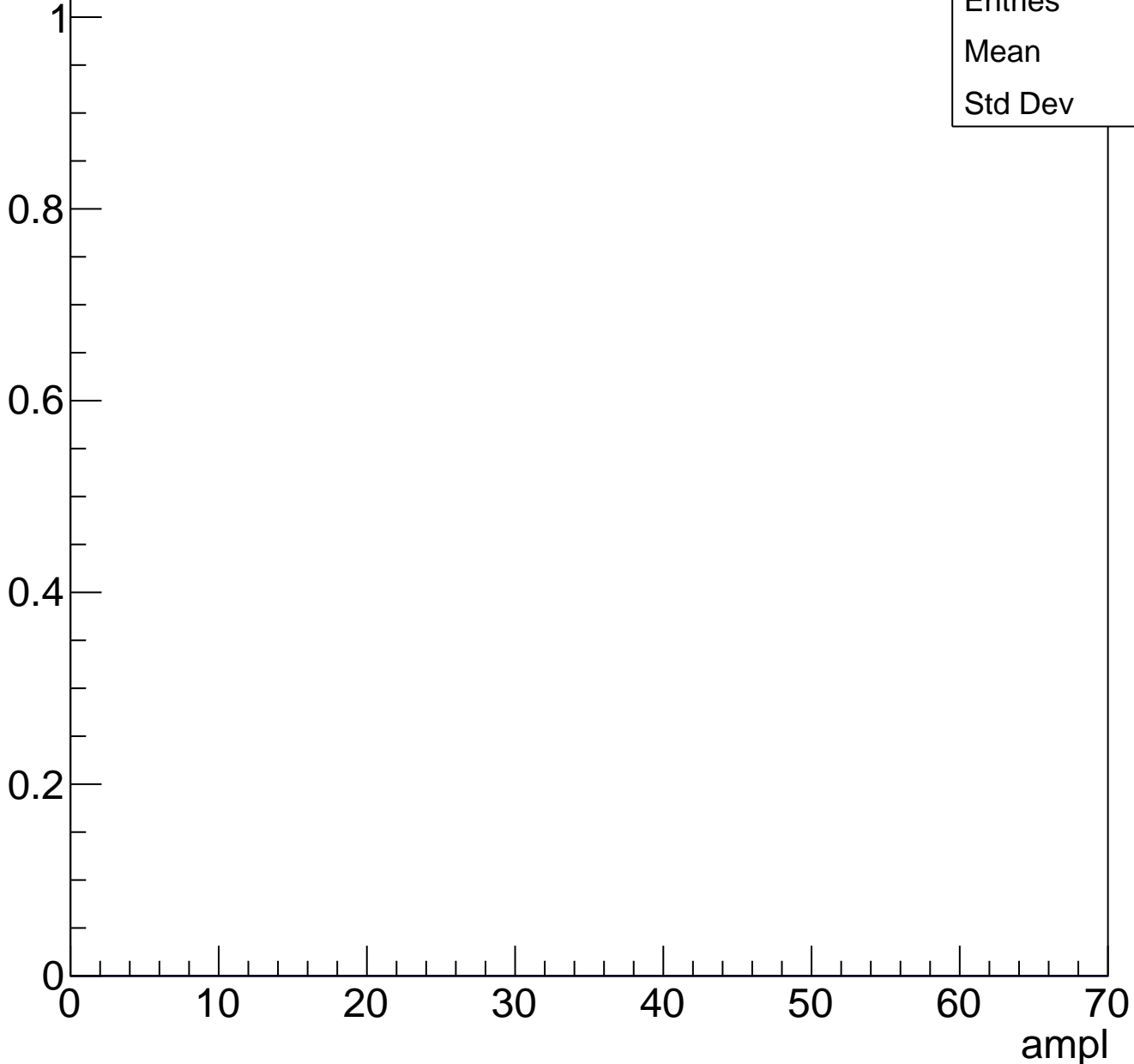




# B0L002S, U1-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch54, adc0

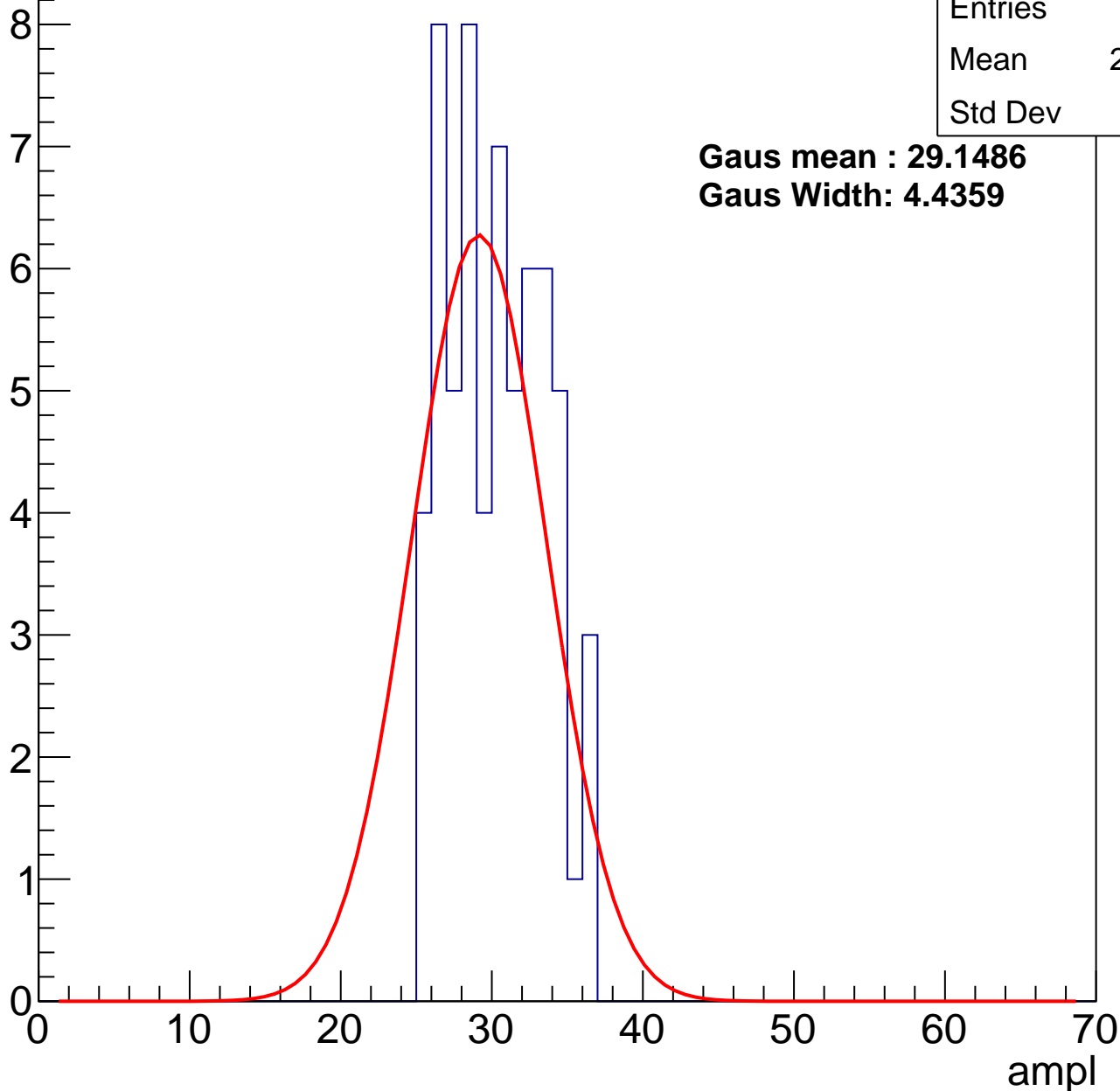
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 29.85 |
| Std Dev | 3.12  |

**Gaus mean : 29.1486**

**Gaus Width: 4.4359**



# B0L002S, U1-ch54, adc1

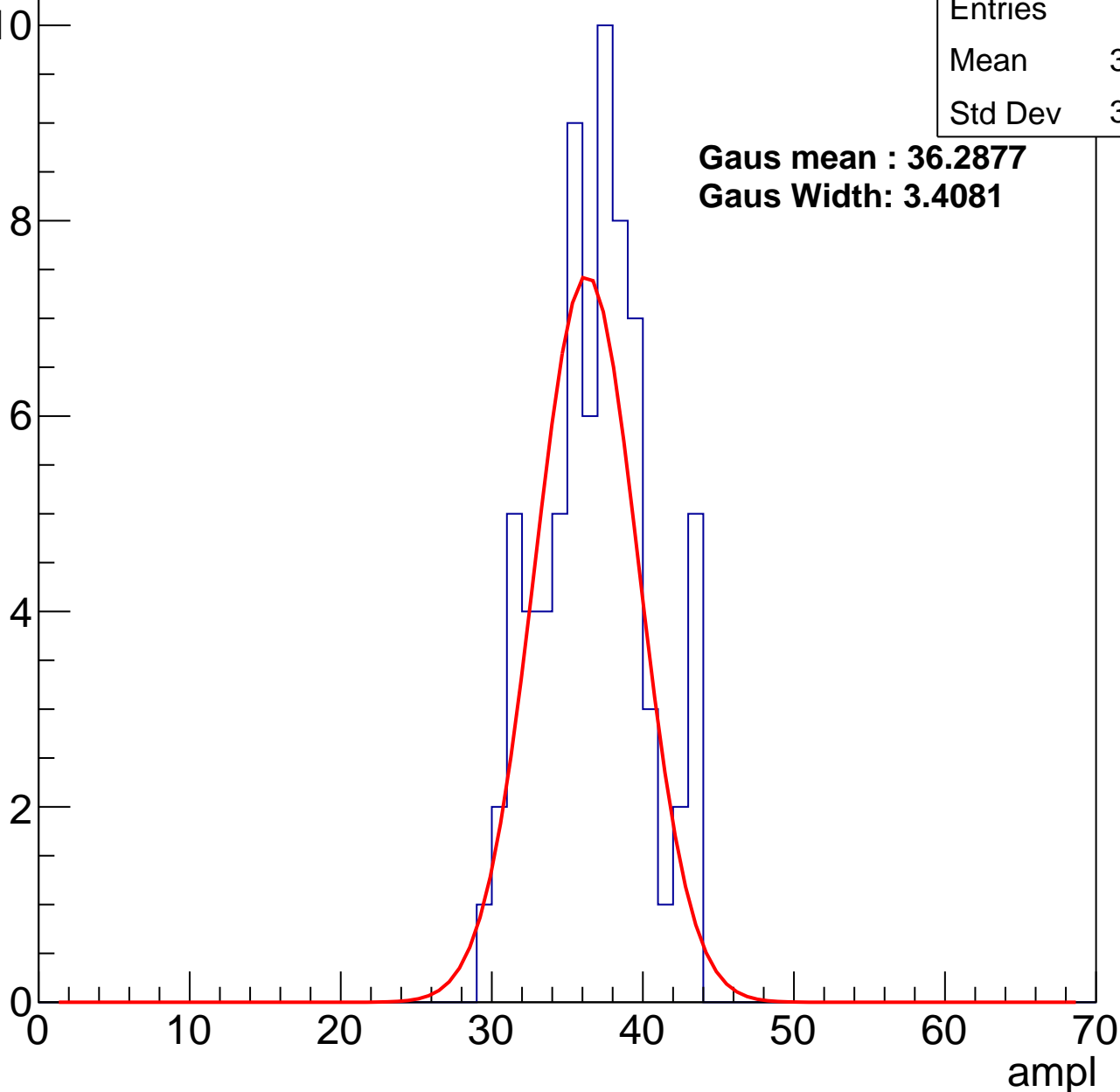
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 36.28 |
| Std Dev | 3.465 |

**Gaus mean : 36.2877**

**Gaus Width: 3.4081**



# B0L002S, U1-ch54, adc2

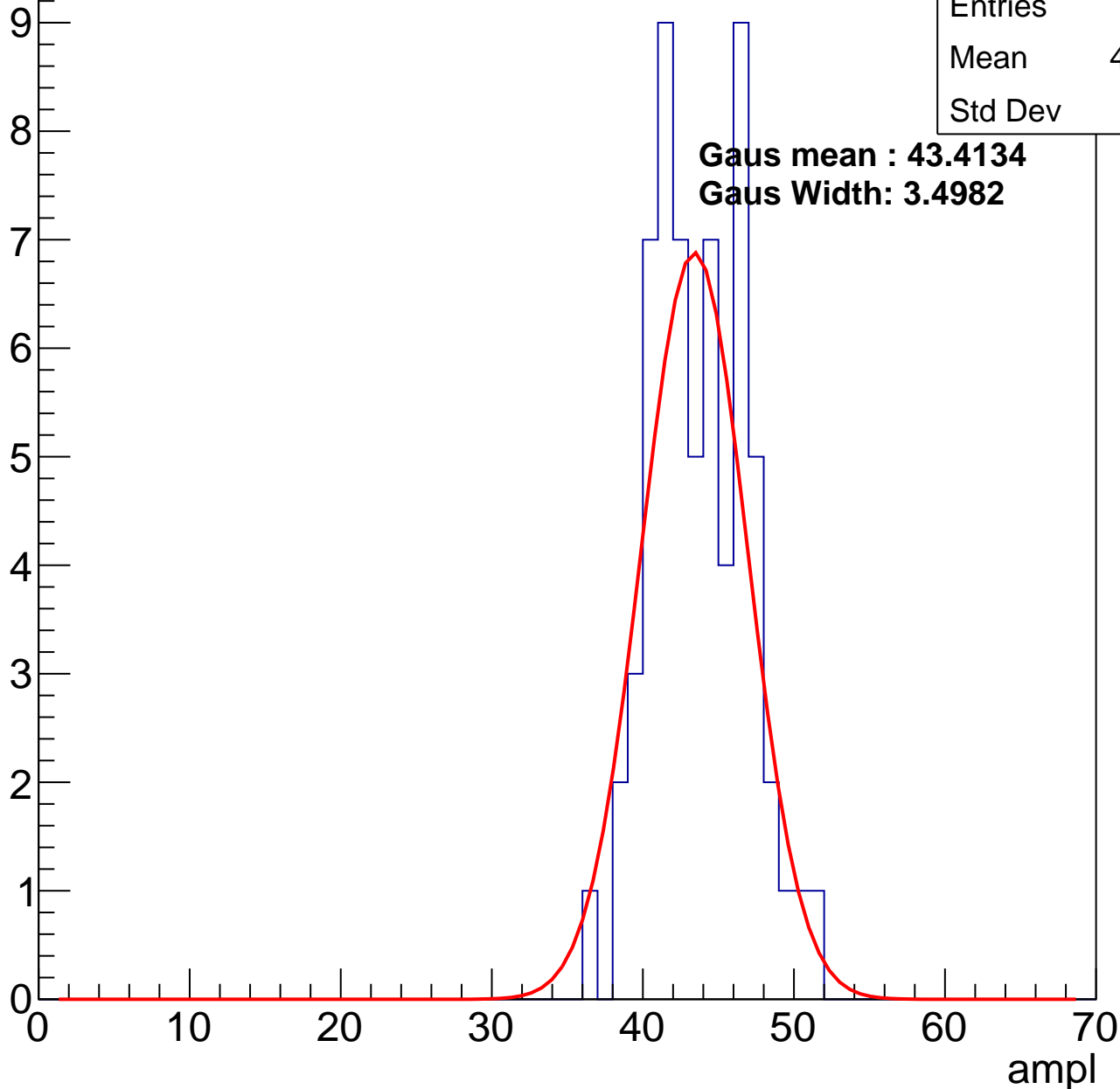
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 43.28 |
| Std Dev | 3.14  |

**Gaus mean : 43.4134**

**Gaus Width: 3.4982**

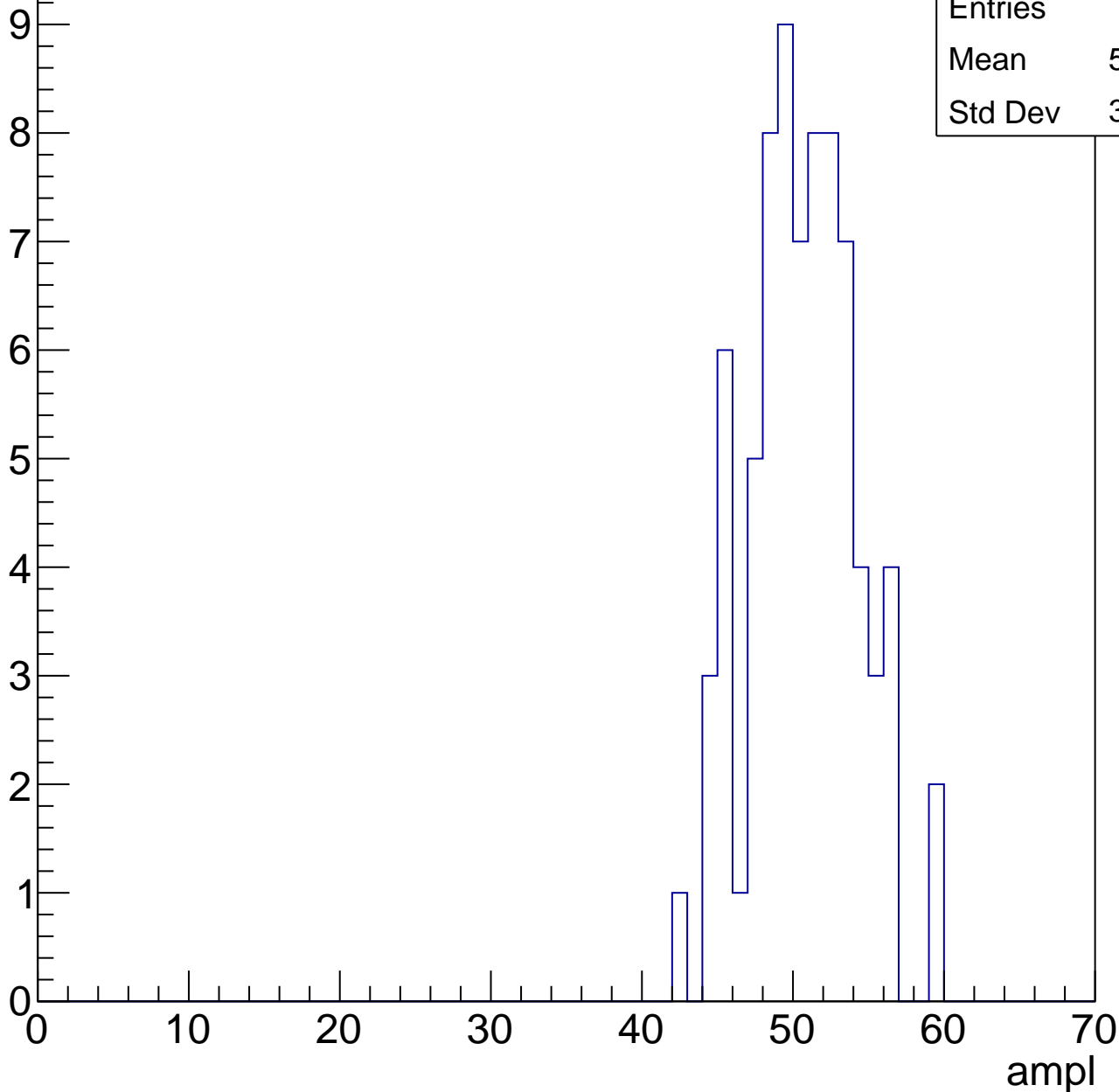


# B0L002S, U1-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

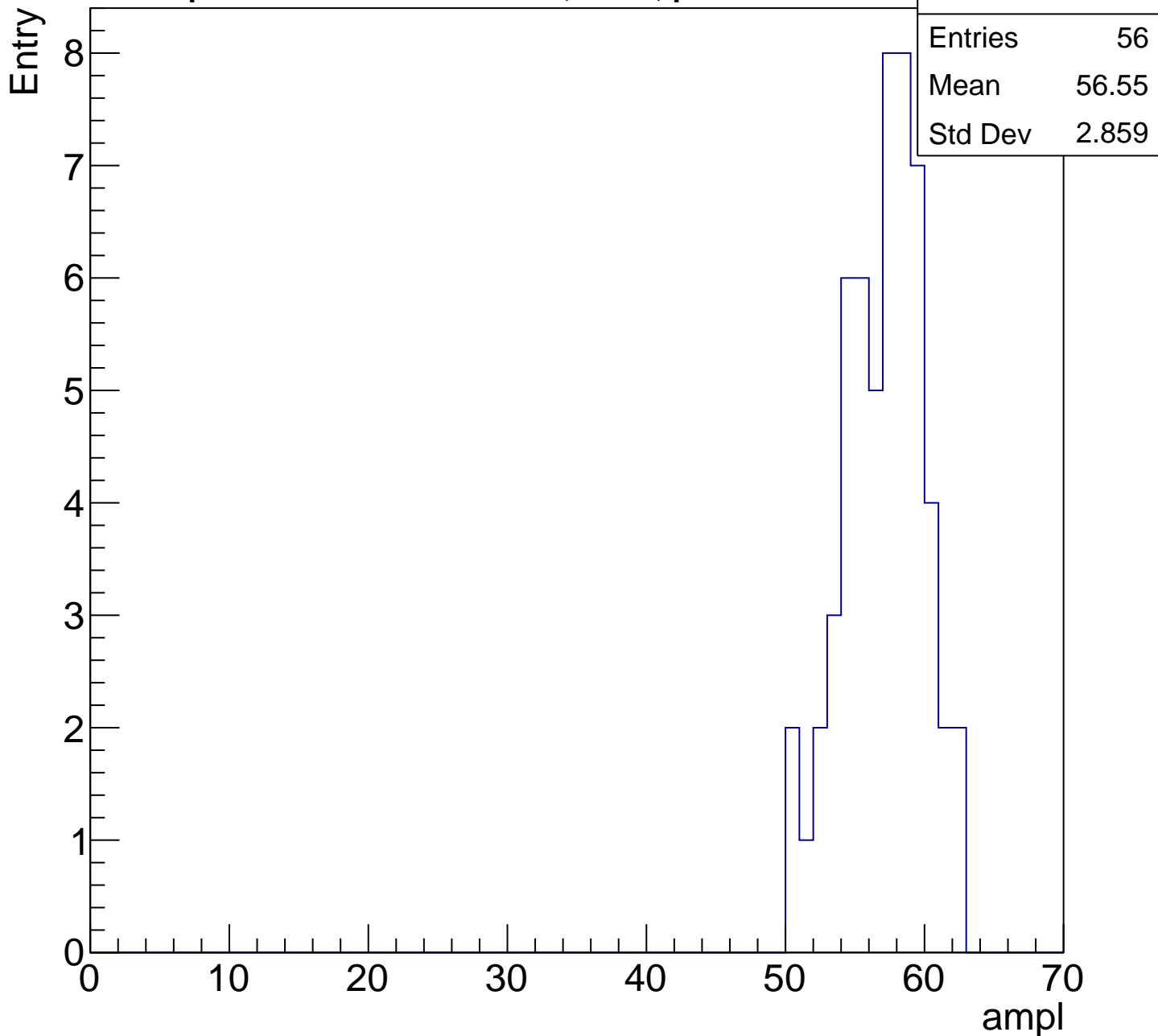
Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 50.24 |
| Std Dev | 3.568 |



# B0L002S, U1-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

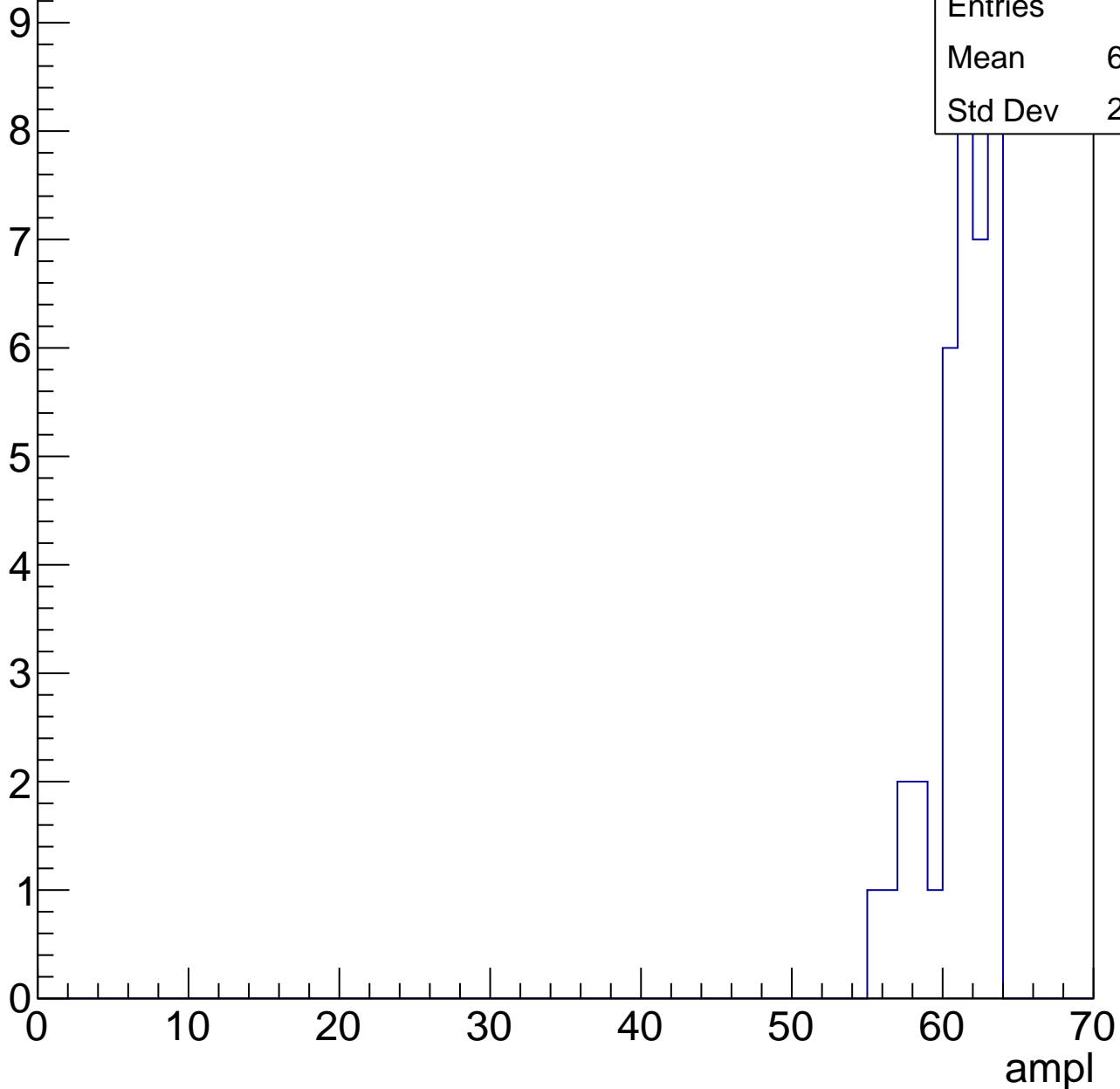


# B0L002S, U1-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 60.78 |
| Std Dev | 2.094 |



# B0L002S, U1-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

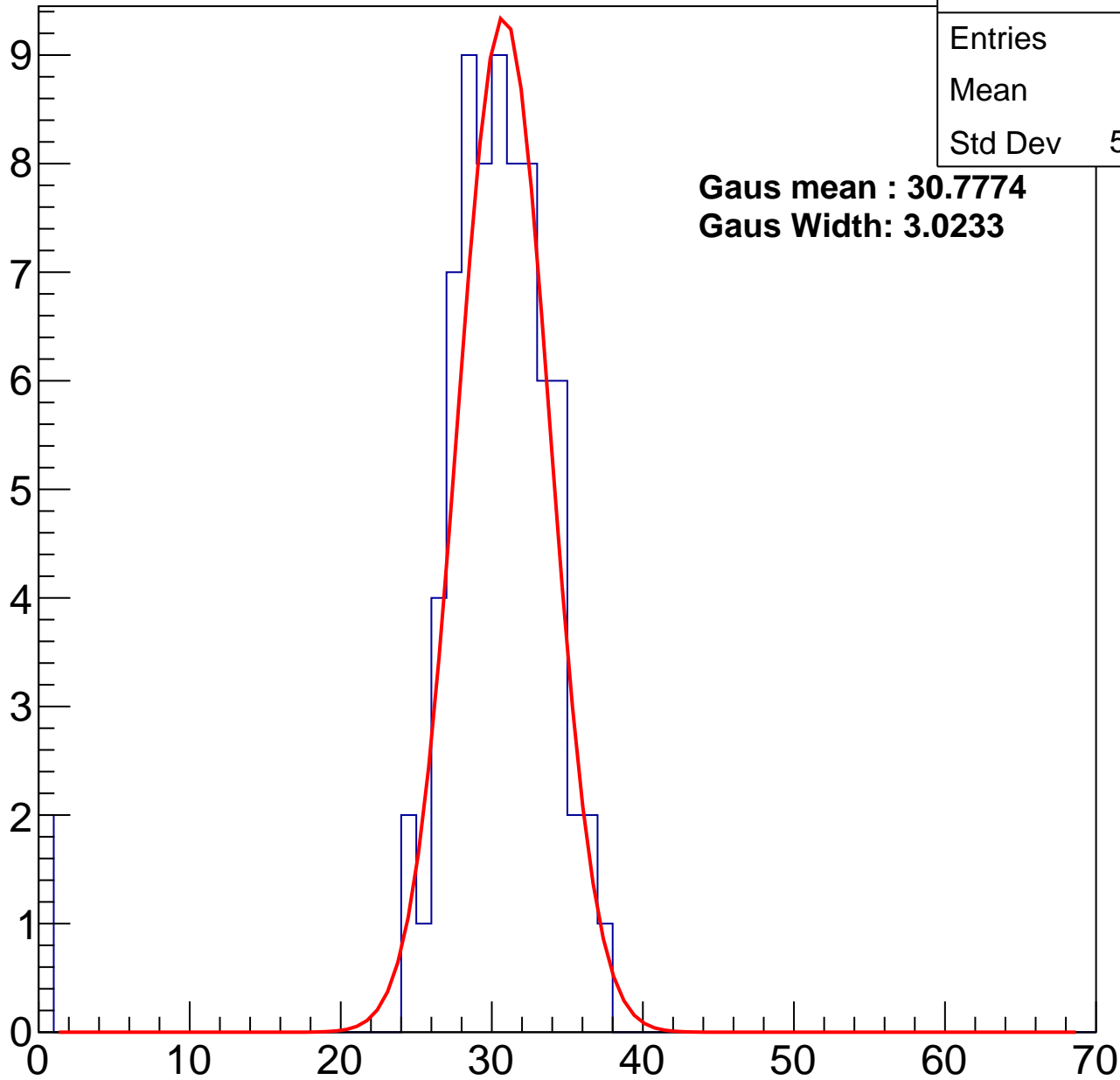
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 29.4  |
| Std Dev | 5.659 |

**Gaus mean : 30.7774**

**Gaus Width: 3.0233**

ampl



# B0L002S, U1-ch55, adc1

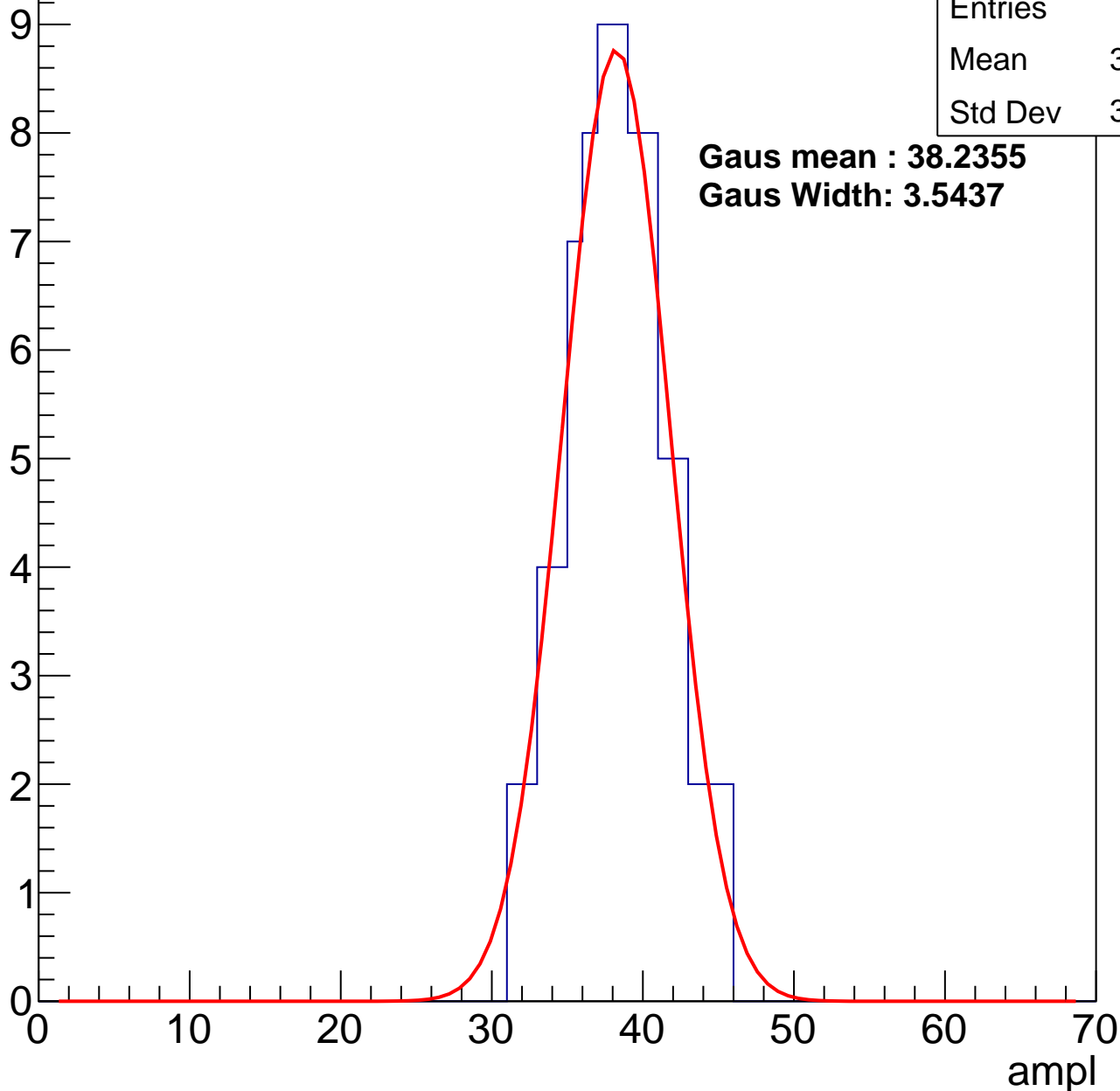
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 37.83 |
| Std Dev | 3.265 |

**Gaus mean : 38.2355**

**Gaus Width: 3.5437**



# B0L002S, U1-ch55, adc2

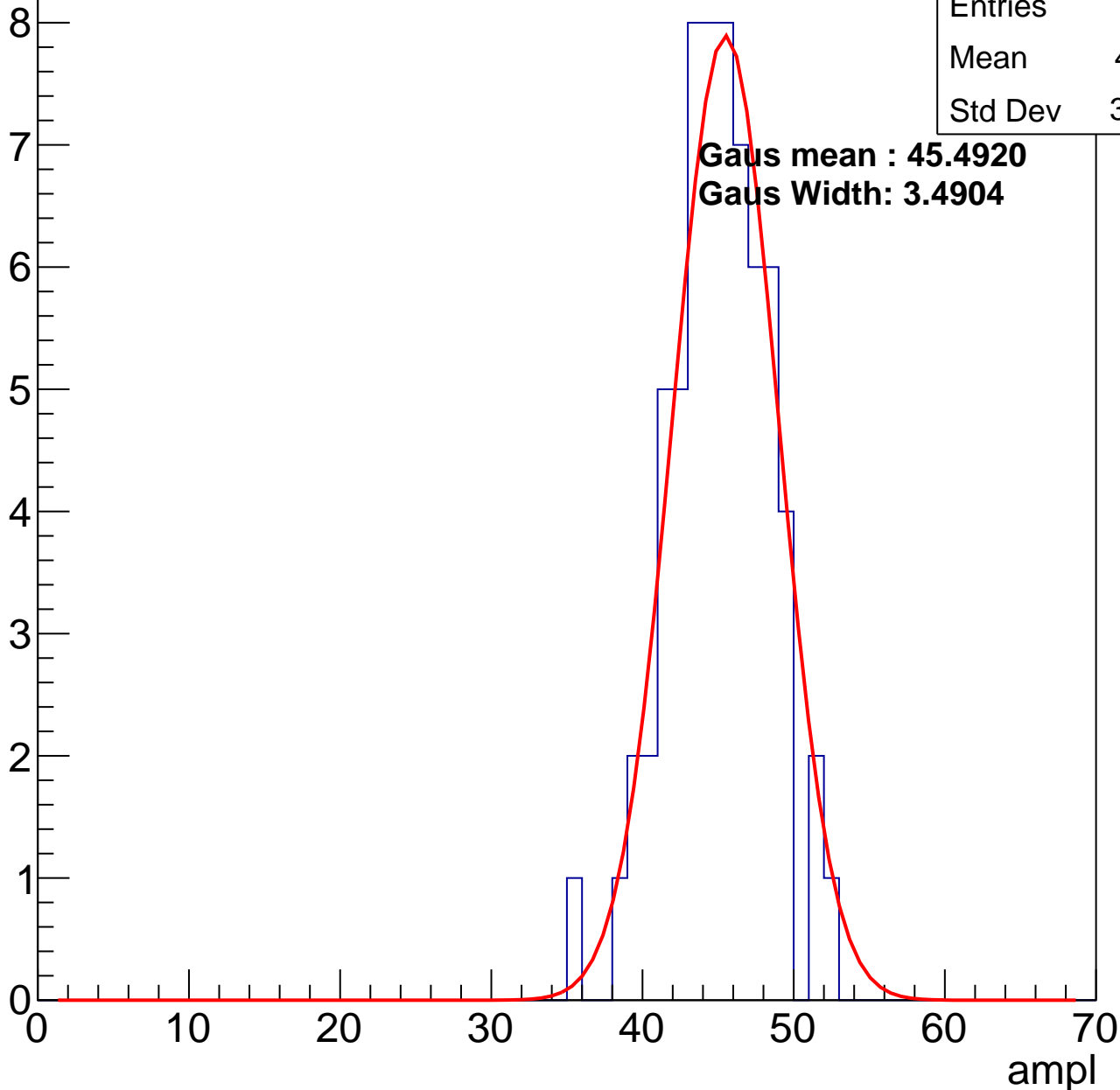
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 44.61 |
| Std Dev | 3.247 |

**Gaus mean : 45.4920**

**Gaus Width: 3.4904**

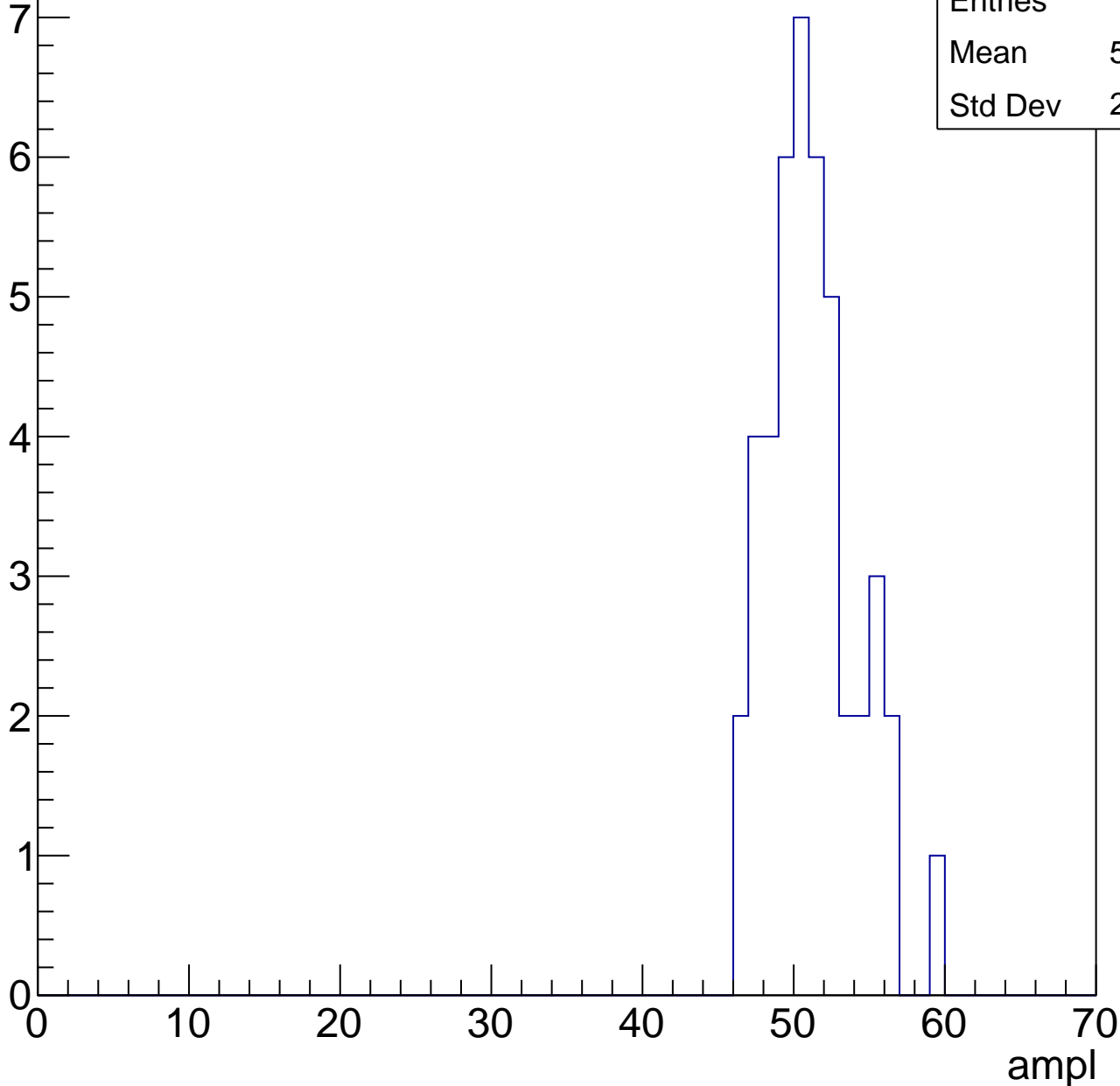


# B0L002S, U1-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 50.73 |
| Std Dev | 2.903 |

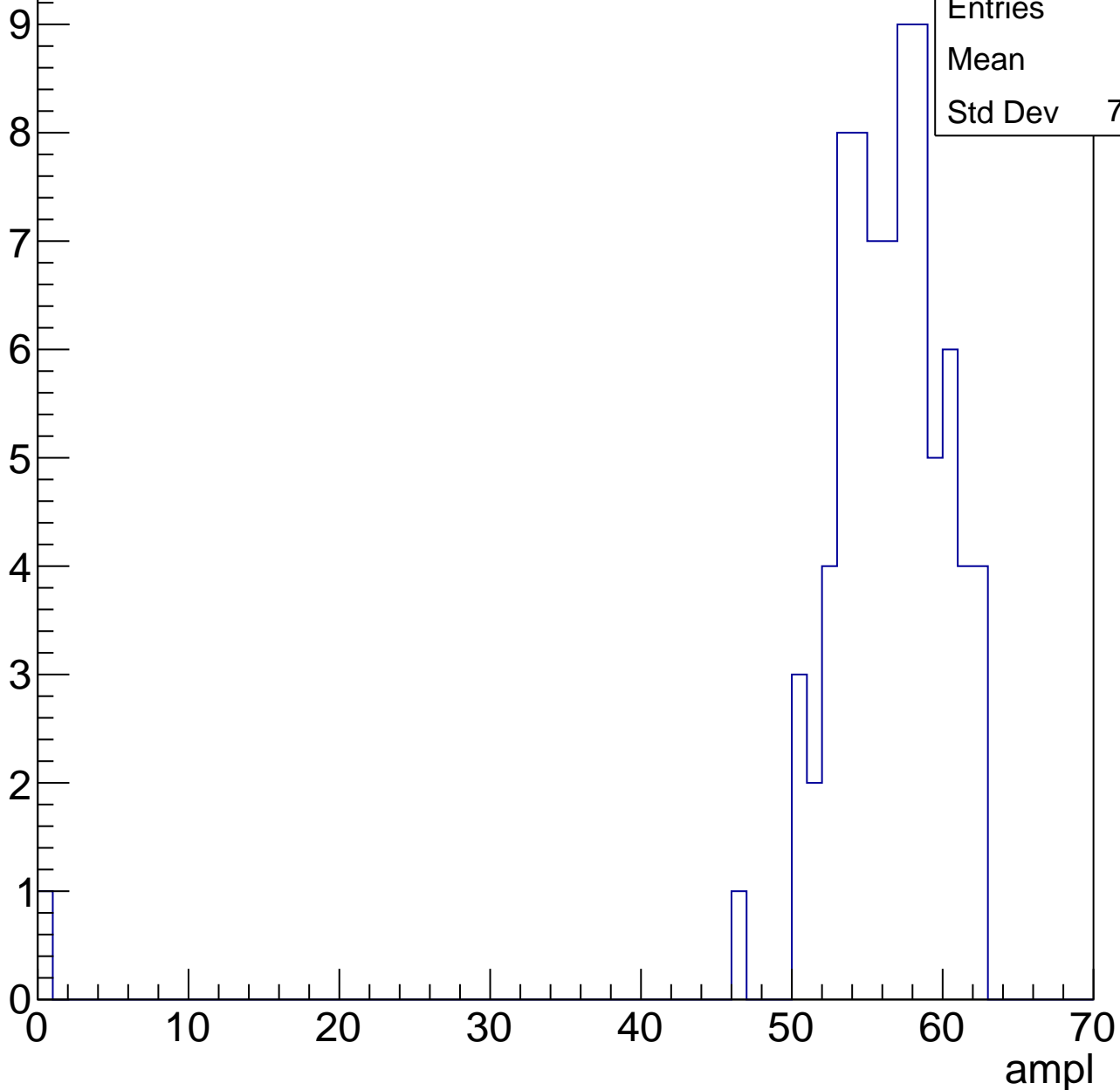


# B0L002S, U1-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 55.4  |
| Std Dev | 7.135 |

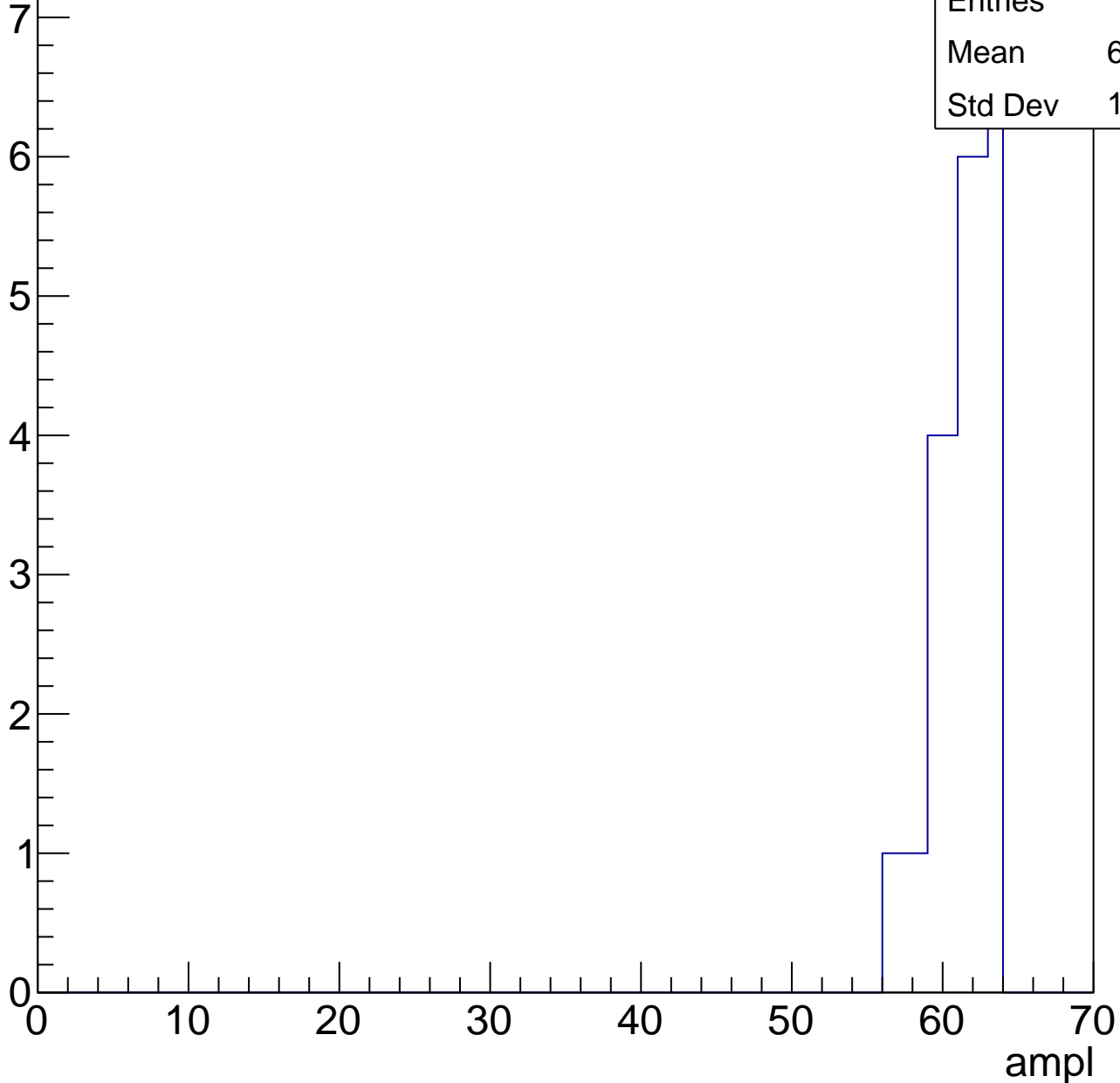


# B0L002S, U1-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 30    |
| Mean    | 60.87 |
| Std Dev | 1.857 |



# B0L002S, U1-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch56, adc0

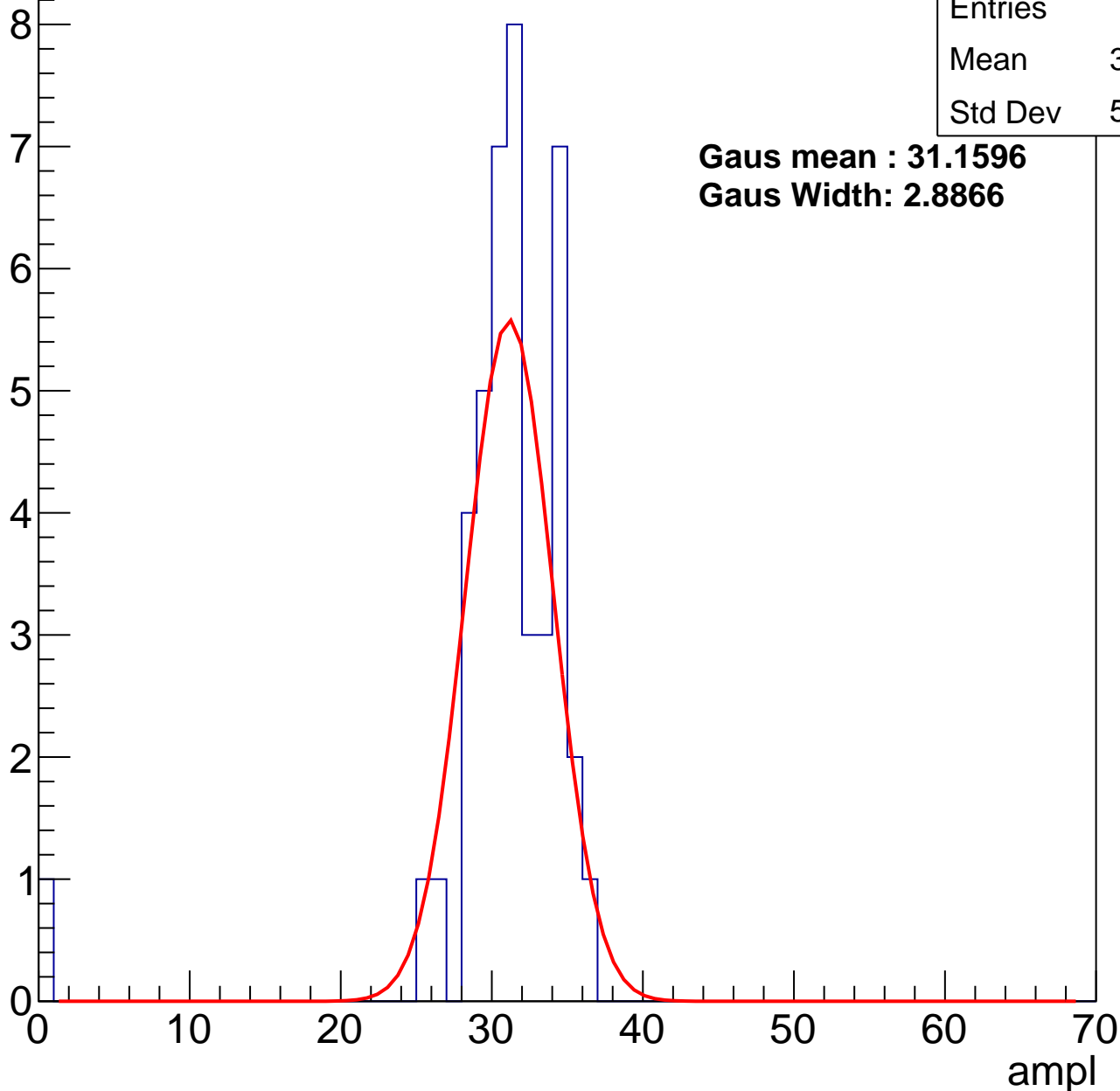
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 30.35 |
| Std Dev | 5.287 |

**Gaus mean : 31.1596**

**Gaus Width: 2.8866**



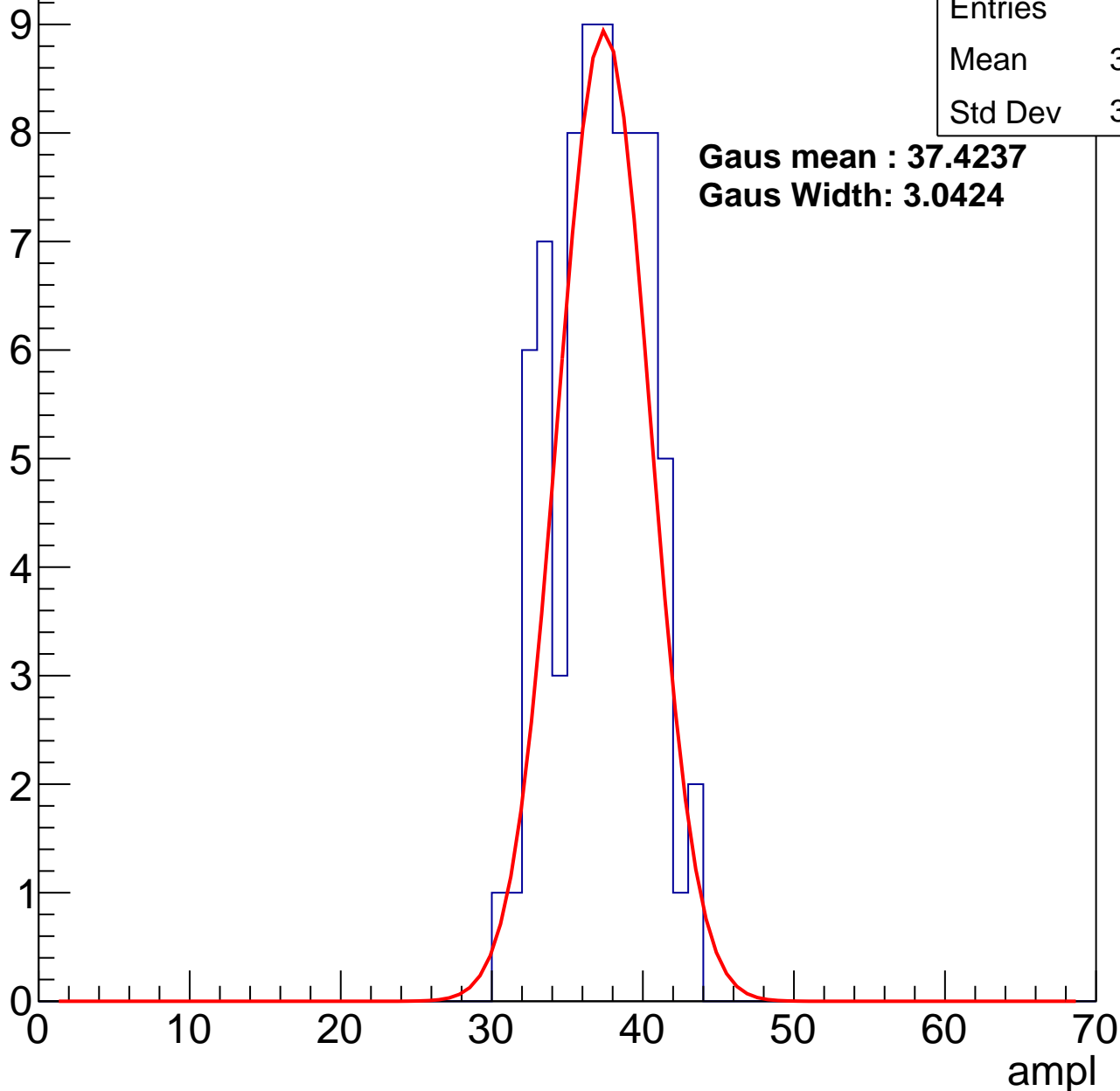
# B0L002S, U1-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 36.74 |
| Std Dev | 3.032 |

**Gaus mean : 37.4237**  
**Gaus Width: 3.0424**



# B0L002S, U1-ch56, adc2

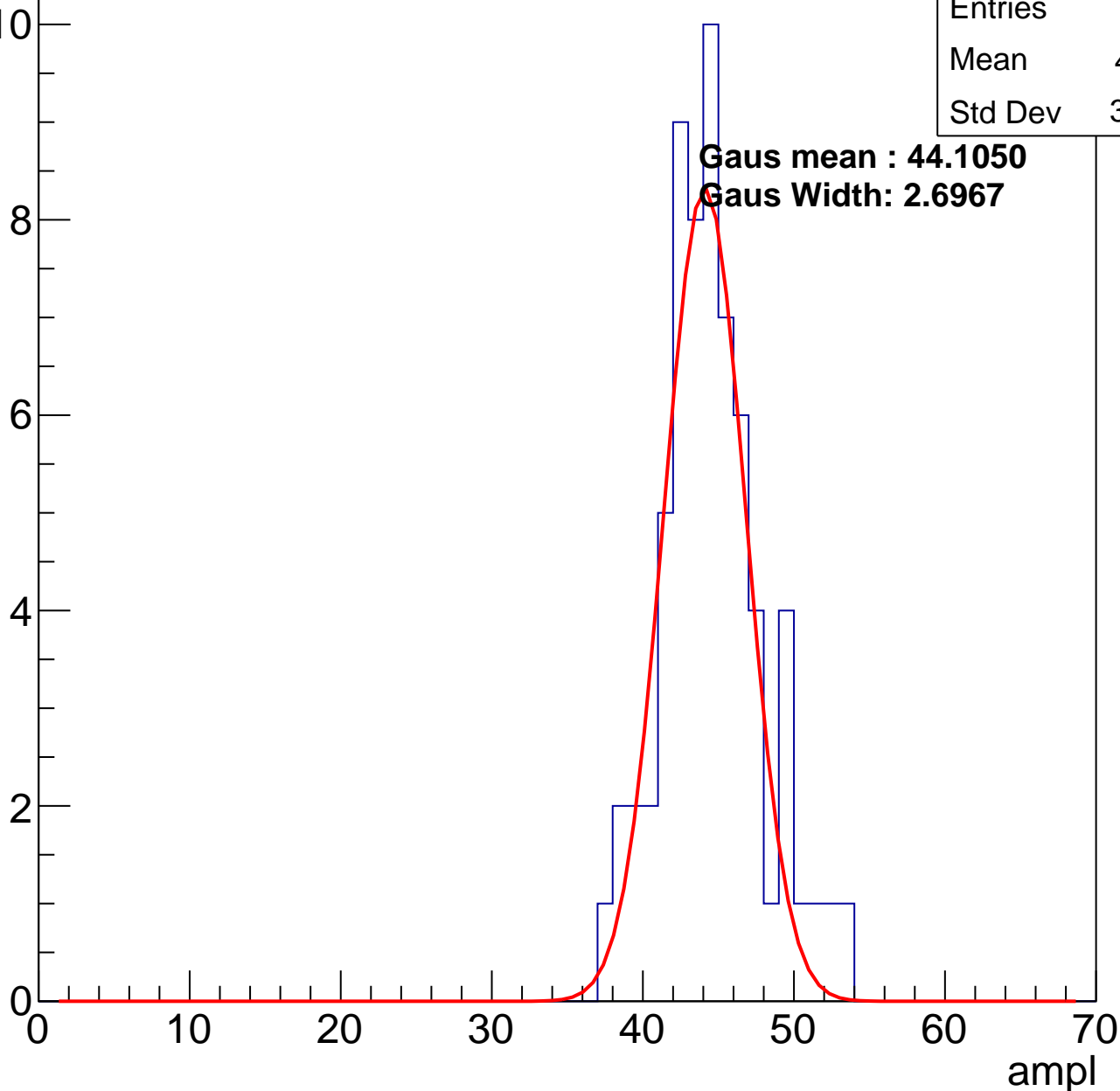
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 44.11 |
| Std Dev | 3.292 |

**Gaus mean : 44.1050**

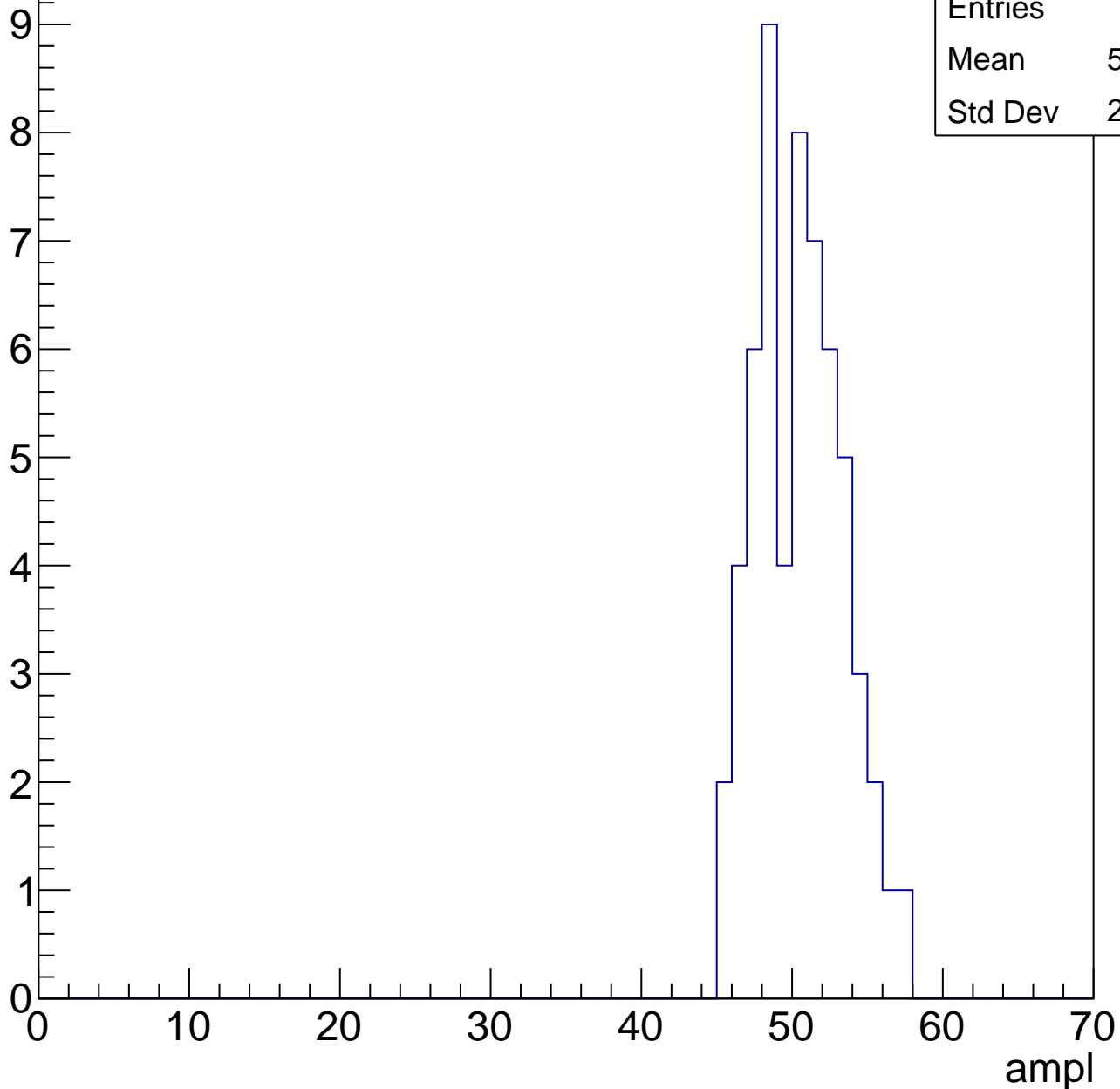
**Gaus Width: 2.6967**



# B0L002S, U1-ch56, adc3

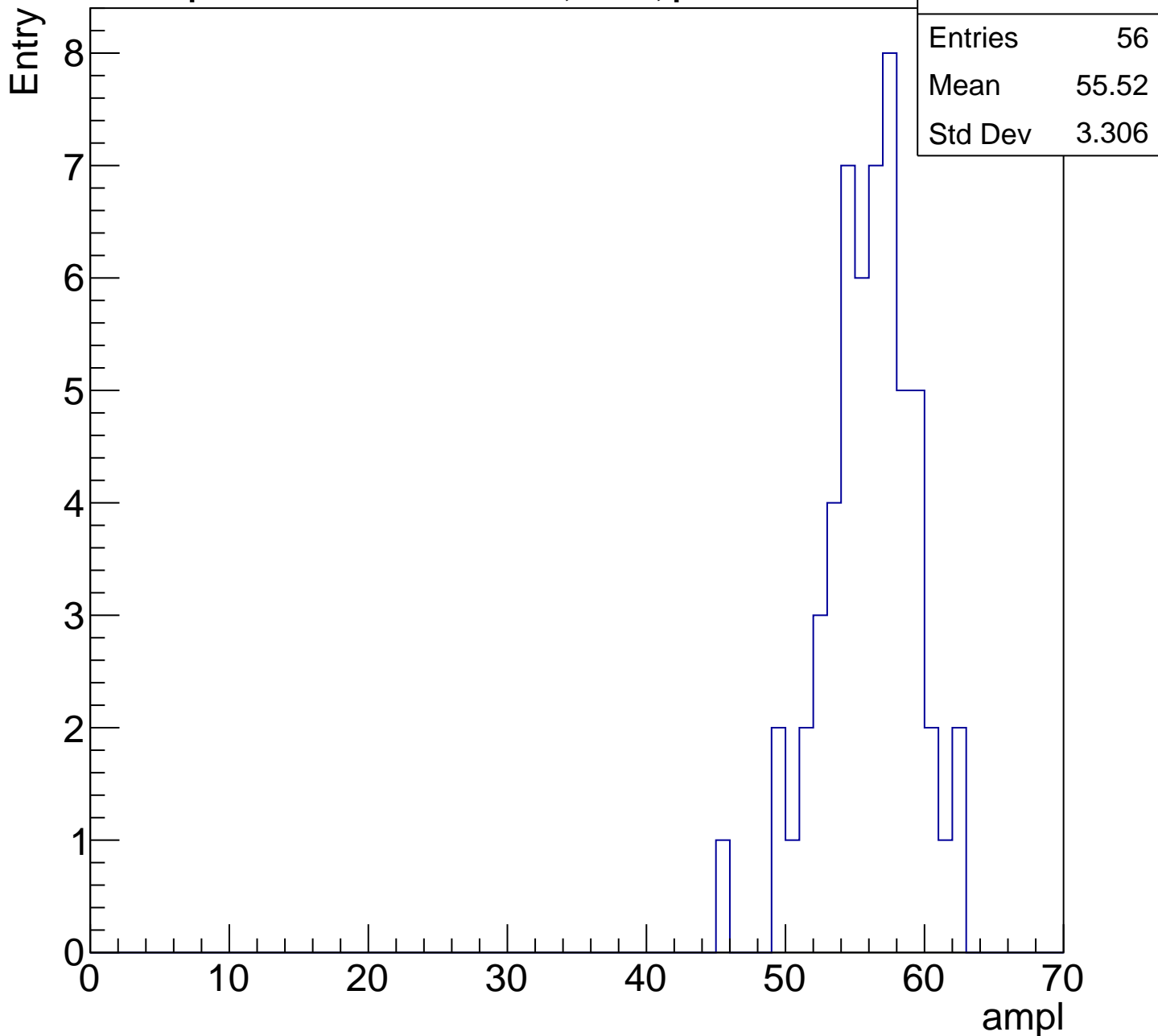
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch56, adc5

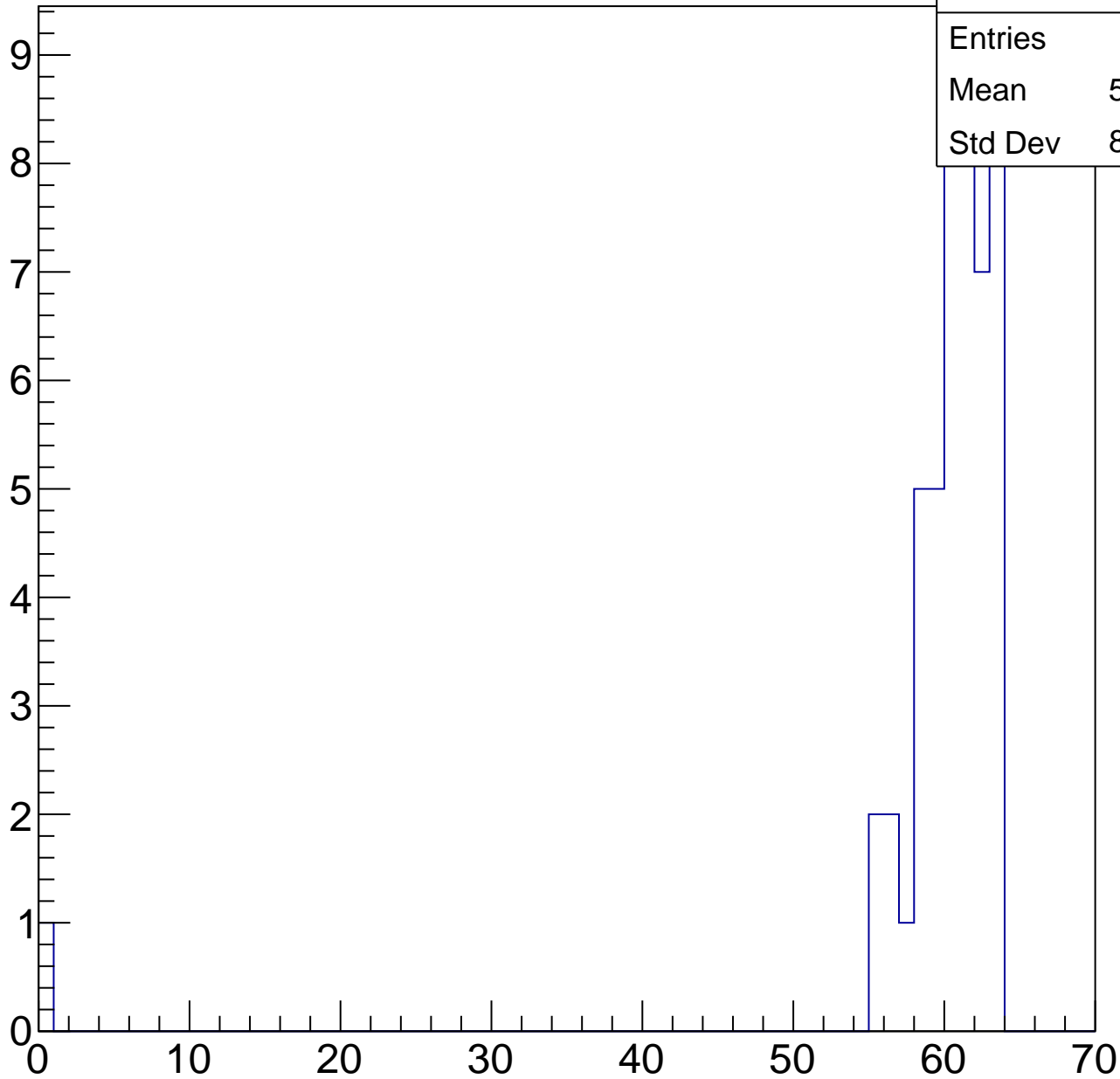
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 59.06 |
| Std Dev | 8.796 |

ampl



# B0L002S, U1-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 28.79 |
| Std Dev | 3.54  |

**Gaus mean : 28.8762**

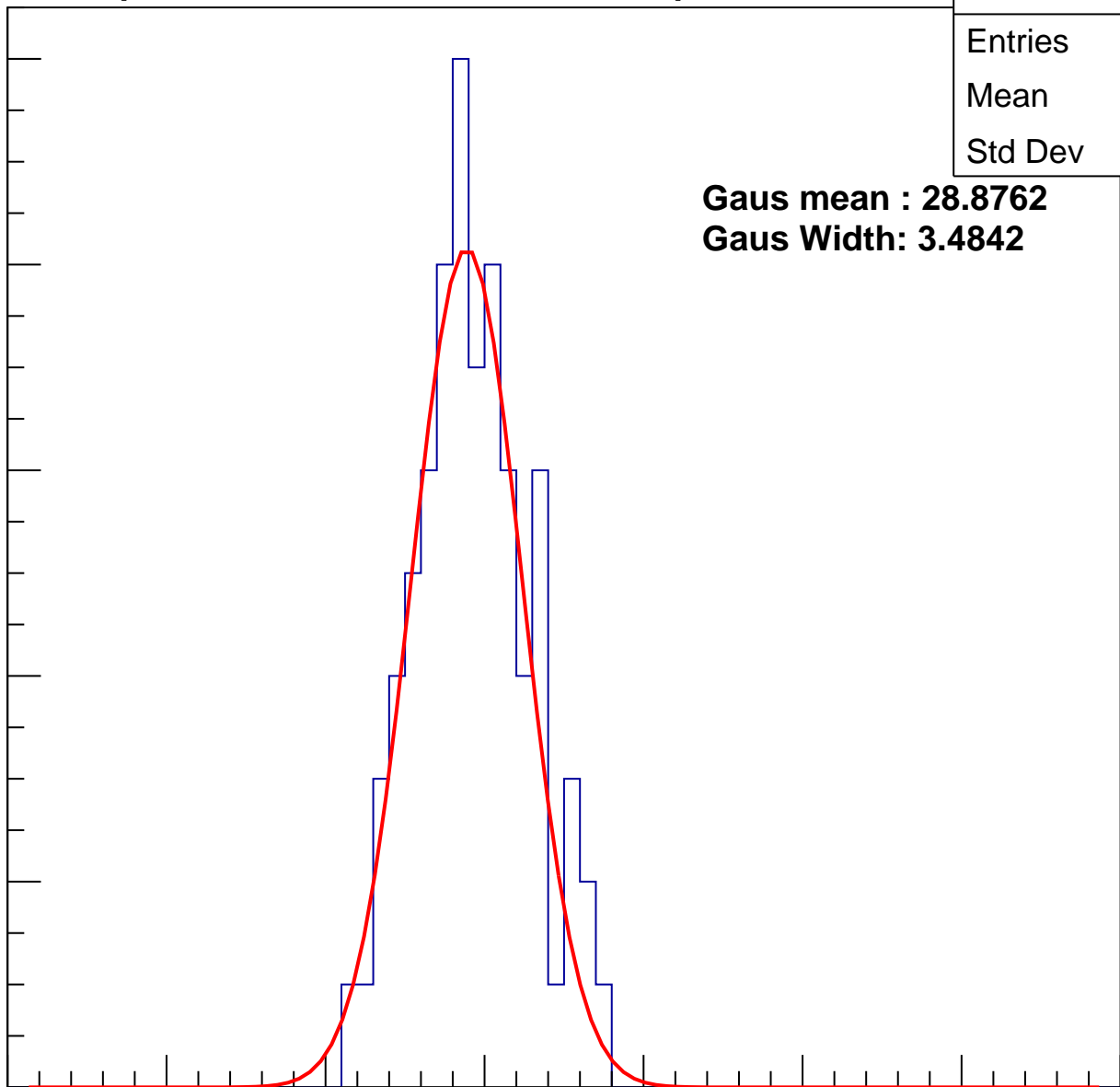
**Gaus Width: 3.4842**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

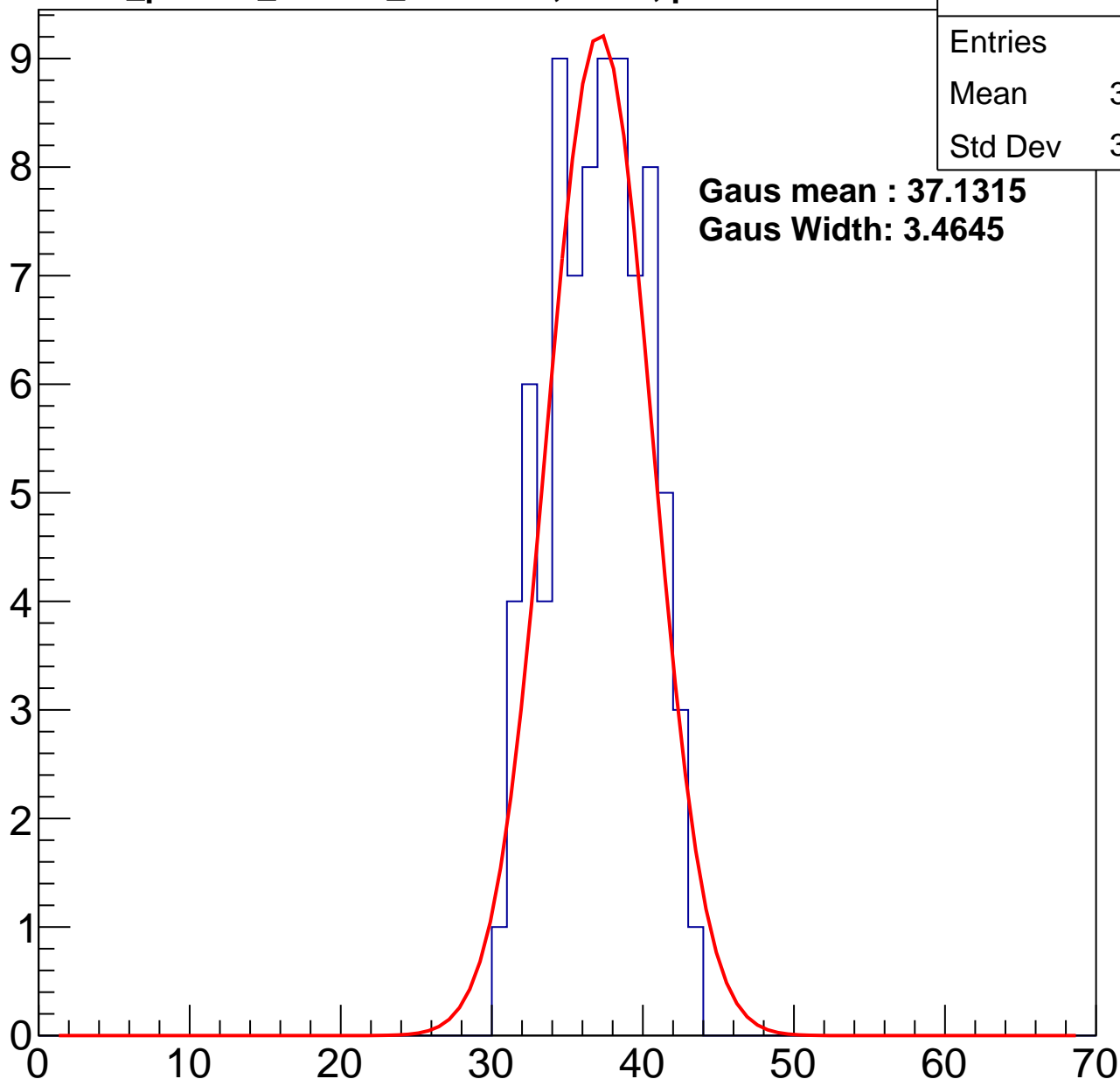
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 36.53 |
| Std Dev | 3.155 |

**Gaus mean : 37.1315**  
**Gaus Width: 3.4645**

ampl



# B0L002S, U1-ch57, adc2

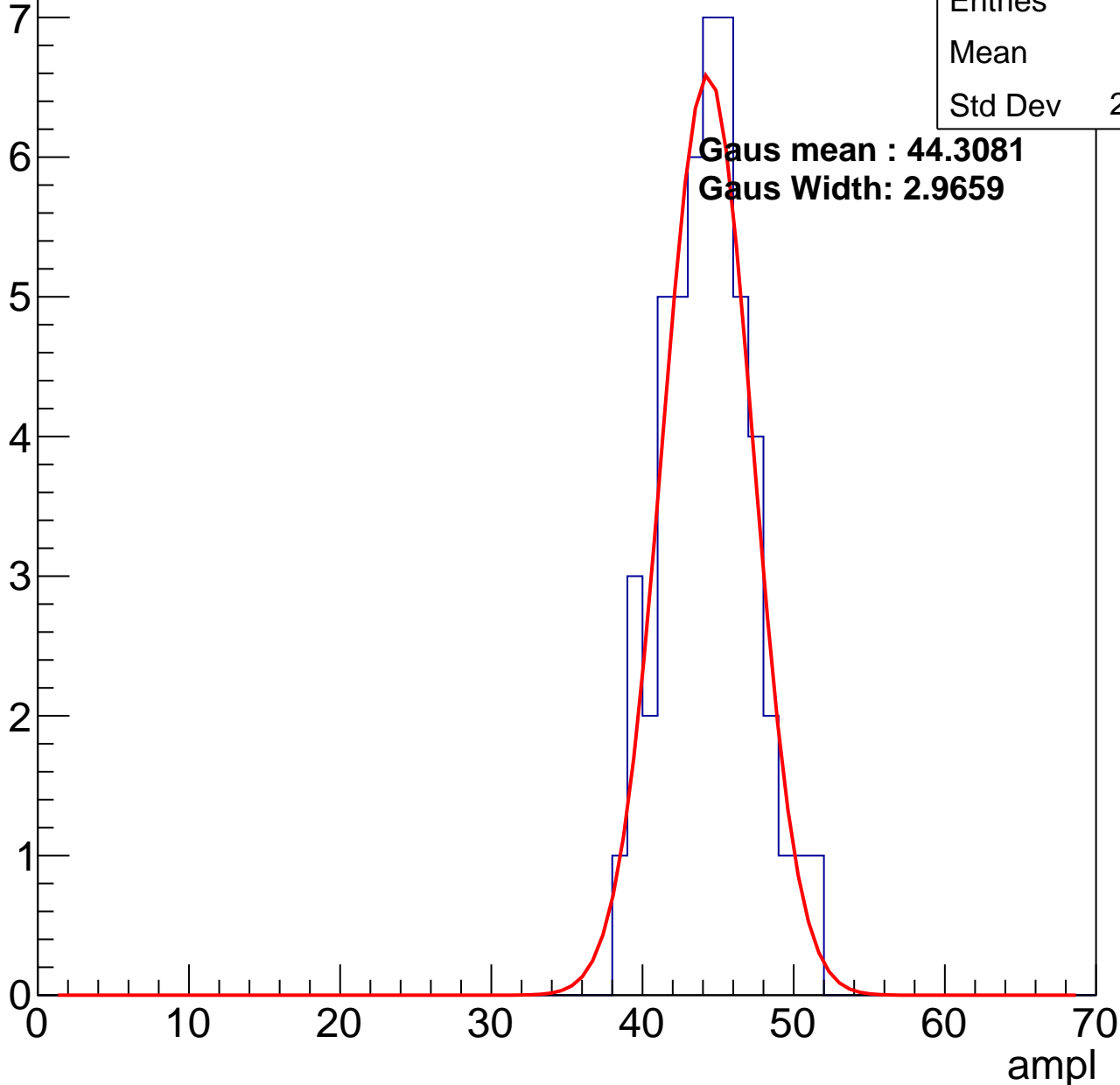
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 43.9  |
| Std Dev | 2.893 |

**Gaus mean : 44.3081**

**Gaus Width: 2.9659**



# B0L002S, U1-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 58

Mean 48.9

Std Dev 3.633

0

10

20

30

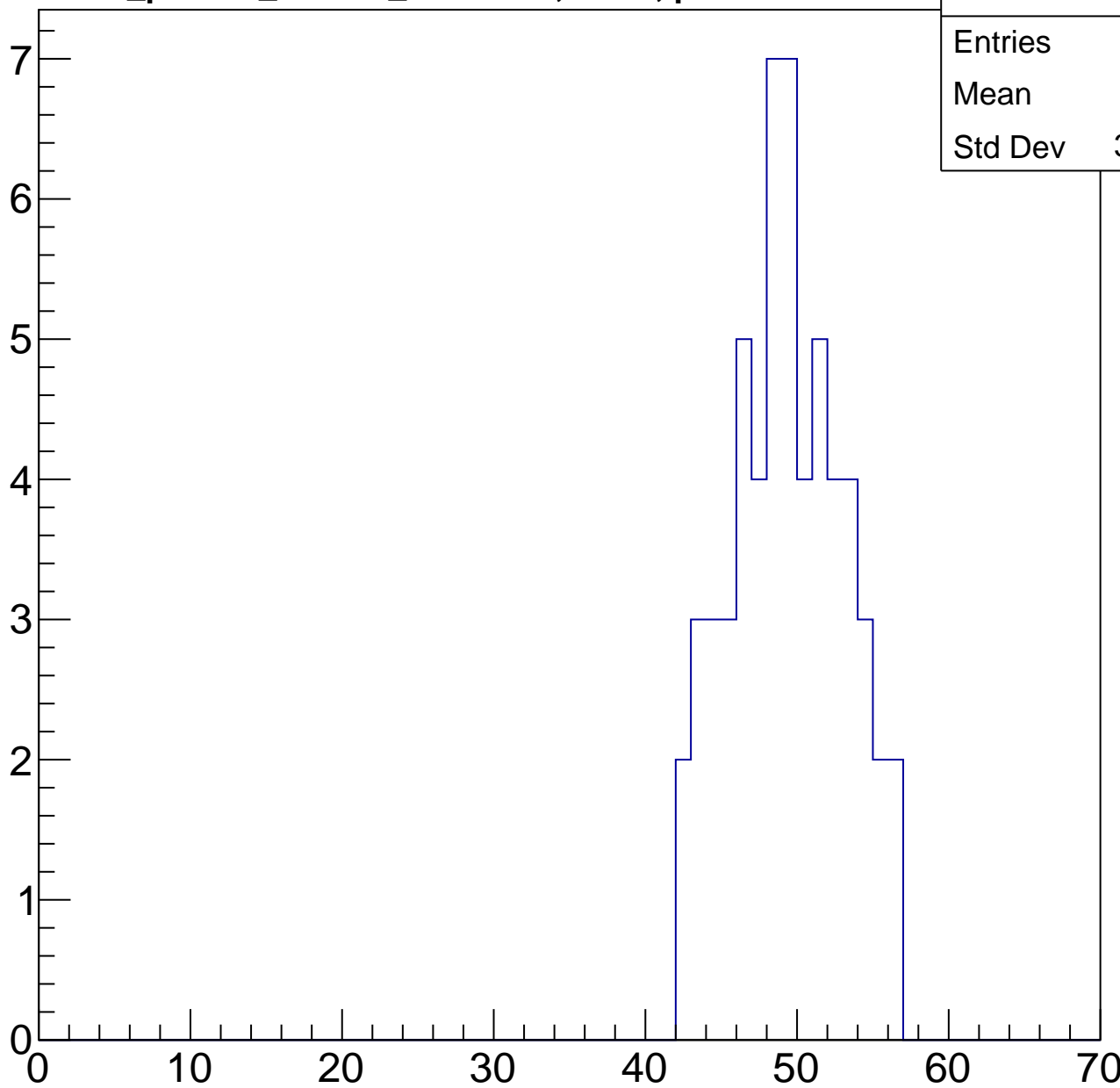
40

50

60

70

ampl

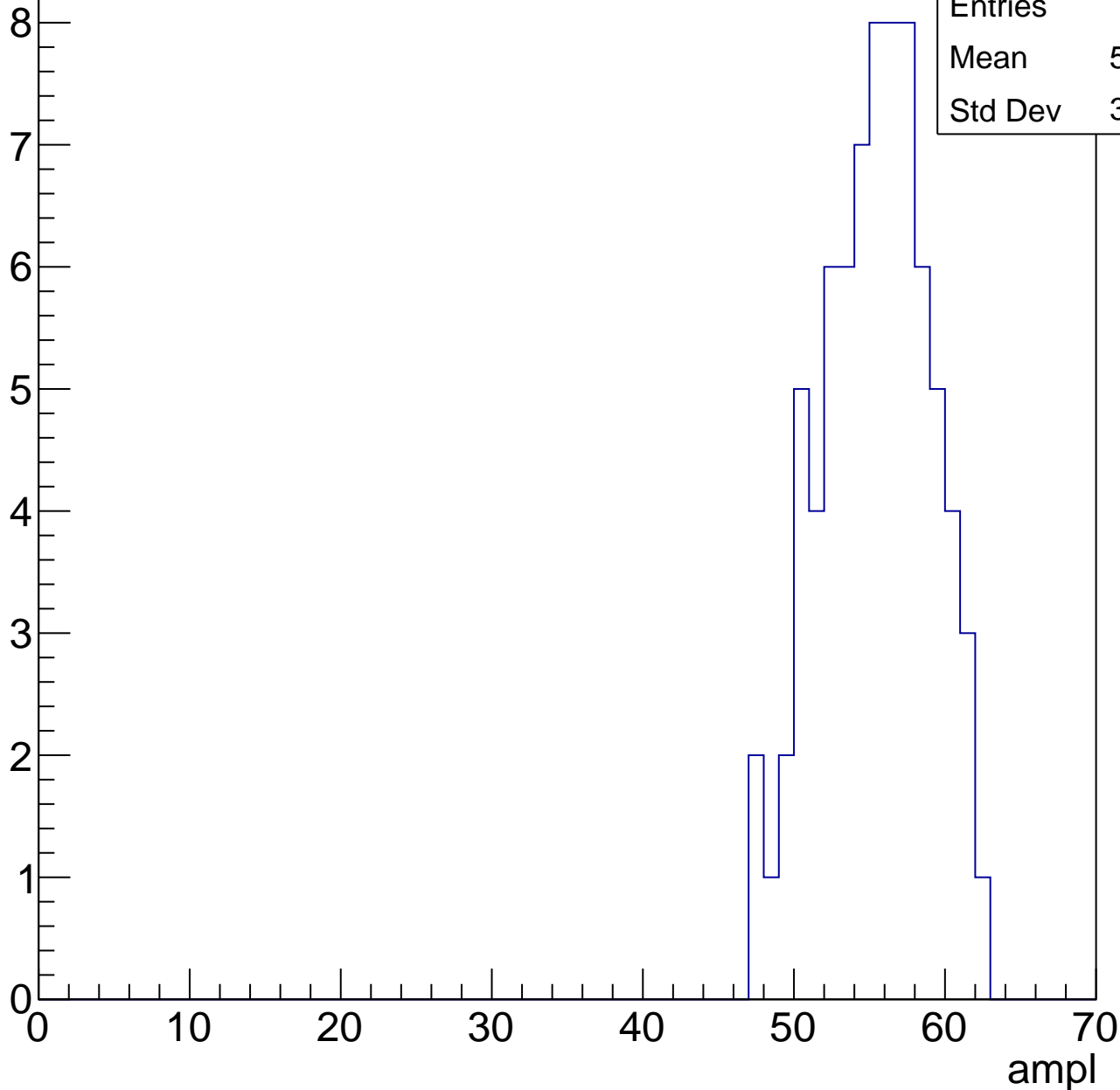


# B0L002S, U1-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 54.92 |
| Std Dev | 3.542 |

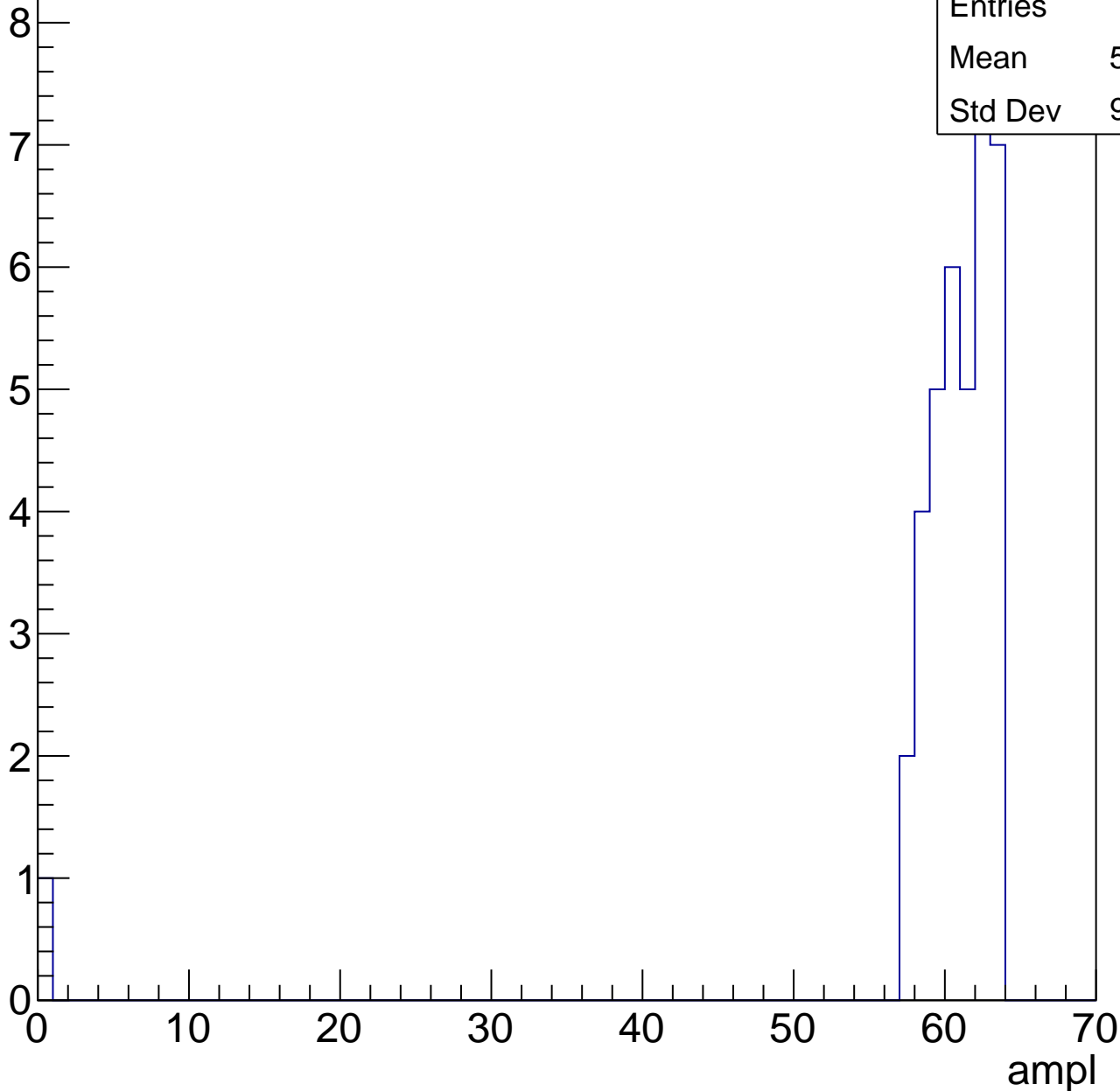


# B0L002S, U1-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 59.03 |
| Std Dev | 9.872 |



# B0L002S, U1-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 20 |
| Std Dev | 0  |

# B0L002S, U1-ch58, adc0

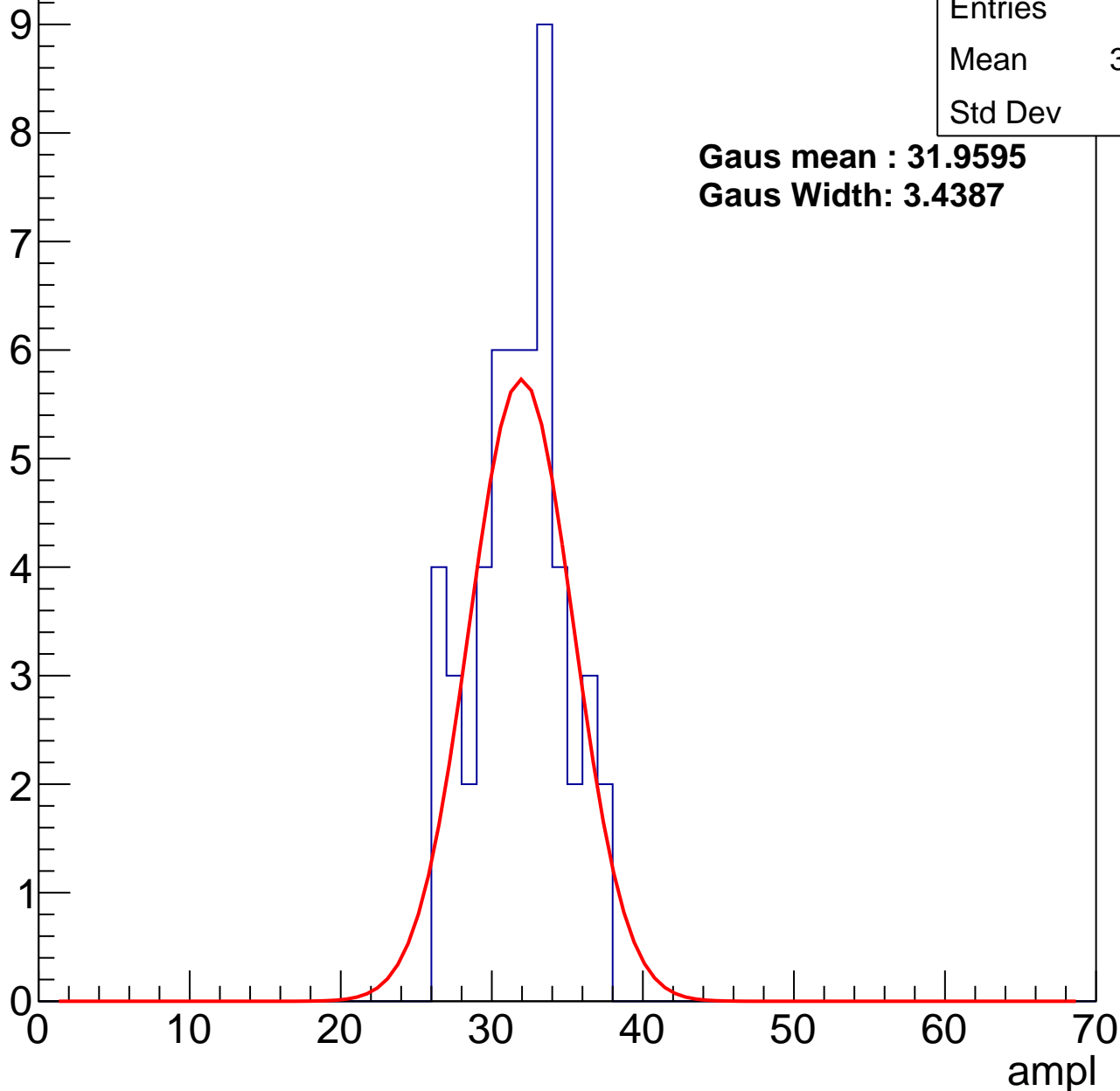
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 31.37 |
| Std Dev | 2.93  |

**Gaus mean : 31.9595**

**Gaus Width: 3.4387**



# B0L002S, U1-ch58, adc1

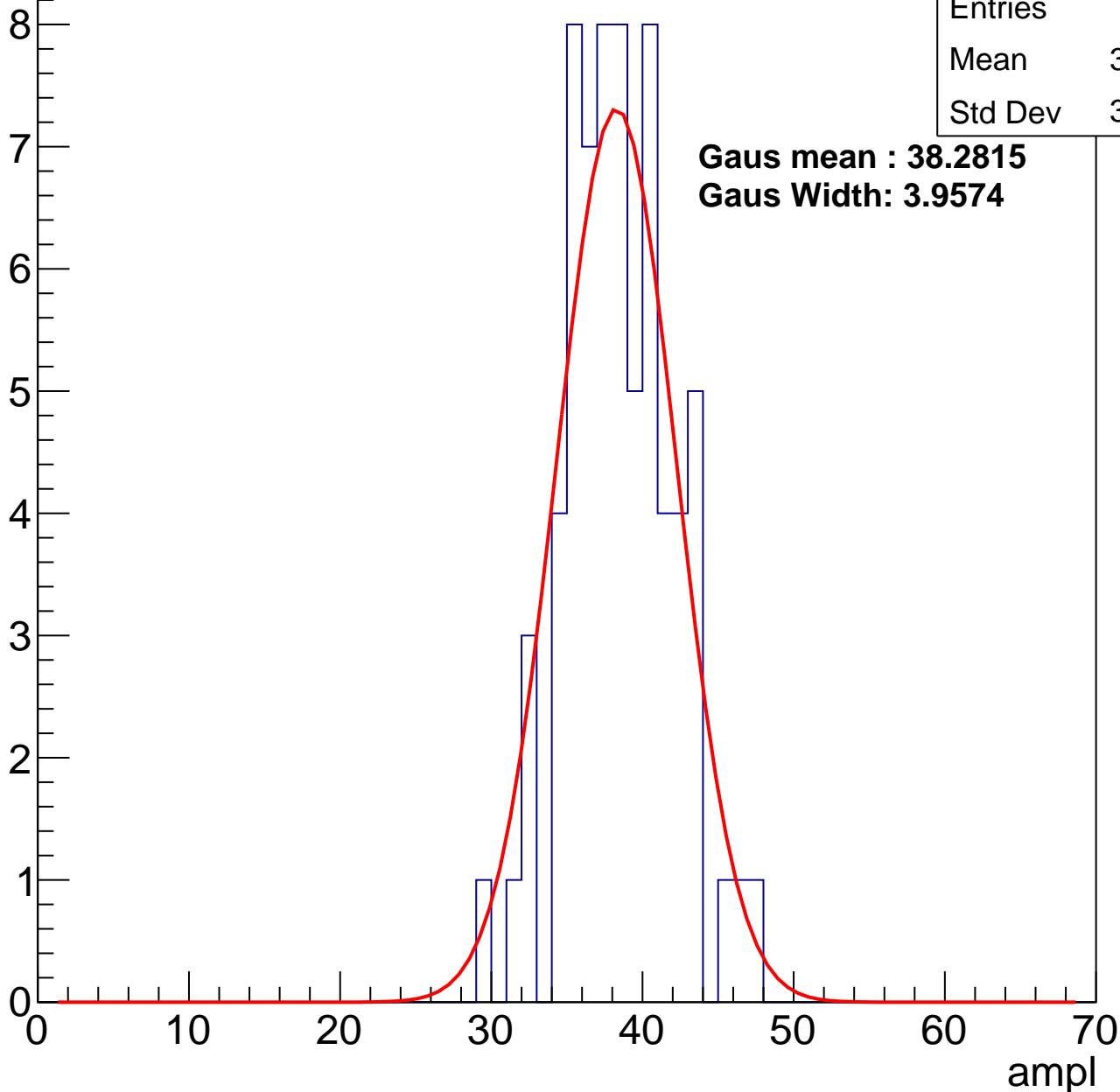
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 38.03 |
| Std Dev | 3.543 |

**Gaus mean : 38.2815**

**Gaus Width: 3.9574**



# B0L002S, U1-ch58, adc2

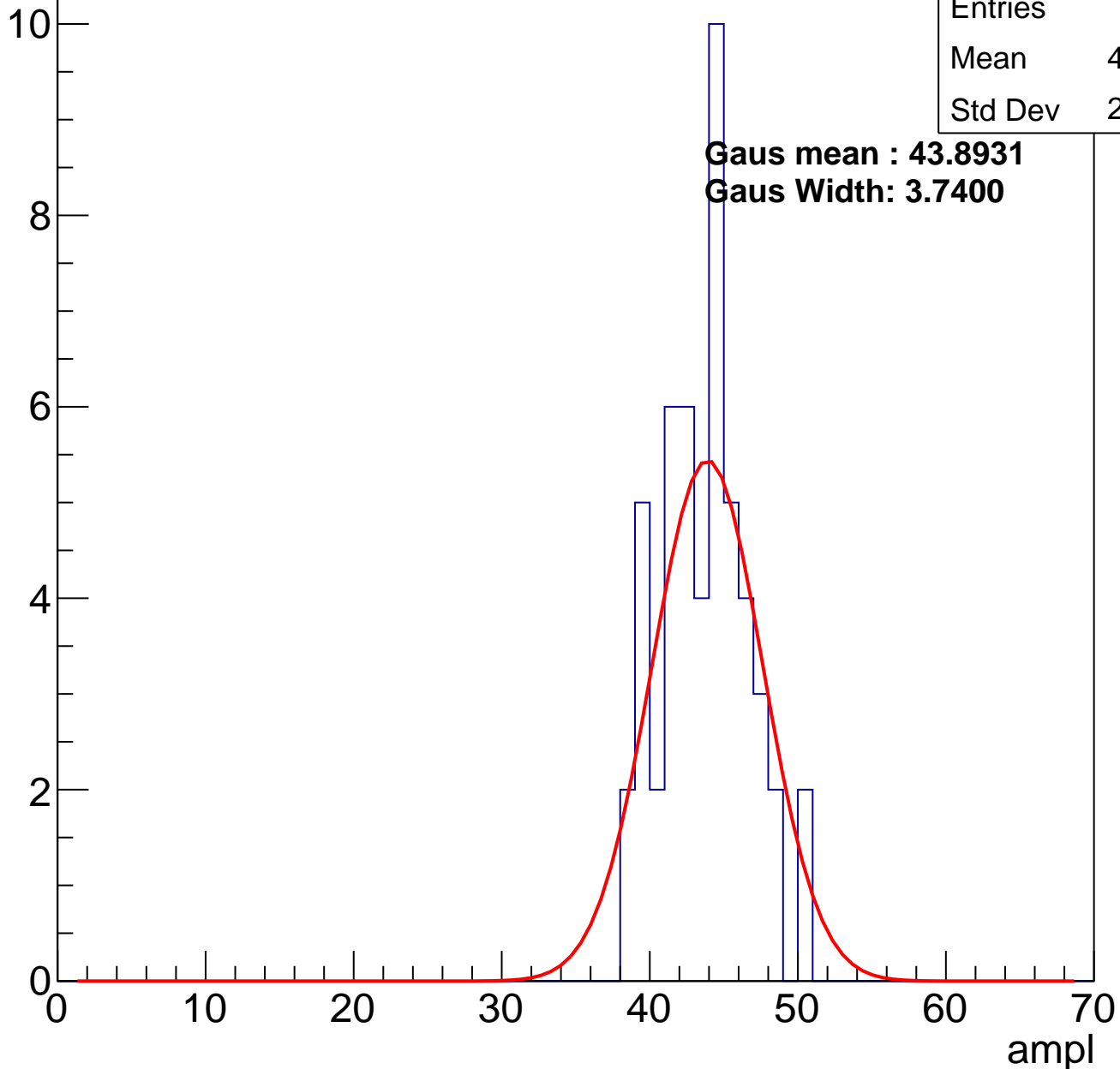
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 43.27 |
| Std Dev | 2.924 |

**Gaus mean : 43.8931**

**Gaus Width: 3.7400**

Entry

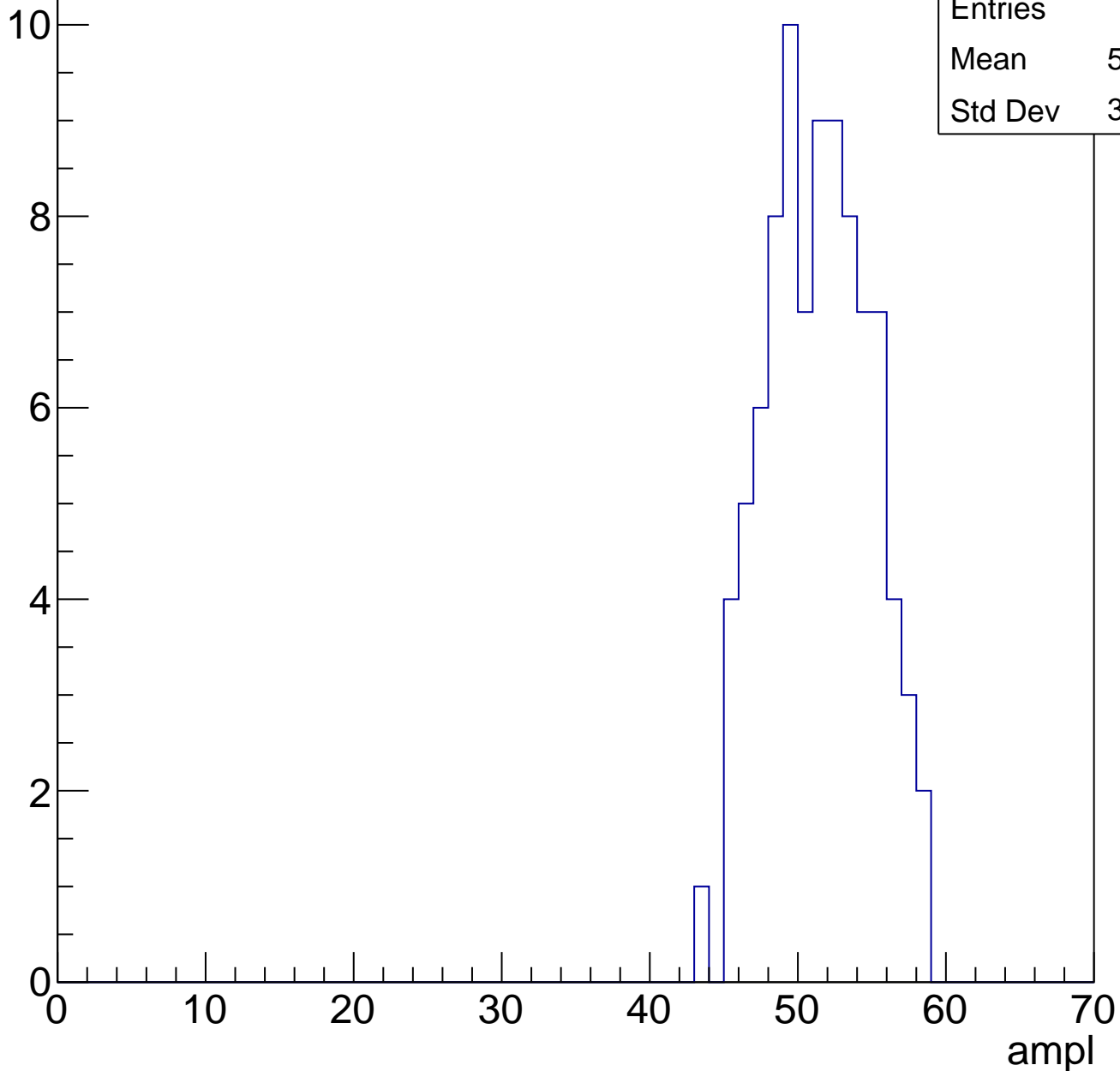


# B0L002S, U1-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 90    |
| Mean    | 50.93 |
| Std Dev | 3.447 |

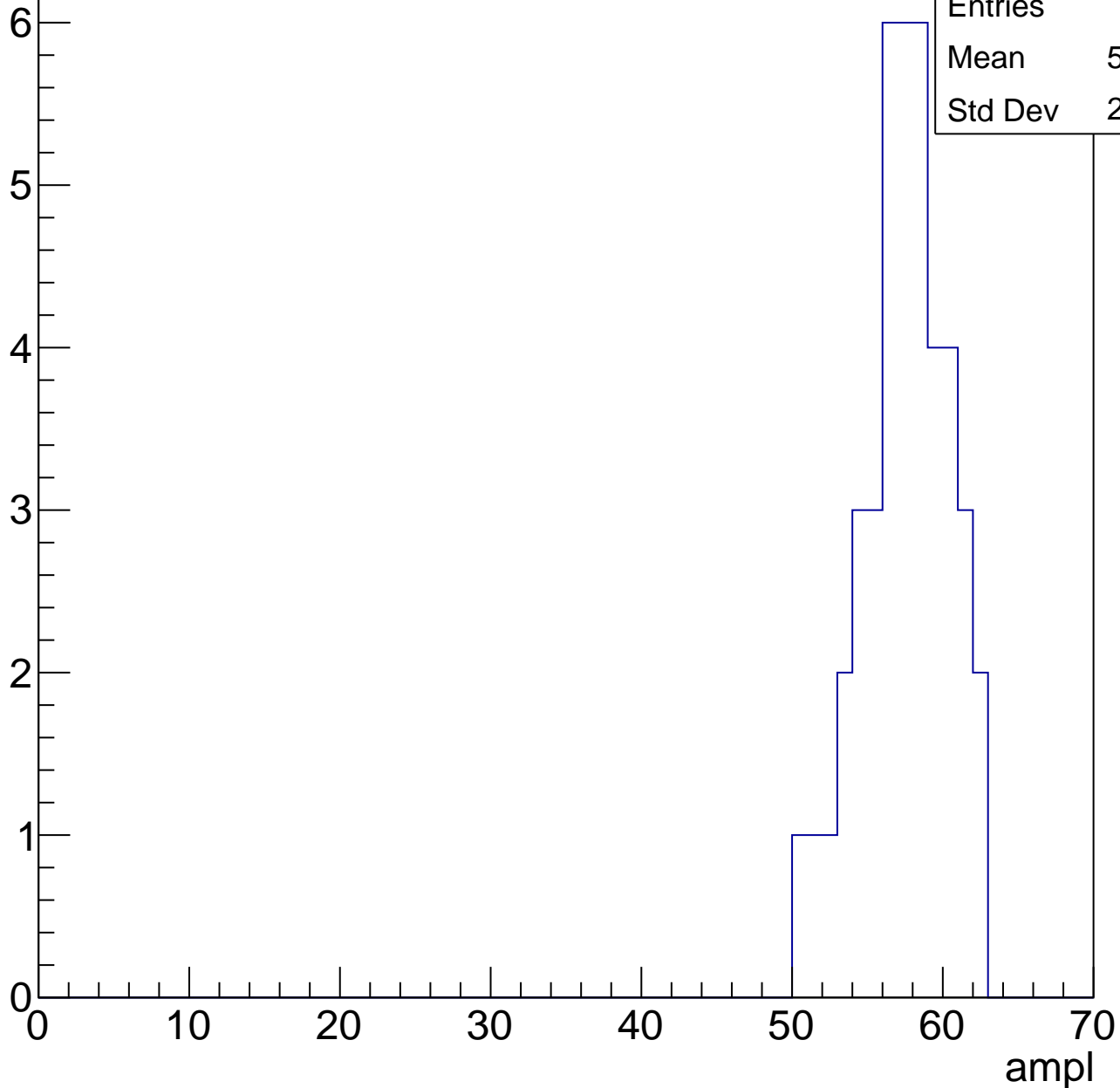
Entry



# B0L002S, U1-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

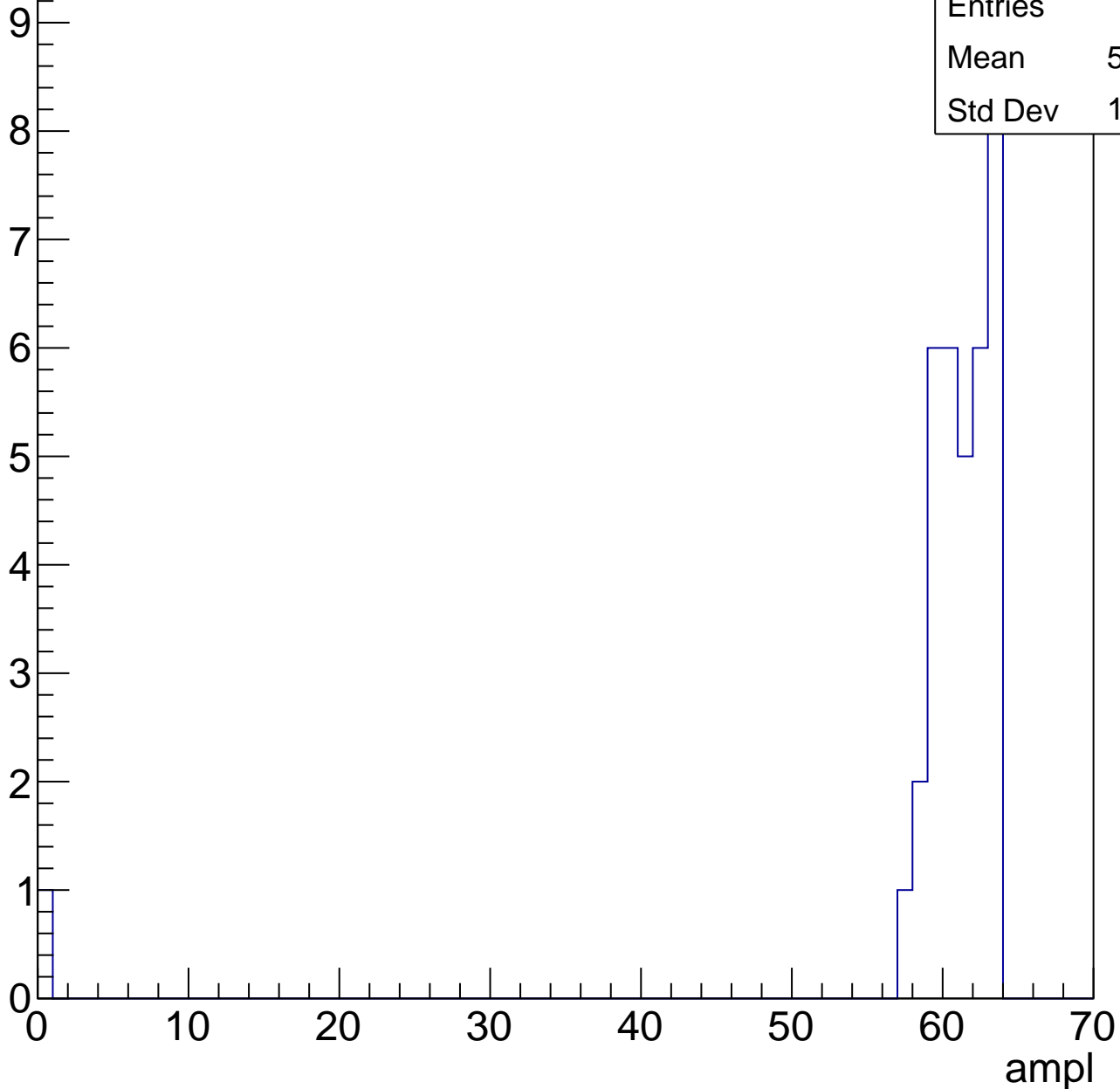


# B0L002S, U1-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

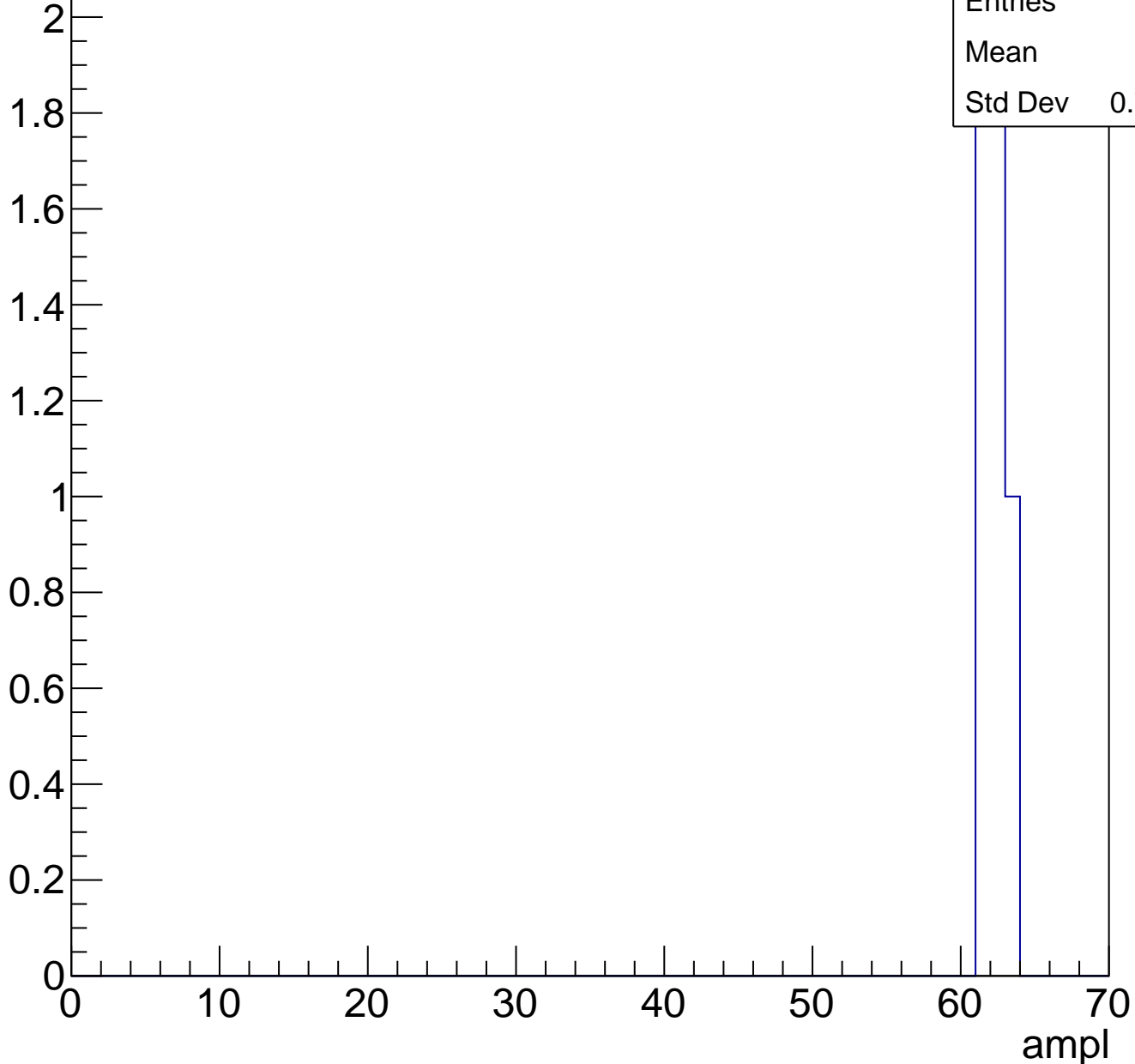
|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 59.19 |
| Std Dev | 10.15 |



# B0L002S, U1-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 24 |
| Std Dev | 0  |

# B0L002S, U1-ch59, adc0

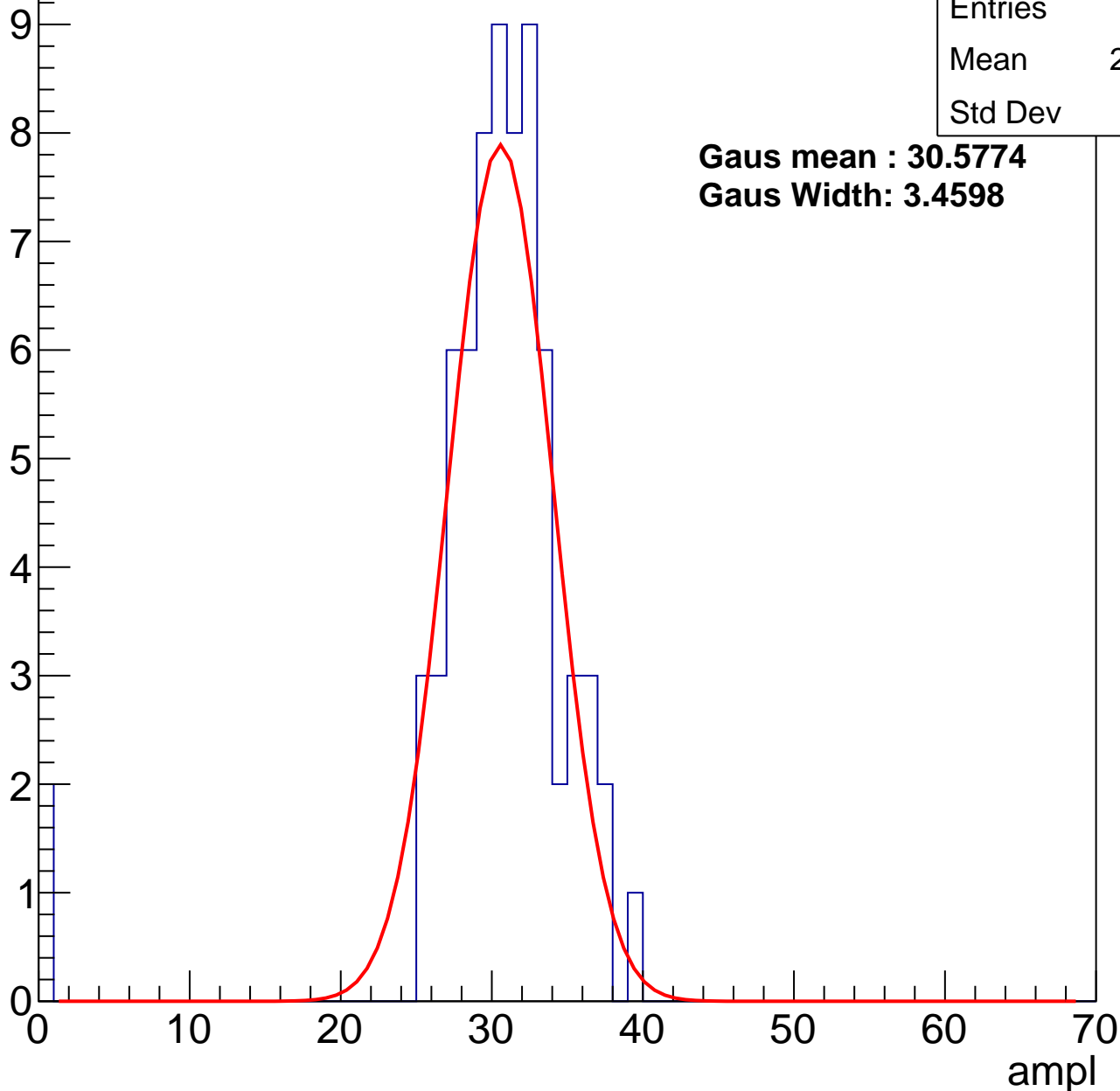
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 29.76 |
| Std Dev | 5.93  |

**Gaus mean : 30.5774**

**Gaus Width: 3.4598**



# B0L002S, U1-ch59, adc1

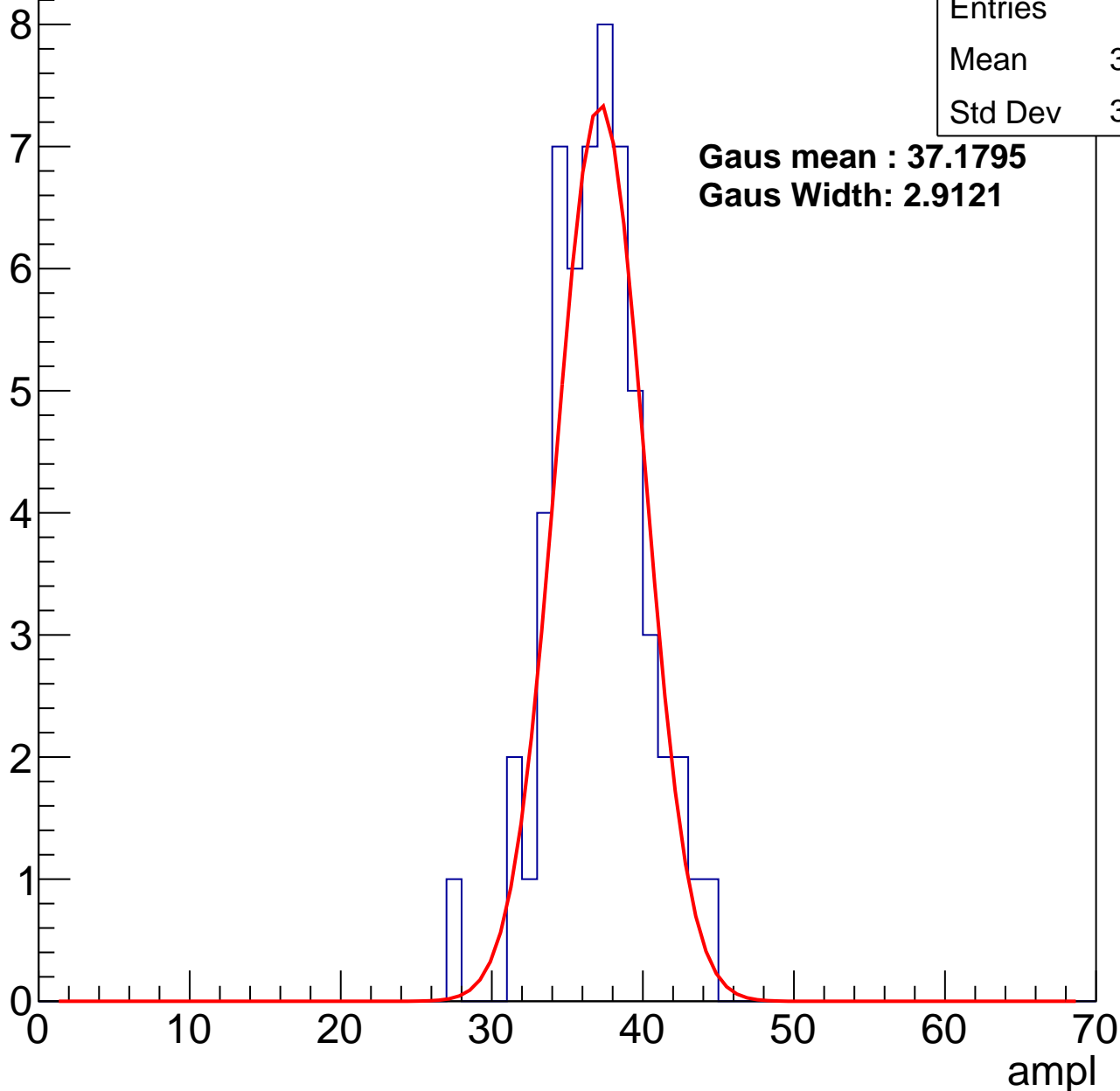
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 36.54 |
| Std Dev | 3.146 |

**Gaus mean : 37.1795**

**Gaus Width: 2.9121**



# B0L002S, U1-ch59, adc2

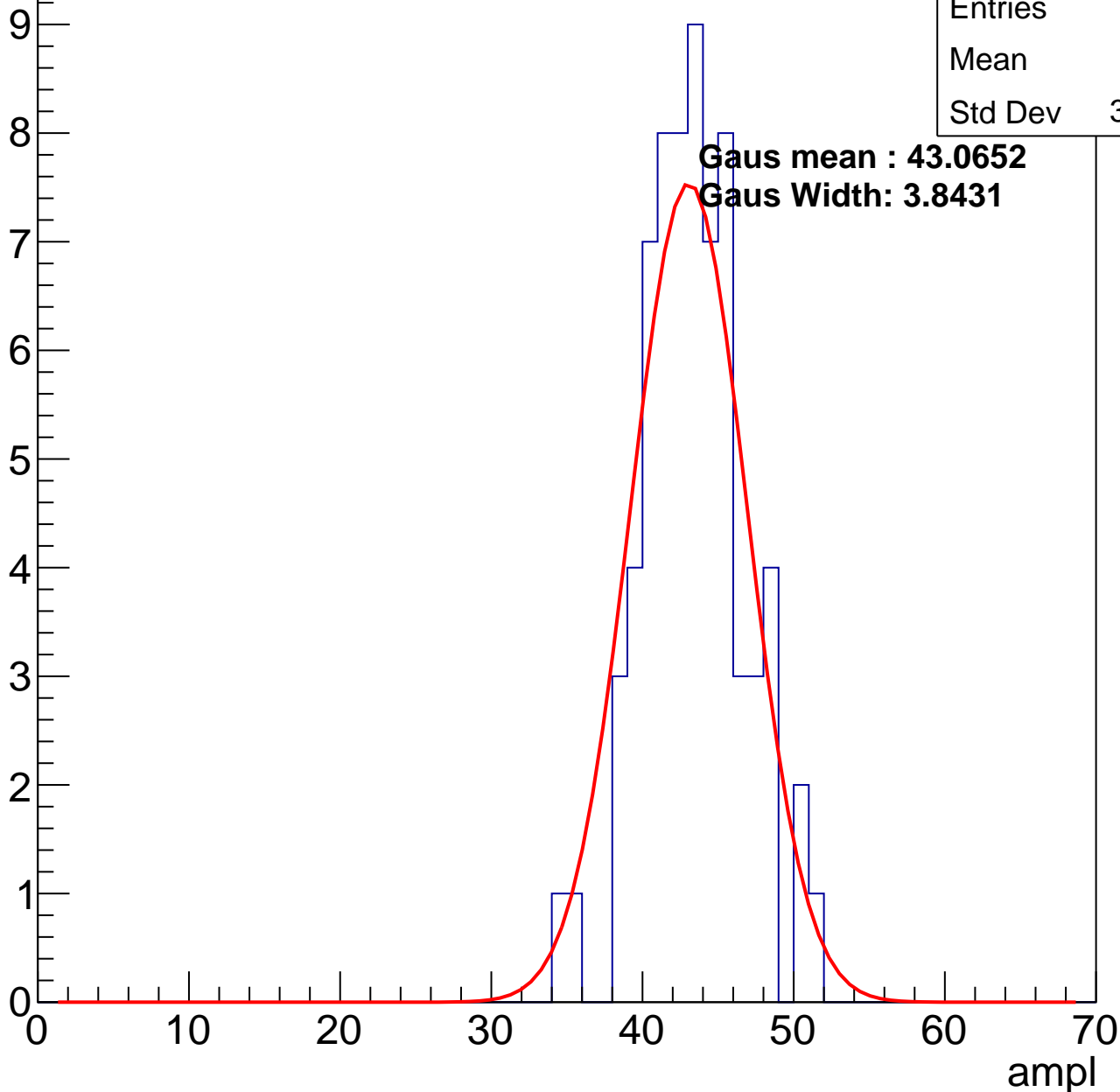
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 42.9  |
| Std Dev | 3.324 |

**Gaus mean : 43.0652**

**Gaus Width: 3.8431**

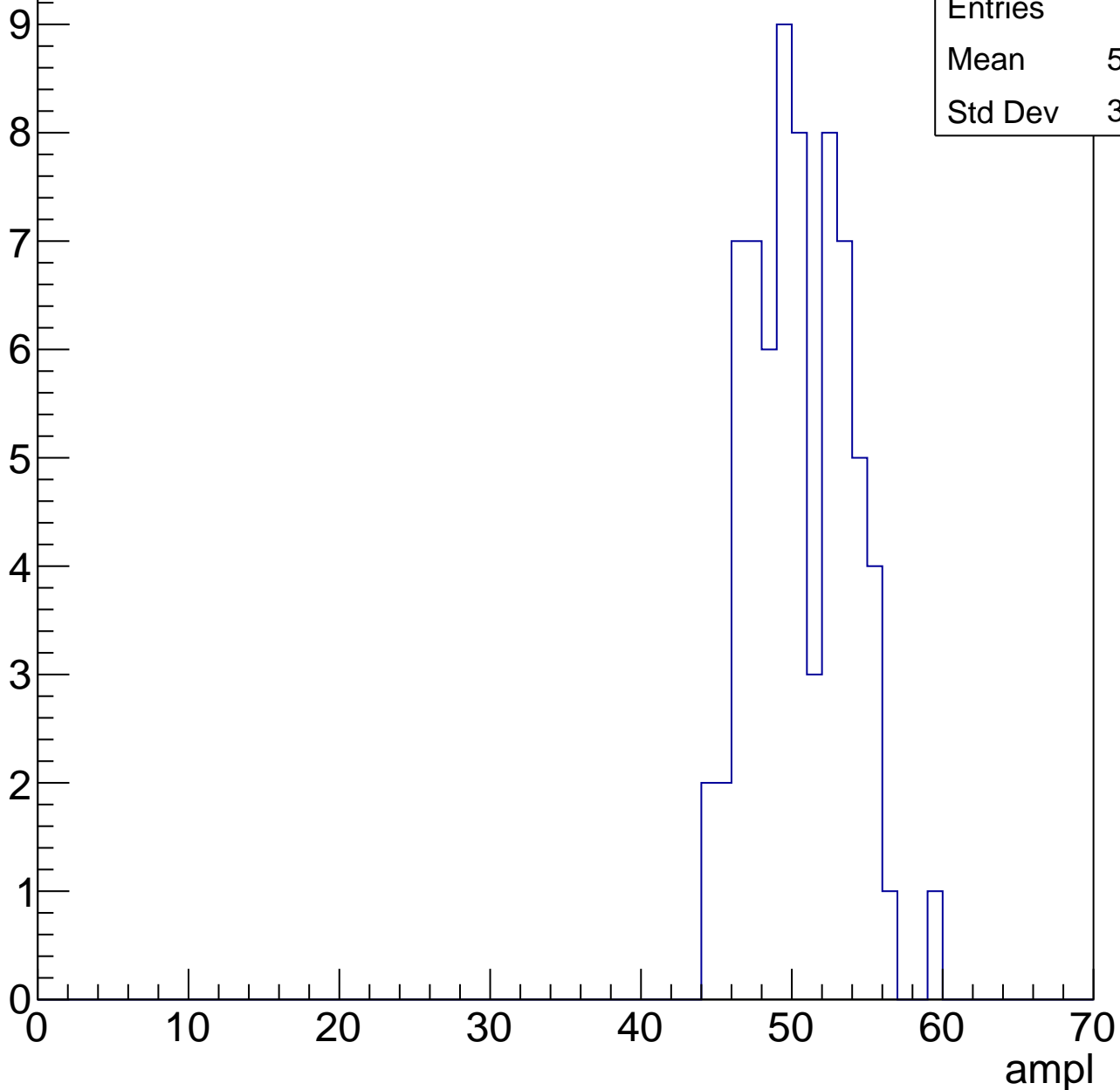


# B0L002S, U1-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 50.04 |
| Std Dev | 3.218 |

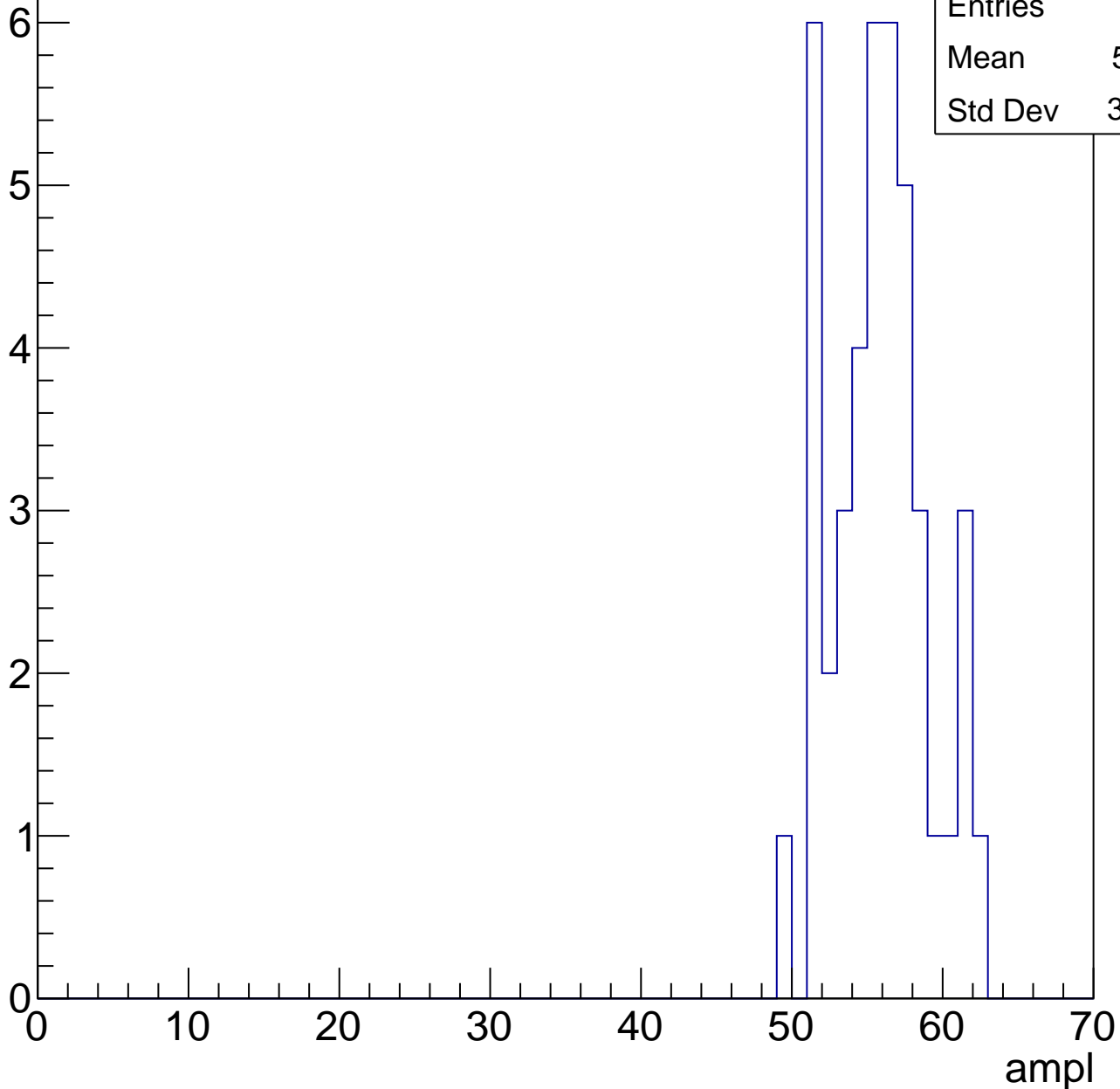


# B0L002S, U1-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 55.31 |
| Std Dev | 3.136 |



# B0L002S, U1-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

8

6

4

2

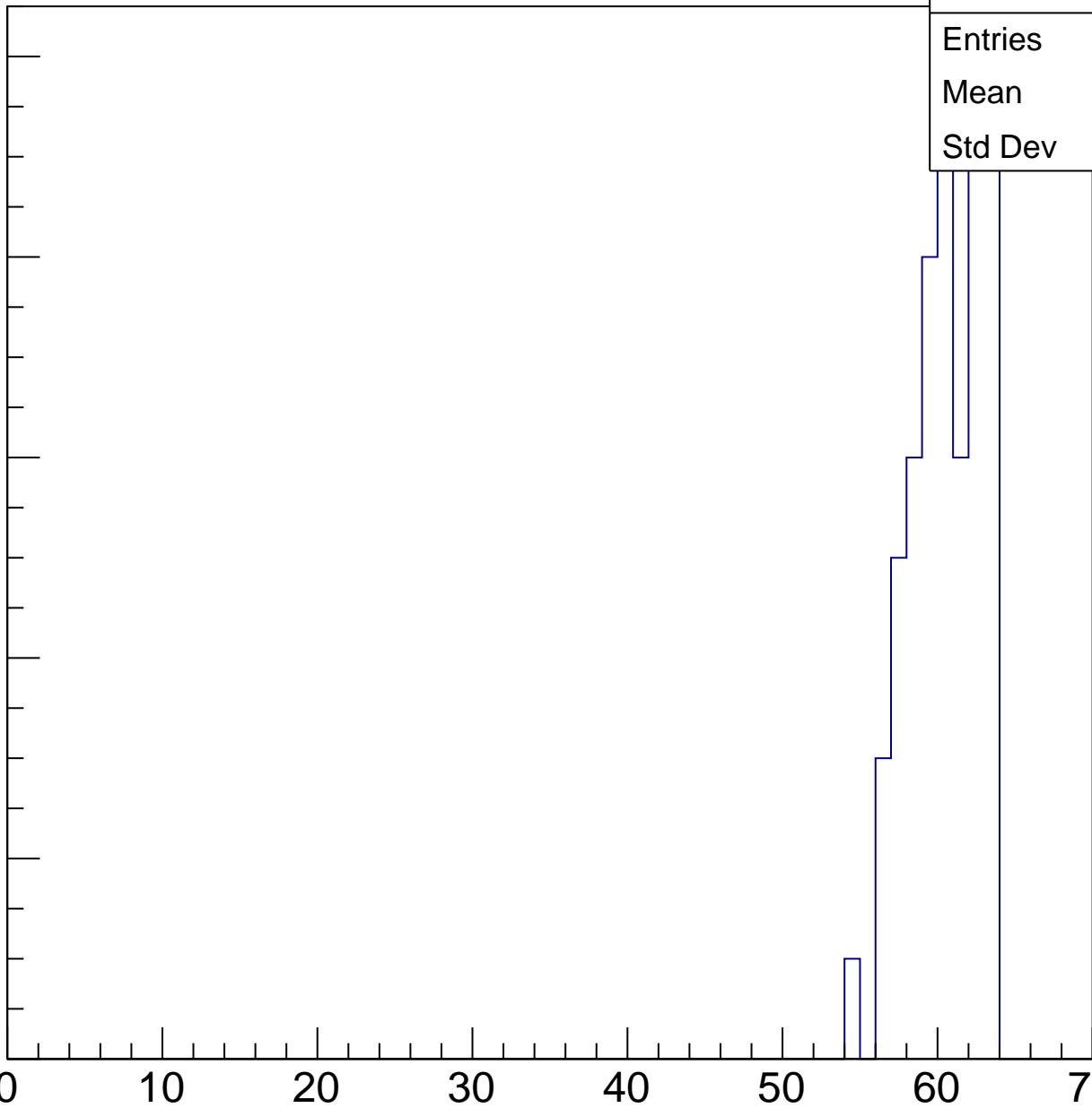
0

|         |    |
|---------|----|
| Entries | 57 |
|---------|----|

|      |       |
|------|-------|
| Mean | 60.02 |
|------|-------|

|         |       |
|---------|-------|
| Std Dev | 2.267 |
|---------|-------|

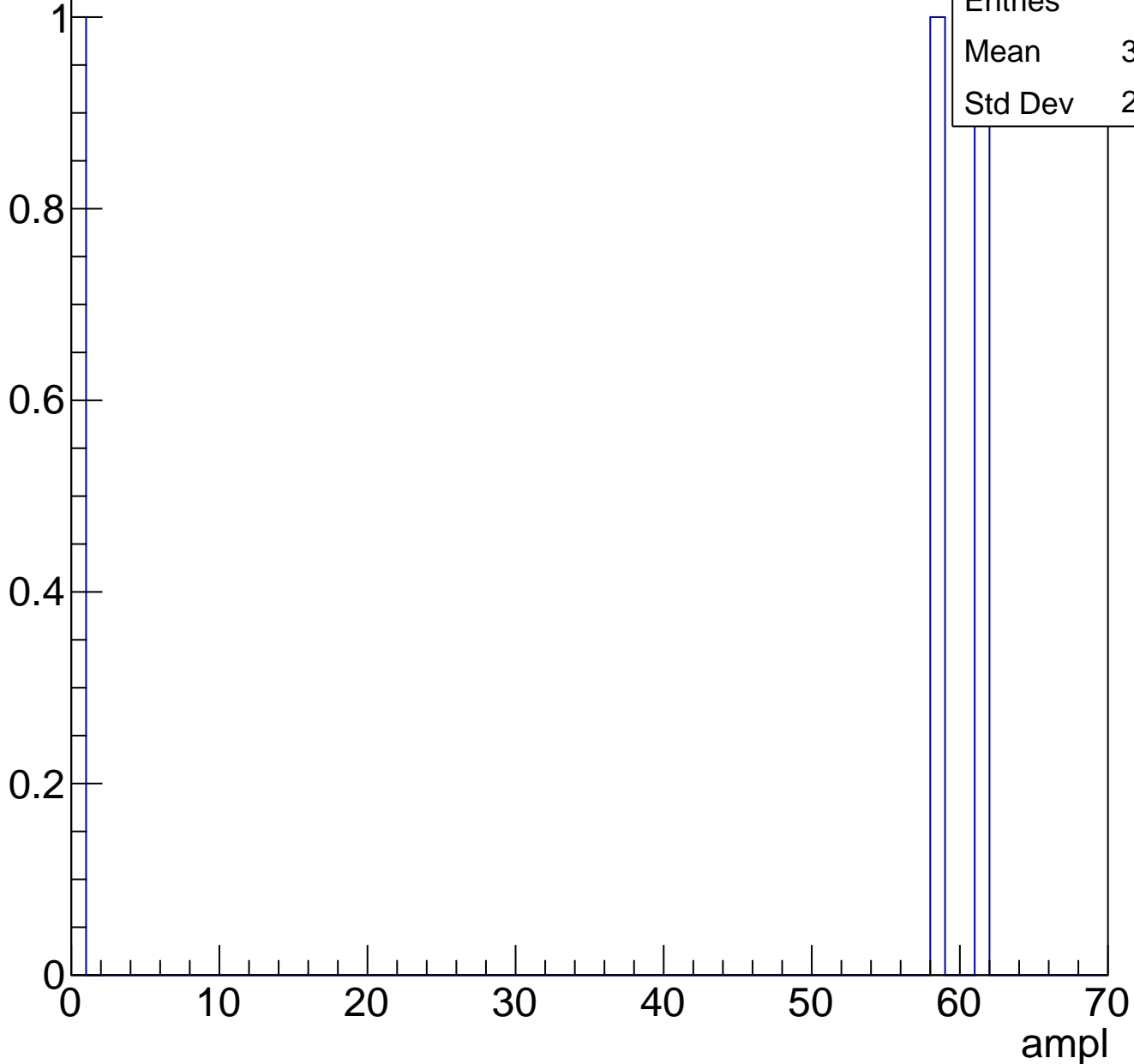
ampl



# B0L002S, U1-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch60, adc0

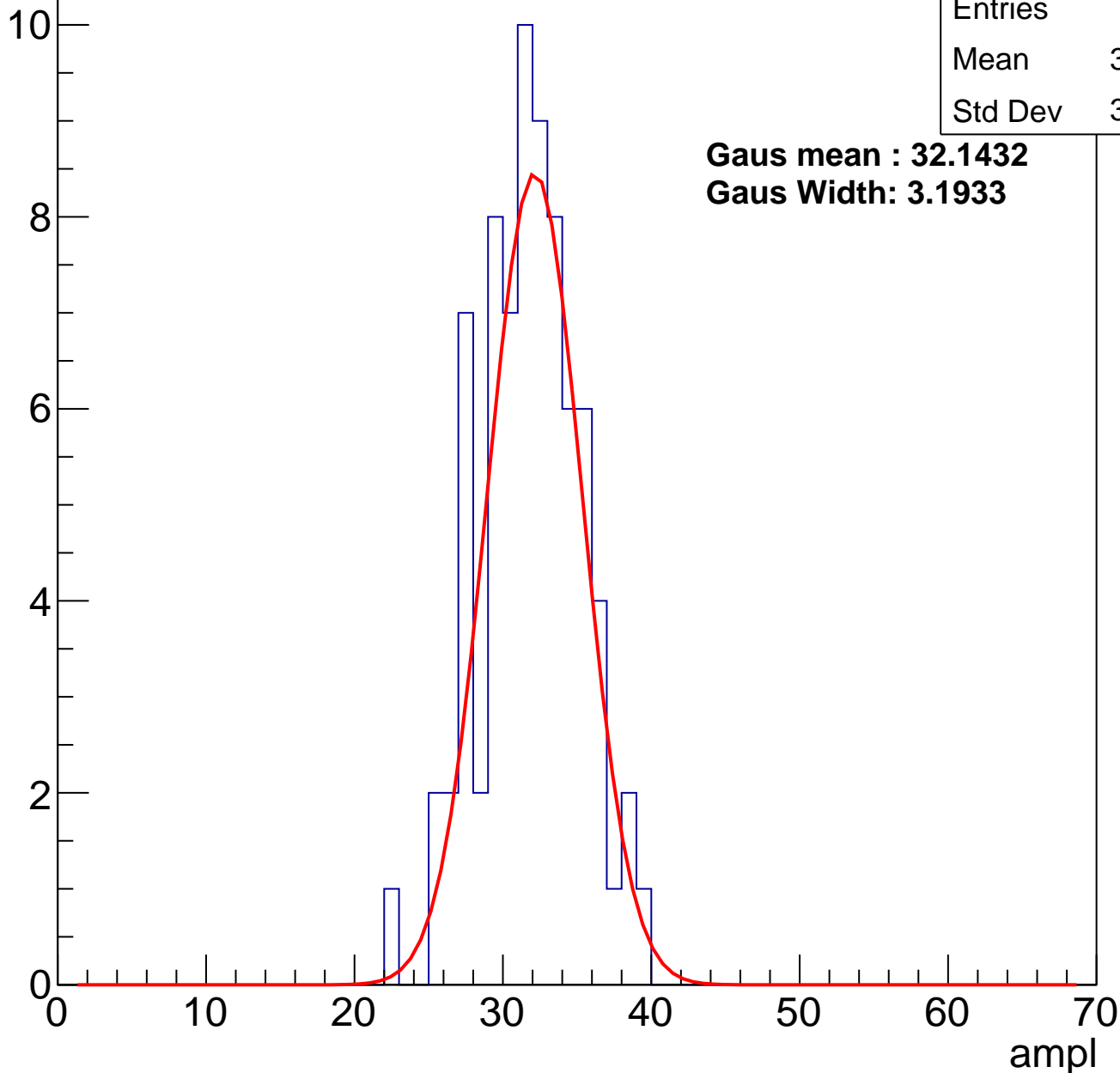
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 31.36 |
| Std Dev | 3.347 |

**Gaus mean : 32.1432**

**Gaus Width: 3.1933**

Entry



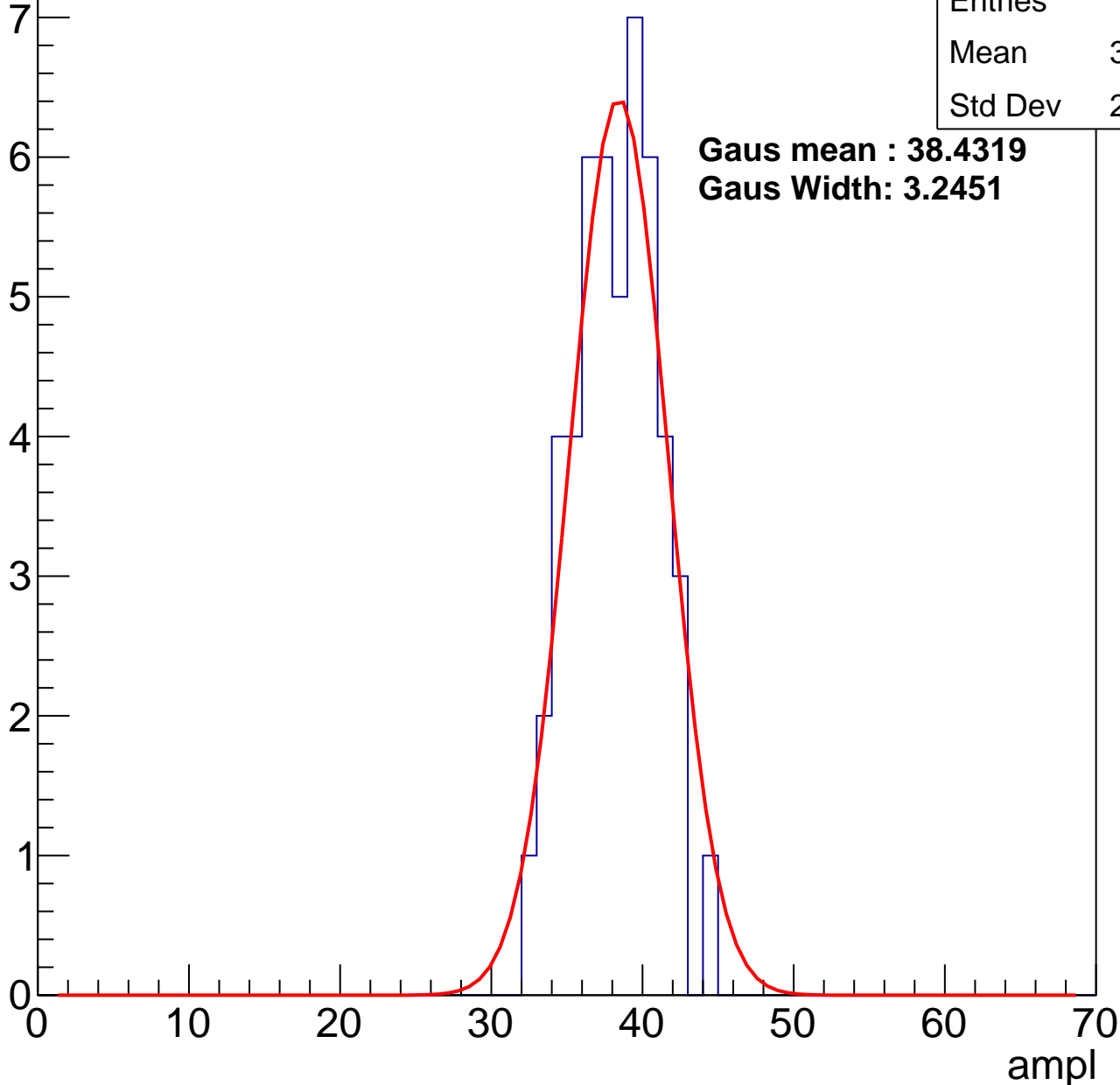
# B0L002S, U1-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 37.73 |
| Std Dev | 2.724 |

**Gaus mean : 38.4319**  
**Gaus Width: 3.2451**



# B0L002S, U1-ch60, adc2

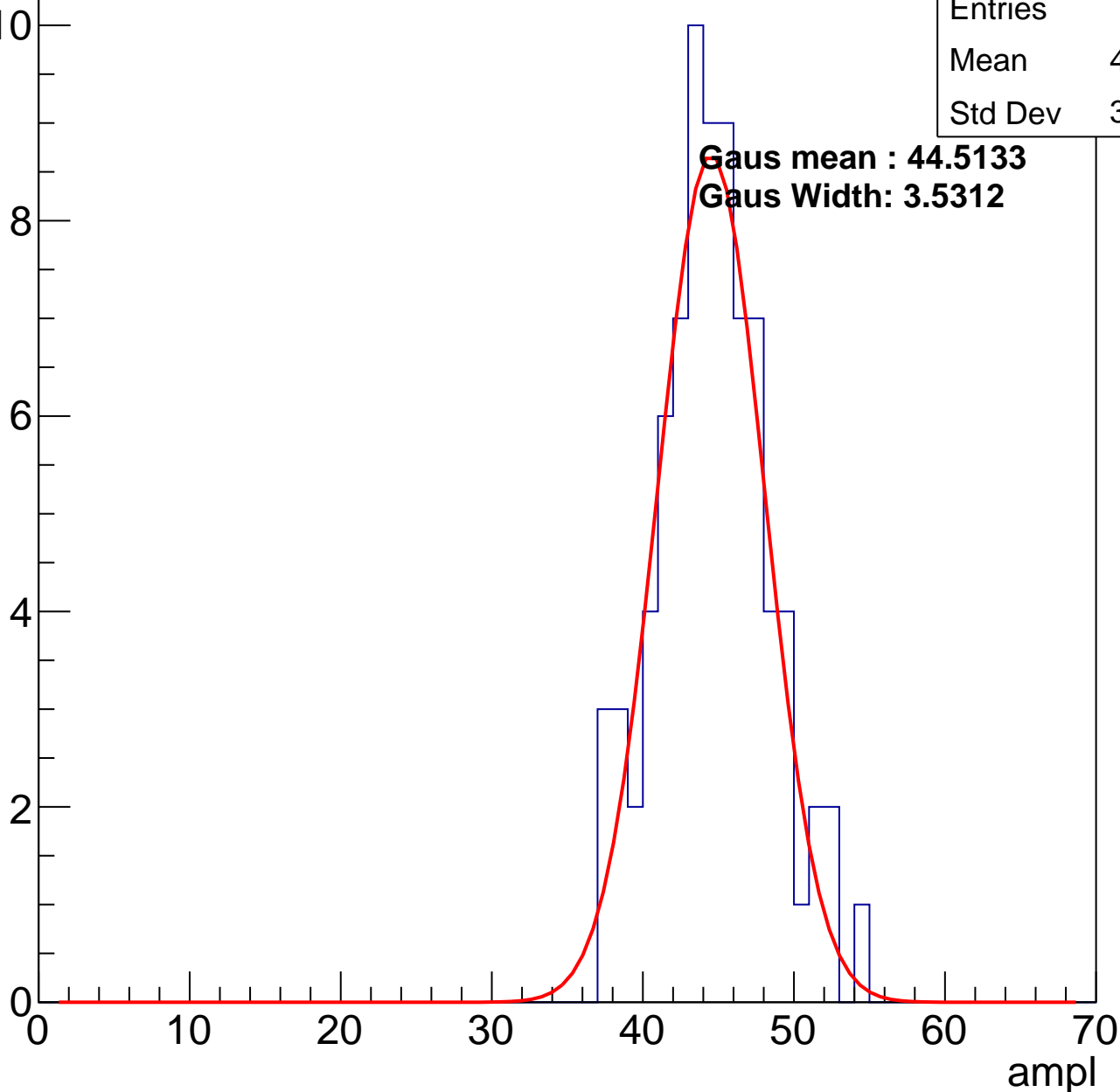
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 44.23 |
| Std Dev | 3.656 |

**Gaus mean : 44.5133**

**Gaus Width: 3.5312**

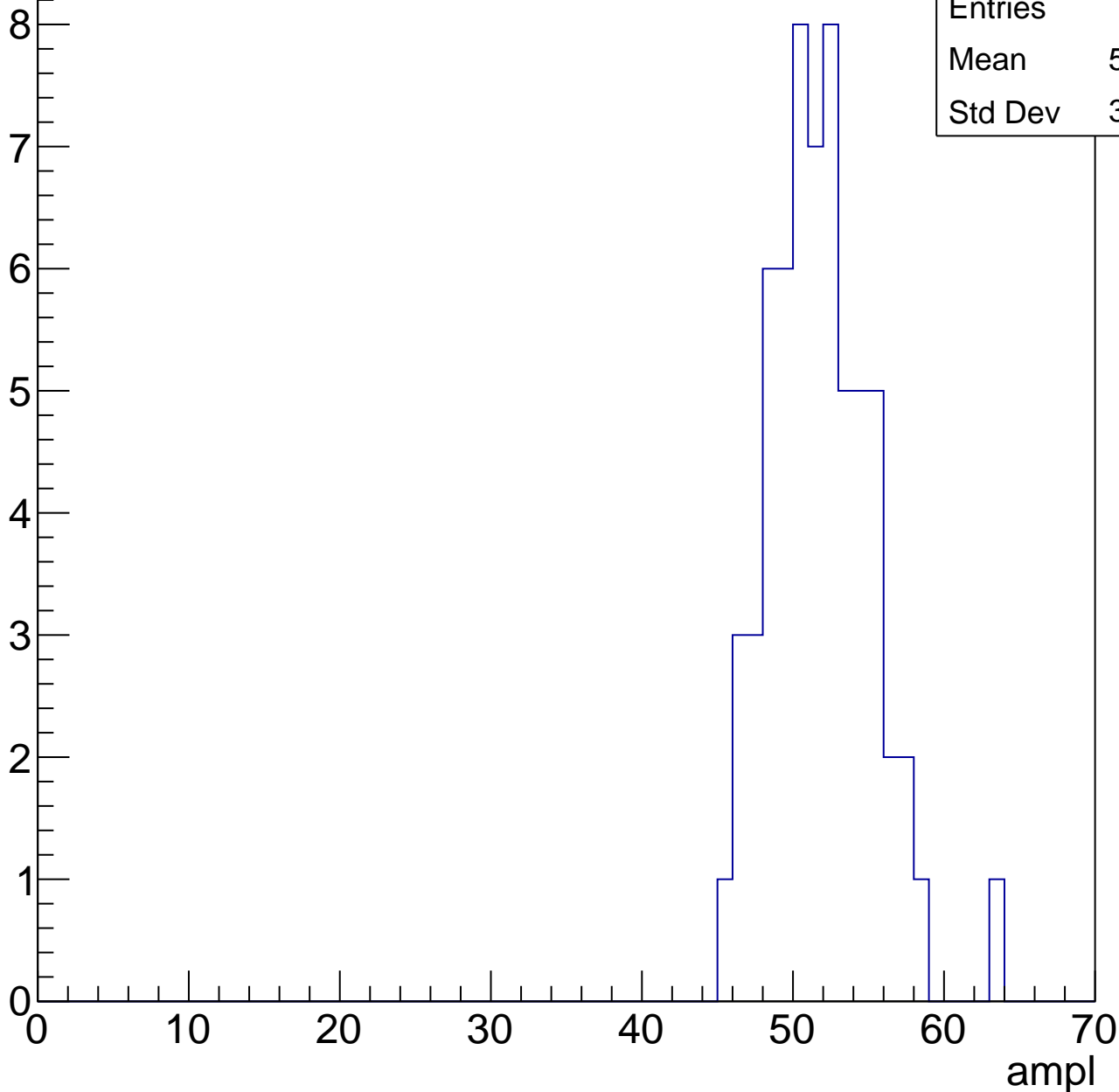


# B0L002S, U1-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

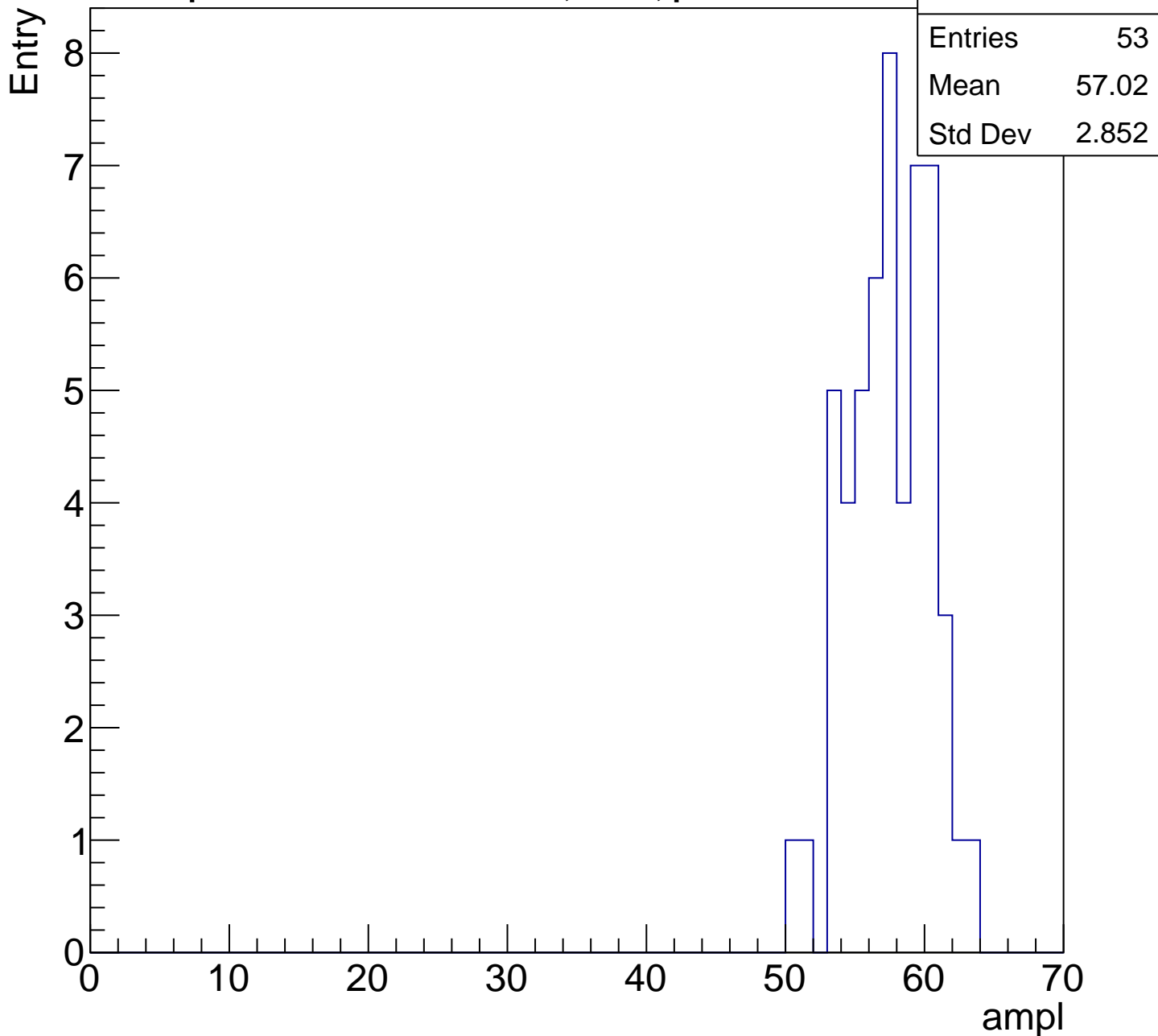
Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 51.37 |
| Std Dev | 3.344 |



# B0L002S, U1-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

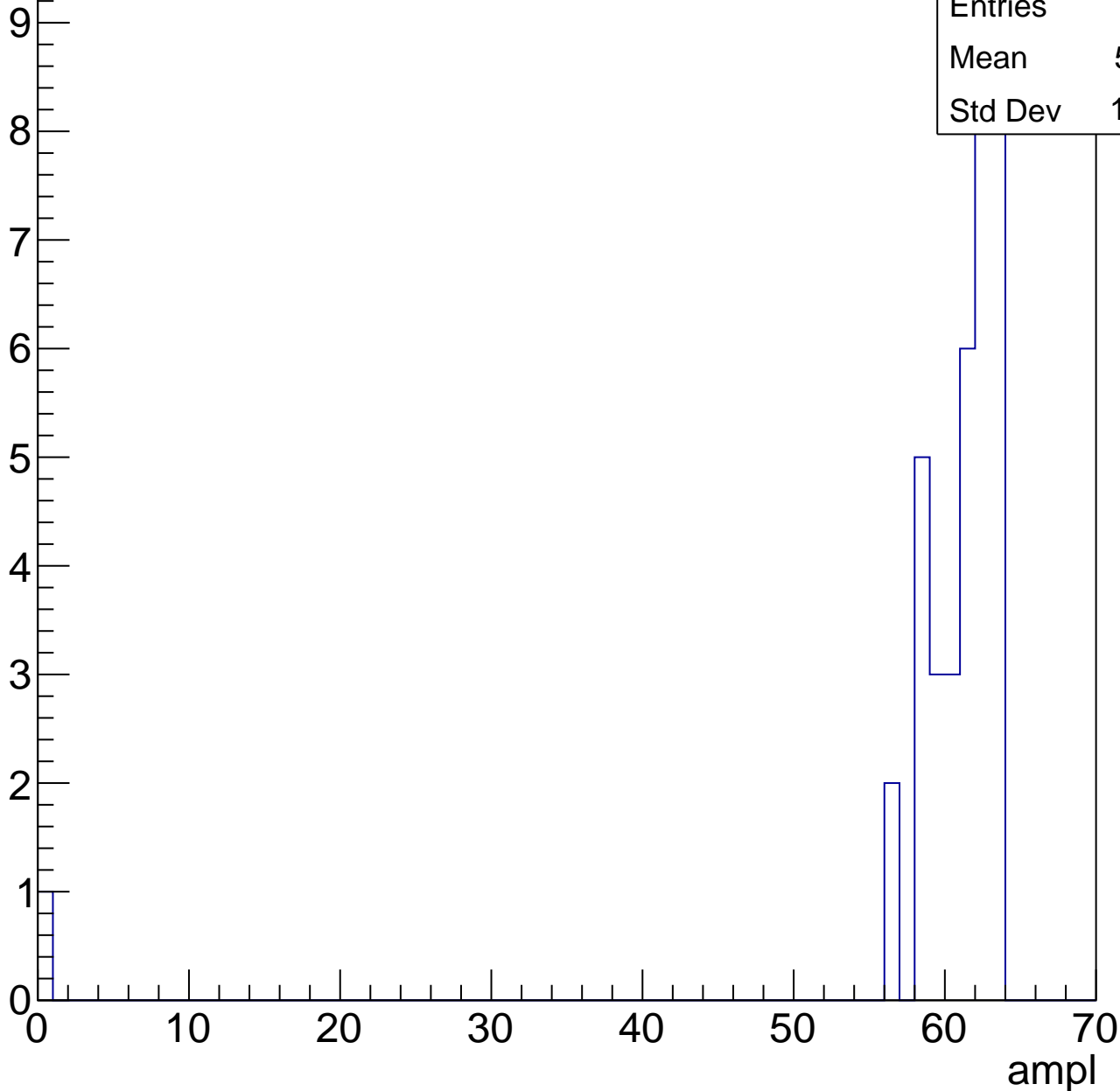


# B0L002S, U1-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 59.11 |
| Std Dev | 10.05 |



# B0L002S, U1-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch61, adc0

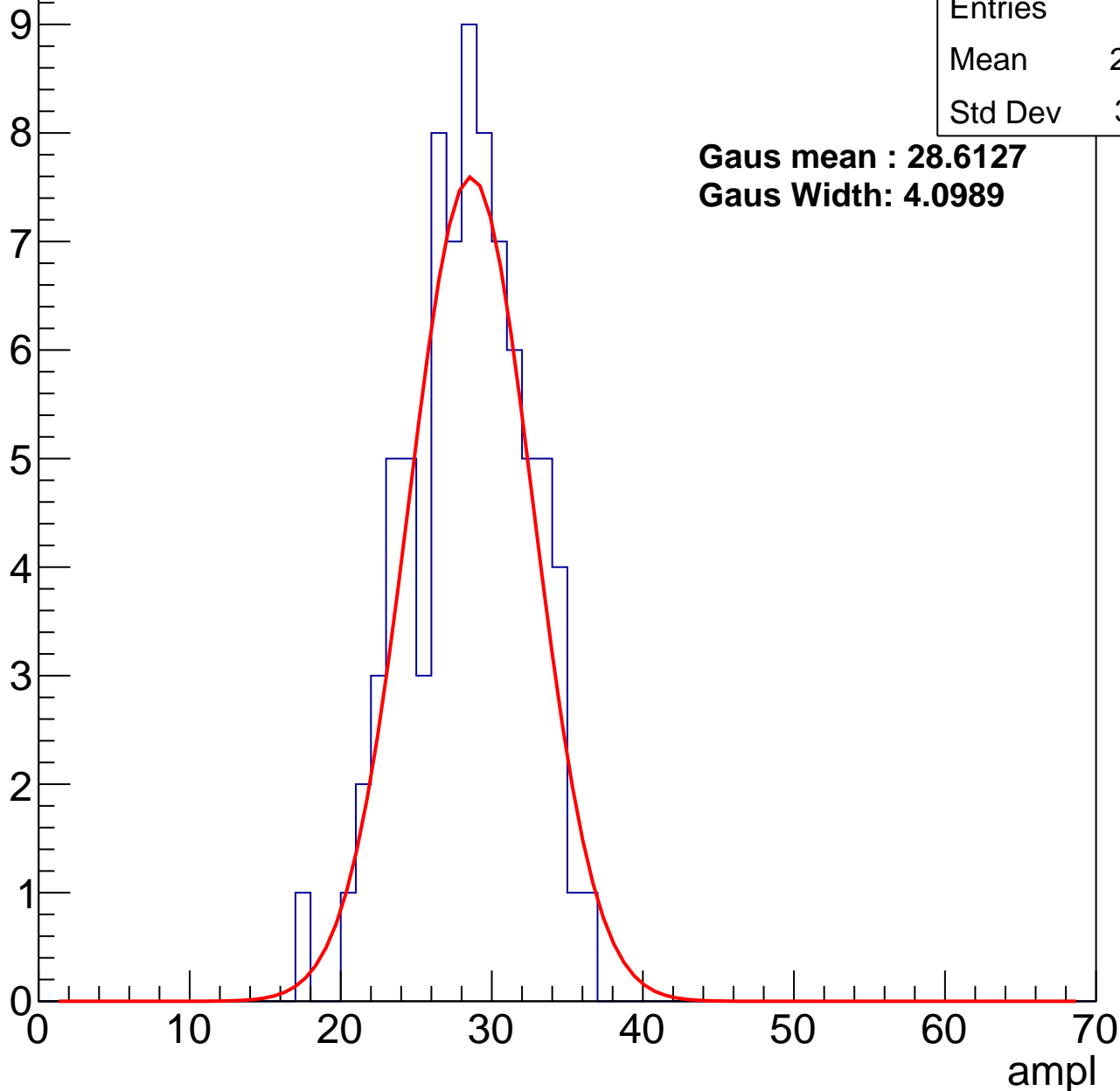
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 27.95 |
| Std Dev | 3.871 |

**Gaus mean : 28.6127**

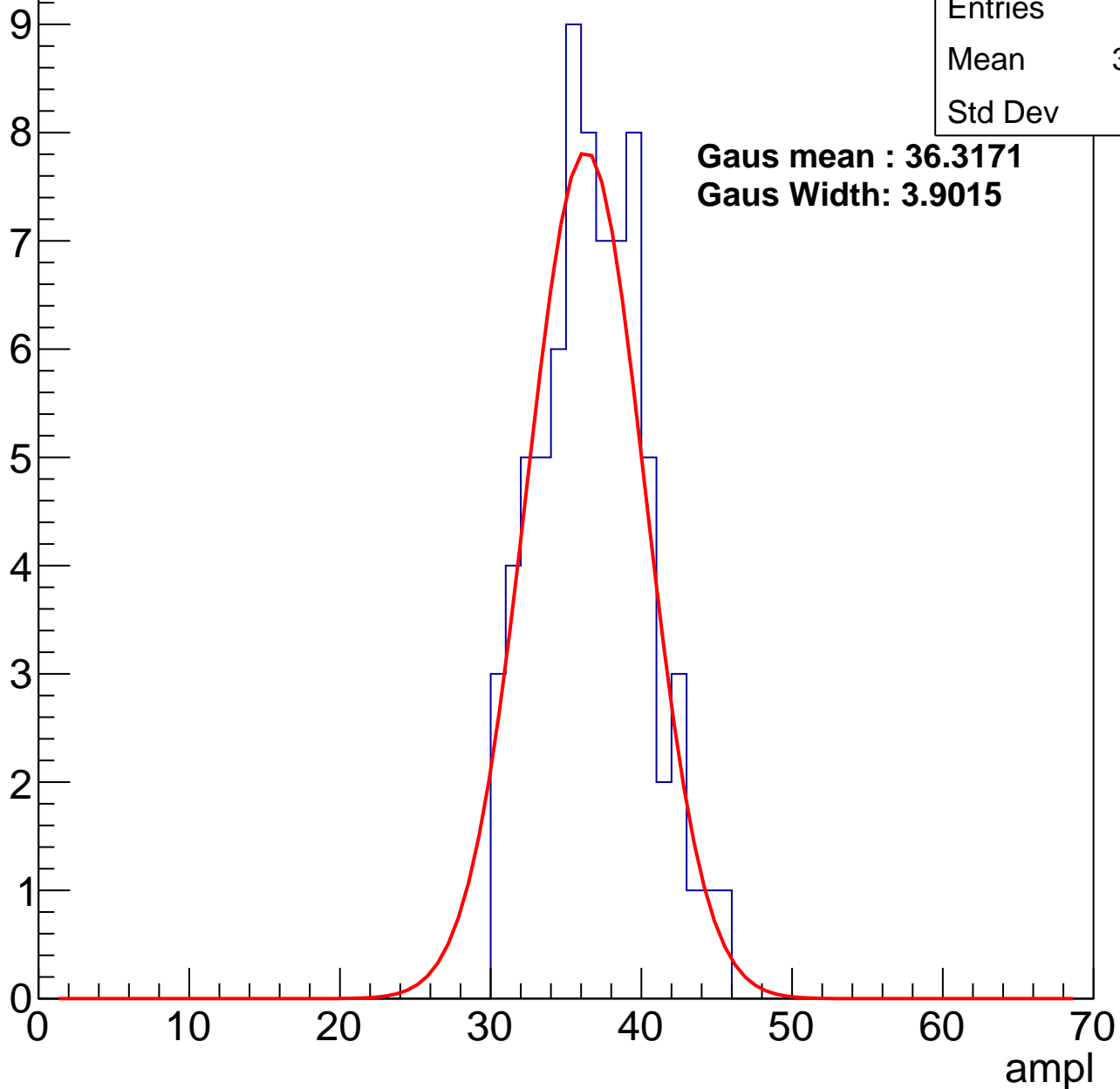
**Gaus Width: 4.0989**



# B0L002S, U1-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch61, adc2

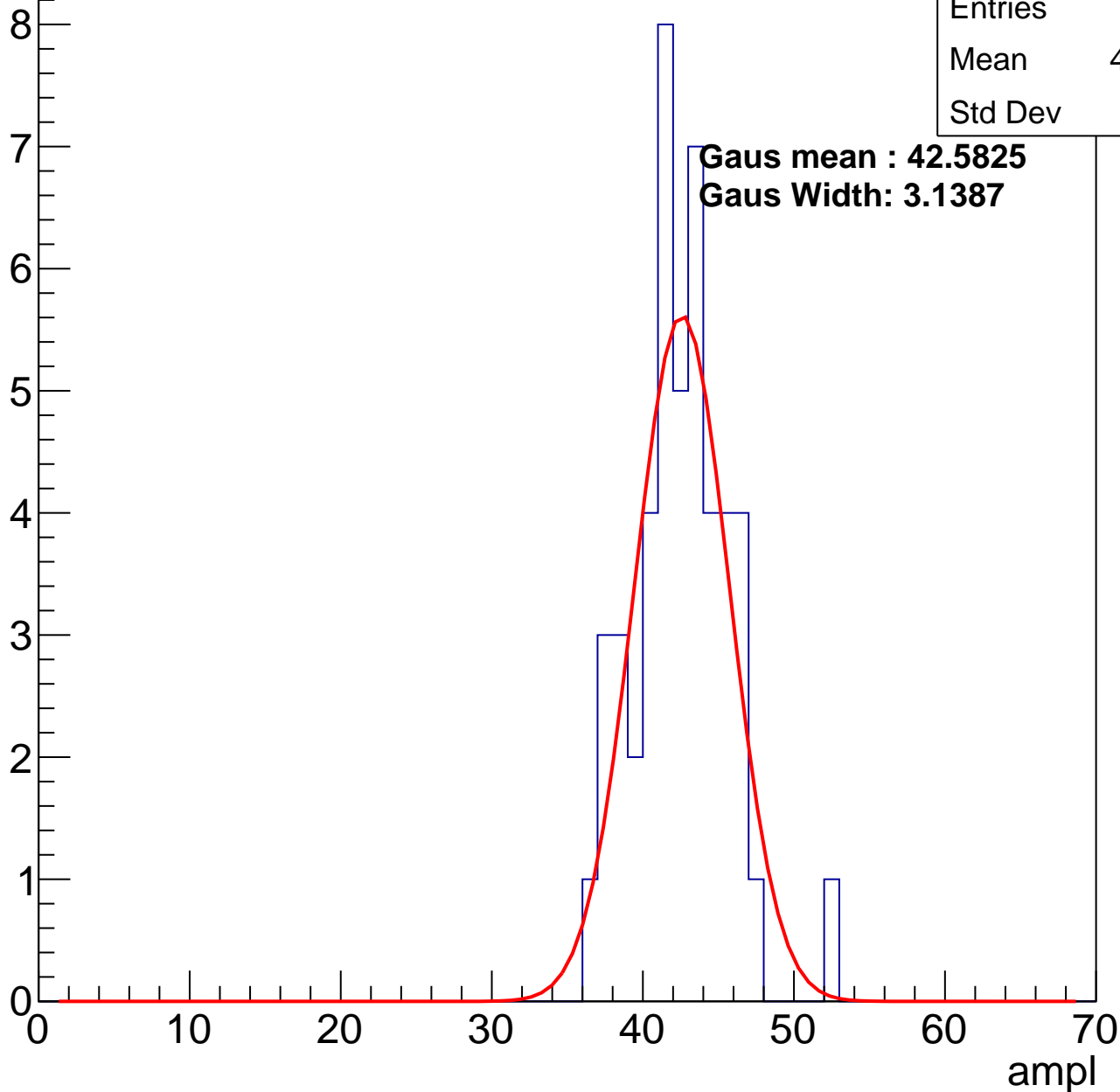
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 42.06 |
| Std Dev | 3.09  |

**Gaus mean : 42.5825**

**Gaus Width: 3.1387**

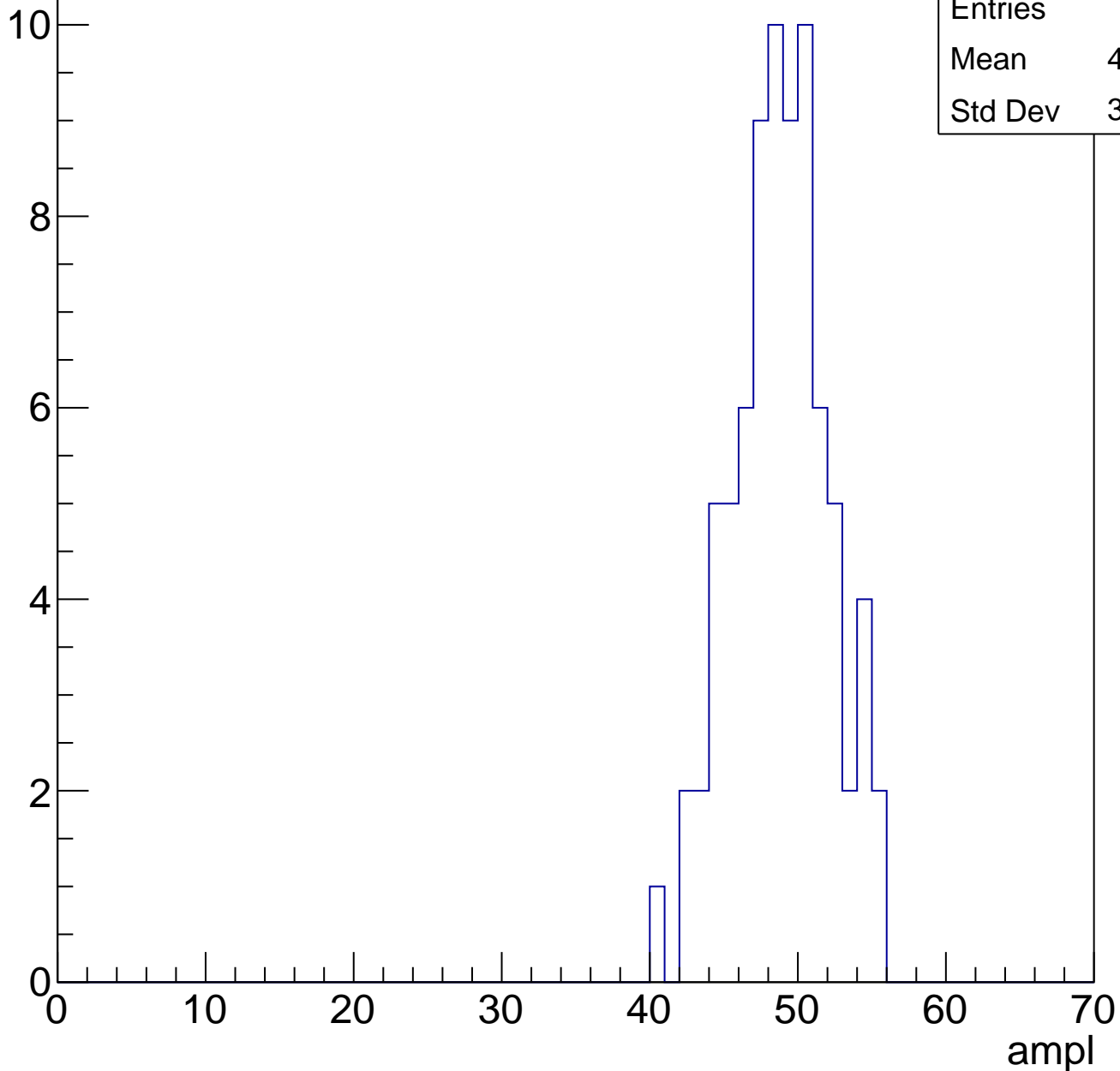


# B0L002S, U1-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

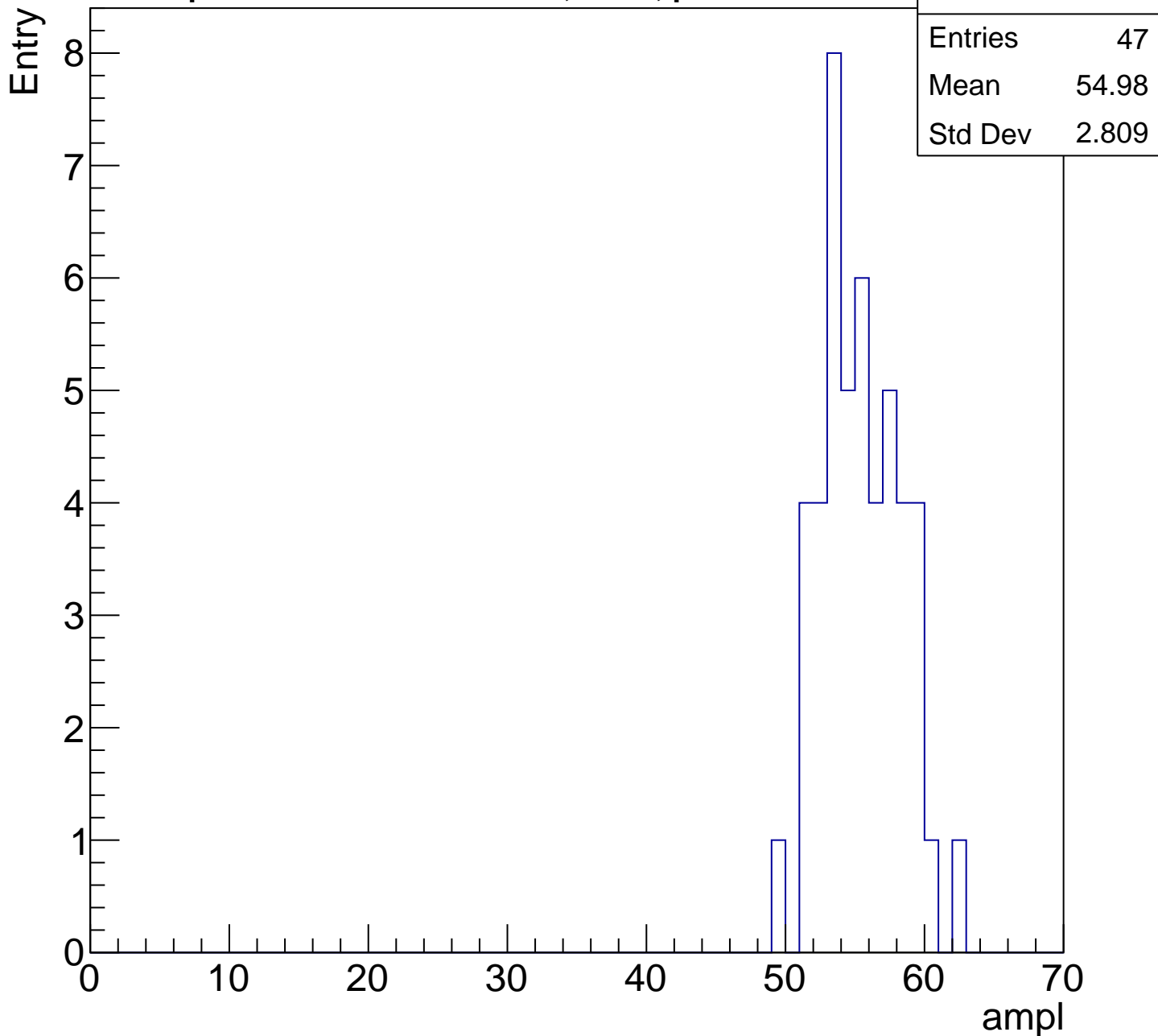
|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 48.37 |
| Std Dev | 3.219 |

Entry



# B0L002S, U1-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

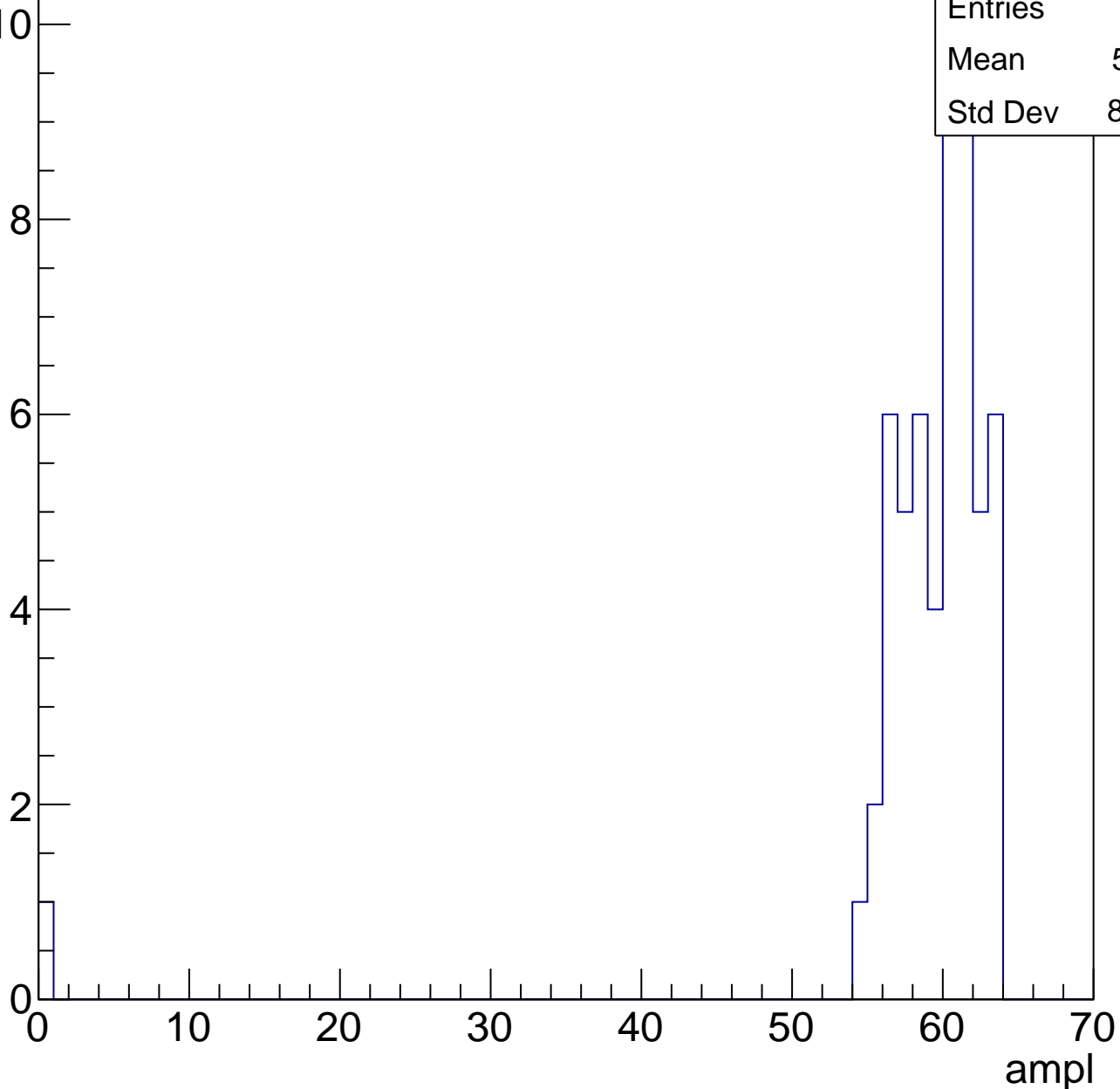


# B0L002S, U1-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

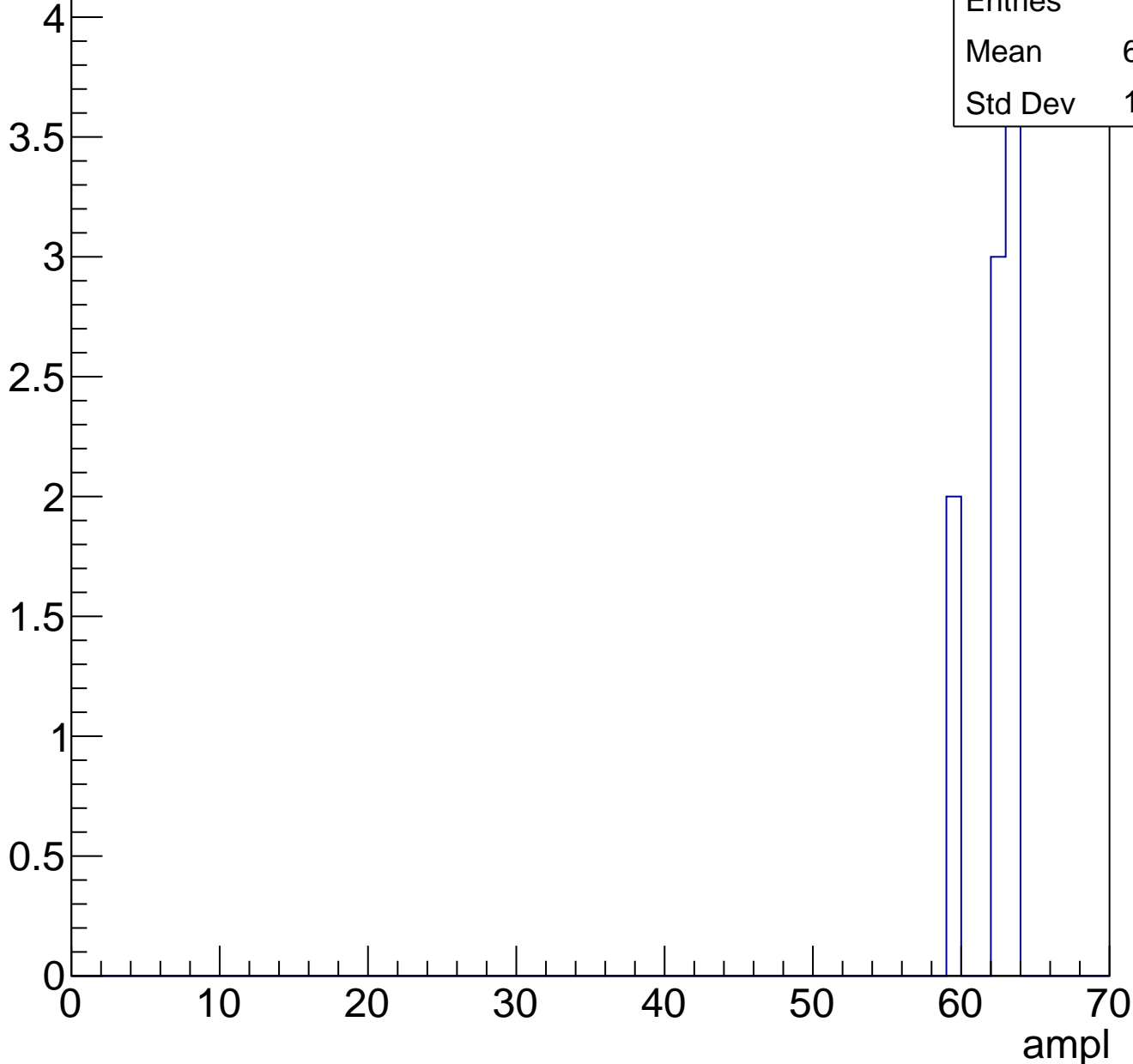
|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 58.31 |
| Std Dev | 8.292 |



# B0L002S, U1-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch62, adc0

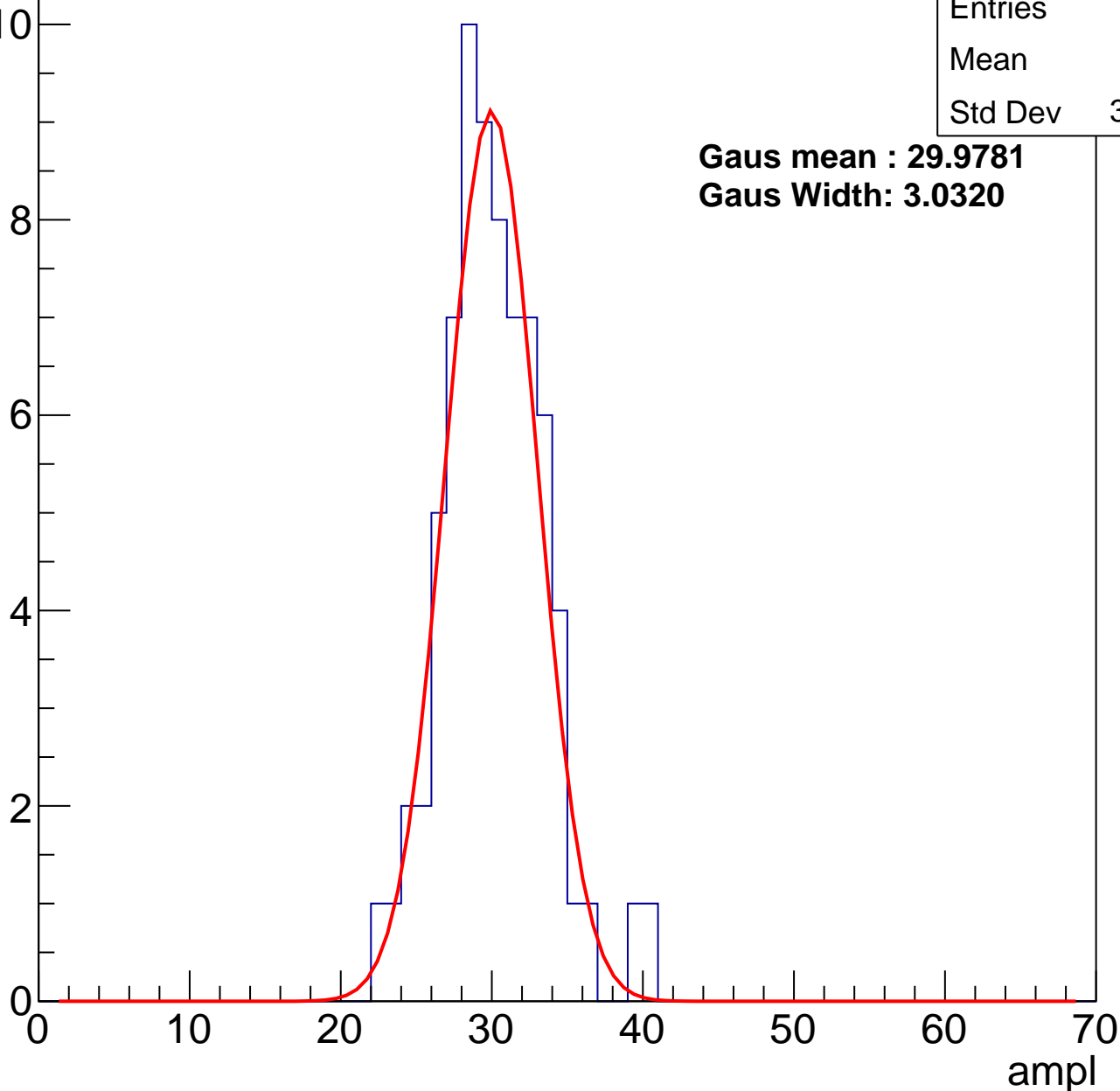
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 29.7  |
| Std Dev | 3.342 |

**Gaus mean : 29.9781**

**Gaus Width: 3.0320**



# B0L002S, U1-ch62, adc1

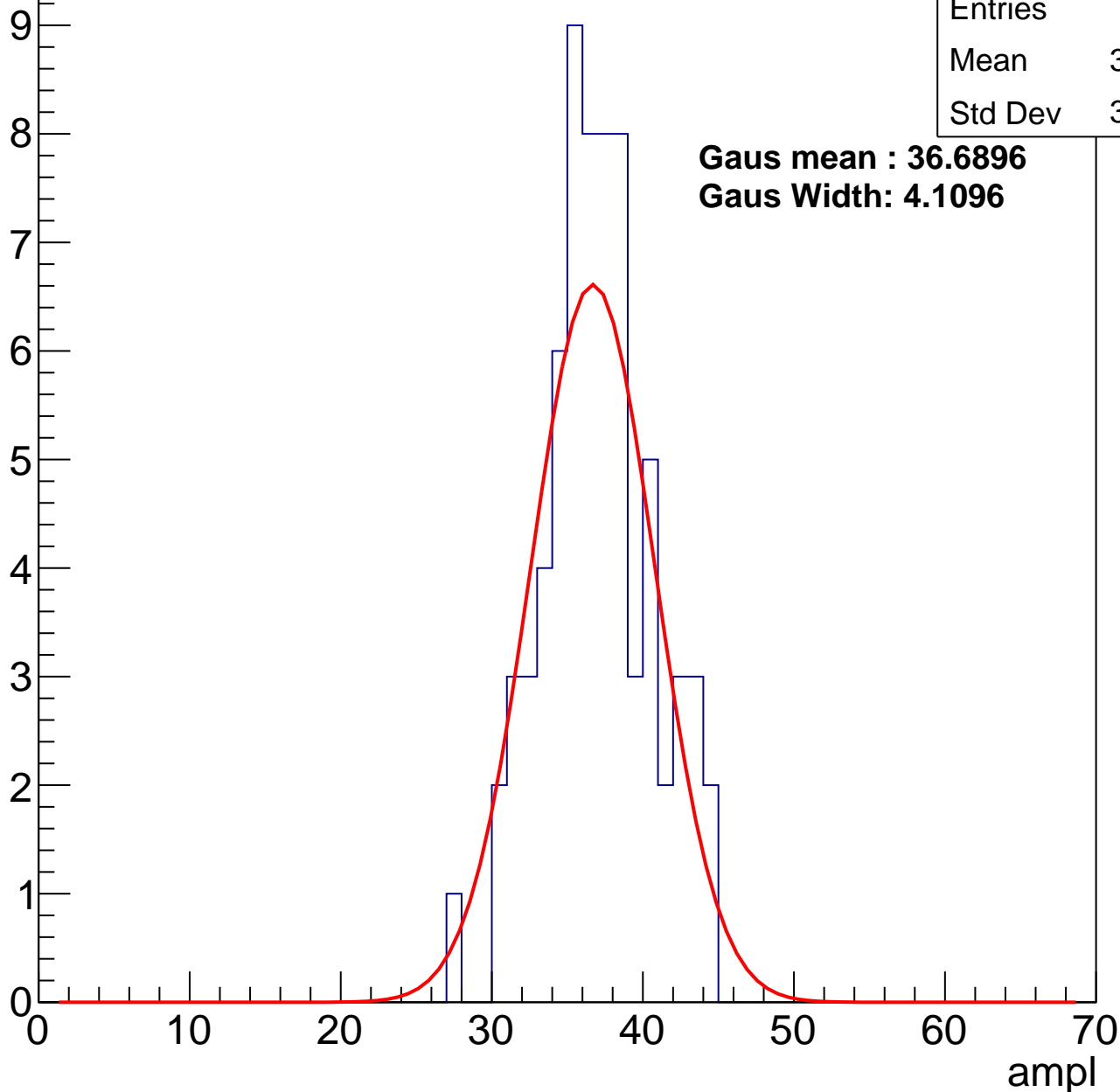
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 36.53 |
| Std Dev | 3.597 |

**Gaus mean : 36.6896**

**Gaus Width: 4.1096**



# B0L002S, U1-ch62, adc2

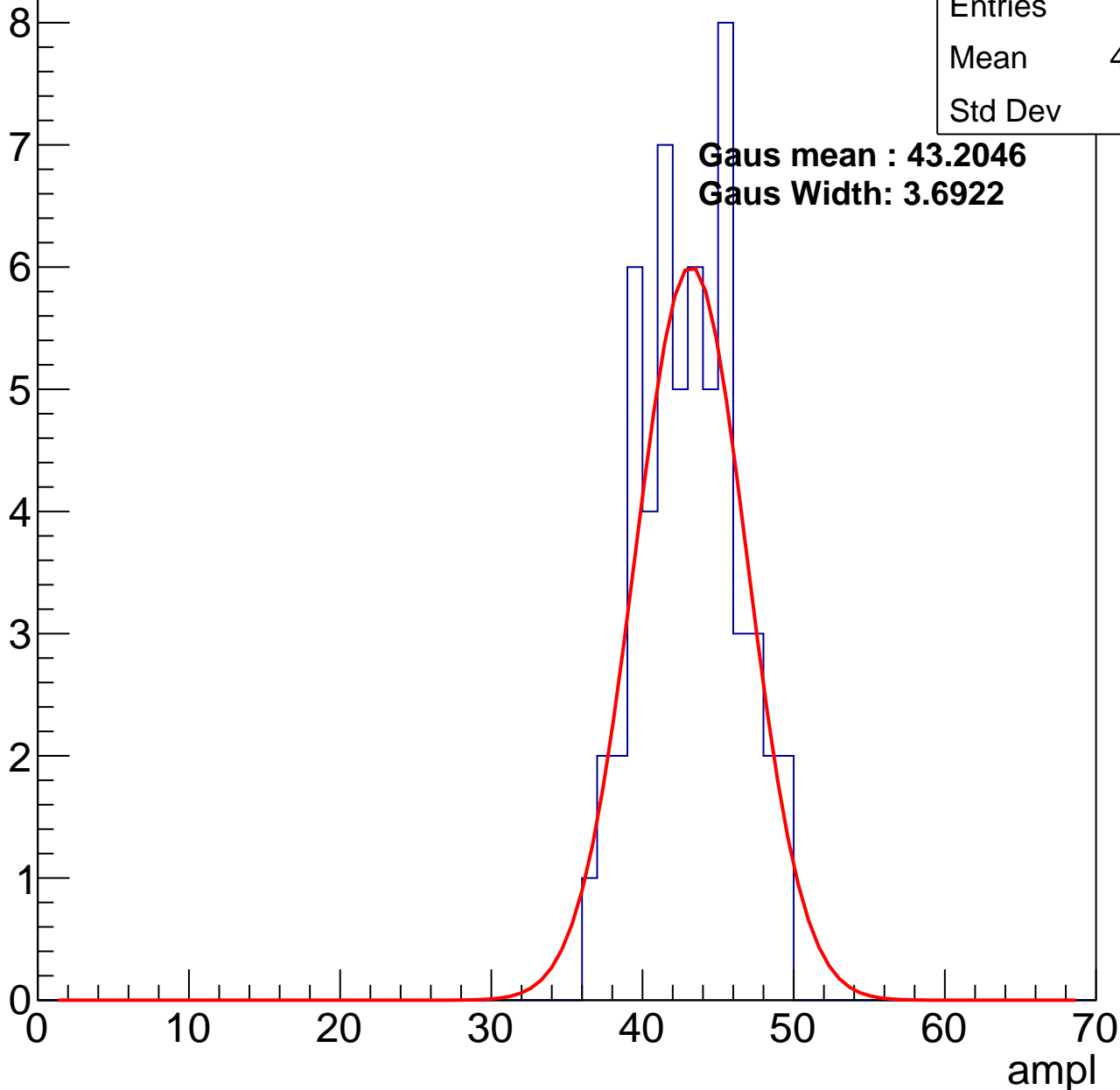
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 42.64 |
| Std Dev | 3.17  |

**Gaus mean : 43.2046**

**Gaus Width: 3.6922**

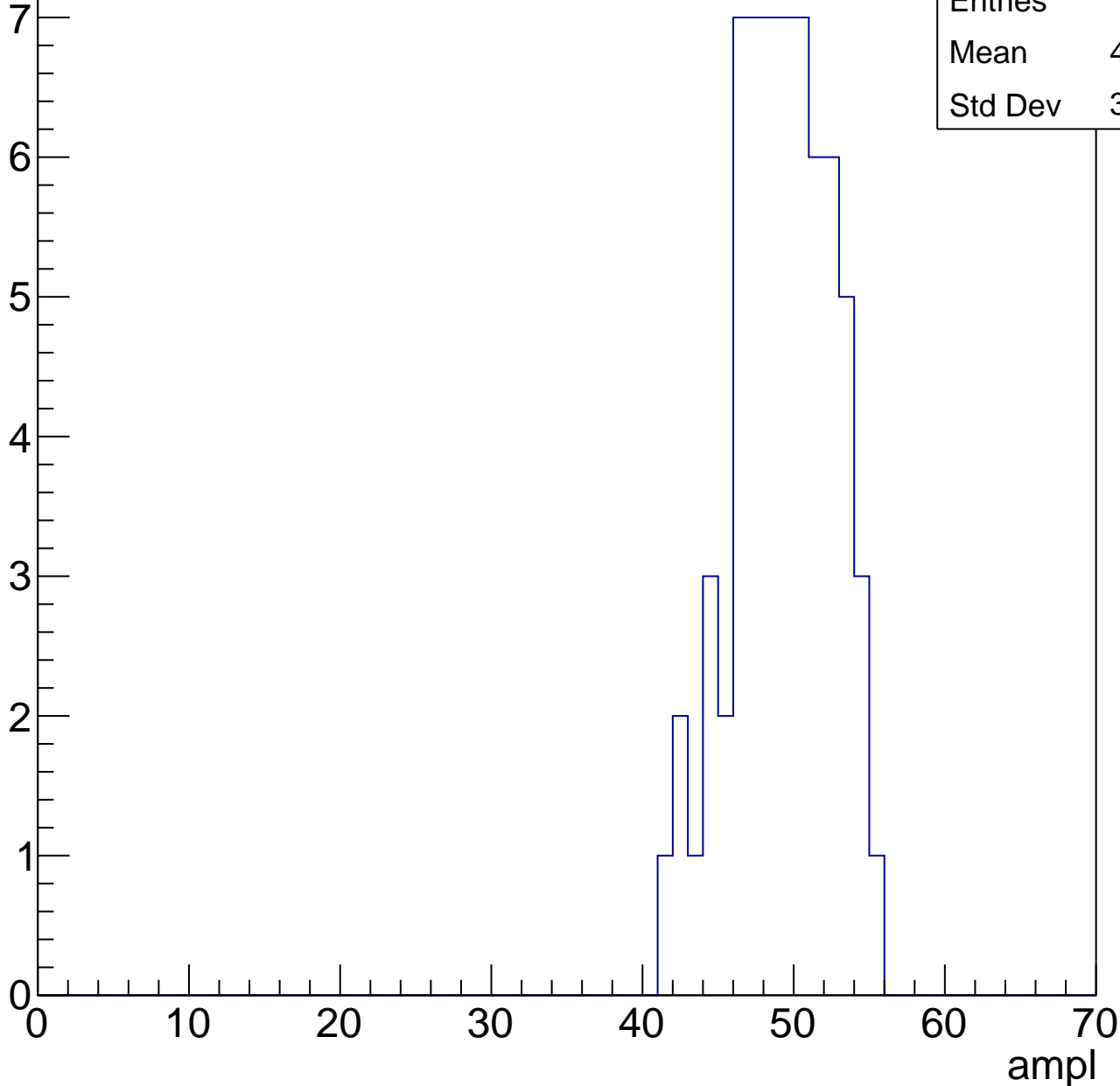


# B0L002S, U1-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 48.77 |
| Std Dev | 3.224 |

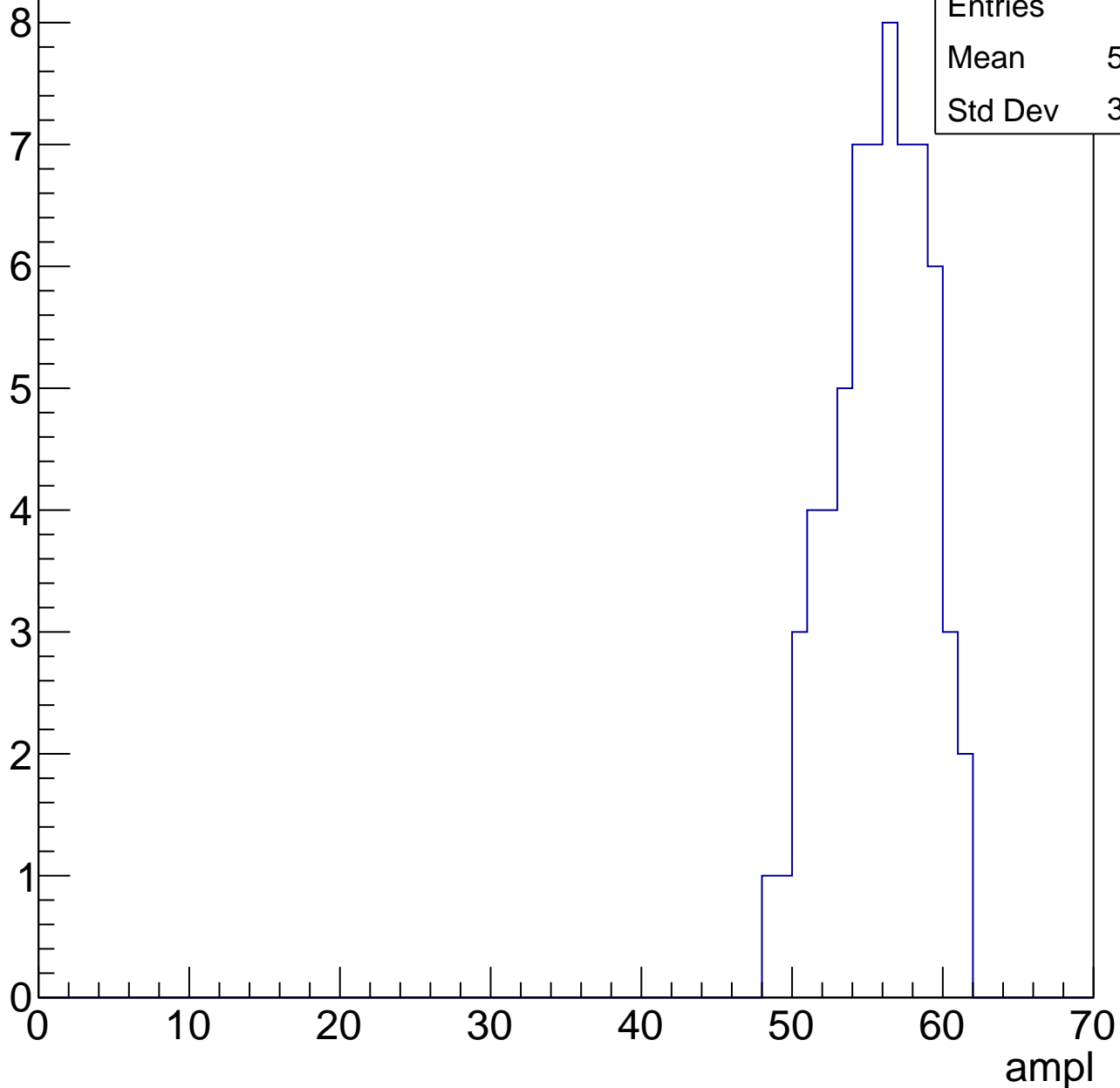


# B0L002S, U1-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 55.32 |
| Std Dev | 3.094 |

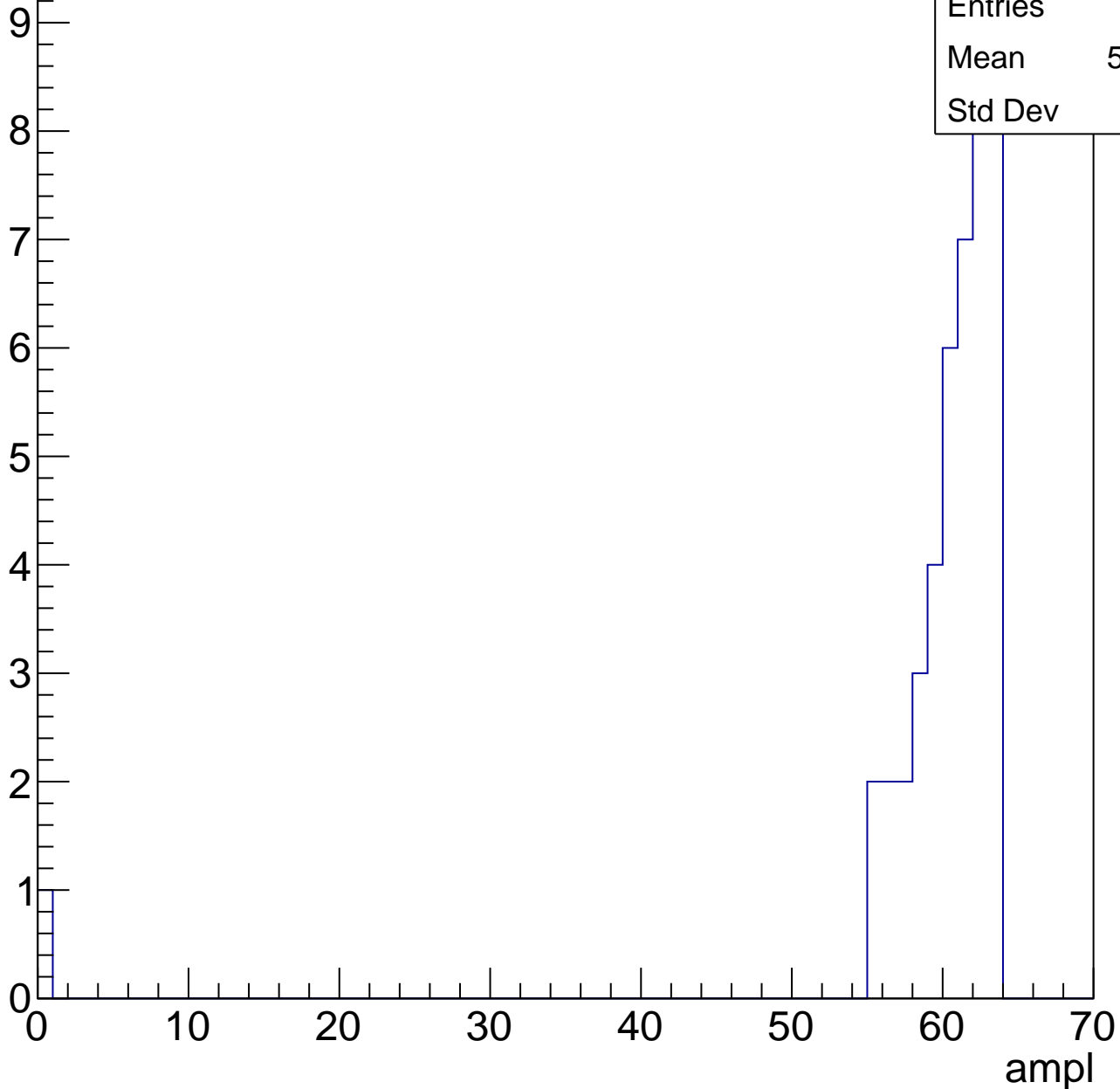


# B0L002S, U1-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

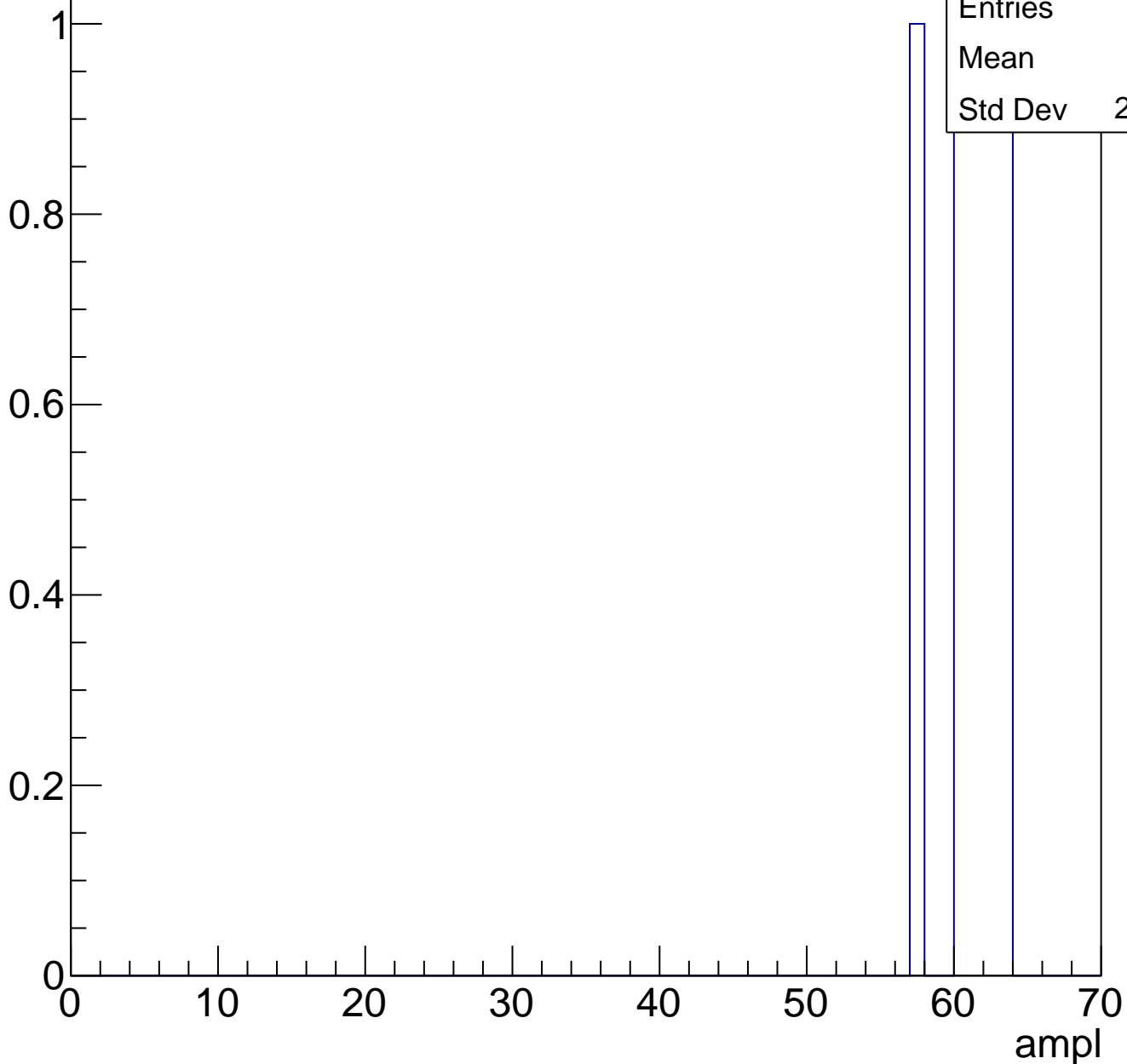
|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 59.07 |
| Std Dev | 9.19  |



# B0L002S, U1-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch63, adc0

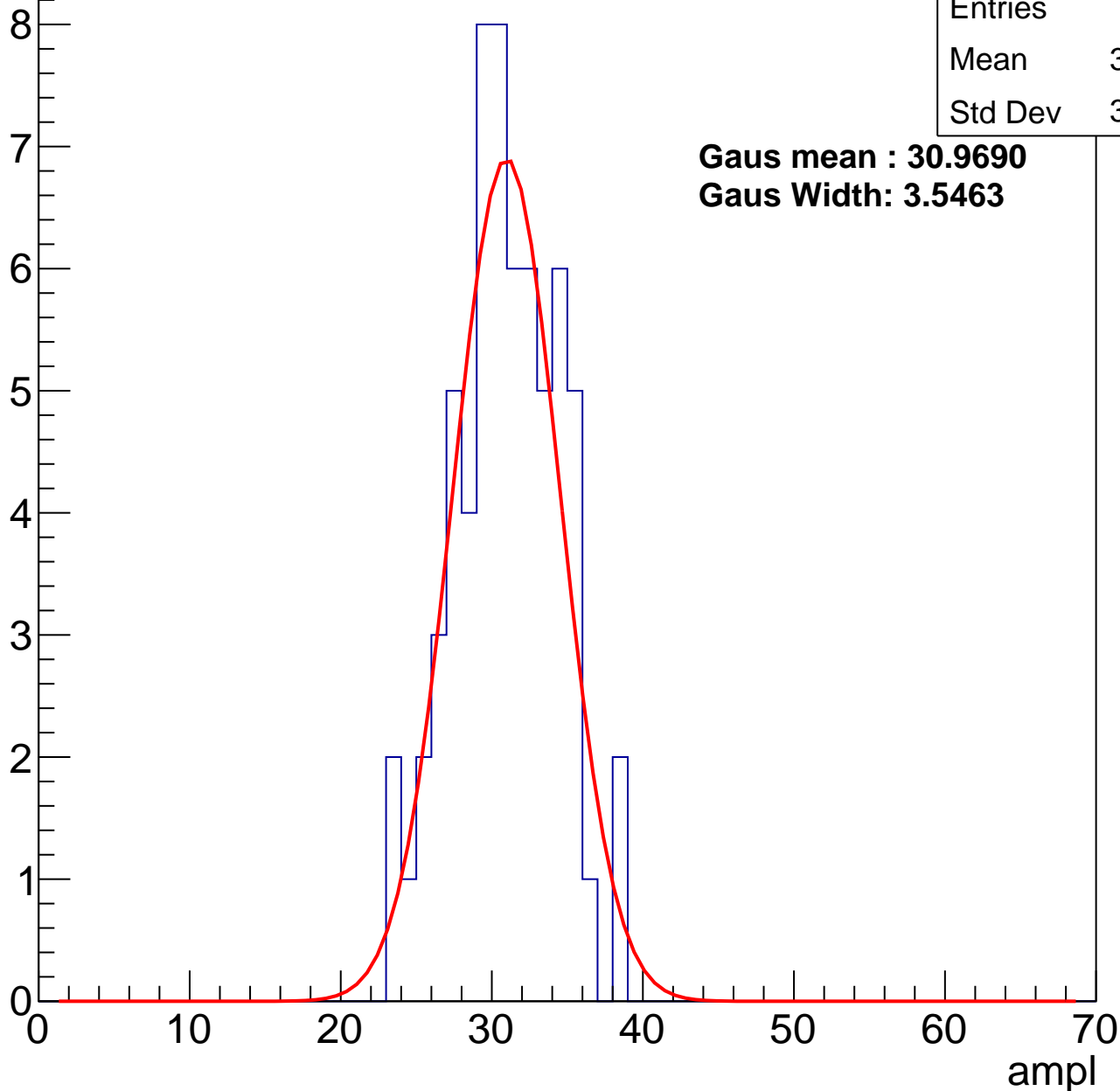
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 30.48 |
| Std Dev | 3.414 |

**Gaus mean : 30.9690**

**Gaus Width: 3.5463**



# B0L002S, U1-ch63, adc1

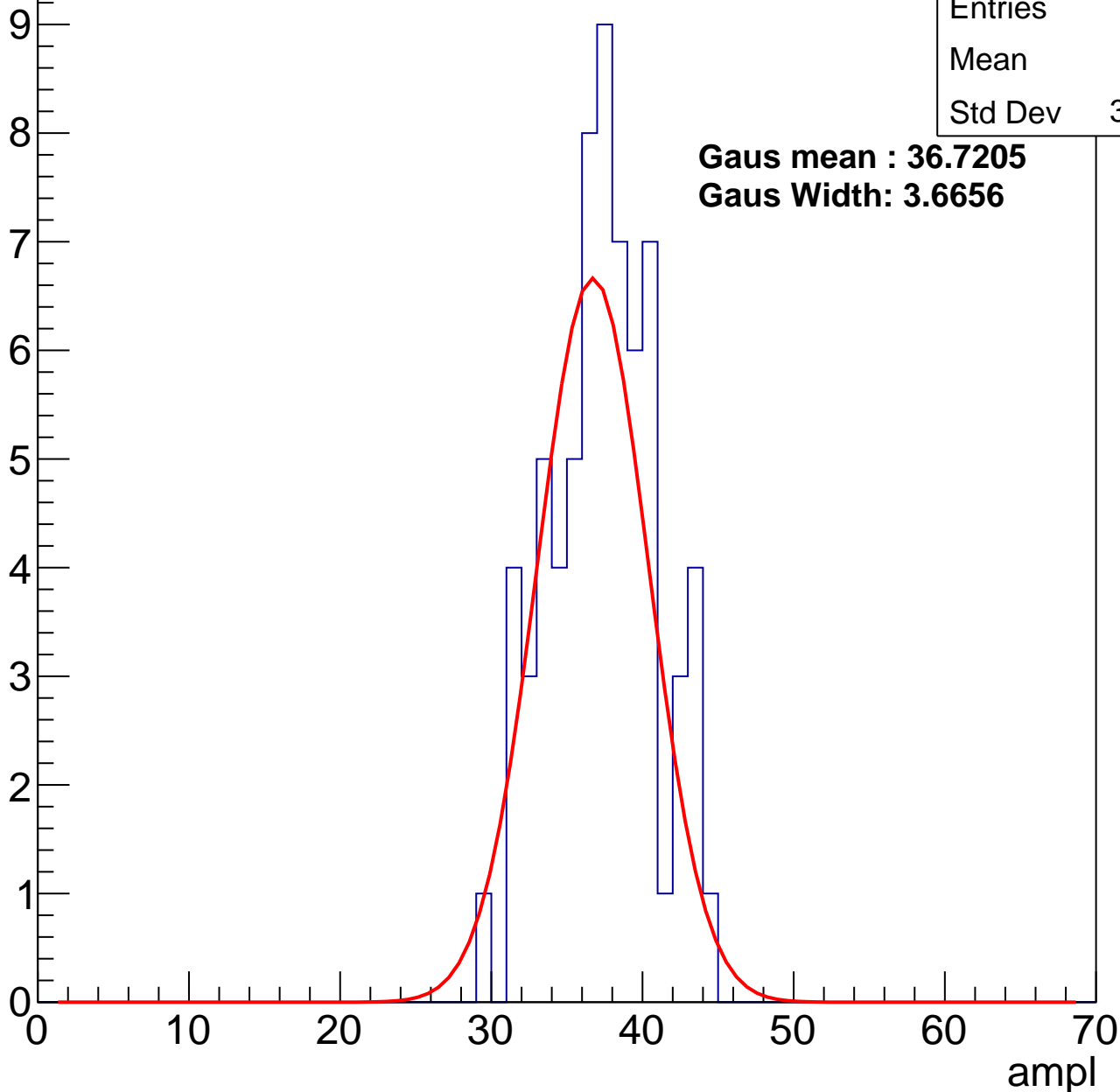
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 36.9  |
| Std Dev | 3.439 |

**Gaus mean : 36.7205**

**Gaus Width: 3.6656**



# B0L002S, U1-ch63, adc2

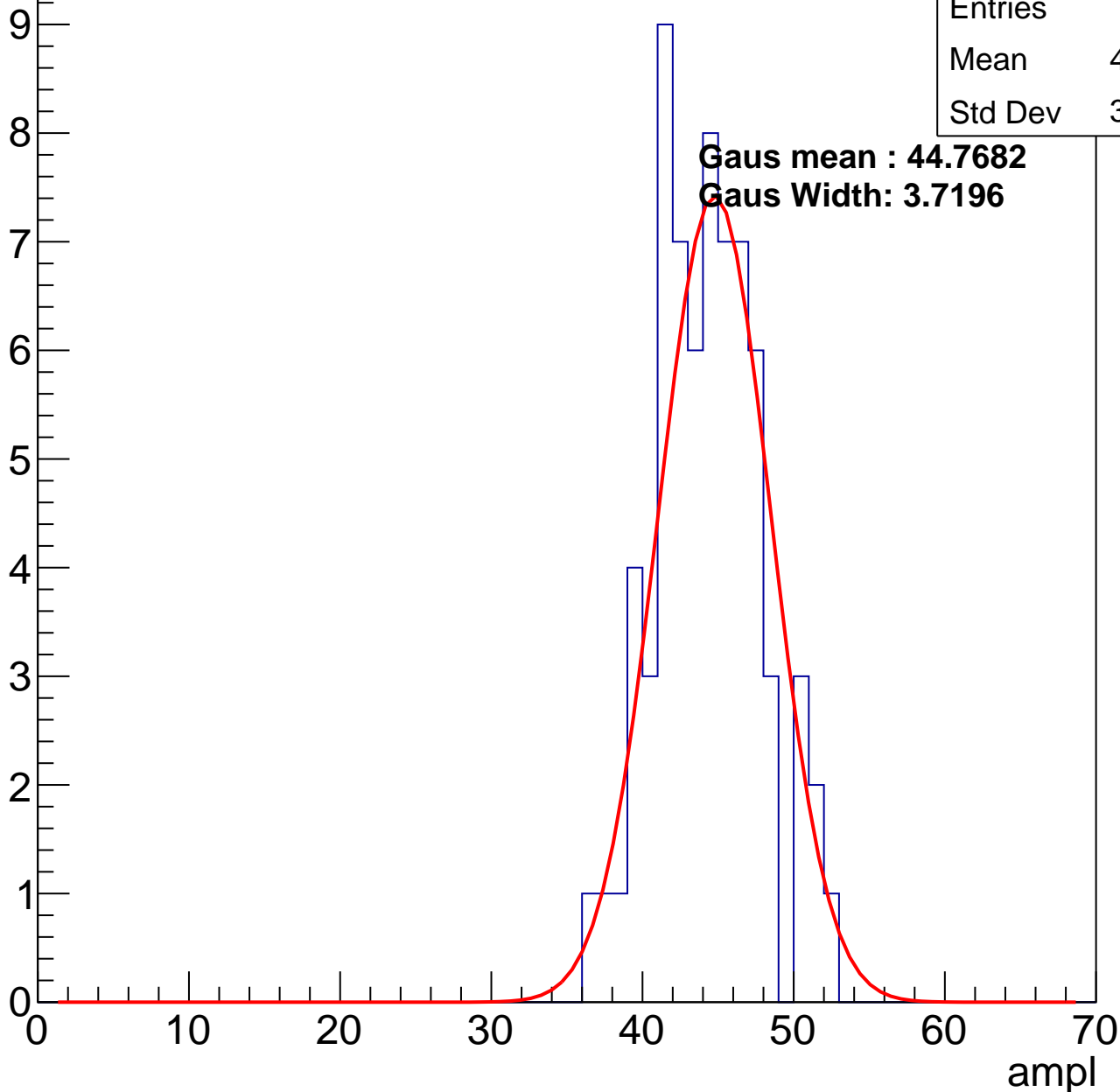
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 43.87 |
| Std Dev | 3.443 |

**Gaus mean : 44.7682**

**Gaus Width: 3.7196**



# B0L002S, U1-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

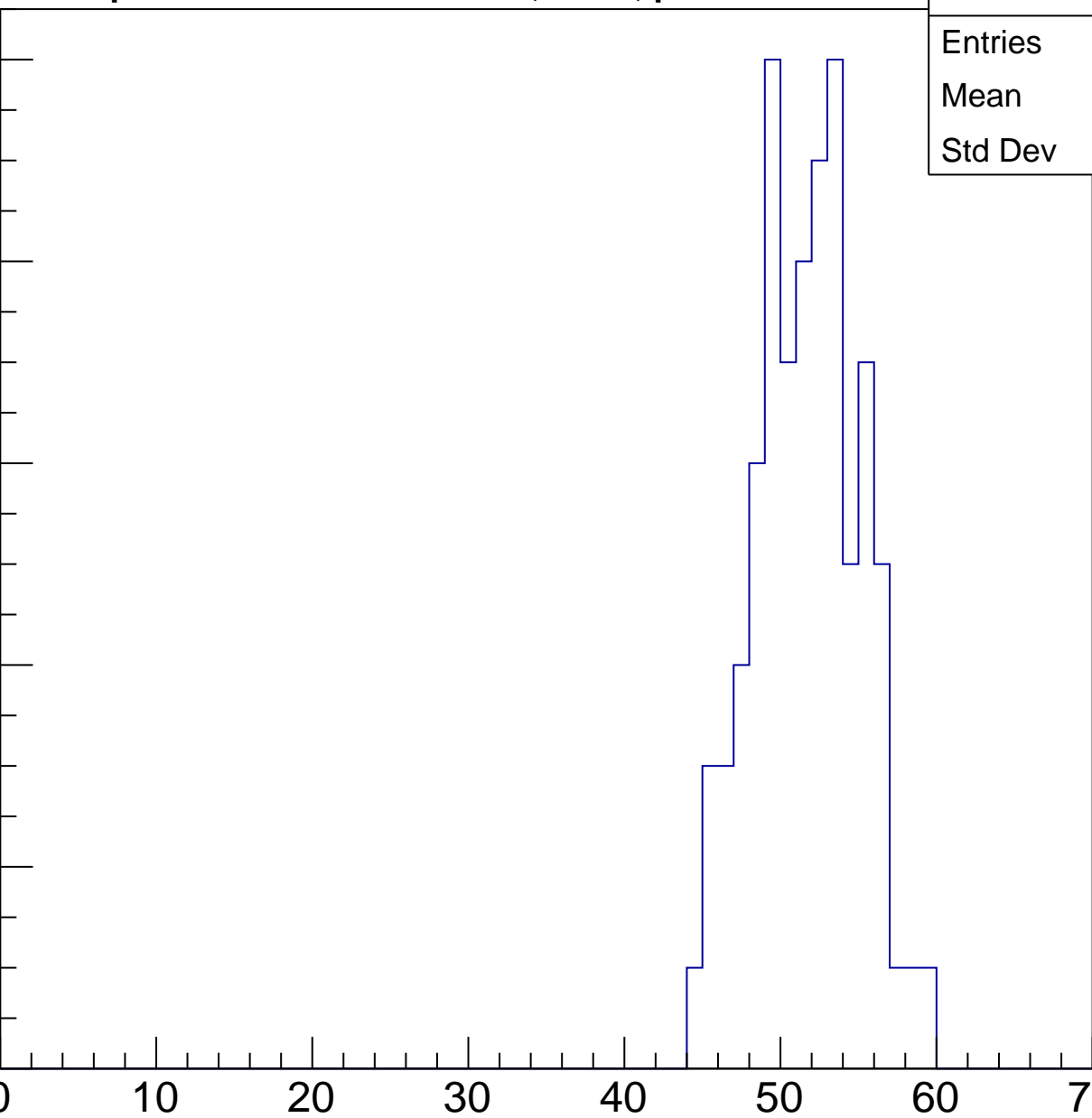
|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 51.21 |
| Std Dev | 3.284 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

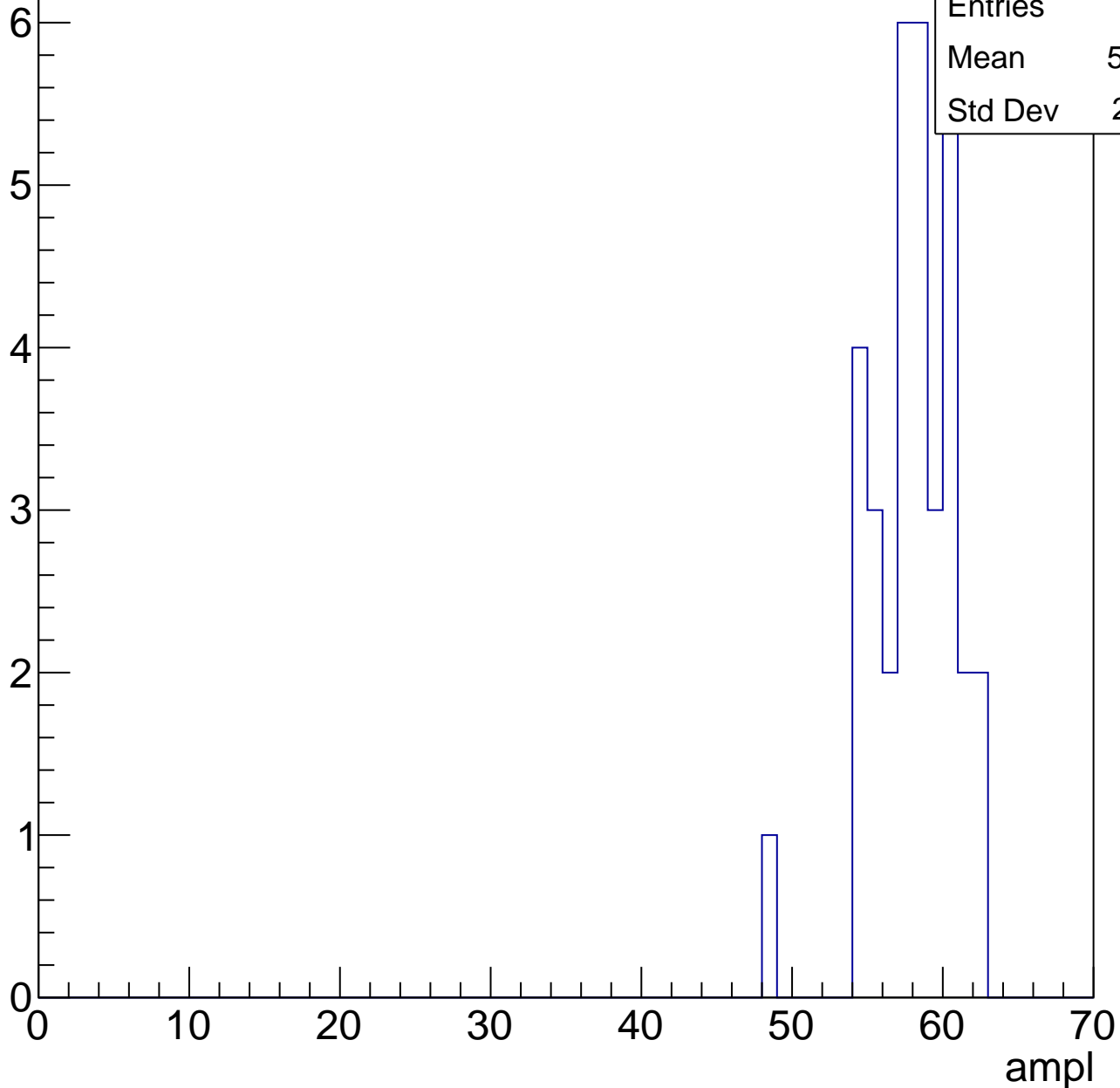
ampl



# B0L002S, U1-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



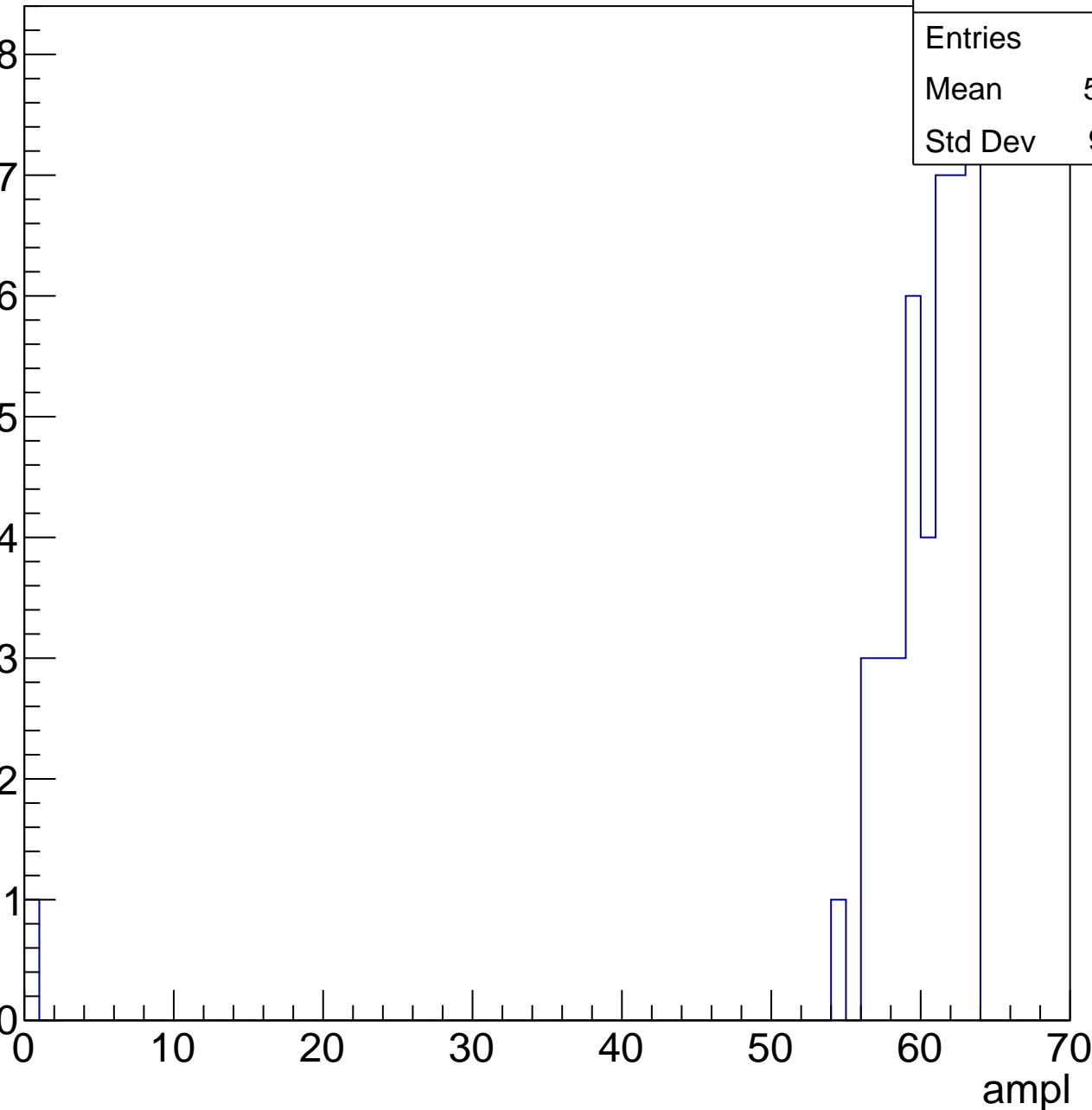
# B0L002S, U1-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 58.74 |
| Std Dev | 9.361 |

8  
7  
6  
5  
4  
3  
2  
1  
0



# B0L002S, U1-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch64, adc0

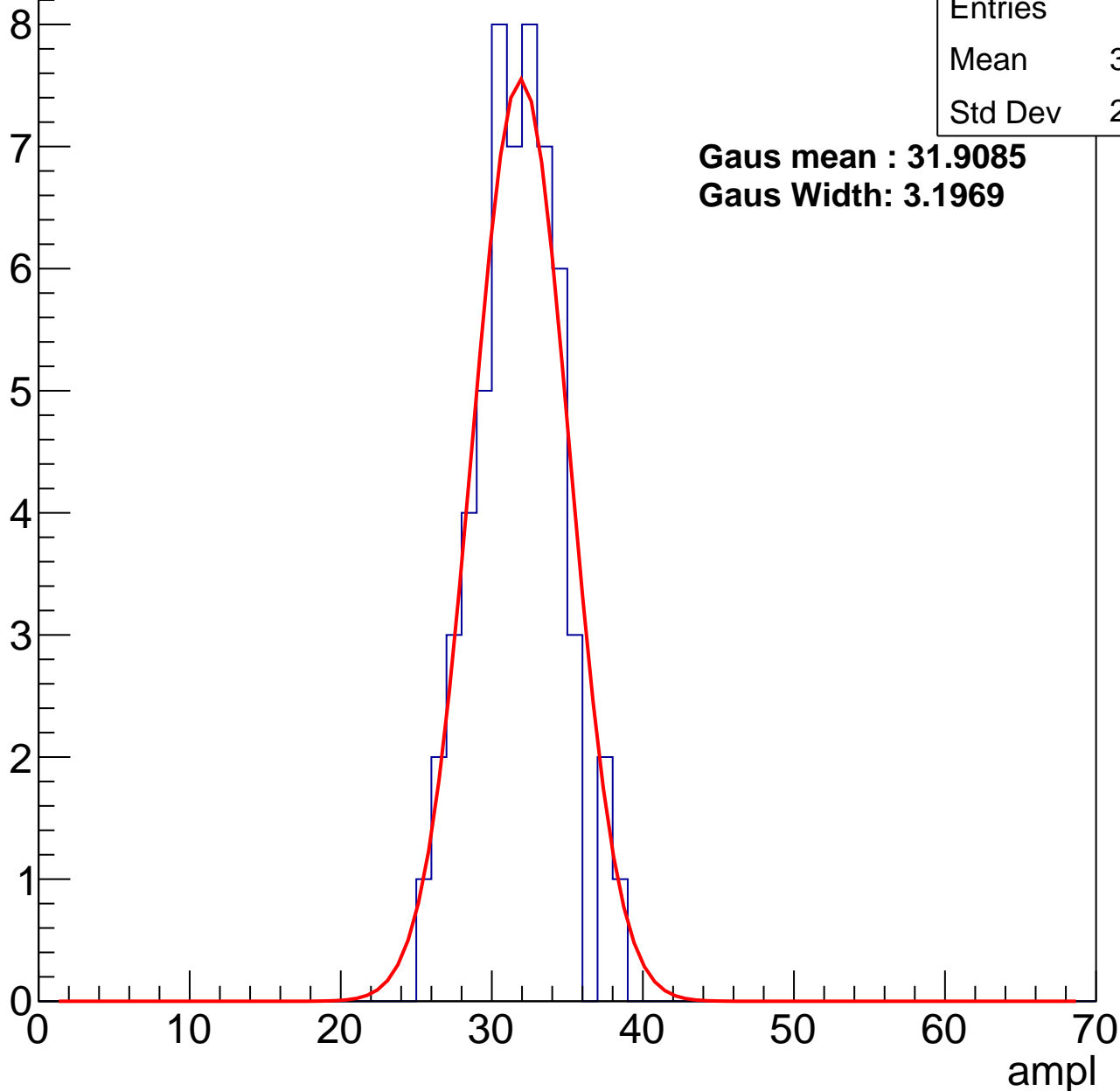
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 31.23 |
| Std Dev | 2.822 |

**Gaus mean : 31.9085**

**Gaus Width: 3.1969**



# B0L002S, U1-ch64, adc1

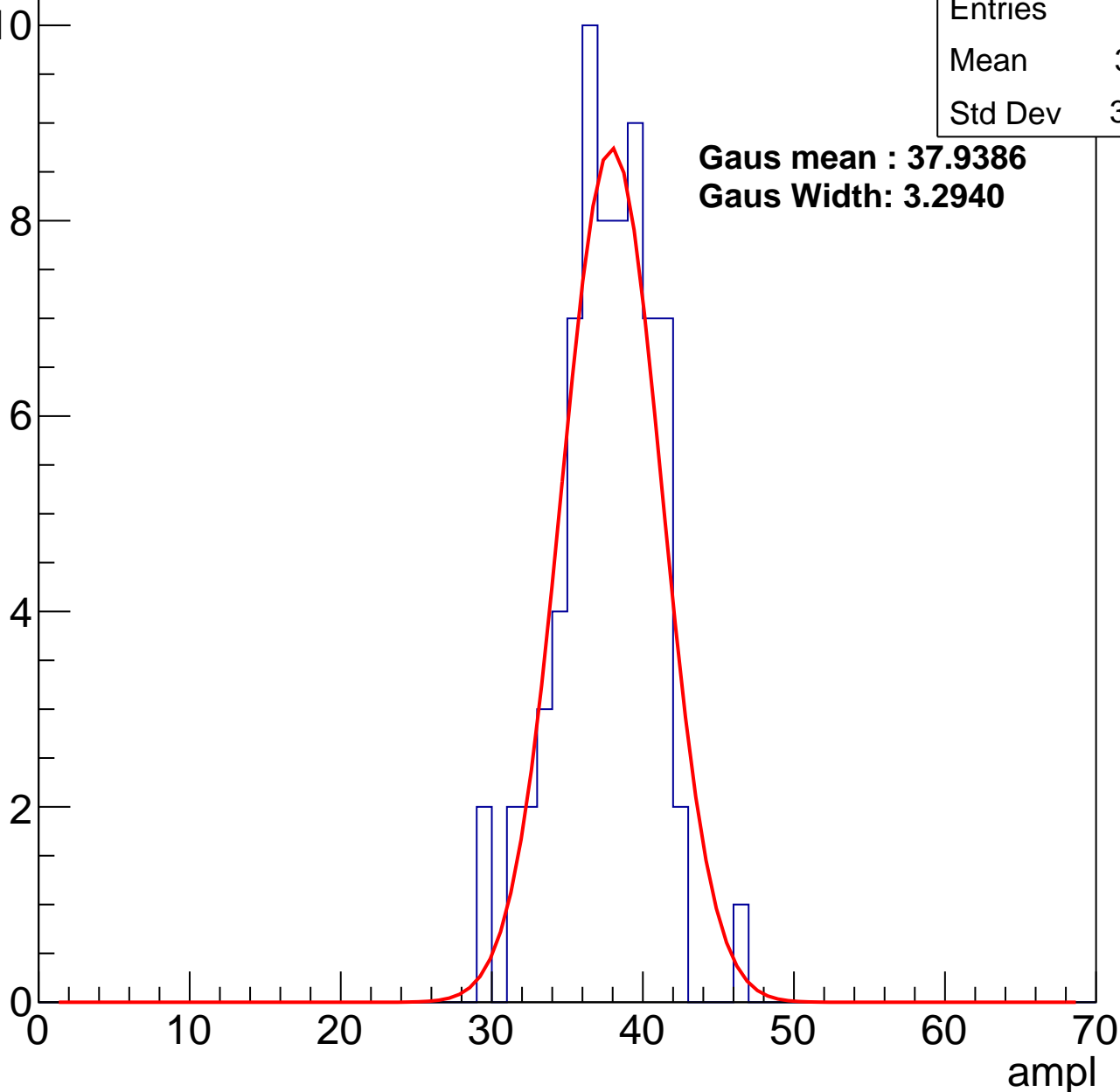
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 37.11 |
| Std Dev | 3.165 |

**Gaus mean : 37.9386**

**Gaus Width: 3.2940**



# B0L002S, U1-ch64, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 44.55 |
| Std Dev | 2.872 |

**Gaus mean : 45.0301**

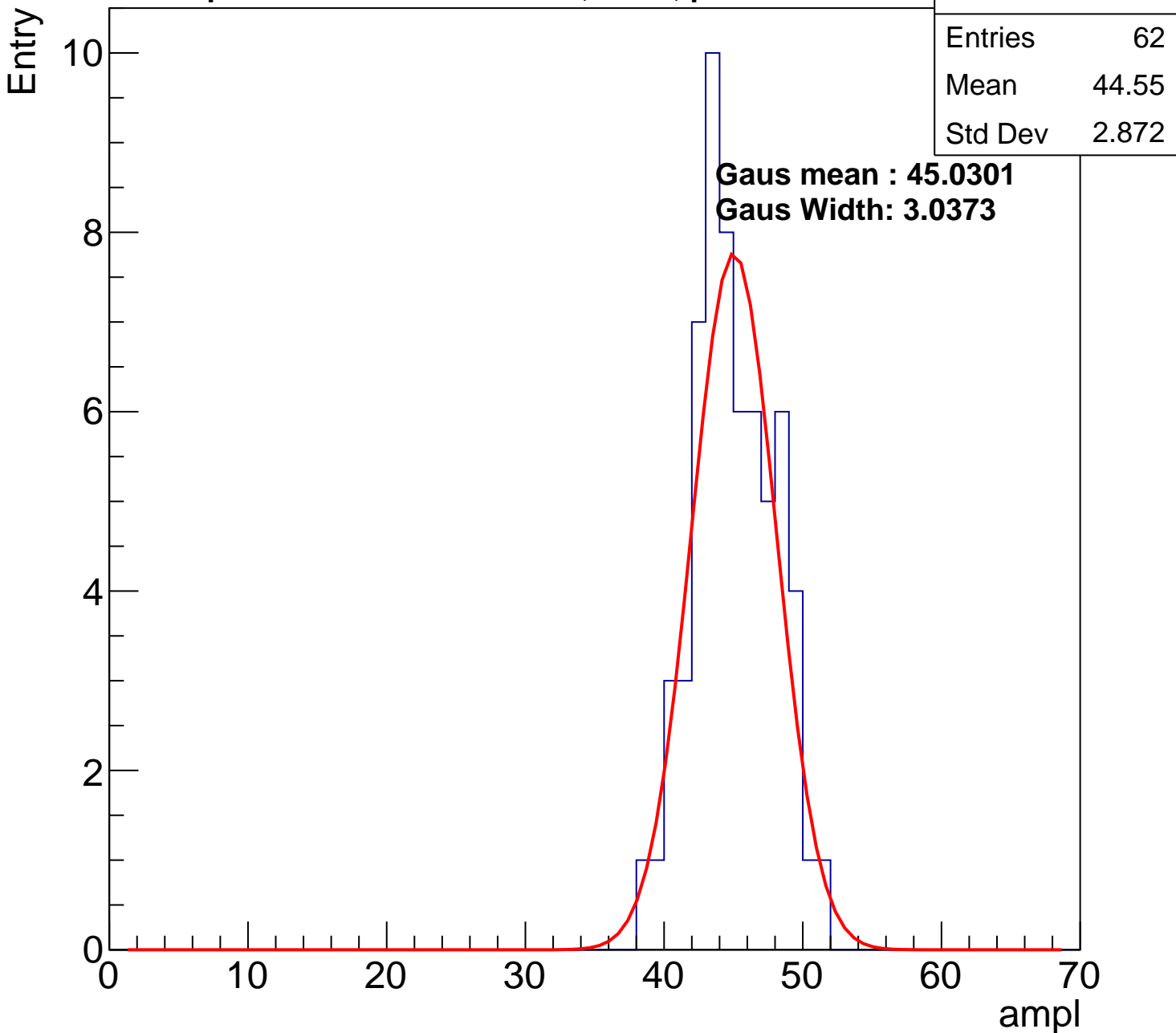
**Gaus Width: 3.0373**

Entry

10  
8  
6  
4  
2  
0

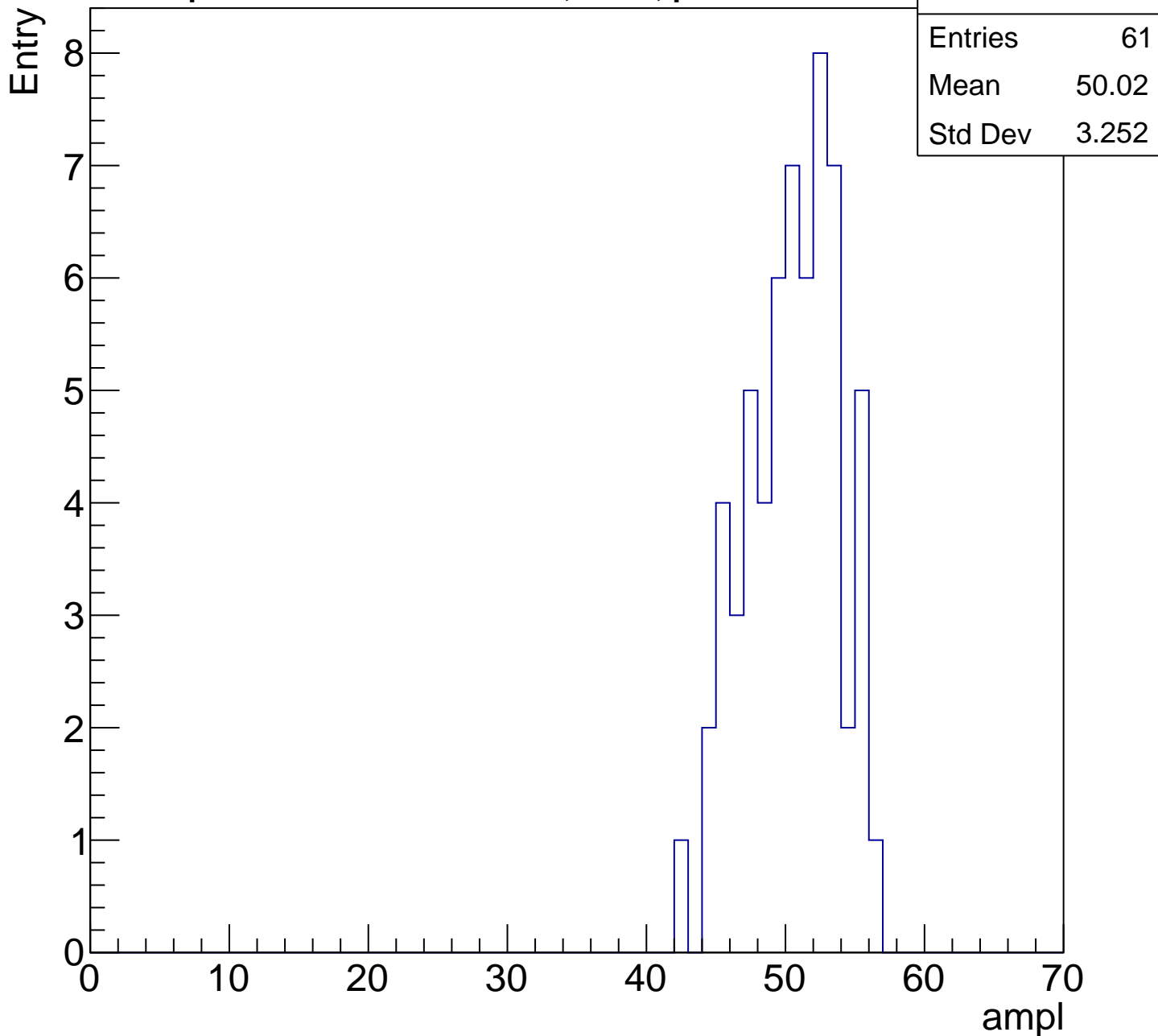
ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

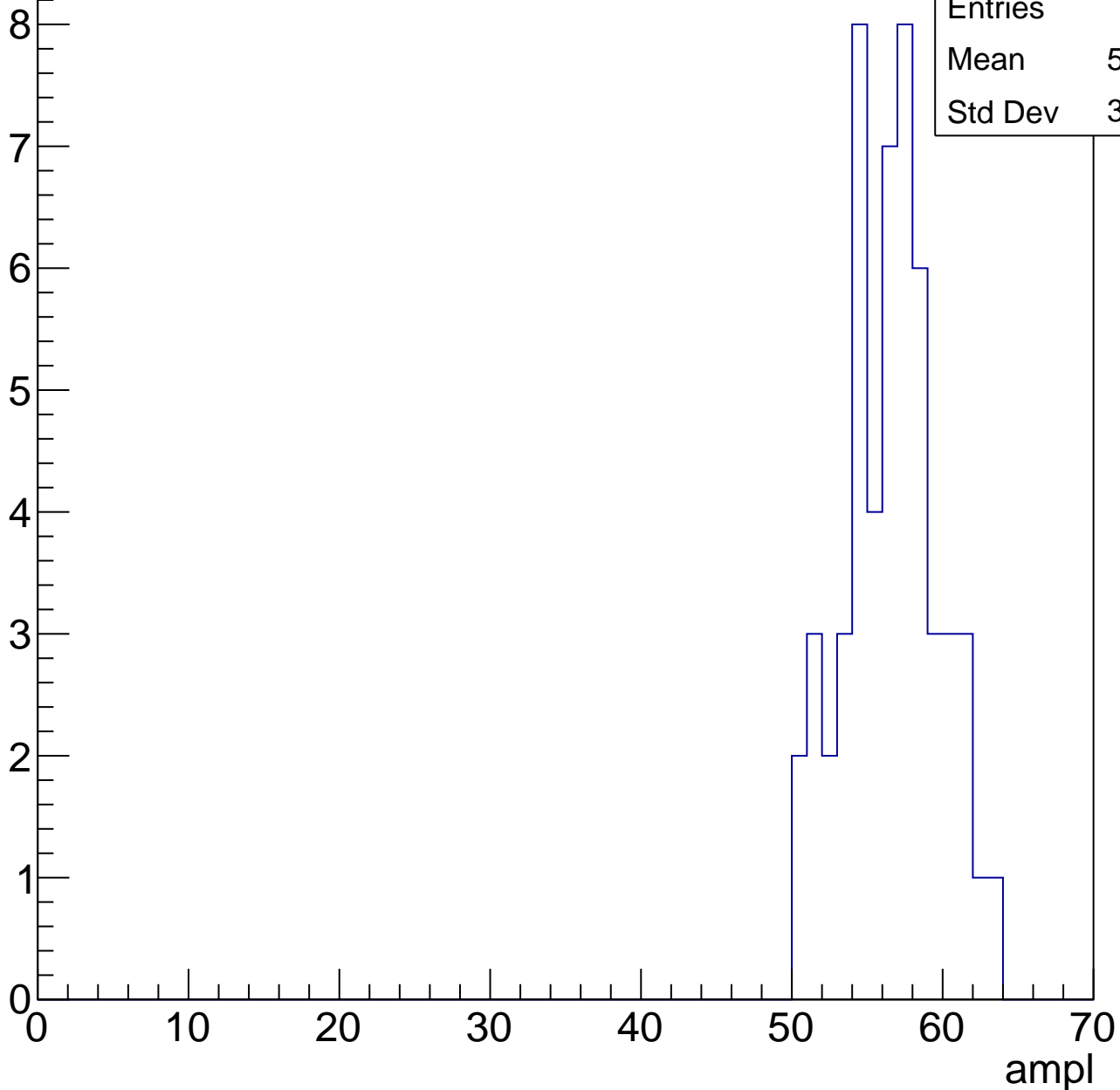


# B0L002S, U1-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 56.09 |
| Std Dev | 3.069 |



# B0L002S, U1-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 60.2  |
| Std Dev | 2.166 |

0

10

20

30

40

50

60

70

ampl

# B0L002S, U1-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

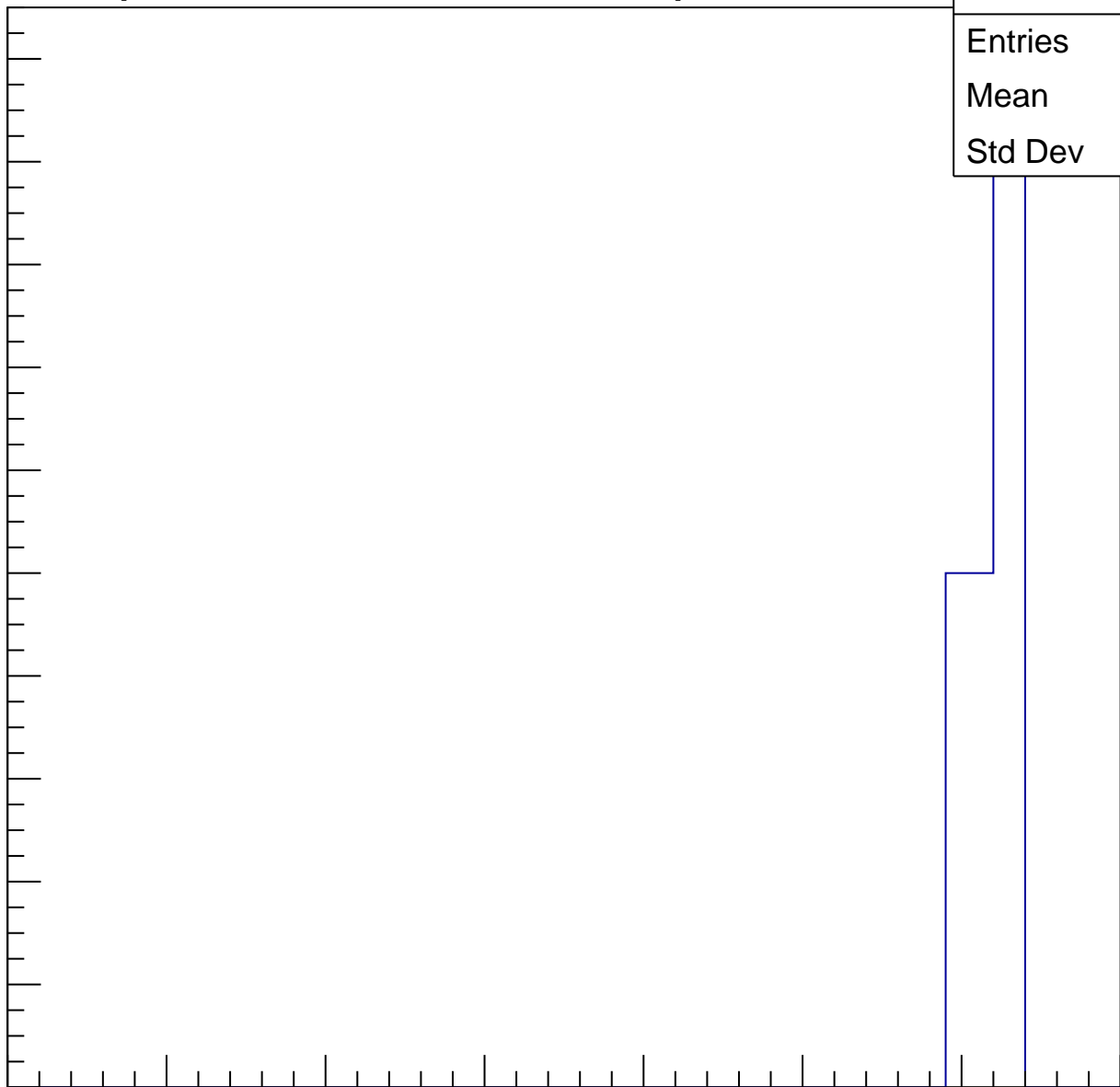
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 7     |
| Mean    | 61.43 |
| Std Dev | 1.4   |

0 10 20 30 40 50 60 70

ampl





# B0L002S, U1-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

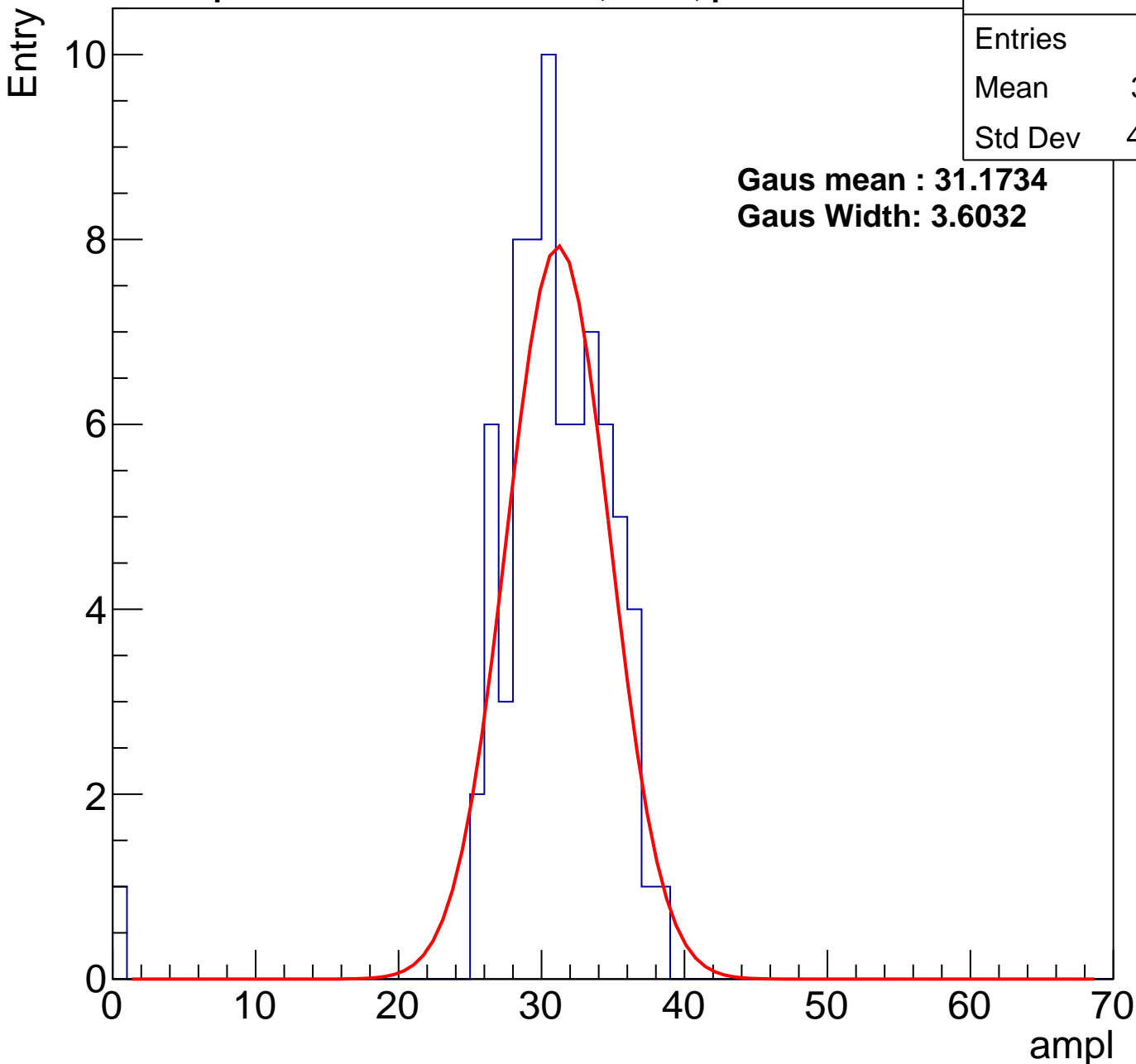
# B0L002S, U1-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 30.41 |
| Std Dev | 4.753 |

**Gaus mean : 31.1734**

**Gaus Width: 3.6032**



# B0L002S, U1-ch65, adc1

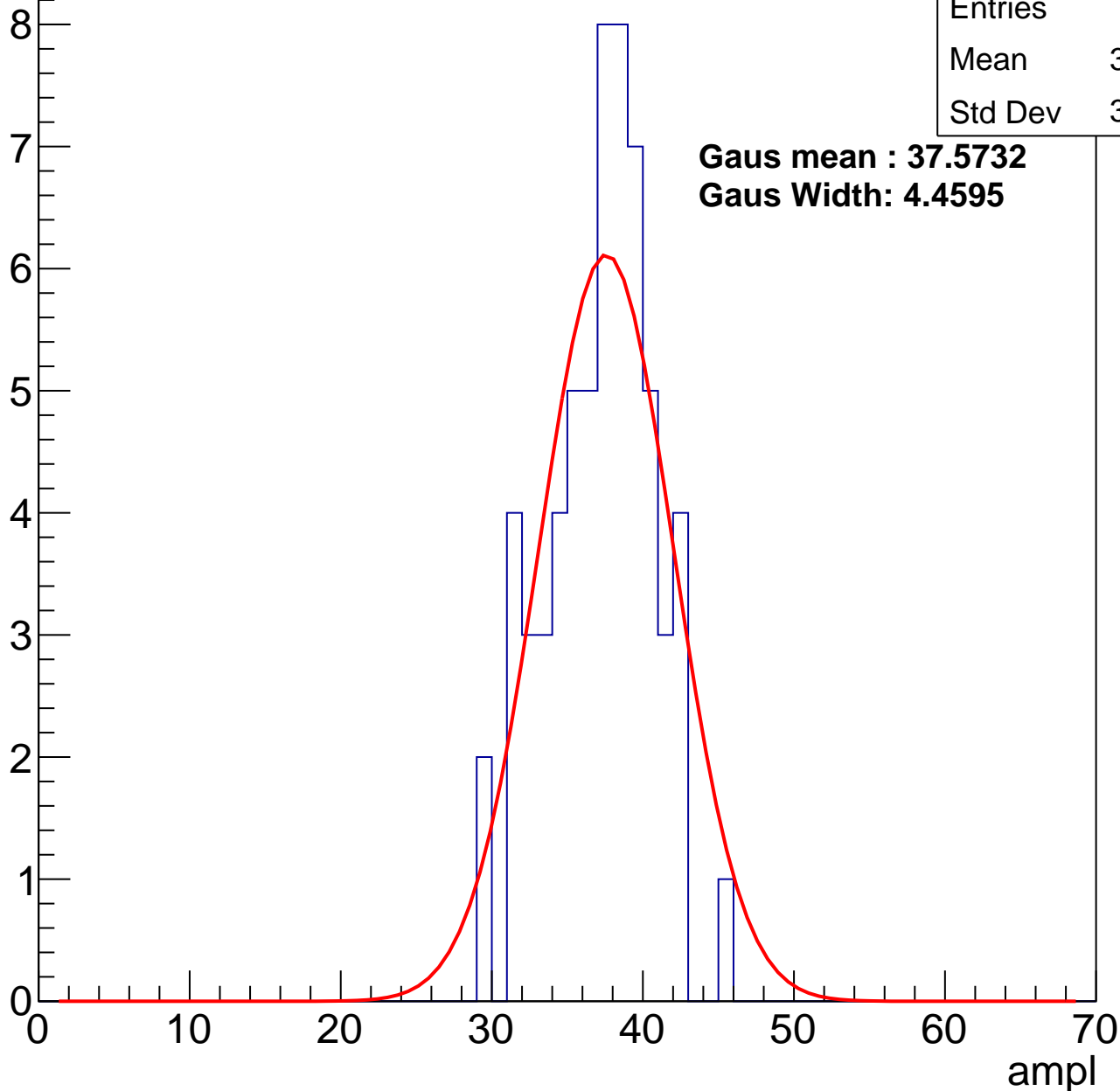
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 36.73 |
| Std Dev | 3.465 |

**Gaus mean : 37.5732**

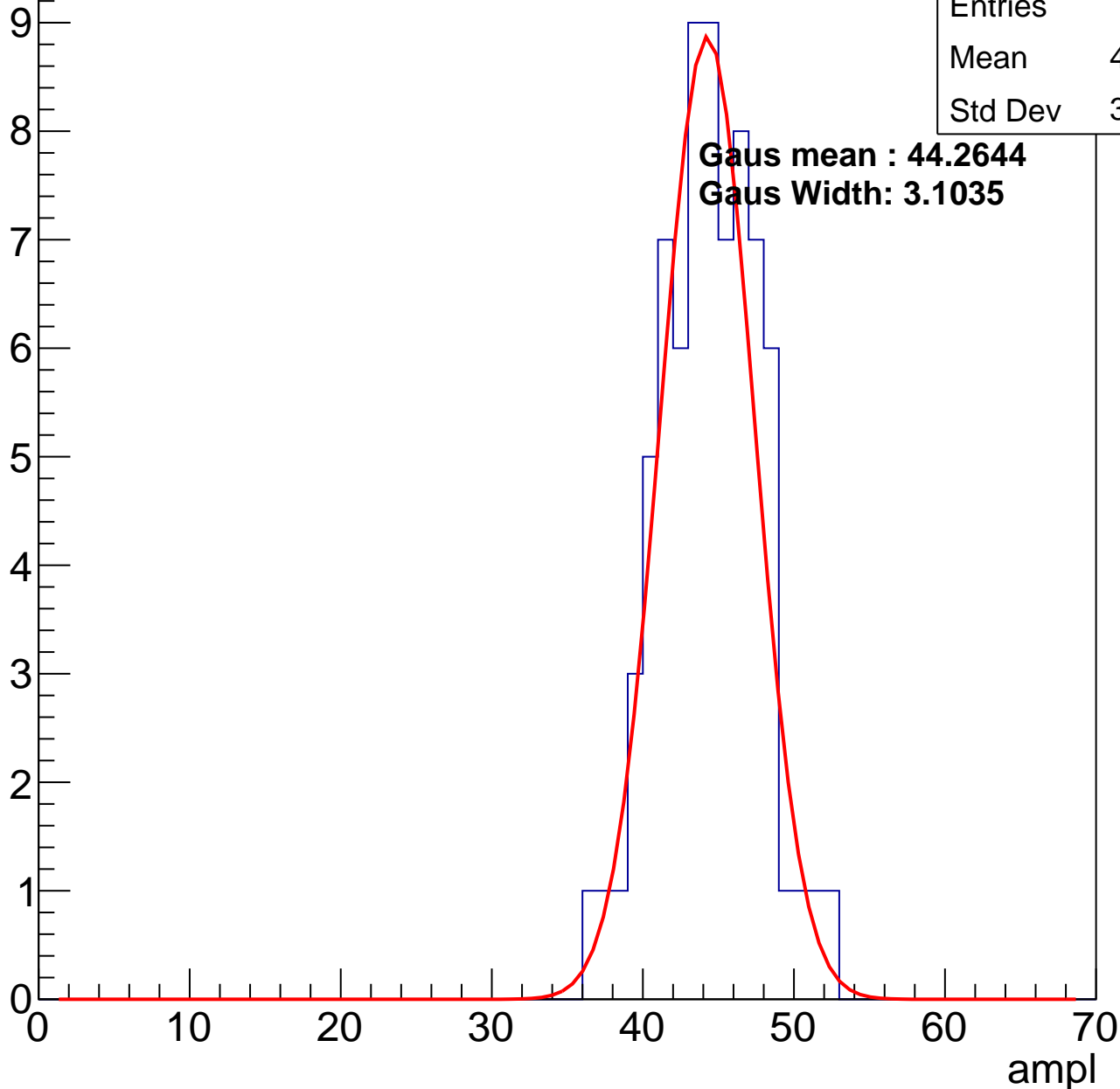
**Gaus Width: 4.4595**



# B0L002S, U1-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

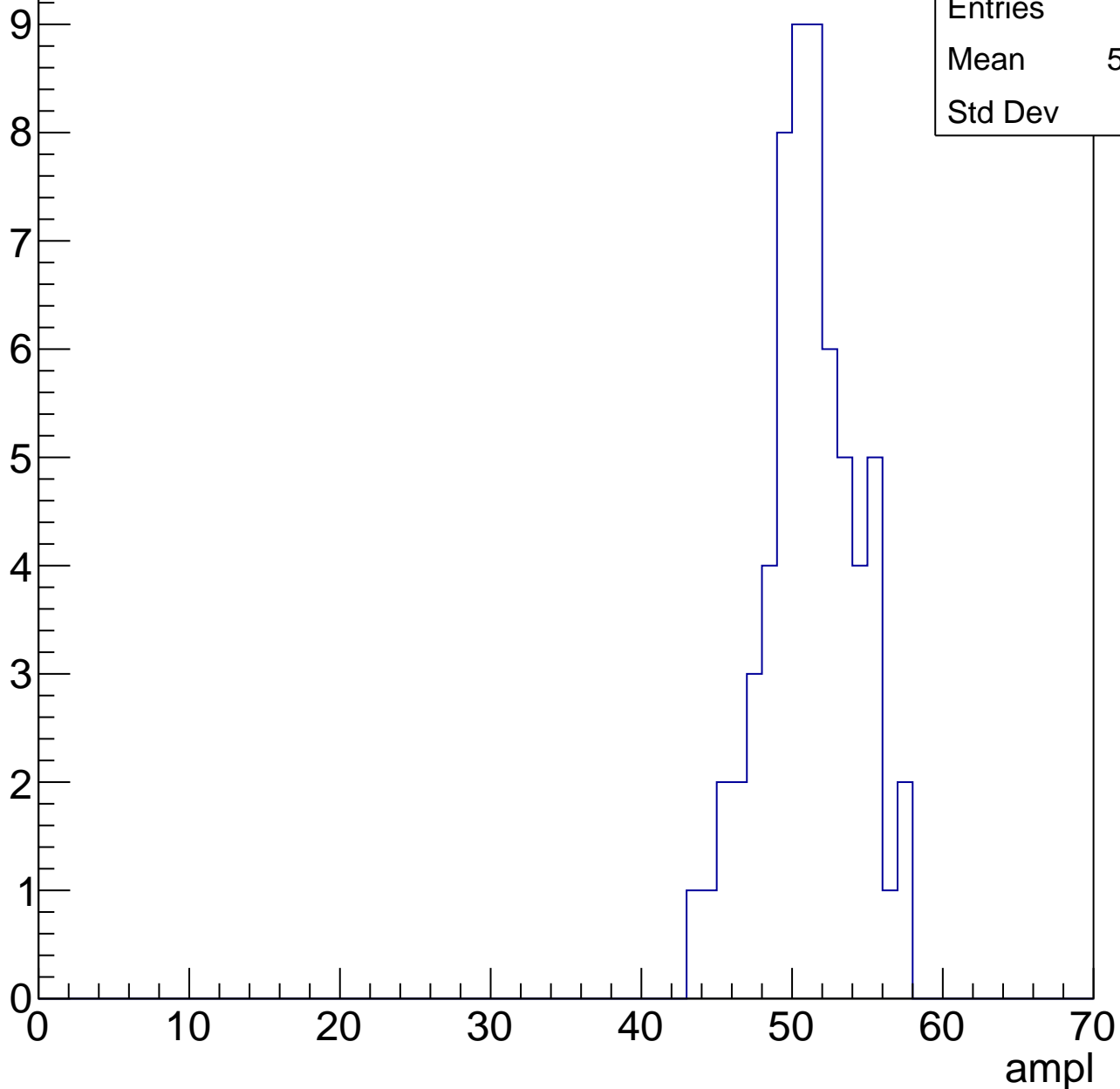
Entry



# B0L002S, U1-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

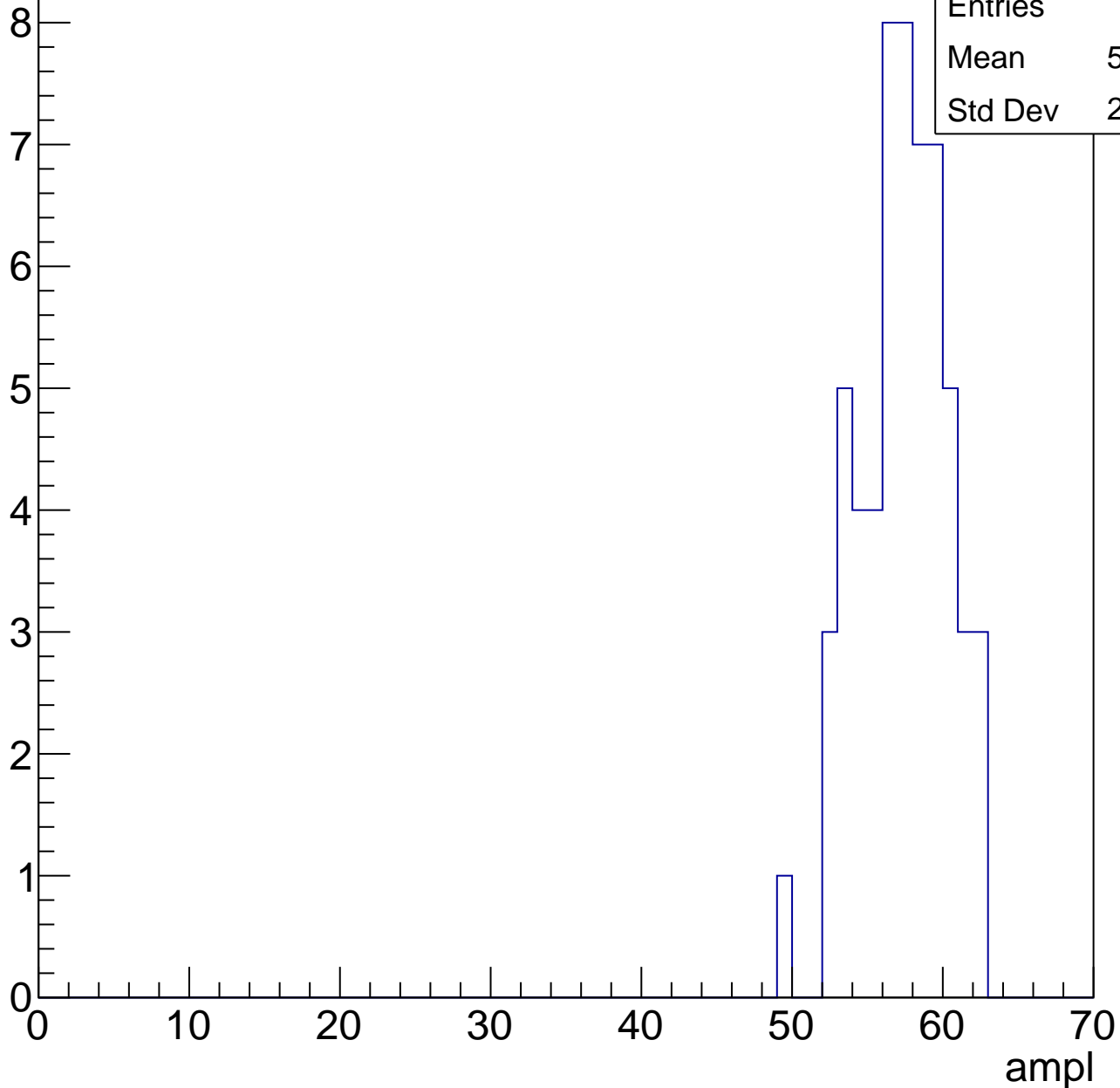


|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 50.66 |
| Std Dev | 3.09  |

# B0L002S, U1-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

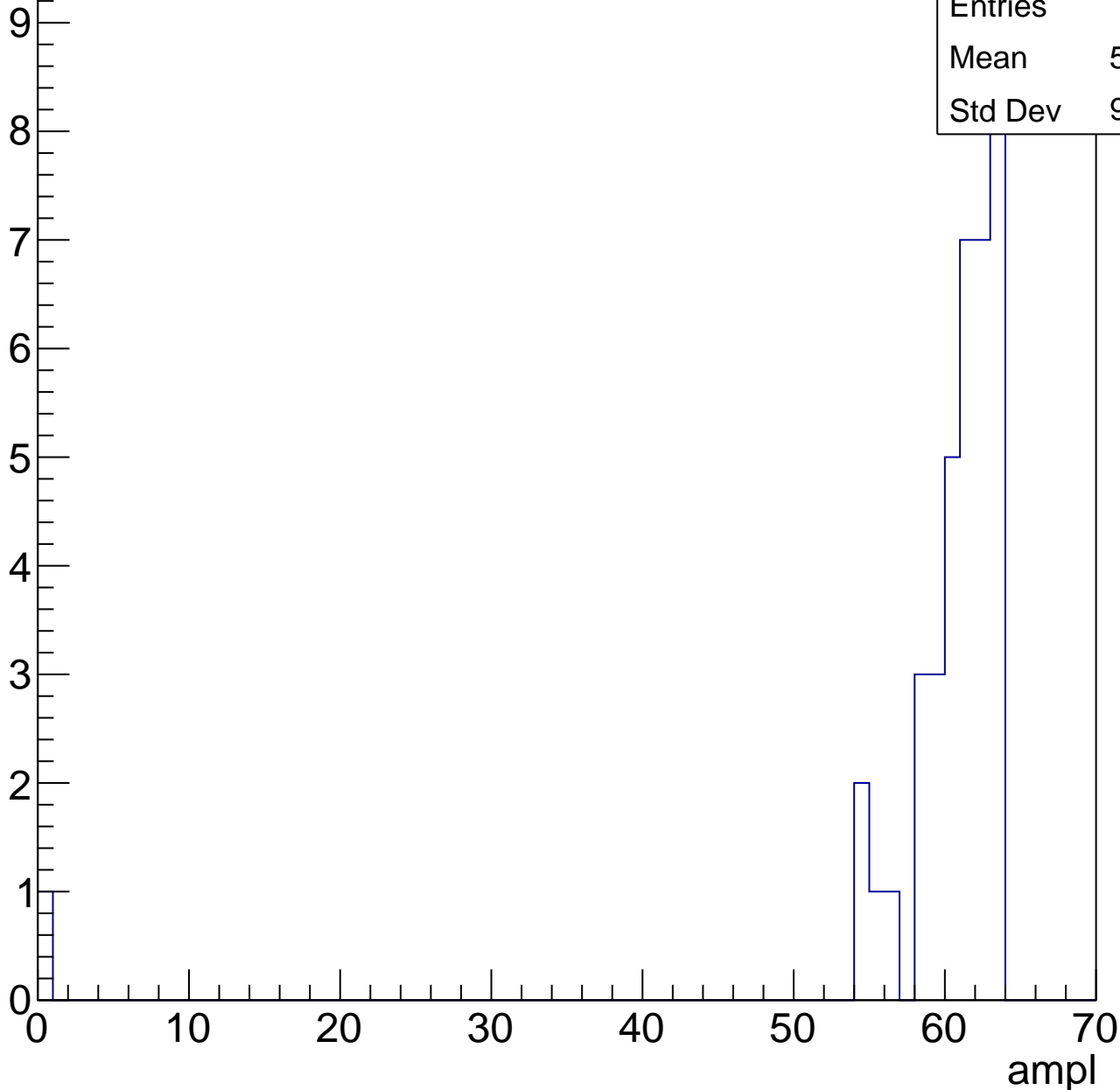


# B0L002S, U1-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 58.92 |
| Std Dev | 9.869 |



# B0L002S, U1-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 12.5 |
| Std Dev | 12.5 |

# B0L002S, U1-ch66, adc0

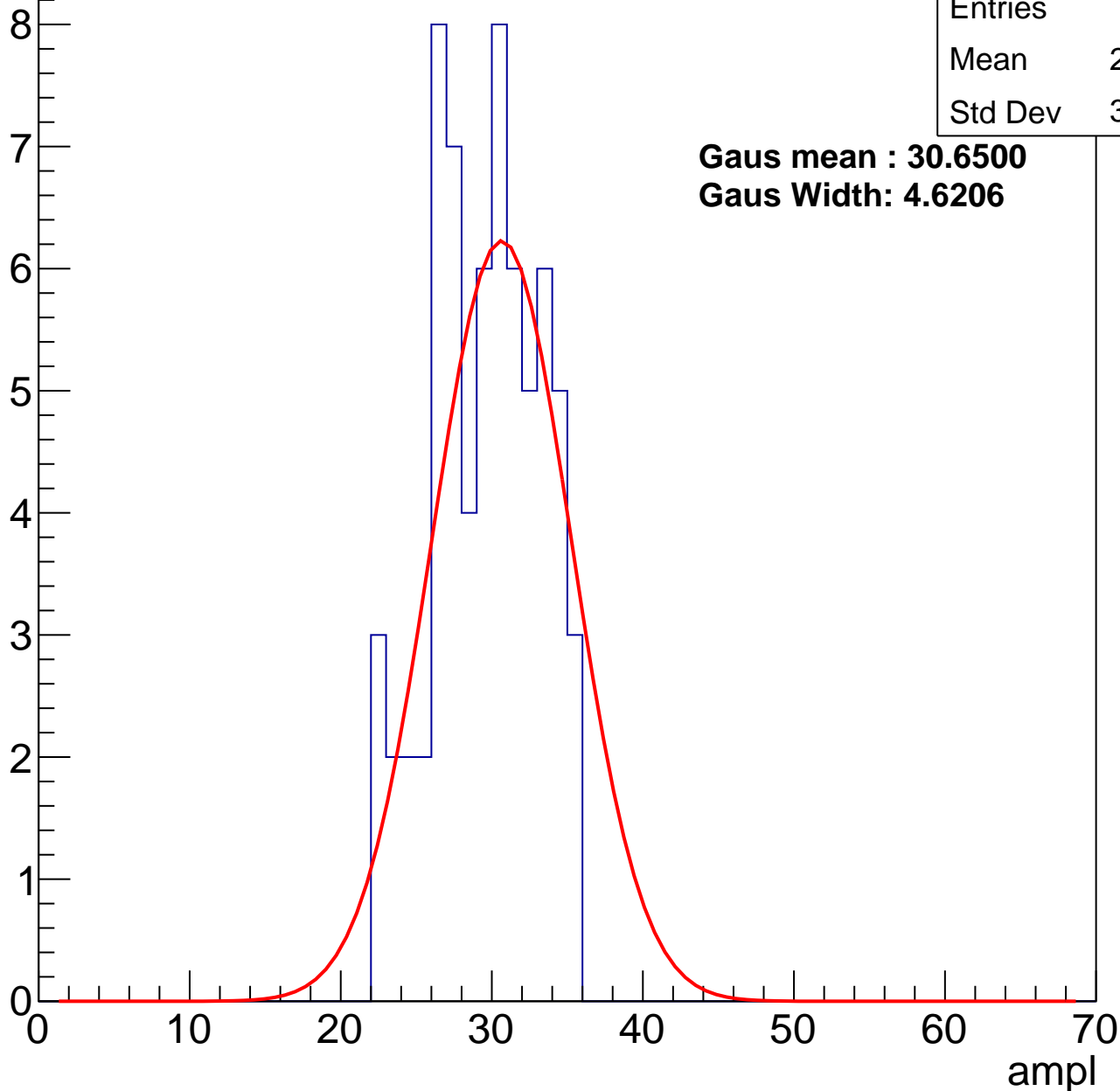
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 29.13 |
| Std Dev | 3.477 |

**Gaus mean : 30.6500**

**Gaus Width: 4.6206**



# B0L002S, U1-ch66, adc1

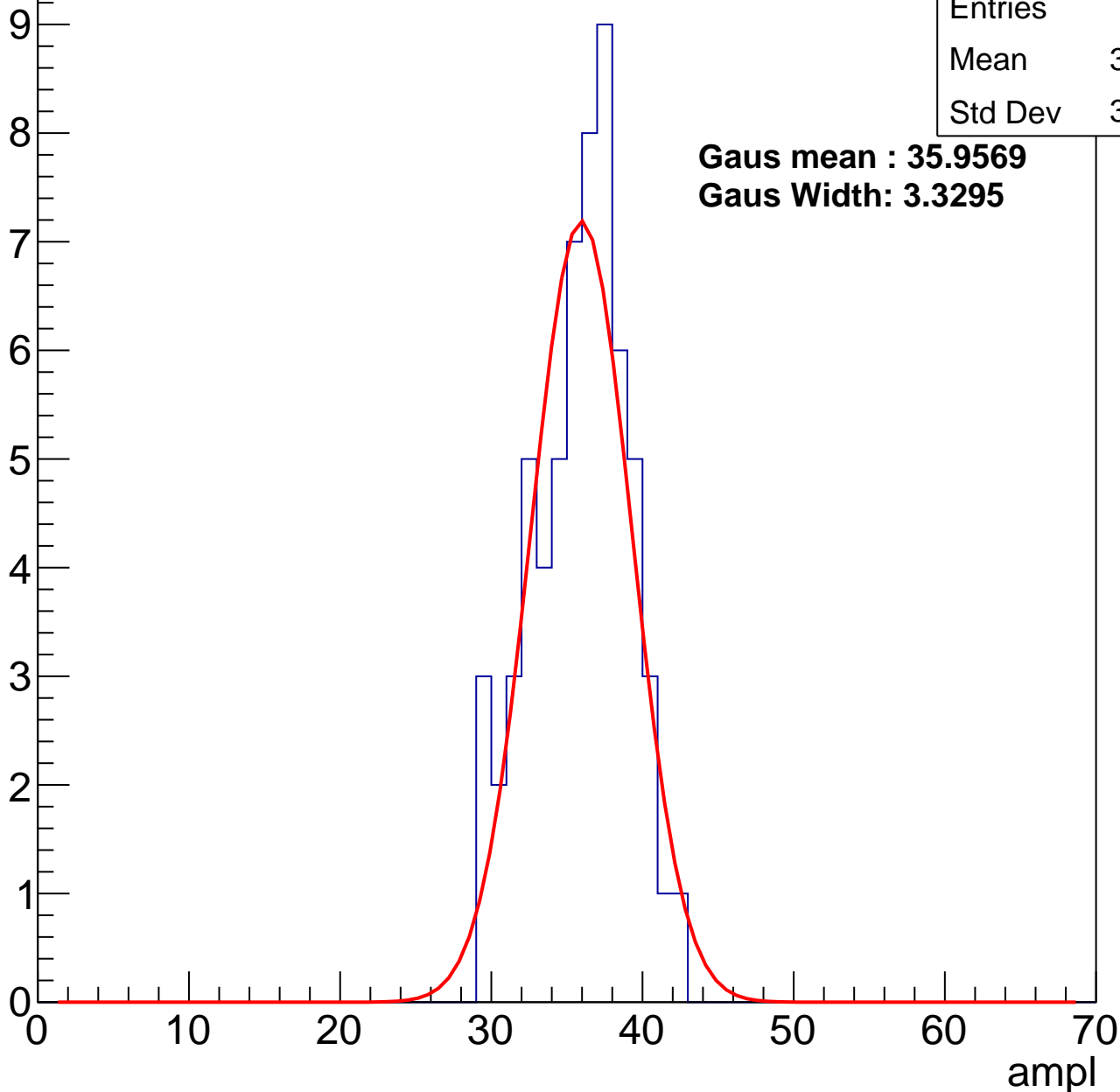
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 35.39 |
| Std Dev | 3.113 |

**Gaus mean : 35.9569**

**Gaus Width: 3.3295**



# B0L002S, U1-ch66, adc2

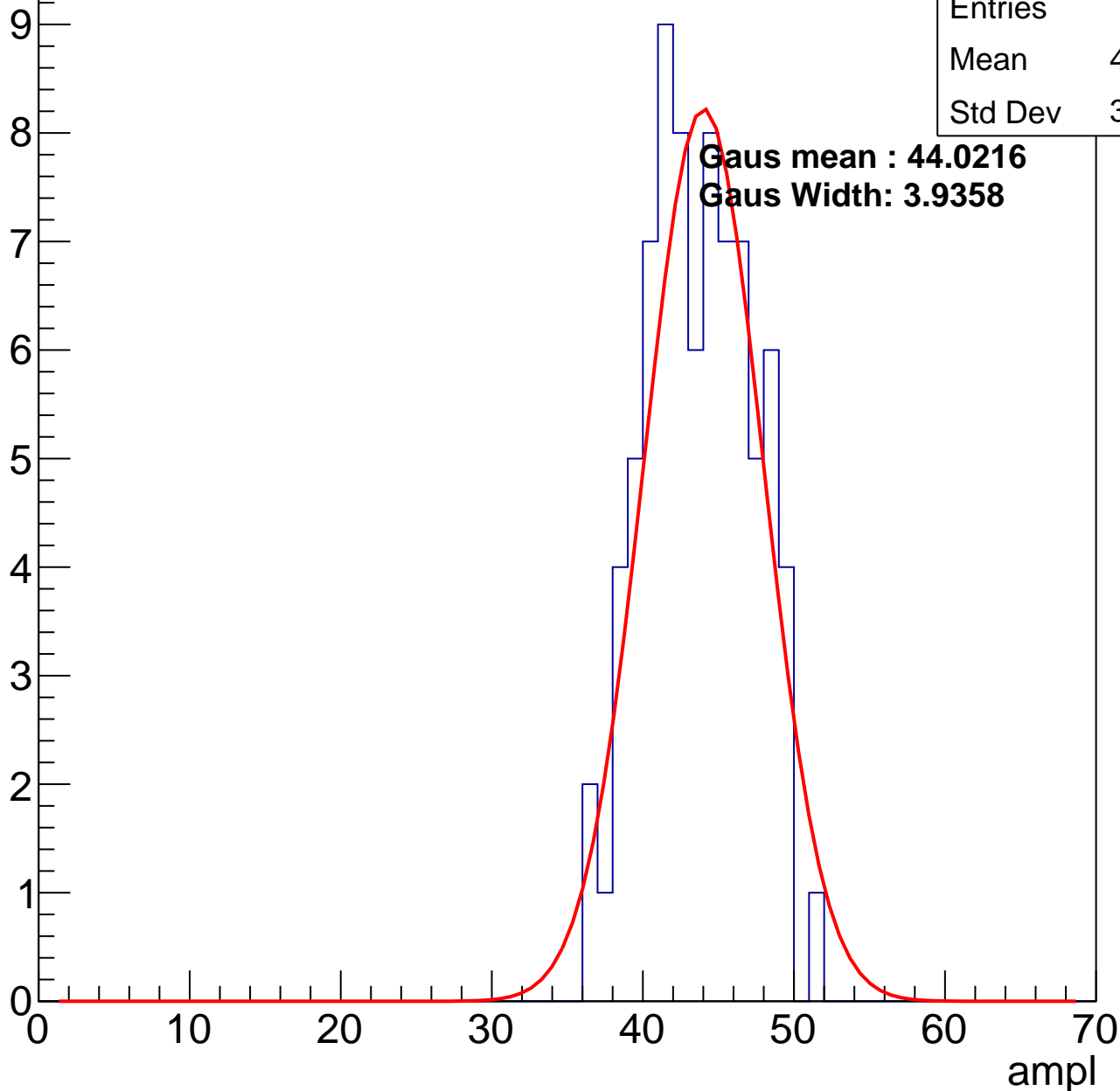
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 43.23 |
| Std Dev | 3.453 |

**Gaus mean : 44.0216**

**Gaus Width: 3.9358**

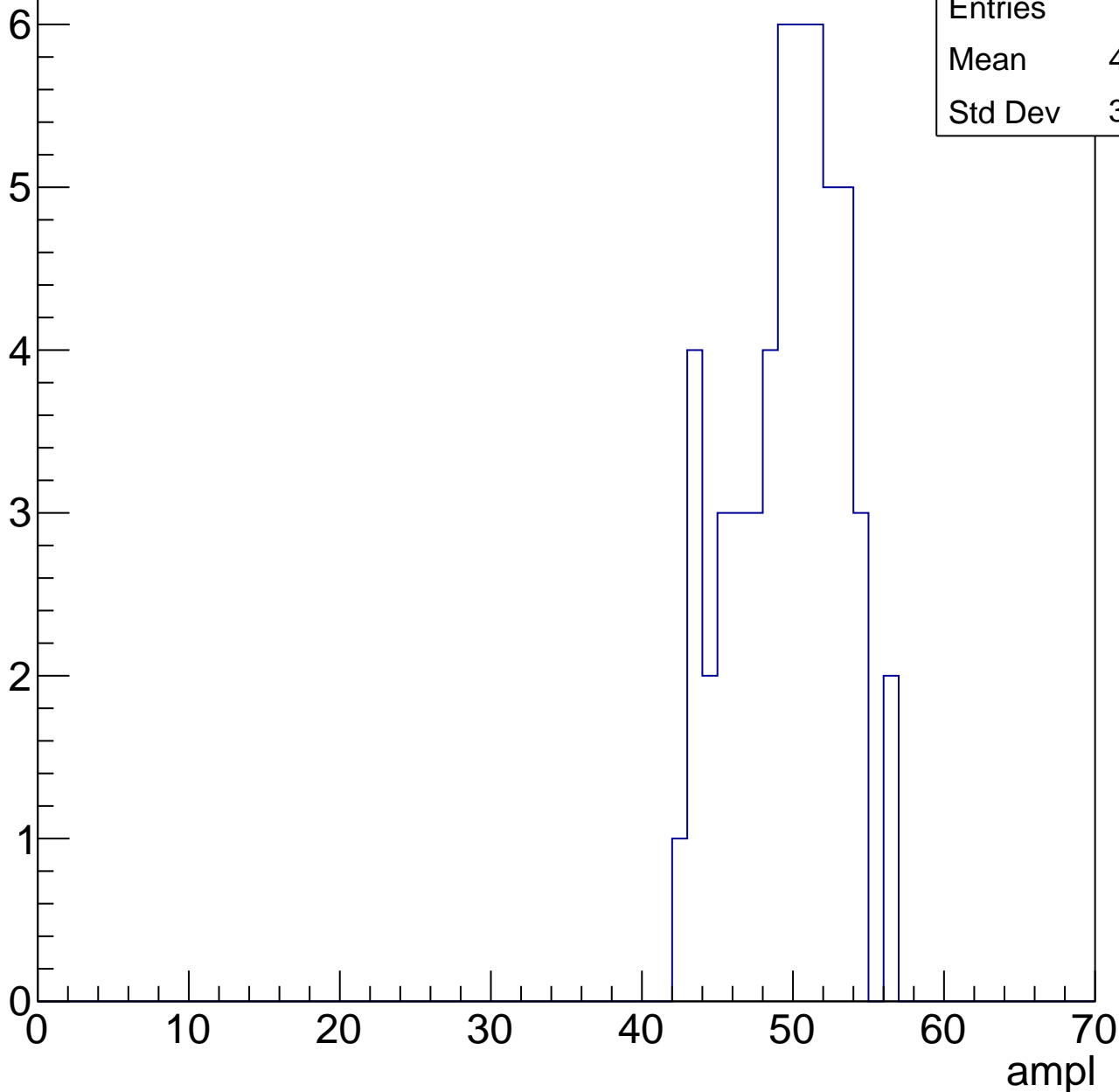


# B0L002S, U1-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 49.19 |
| Std Dev | 3.529 |

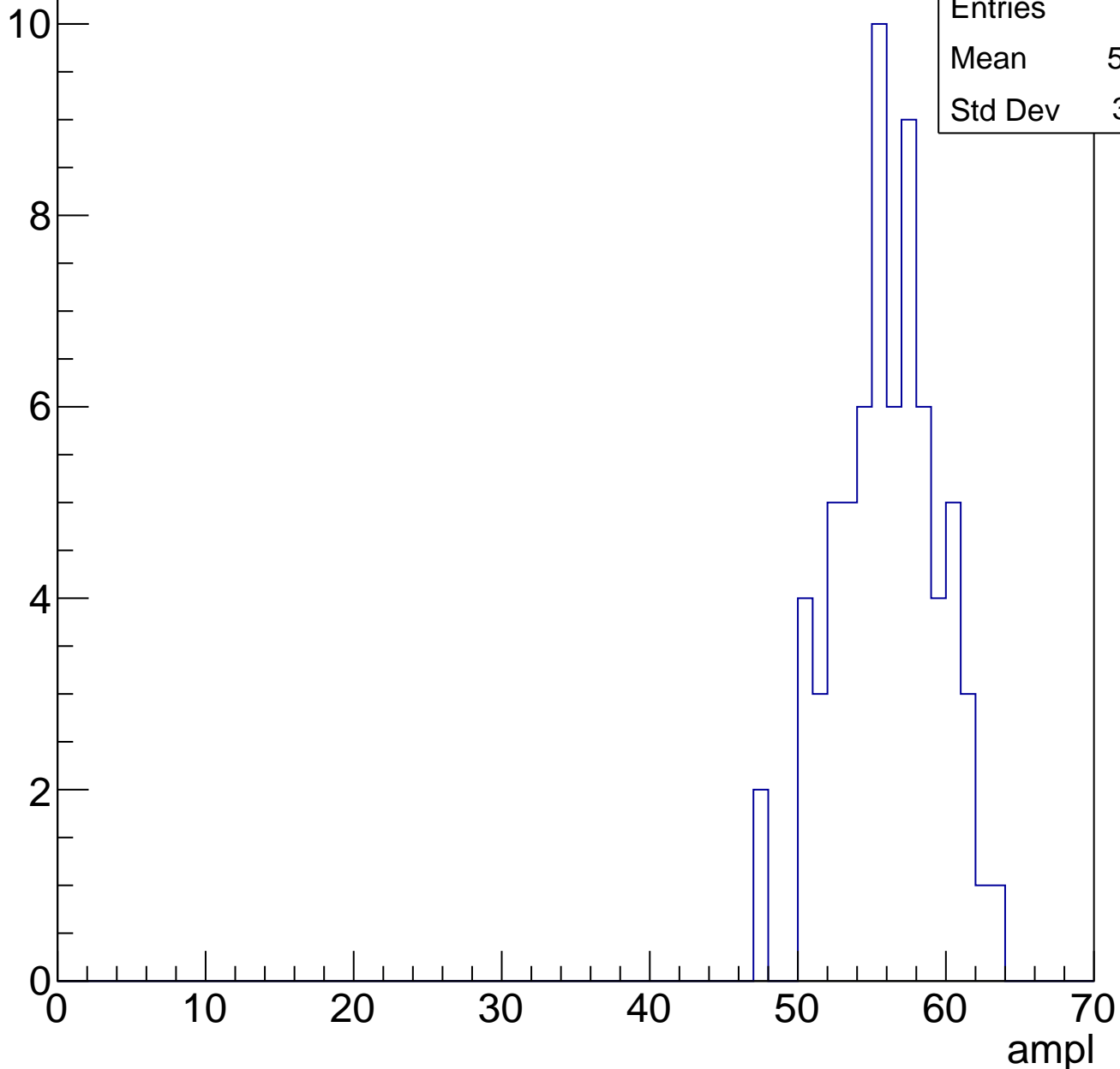


# B0L002S, U1-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 55.53 |
| Std Dev | 3.451 |

Entry

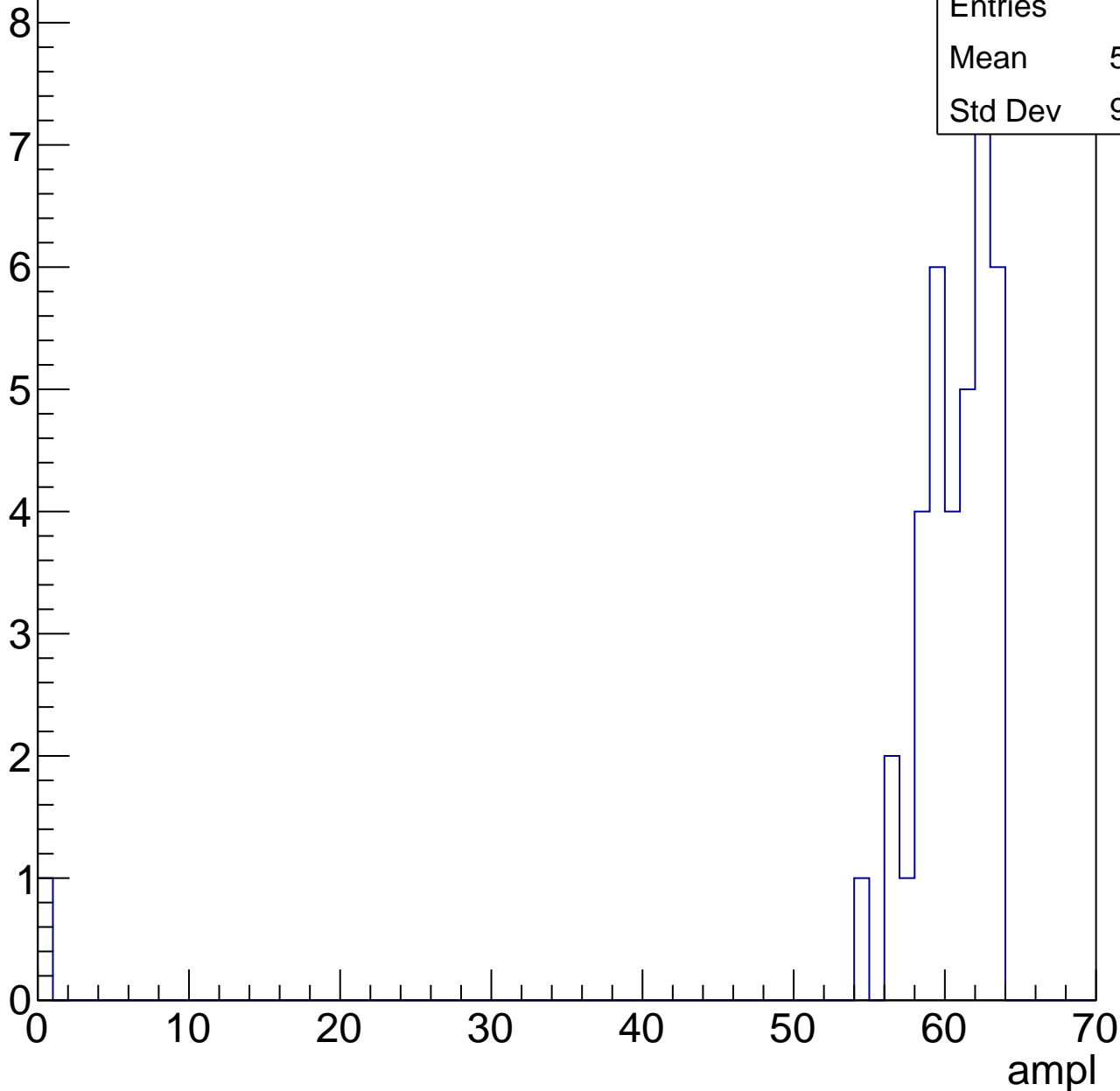


# B0L002S, U1-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 58.63 |
| Std Dev | 9.893 |



# B0L002S, U1-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch67, adc0

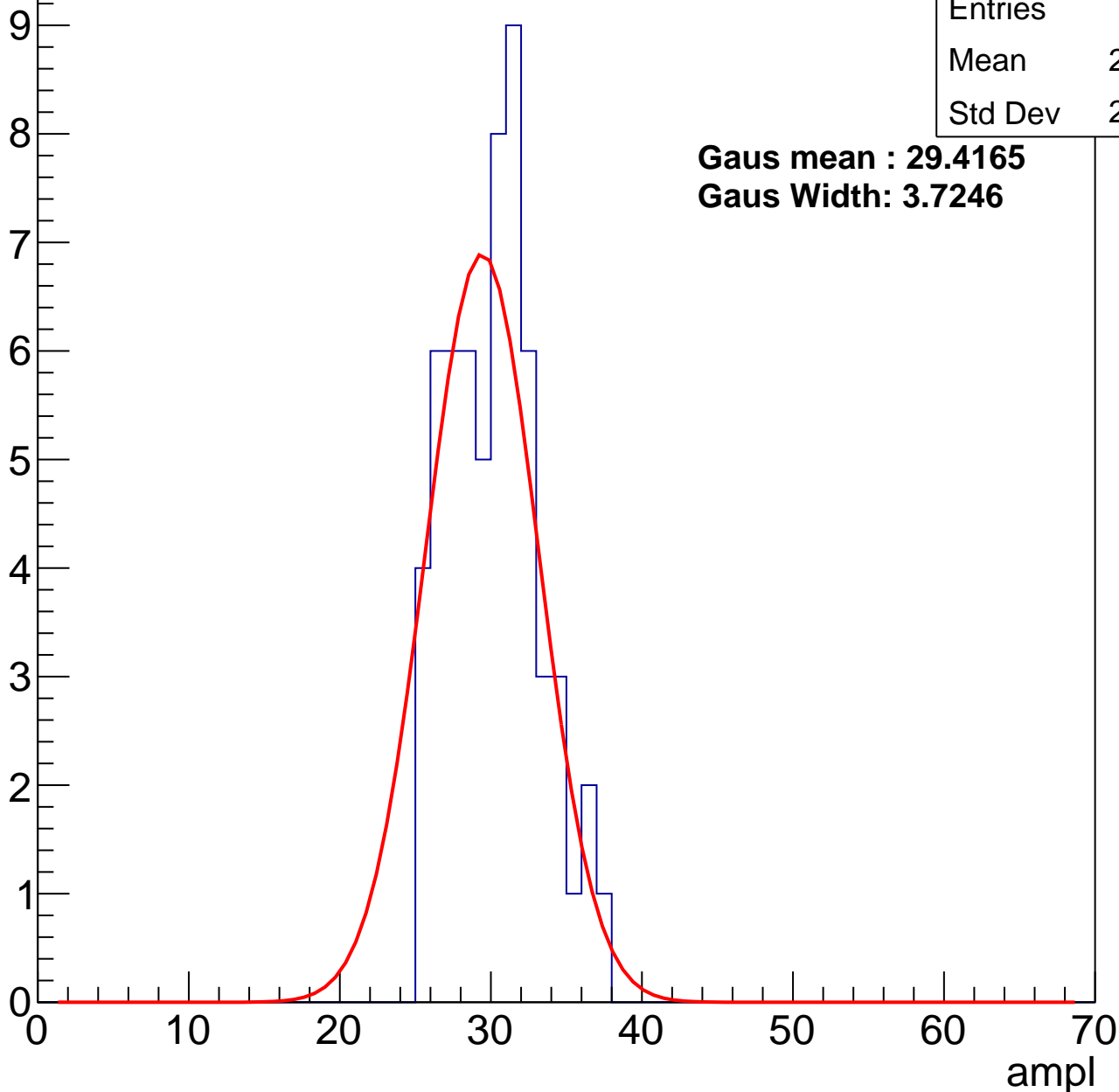
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 29.78 |
| Std Dev | 2.973 |

**Gaus mean : 29.4165**

**Gaus Width: 3.7246**



# B0L002S, U1-ch67, adc1

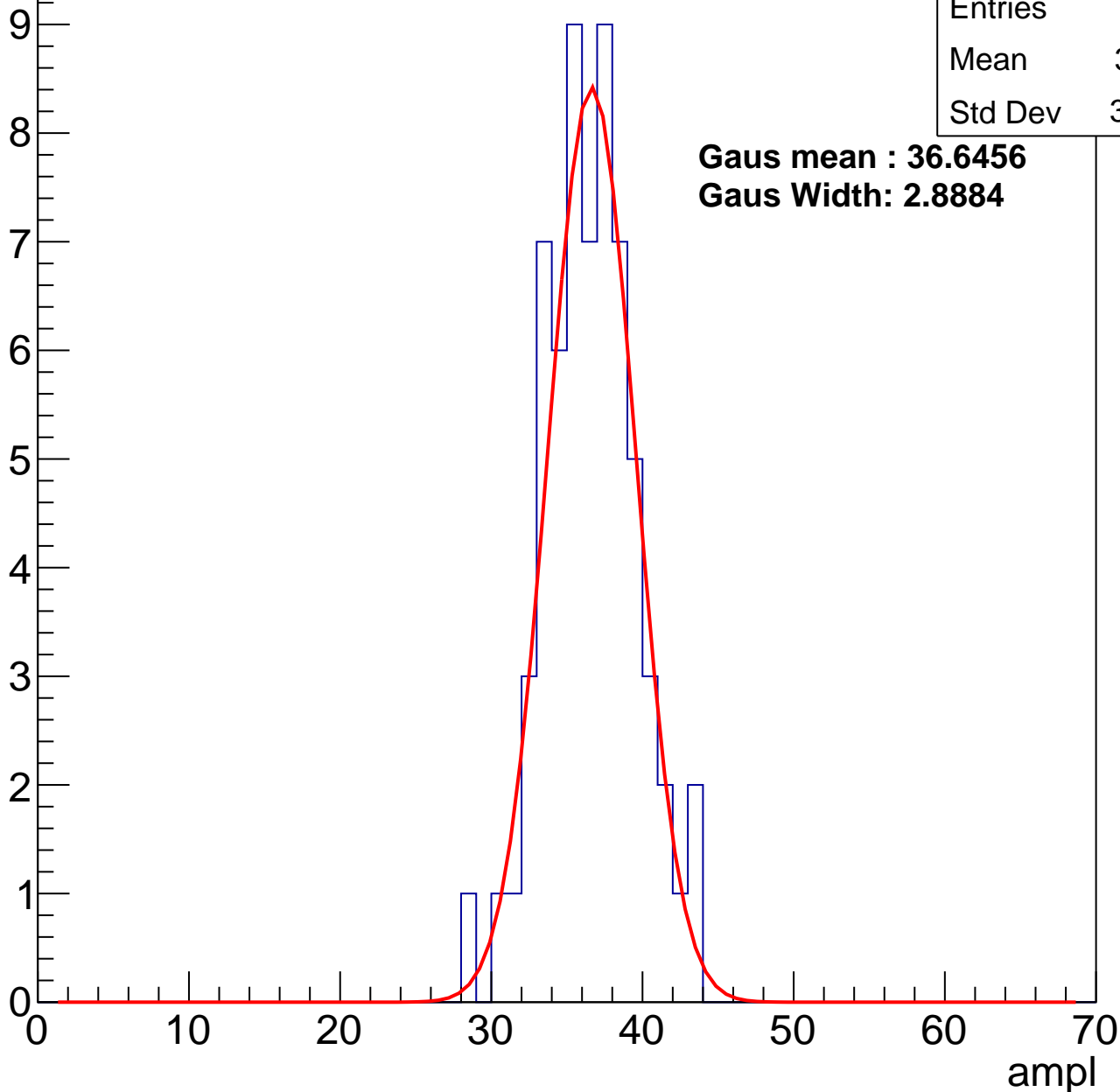
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 36.11 |
| Std Dev | 3.016 |

**Gaus mean : 36.6456**

**Gaus Width: 2.8884**



# B0L002S, U1-ch67, adc2

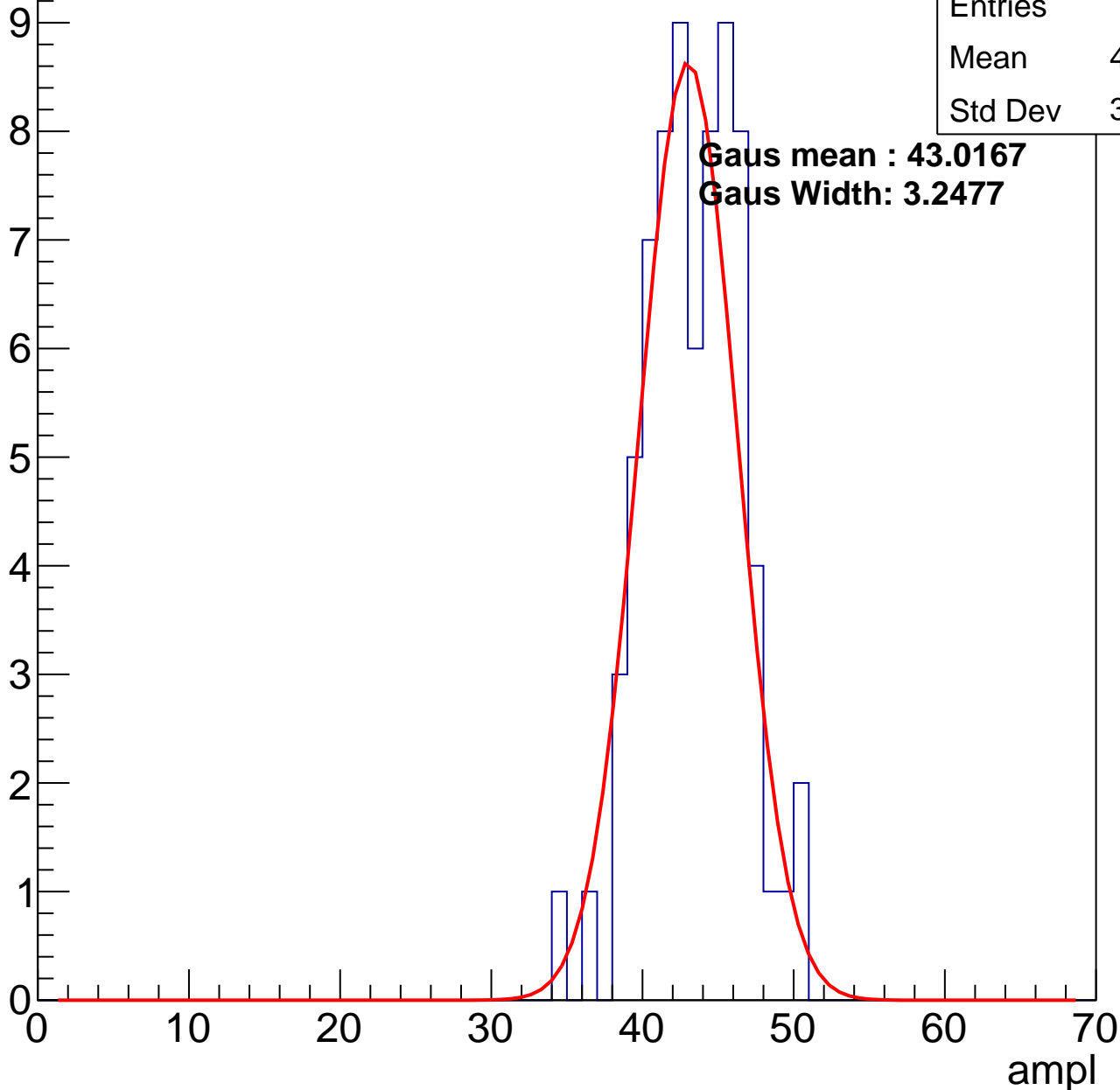
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 42.92 |
| Std Dev | 3.157 |

**Gaus mean : 43.0167**

**Gaus Width: 3.2477**

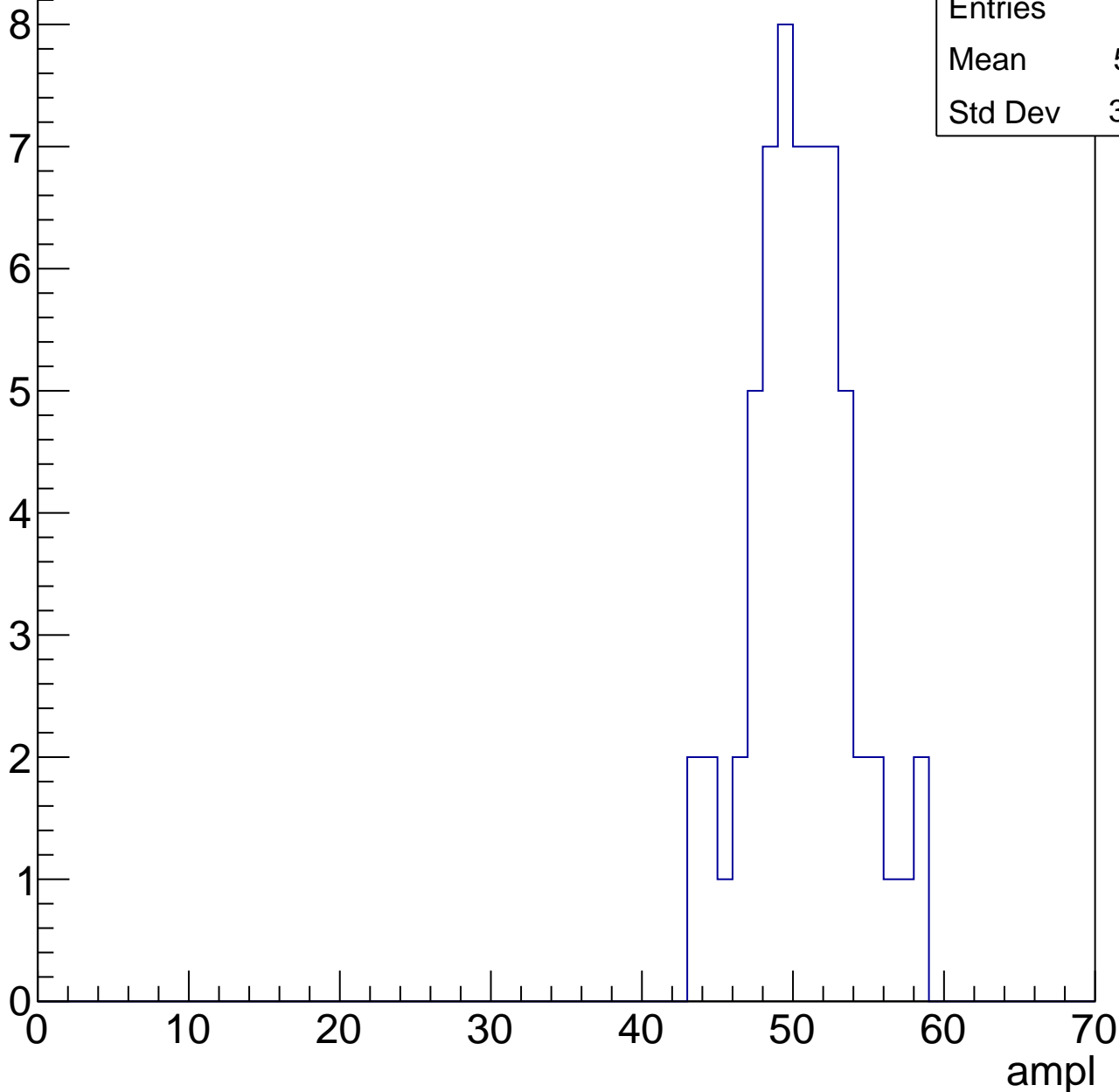


# B0L002S, U1-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

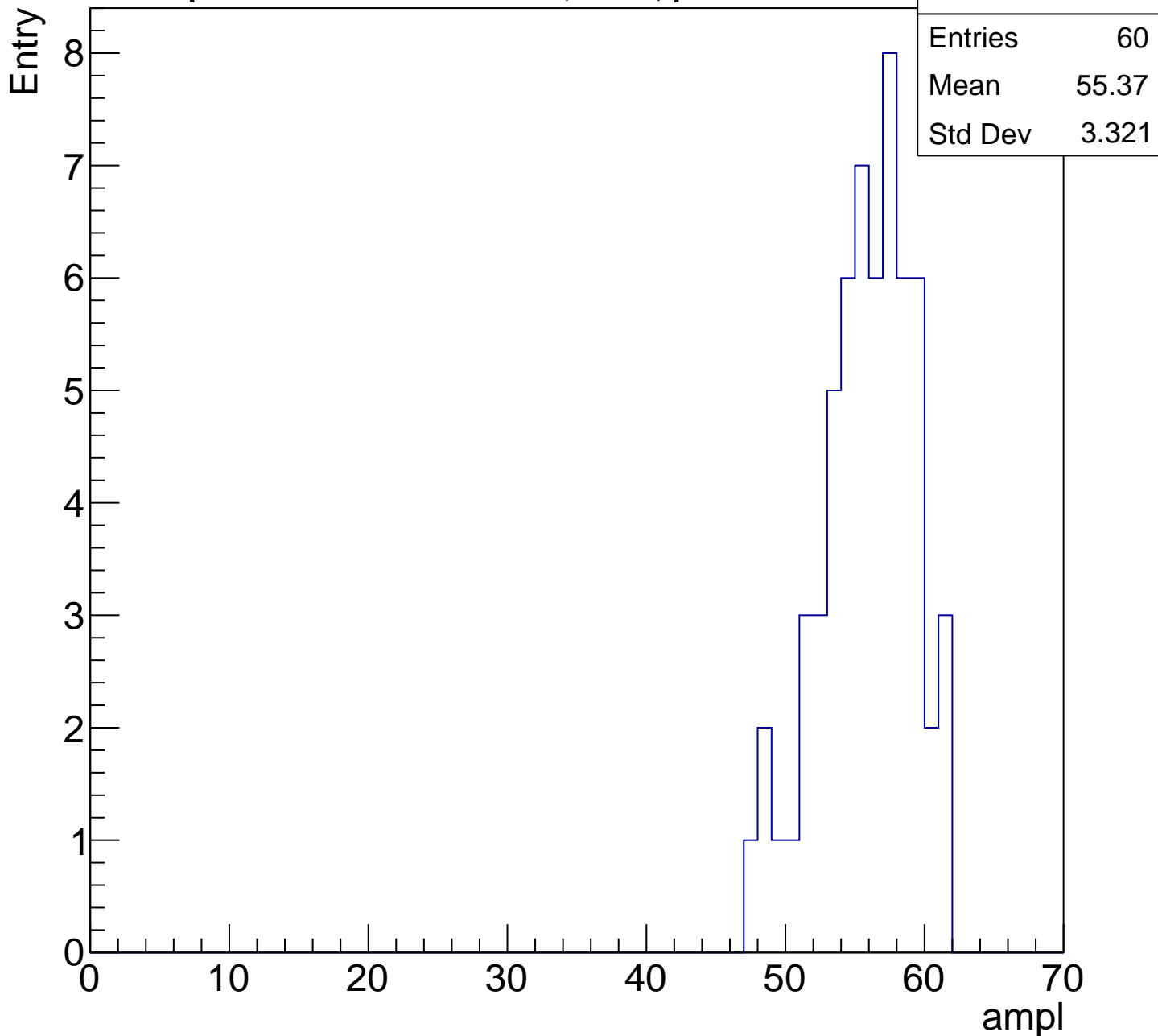
Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 50.11 |
| Std Dev | 3.344 |



# B0L002S, U1-ch67, adc4

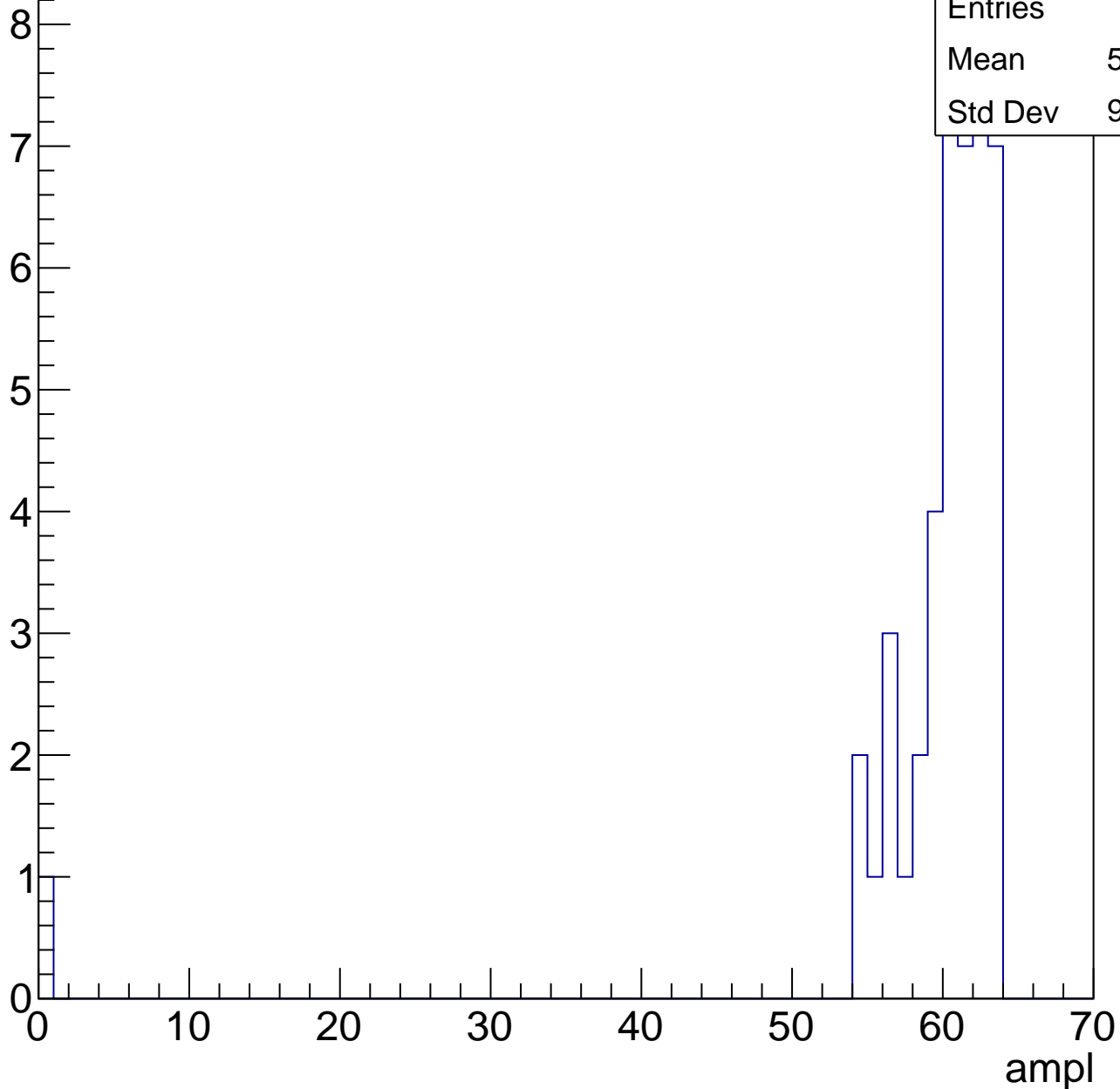
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

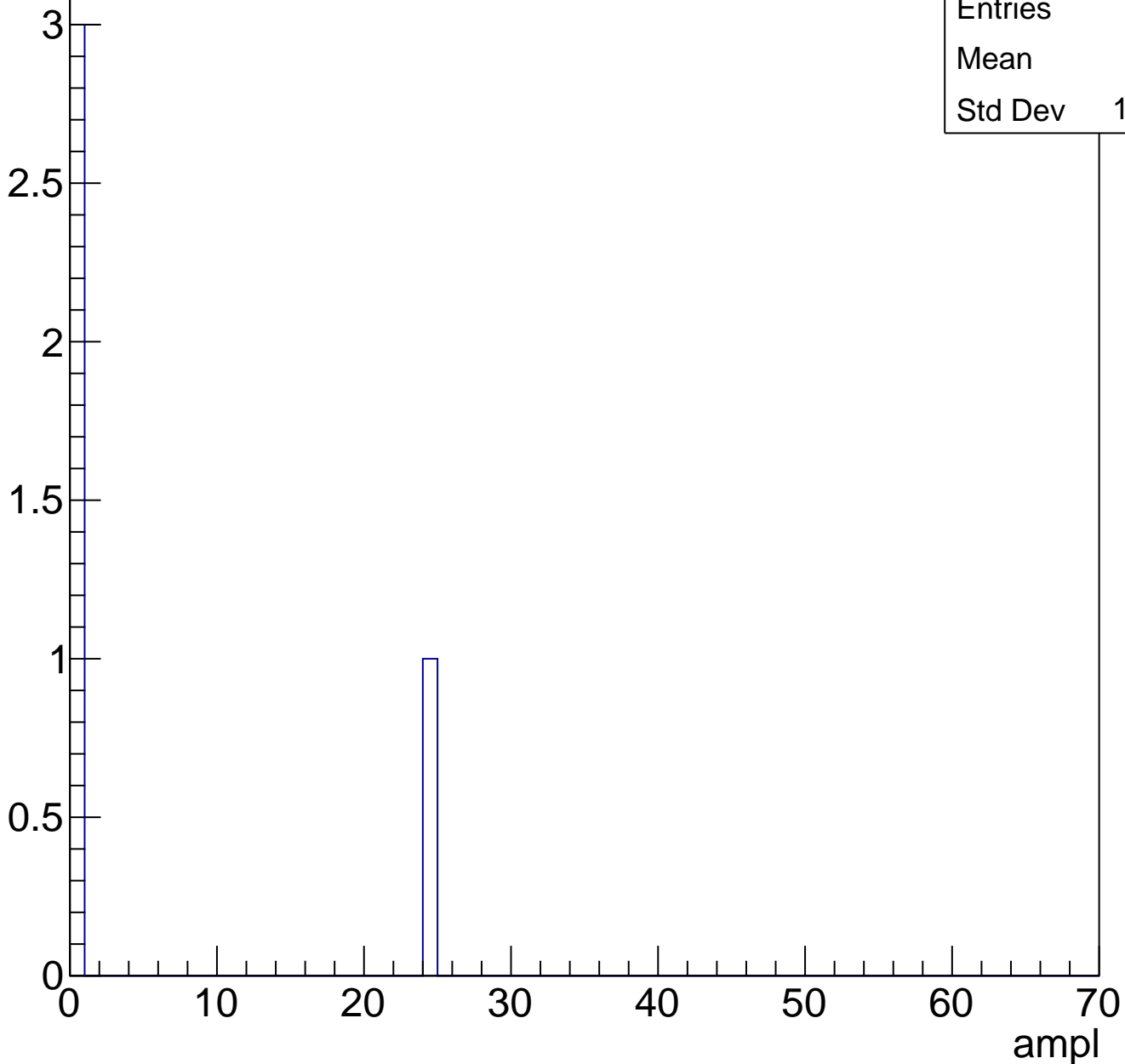




# B0L002S, U1-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch68, adc0

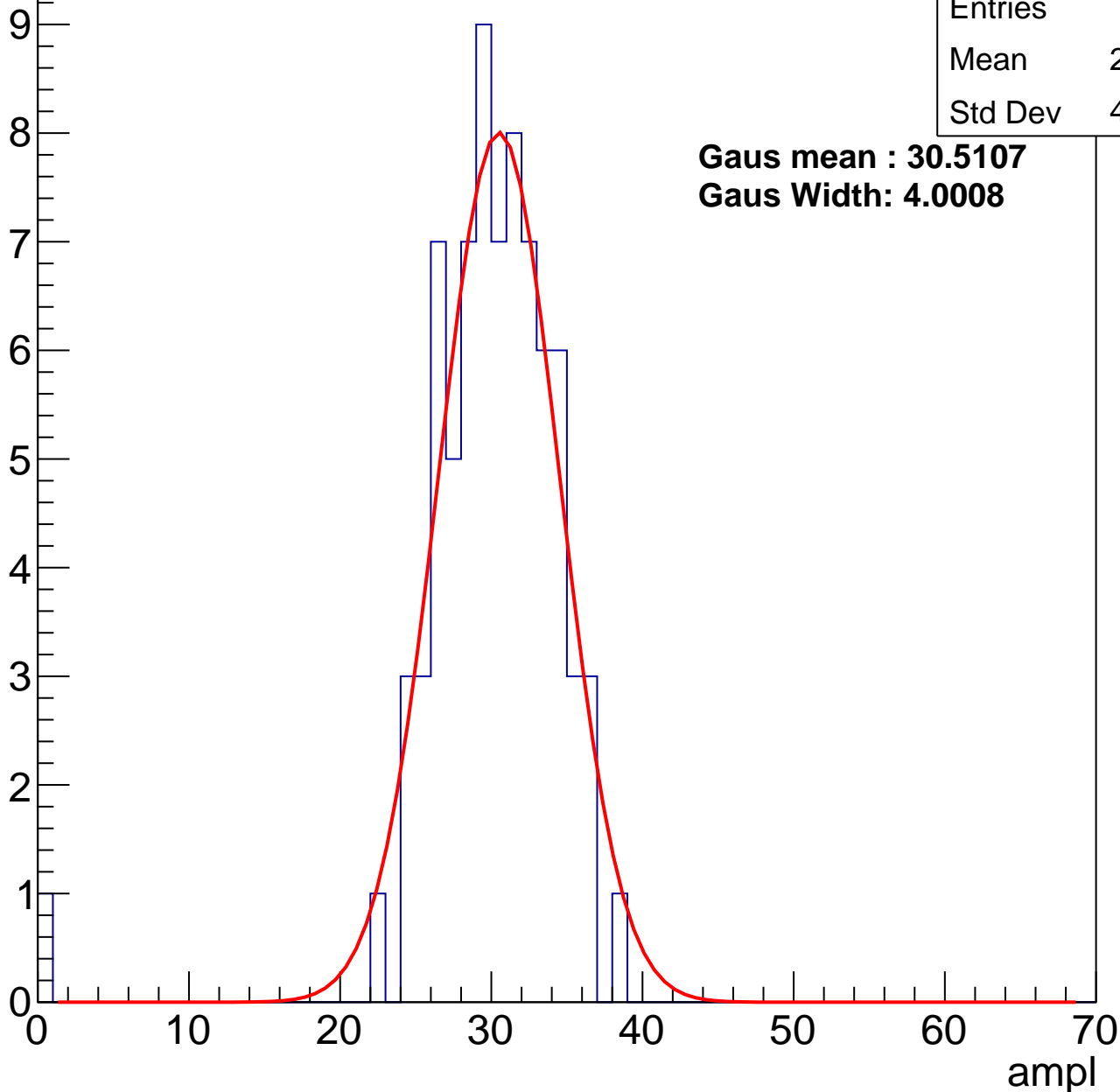
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 29.58 |
| Std Dev | 4.782 |

**Gaus mean : 30.5107**

**Gaus Width: 4.0008**



# B0L002S, U1-ch68, adc1

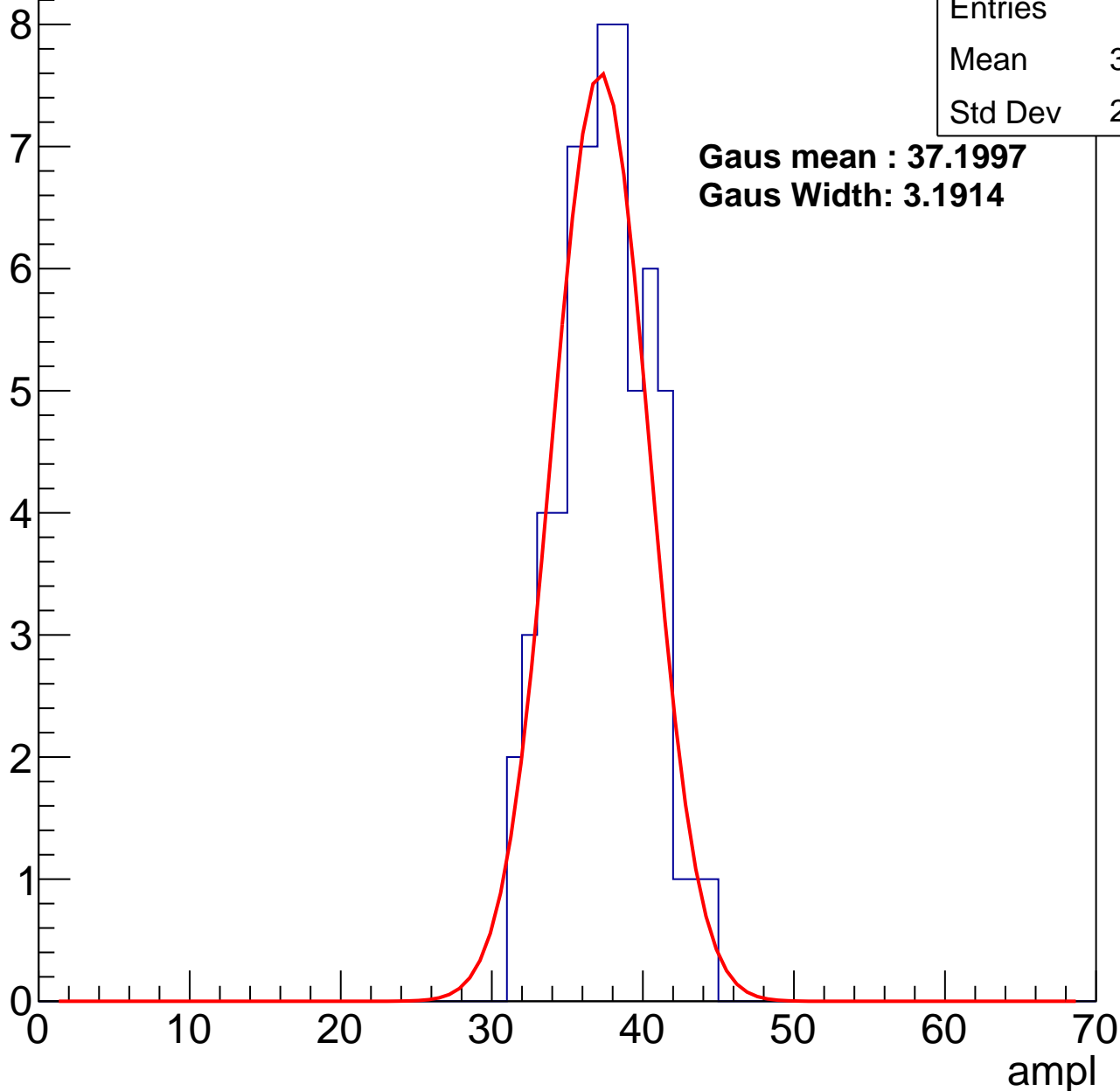
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 36.97 |
| Std Dev | 2.989 |

**Gaus mean : 37.1997**

**Gaus Width: 3.1914**



# B0L002S, U1-ch68, adc2

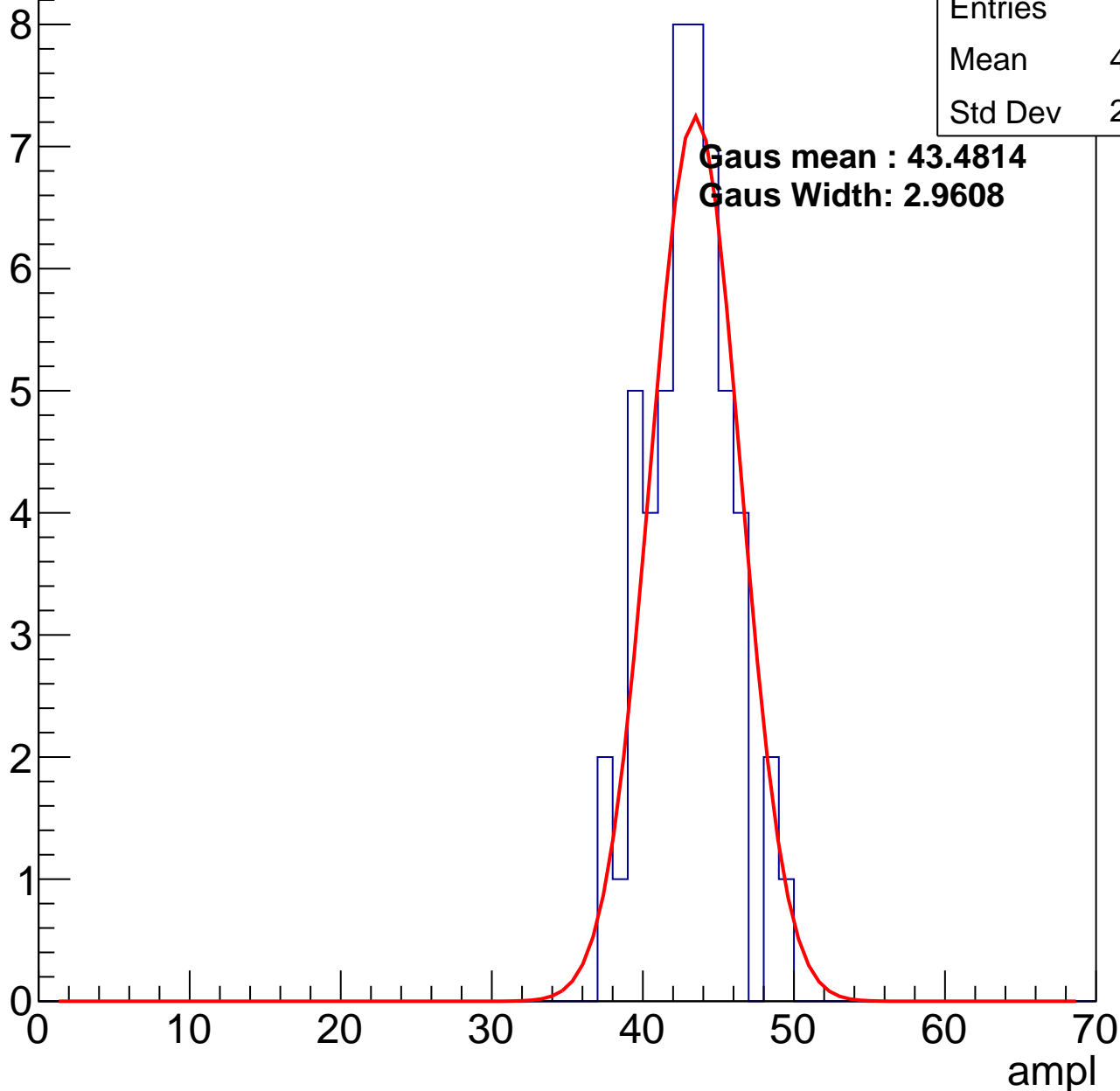
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 42.58 |
| Std Dev | 2.706 |

**Gaus mean : 43.4814**

**Gaus Width: 2.9608**



# B0L002S, U1-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

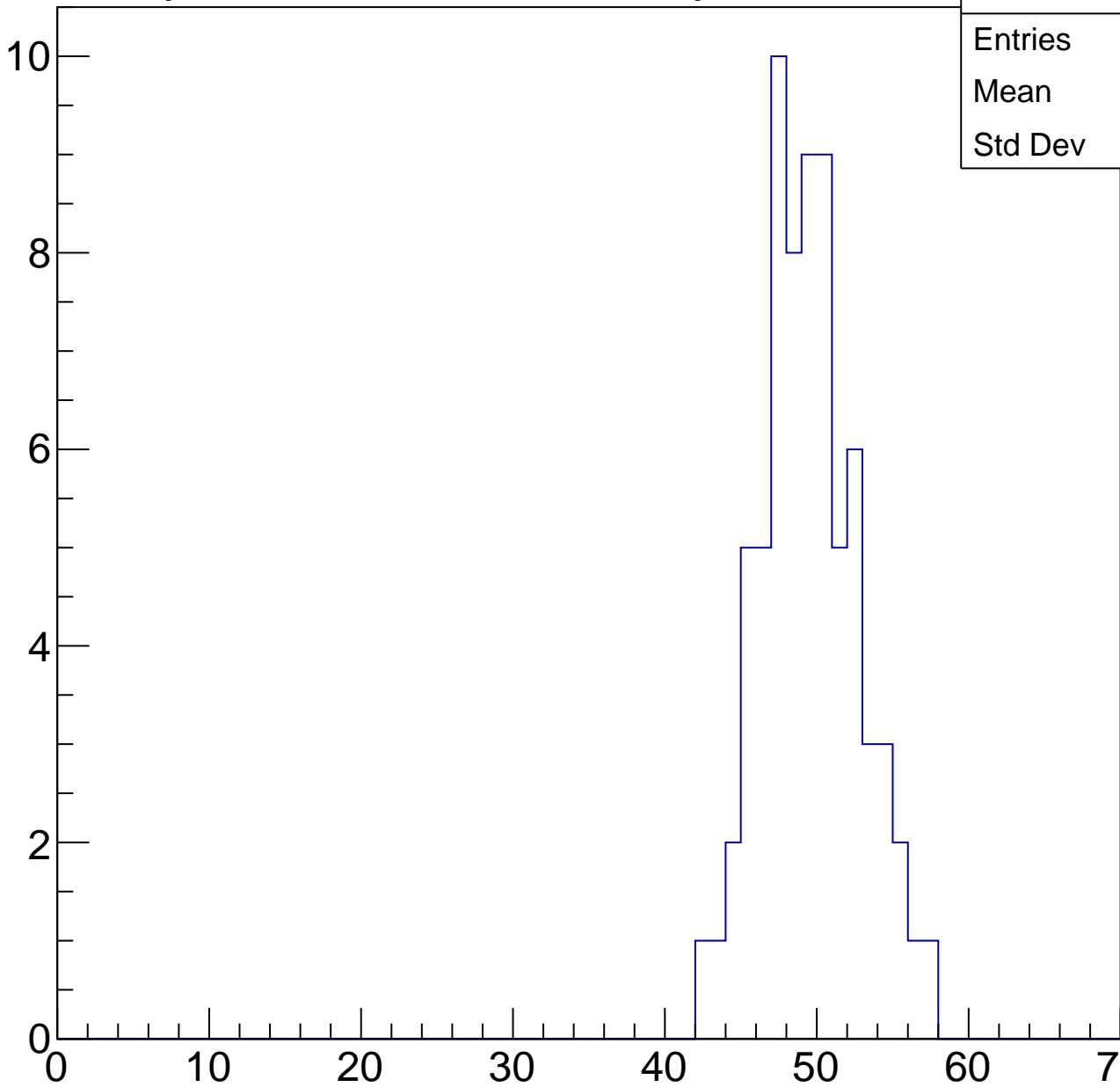
|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 49.07 |
| Std Dev | 3.137 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

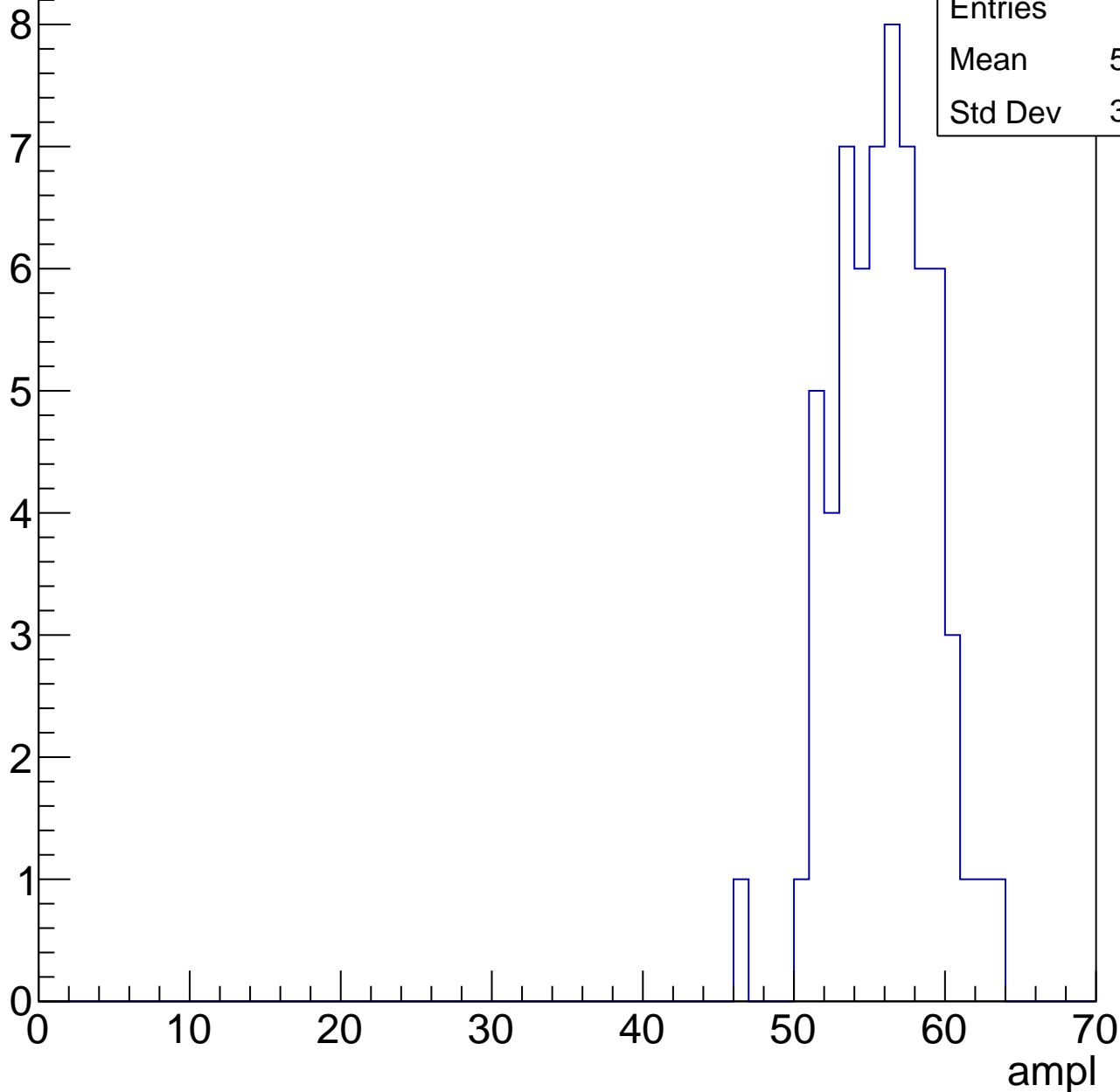


# B0L002S, U1-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

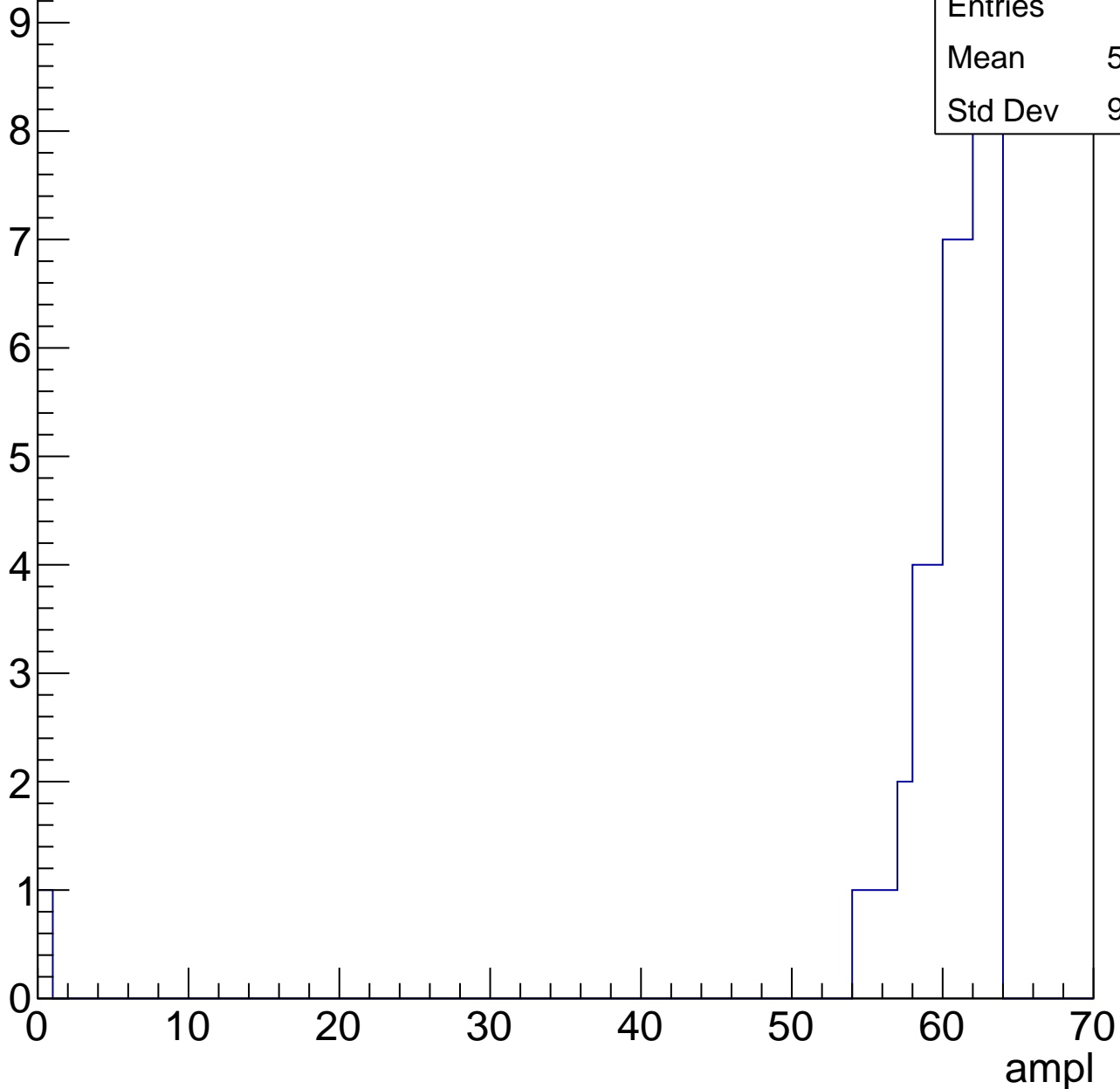
|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 55.53 |
| Std Dev | 3.182 |



# B0L002S, U1-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

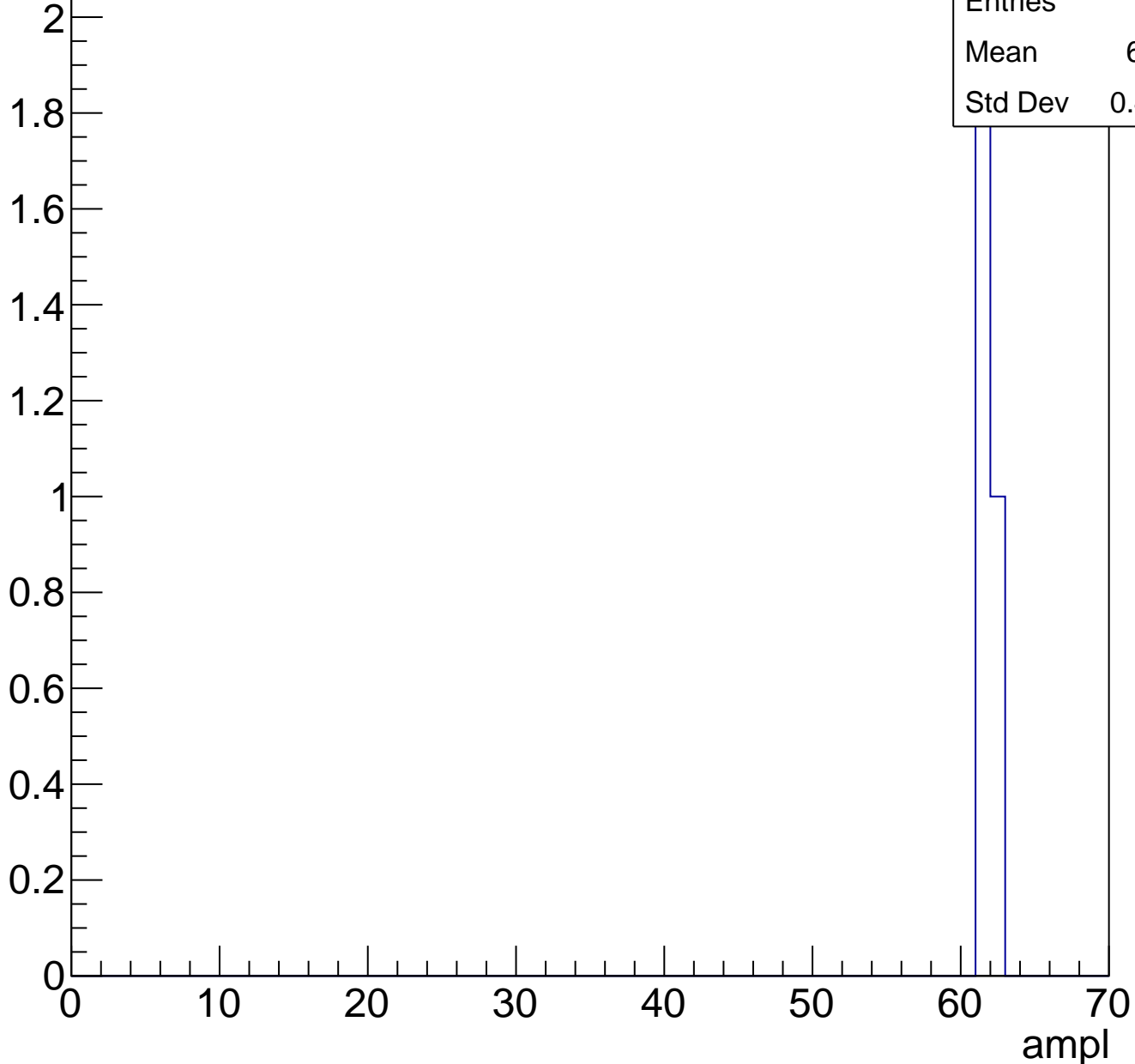
Entry



# B0L002S, U1-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch69, adc0

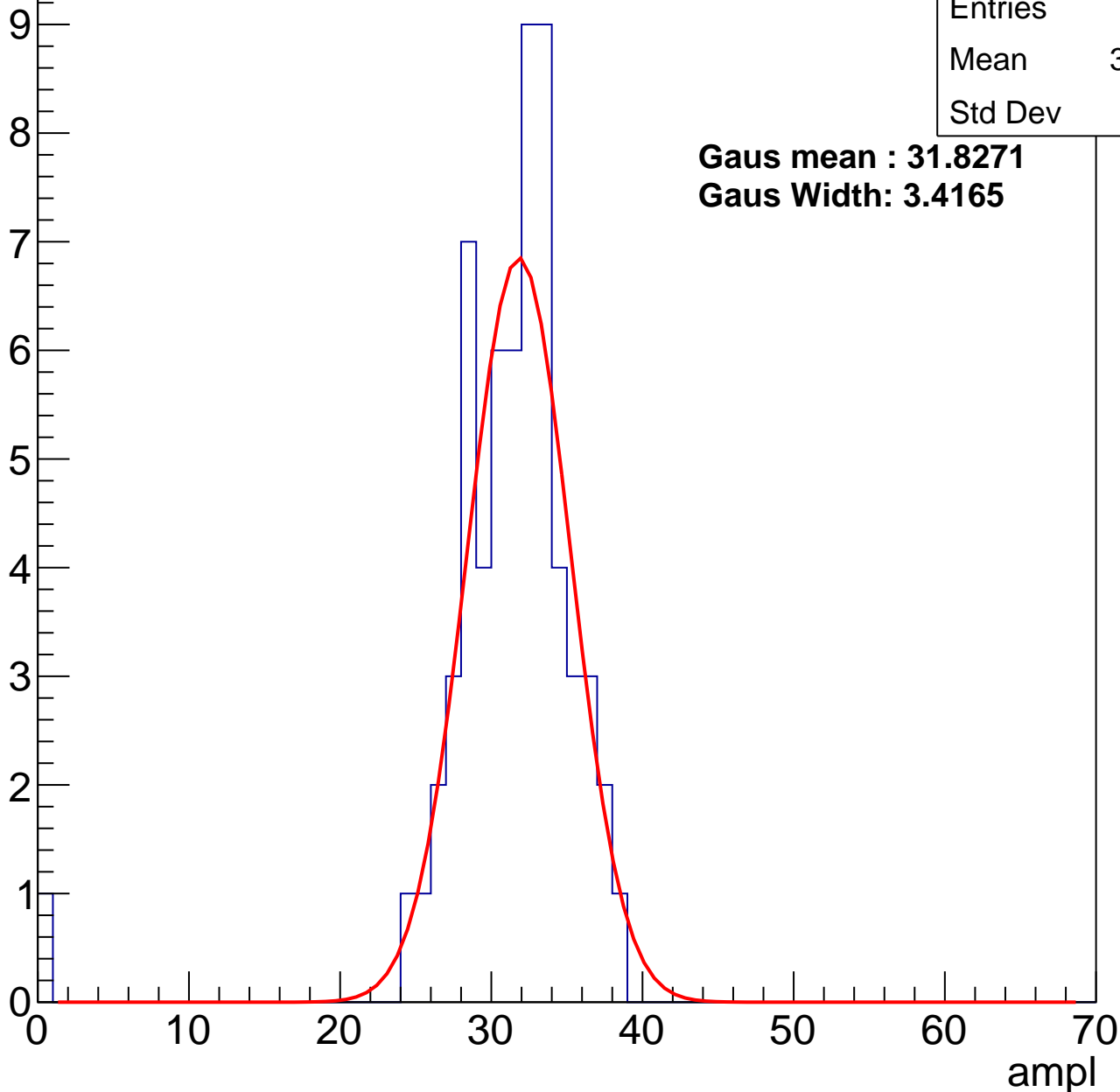
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 30.74 |
| Std Dev | 5     |

**Gaus mean : 31.8271**

**Gaus Width: 3.4165**



# B0L002S, U1-ch69, adc1

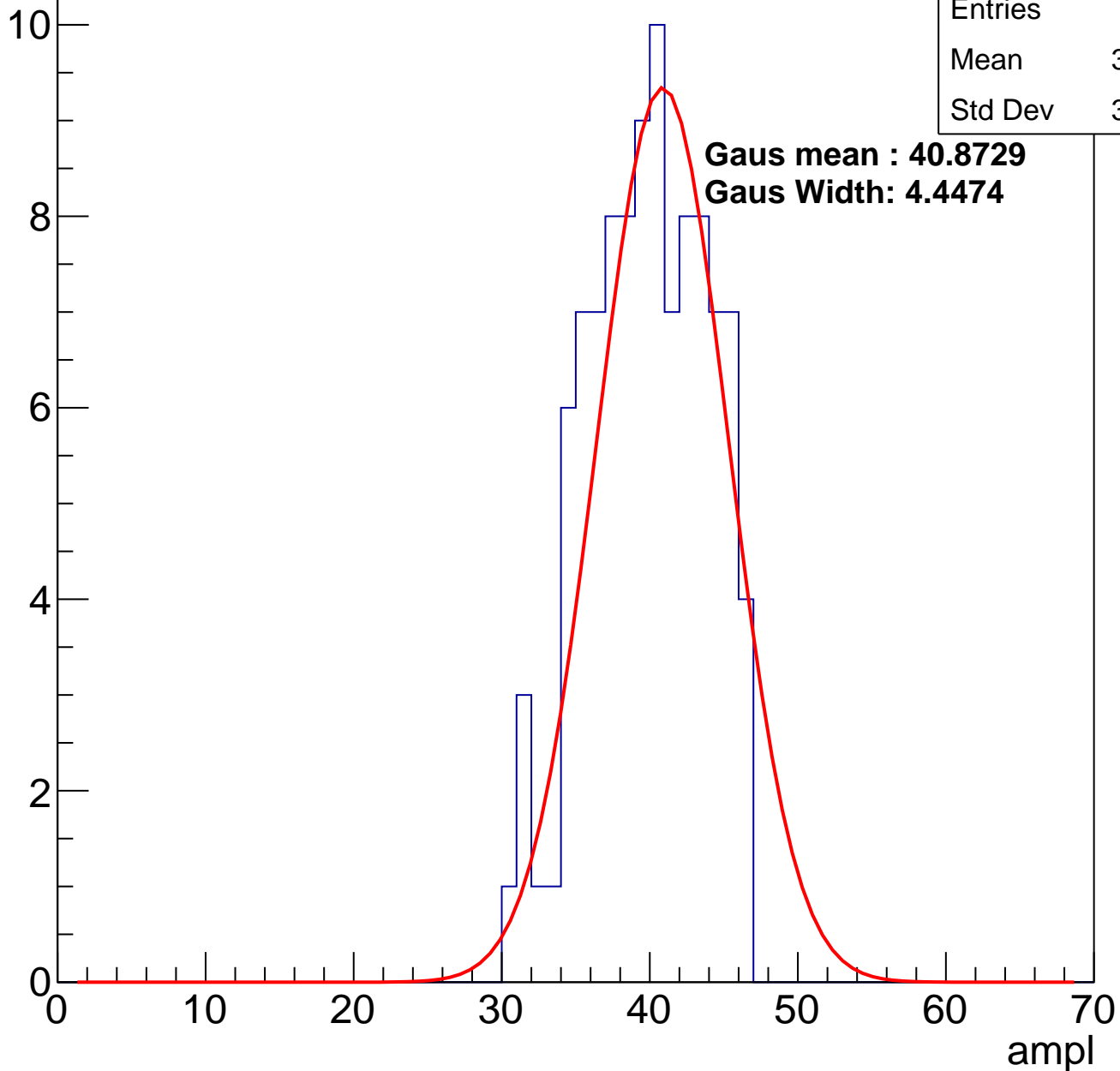
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 102   |
| Mean    | 39.35 |
| Std Dev | 3.925 |

**Gaus mean : 40.8729**

**Gaus Width: 4.4474**

Entry



# B0L002S, U1-ch69, adc2

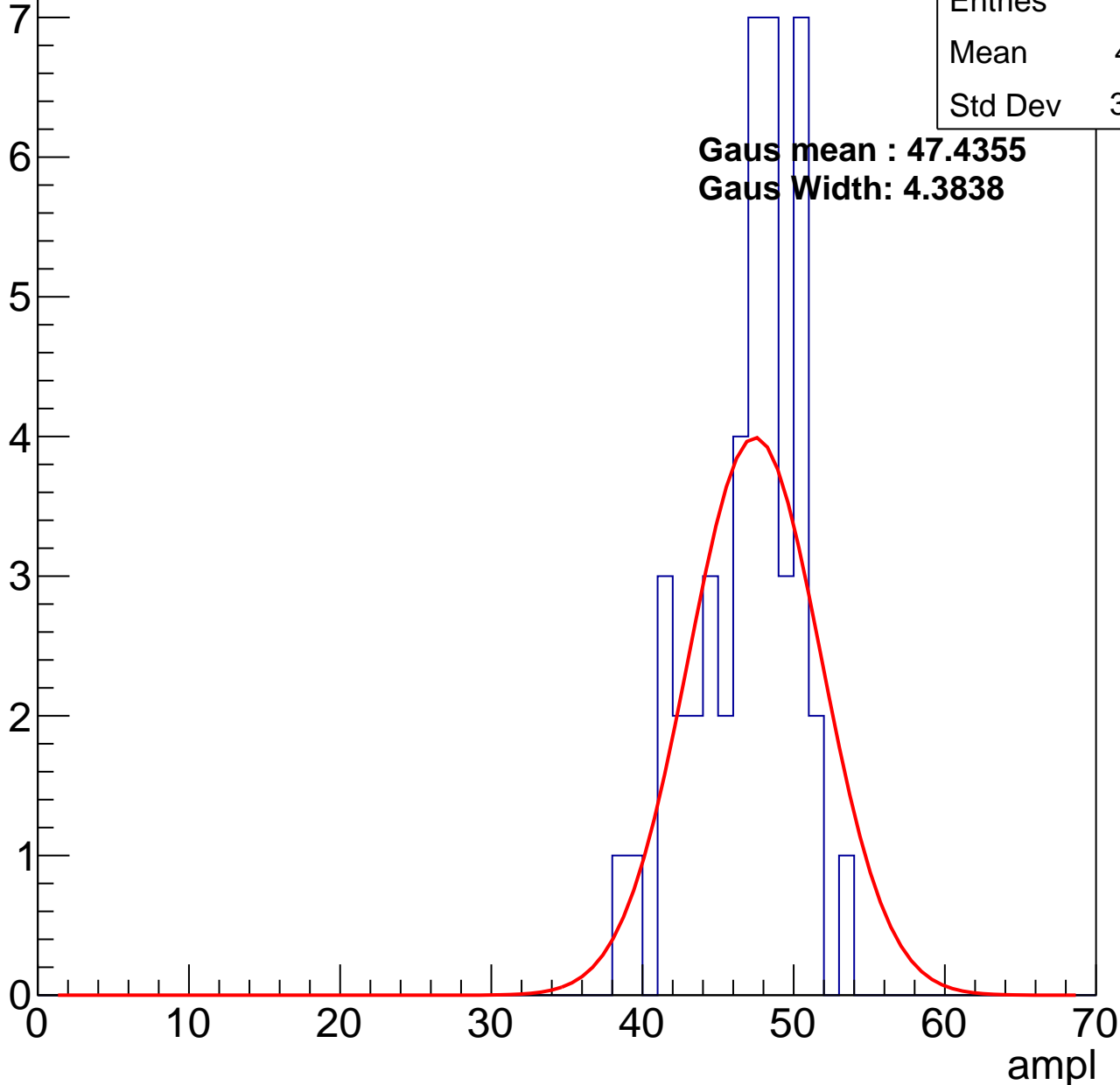
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 46.51 |
| Std Dev | 3.377 |

**Gaus mean : 47.4355**

**Gaus Width: 4.3838**

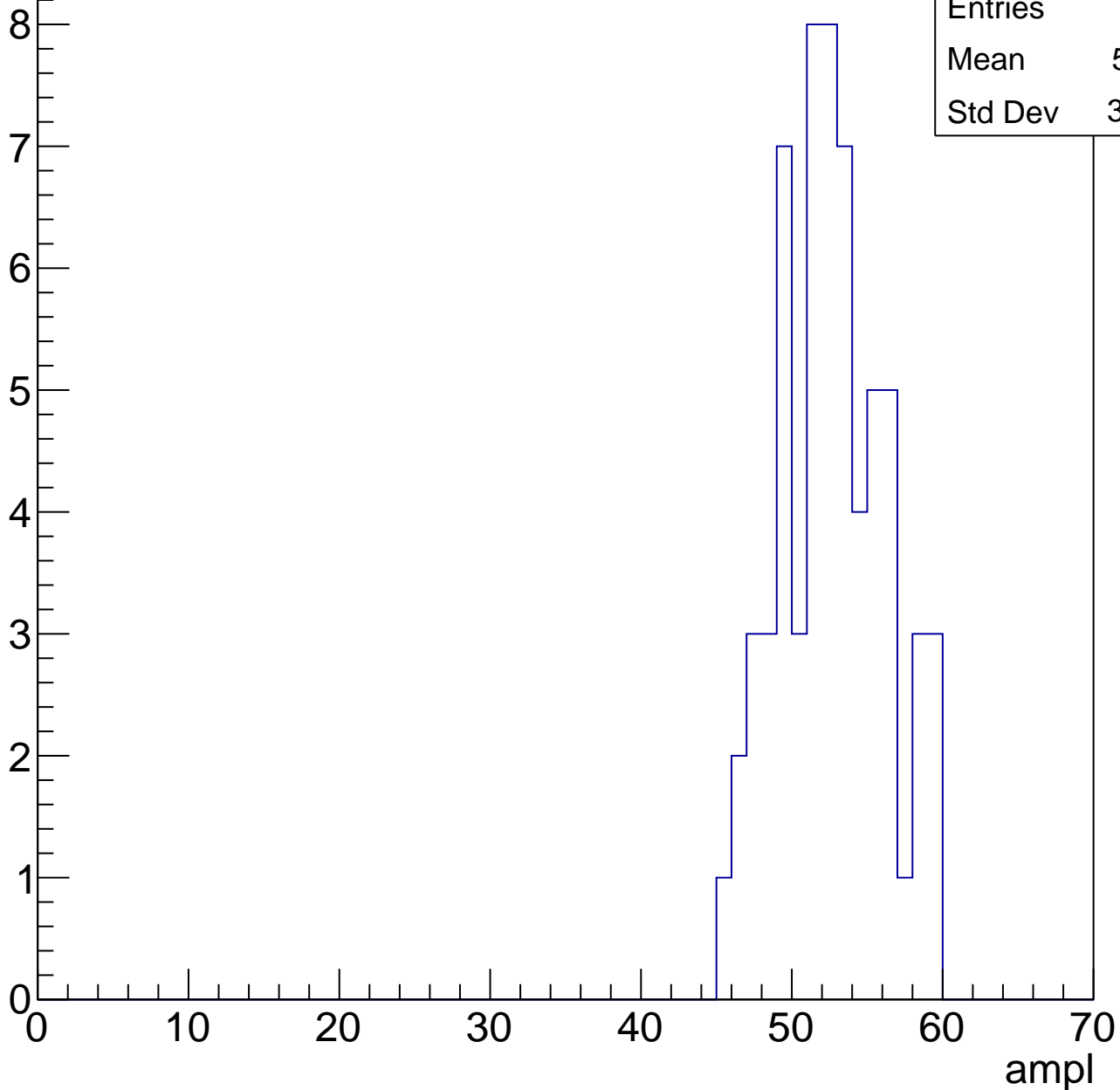


# B0L002S, U1-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 52.21 |
| Std Dev | 3.456 |

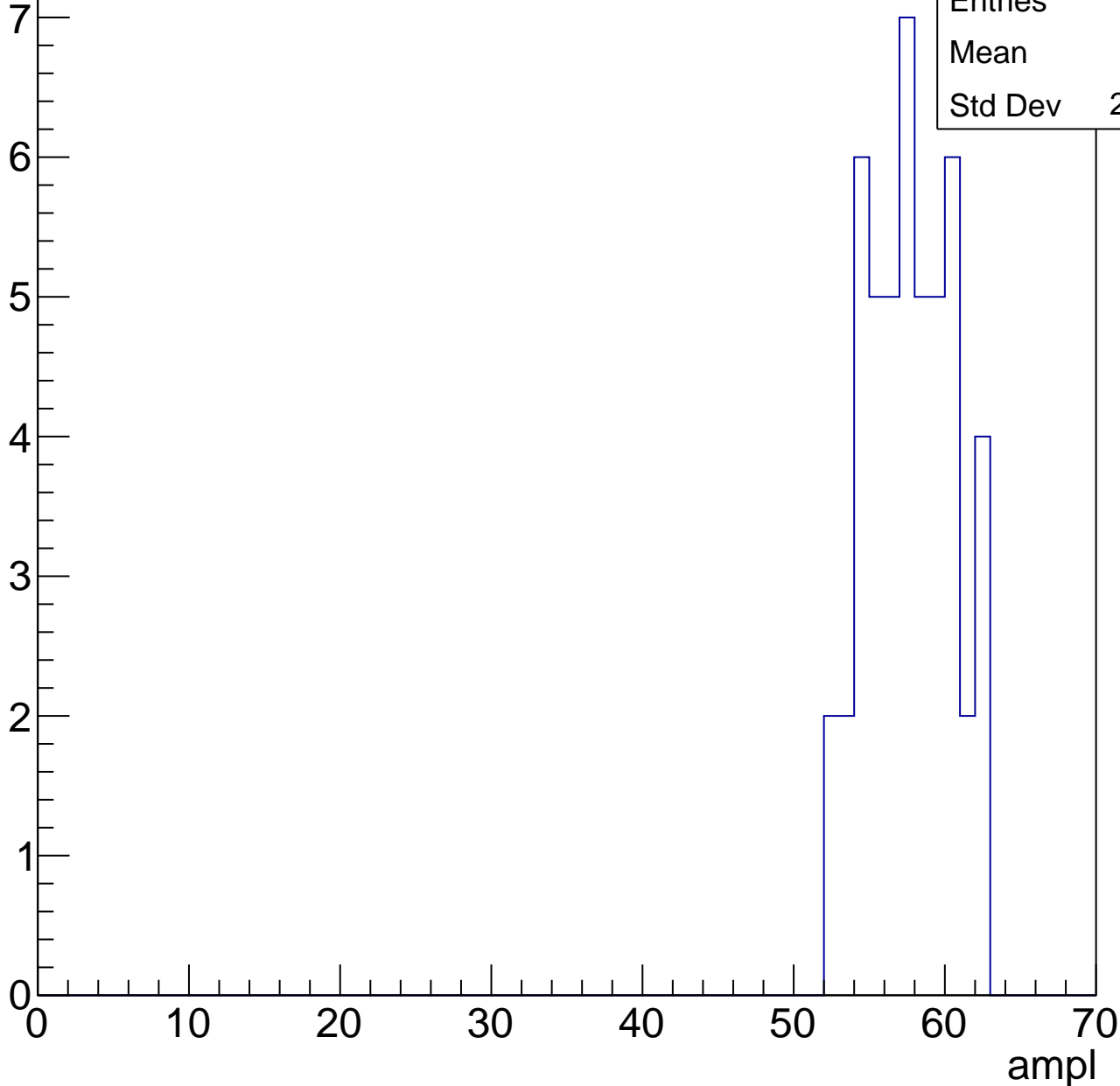


# B0L002S, U1-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

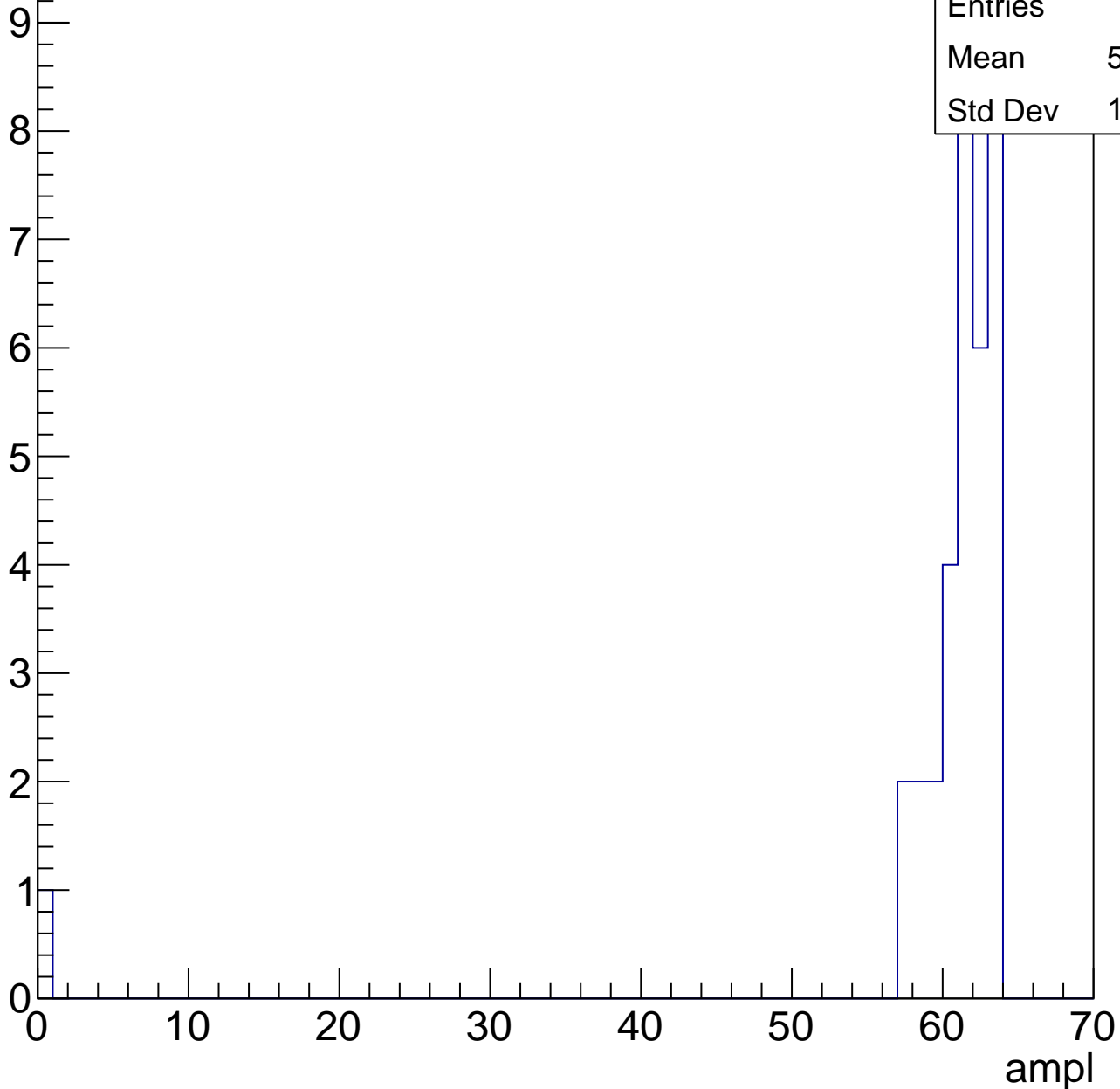
|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 57.2  |
| Std Dev | 2.748 |



# B0L002S, U1-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch70, adc0

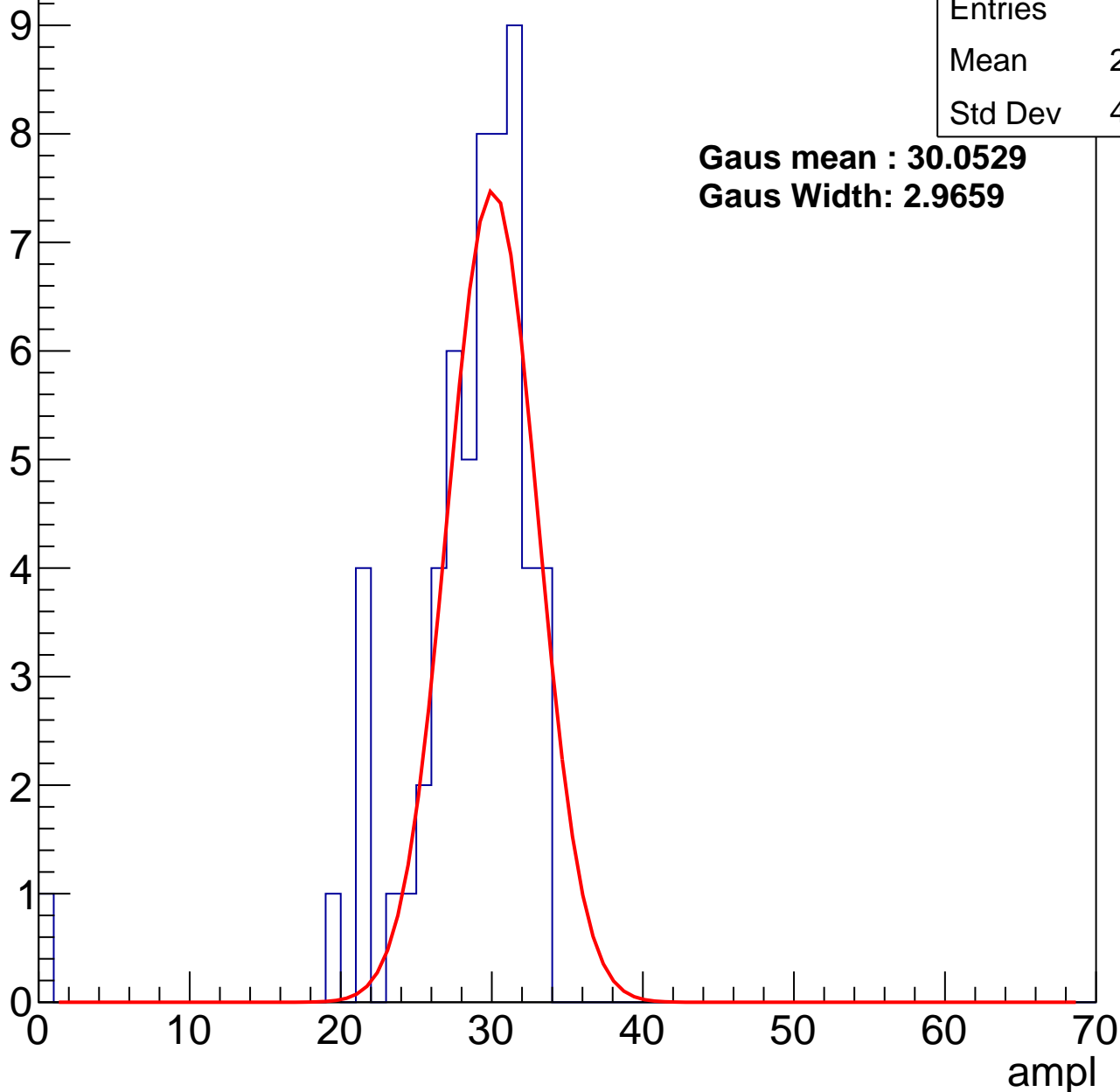
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 27.88 |
| Std Dev | 4.959 |

**Gaus mean : 30.0529**

**Gaus Width: 2.9659**



# B0L002S, U1-ch70, adc1

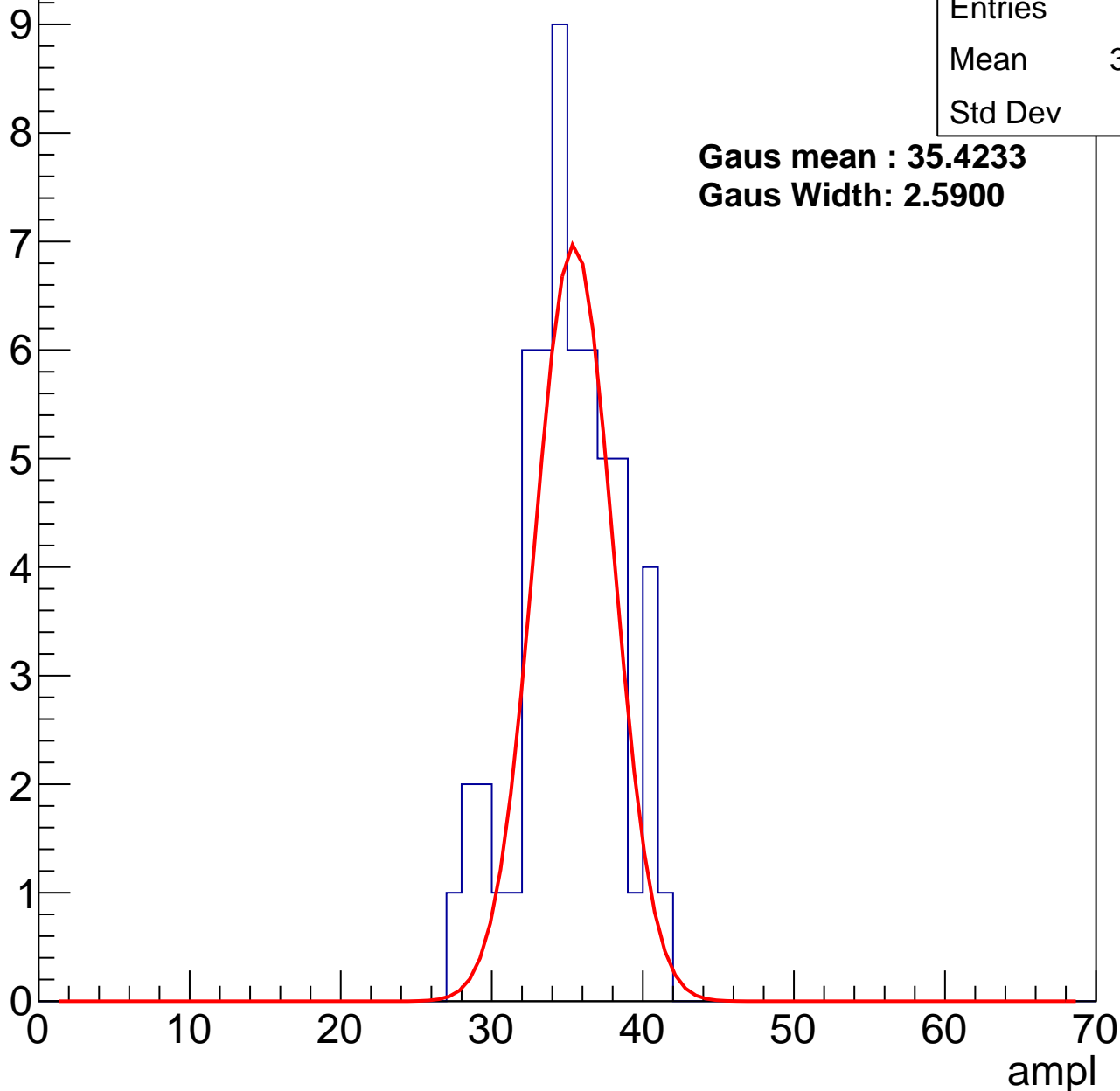
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 34.62 |
| Std Dev | 3.21  |

**Gaus mean : 35.4233**

**Gaus Width: 2.5900**



# B0L002S, U1-ch70, adc2

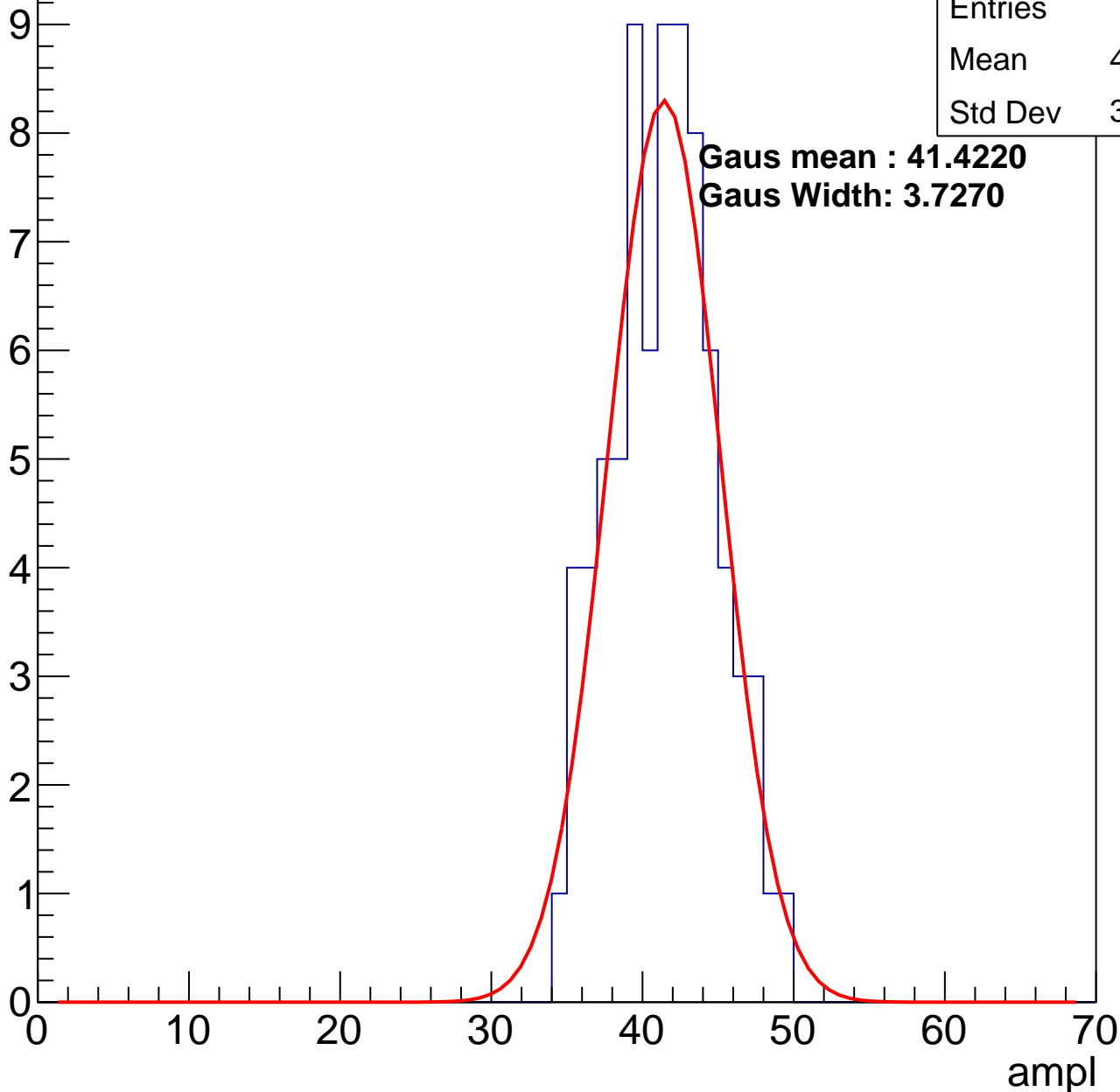
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 40.96 |
| Std Dev | 3.425 |

**Gaus mean : 41.4220**

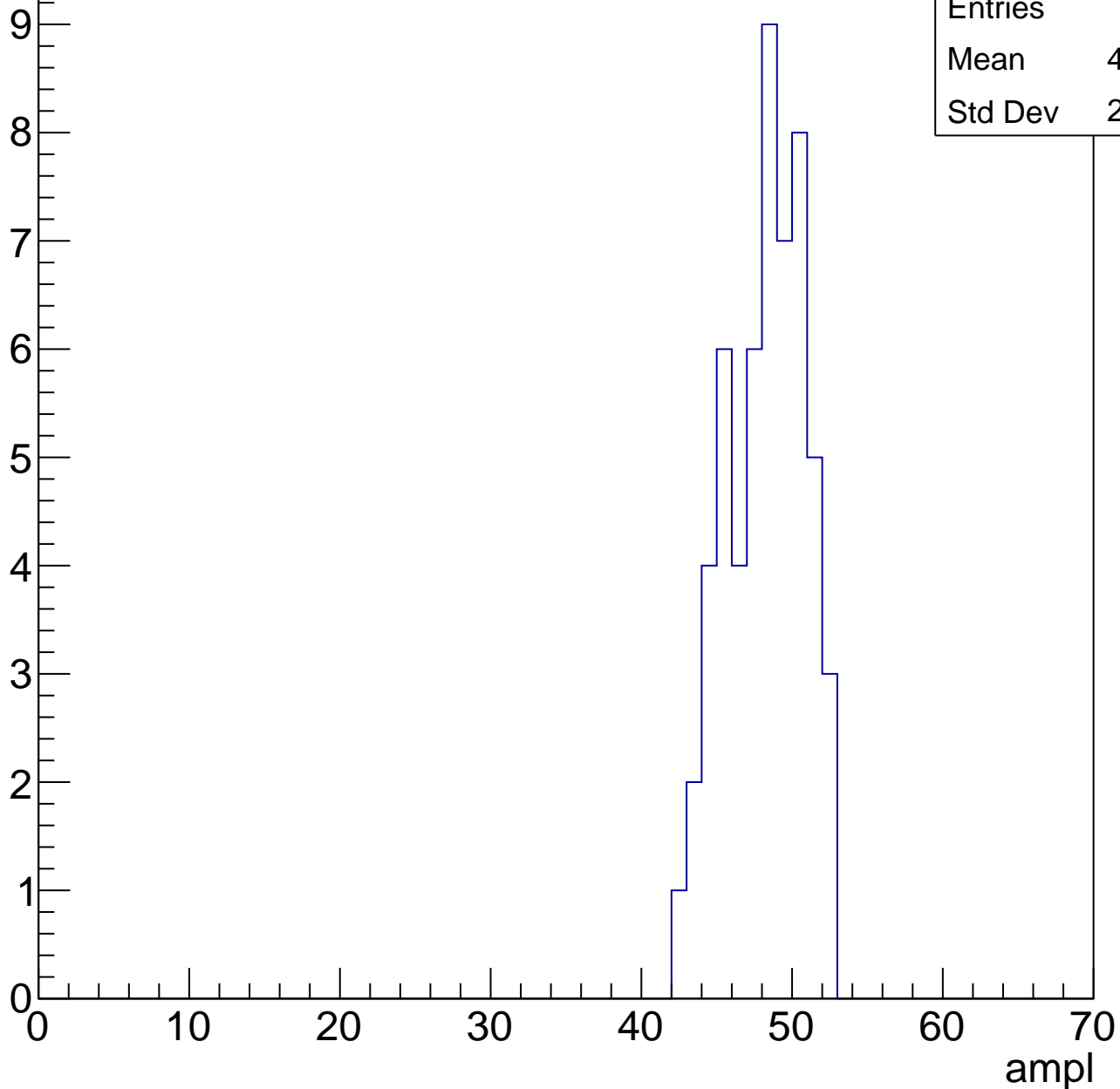
**Gaus Width: 3.7270**



# B0L002S, U1-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

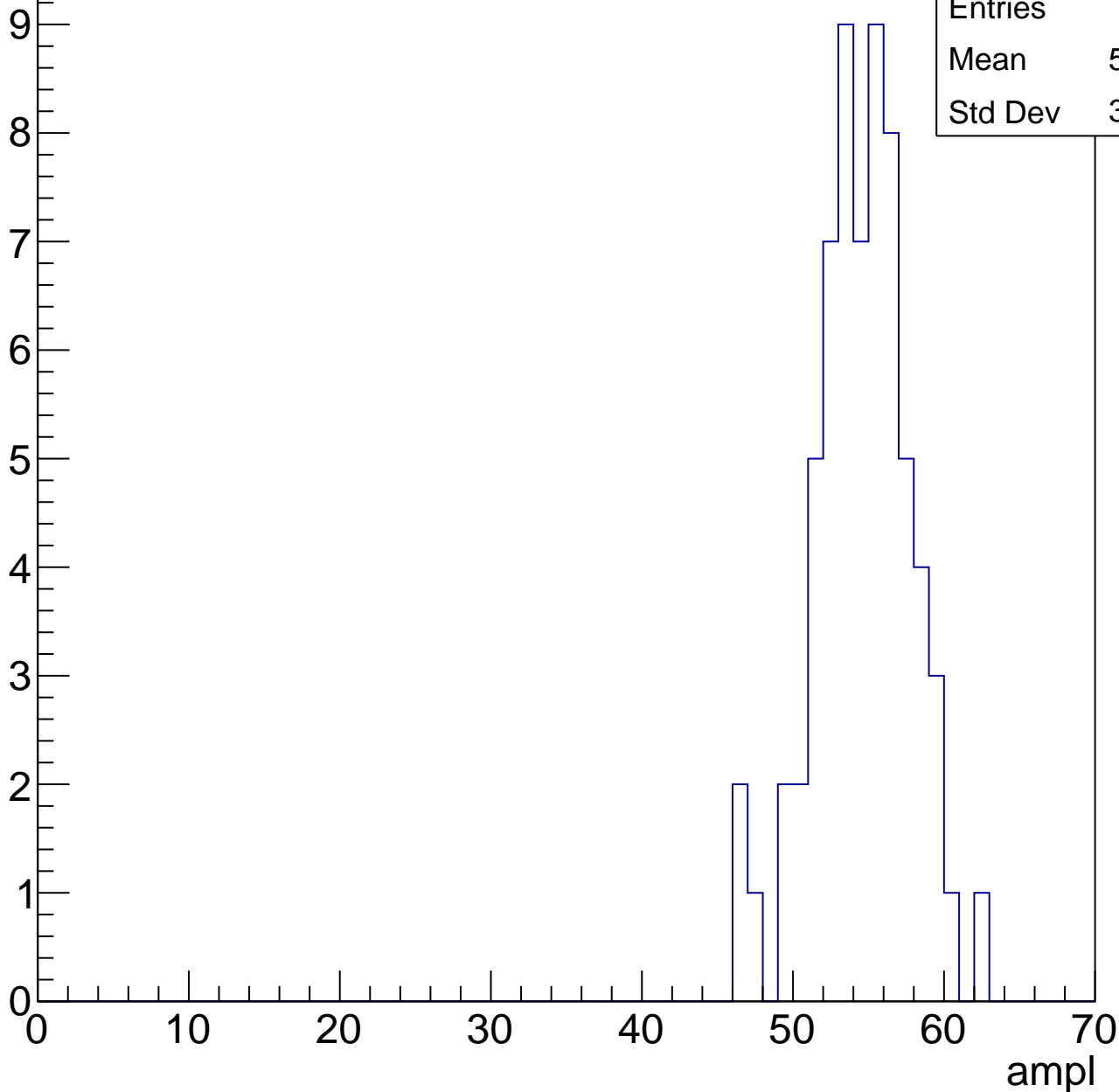


# B0L002S, U1-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

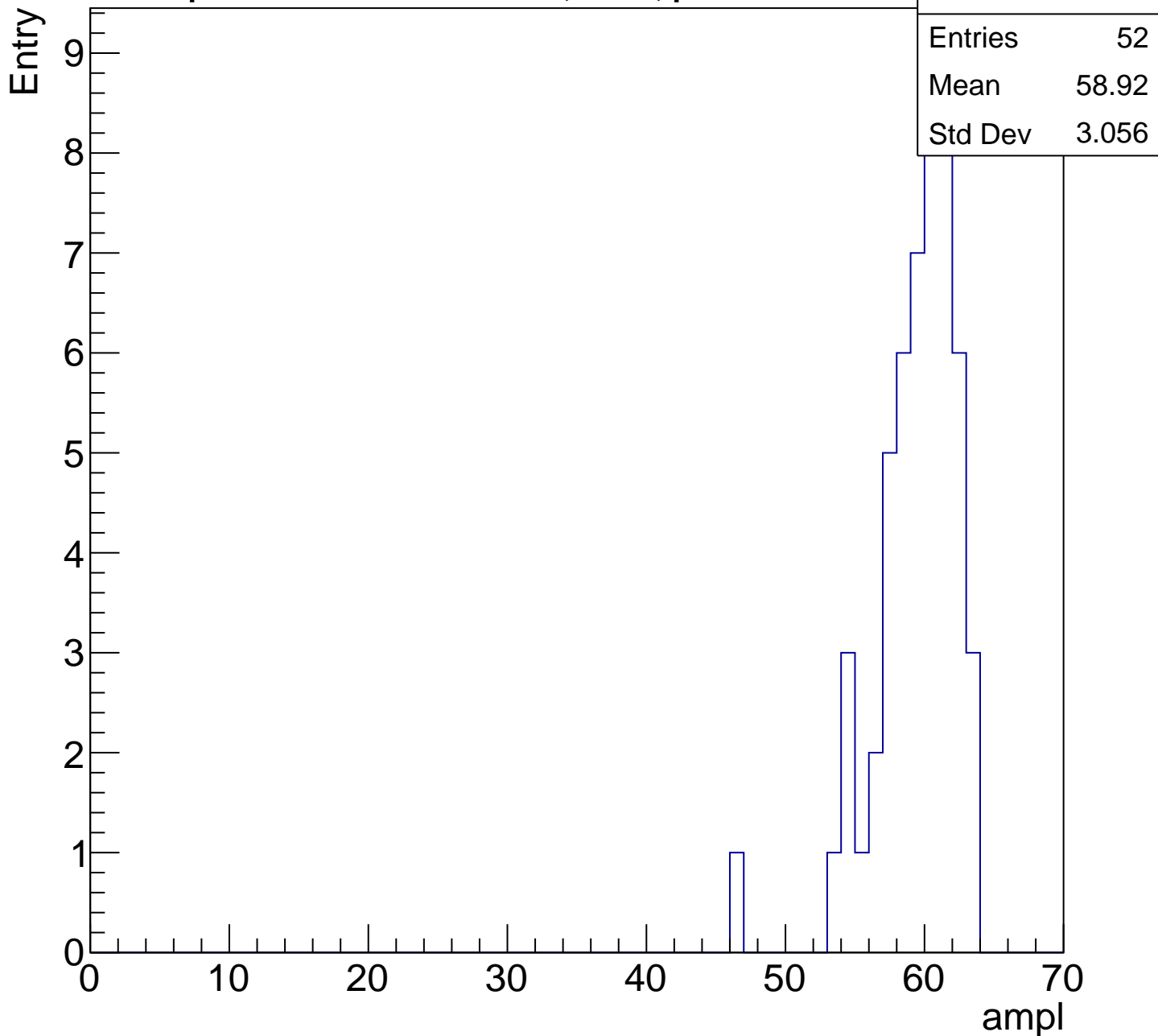
Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 54.09 |
| Std Dev | 3.175 |



# B0L002S, U1-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

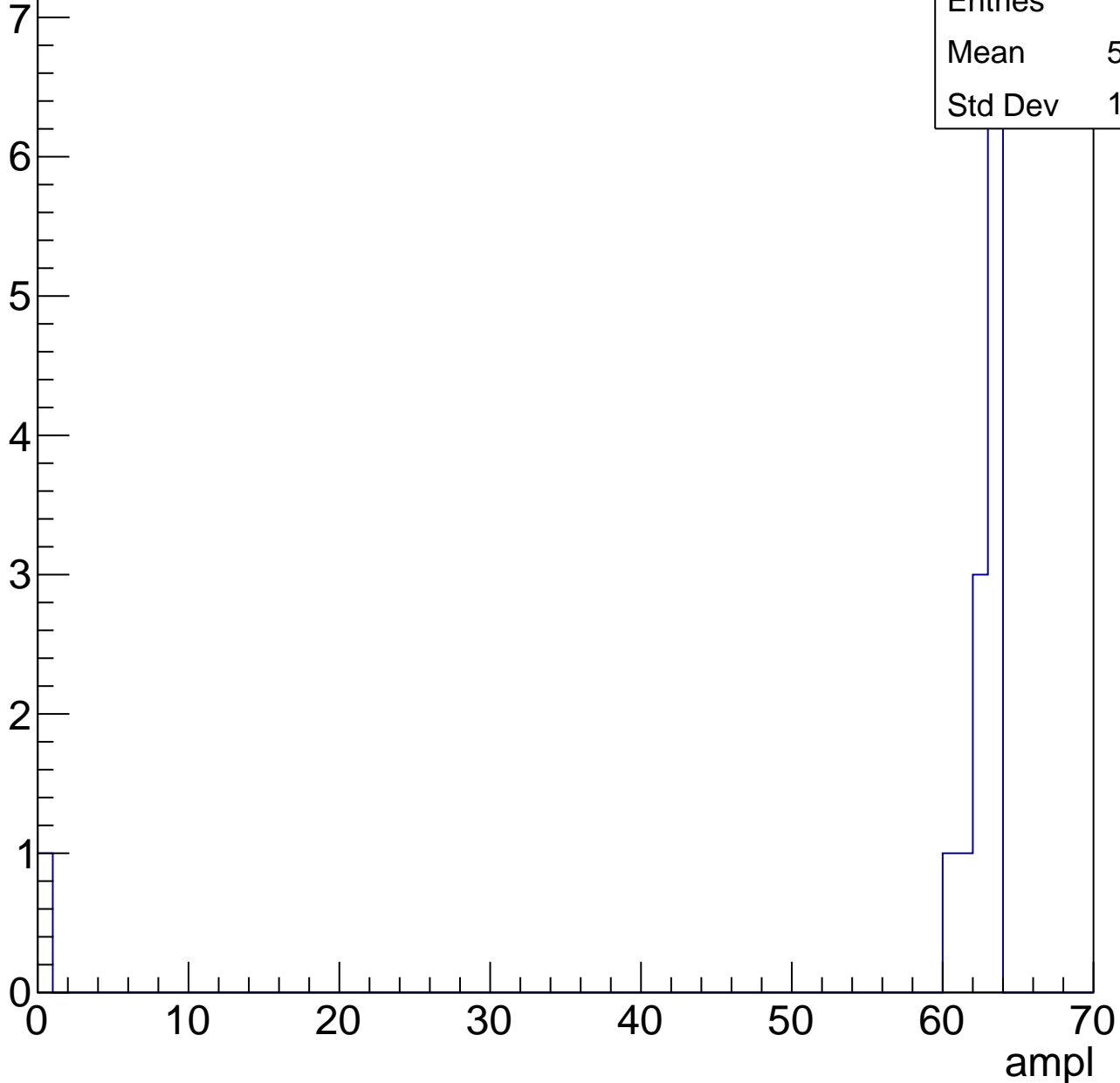


# B0L002S, U1-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 13    |
| Mean    | 57.54 |
| Std Dev | 16.63 |





# B0L002S, U1-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch71, adc0

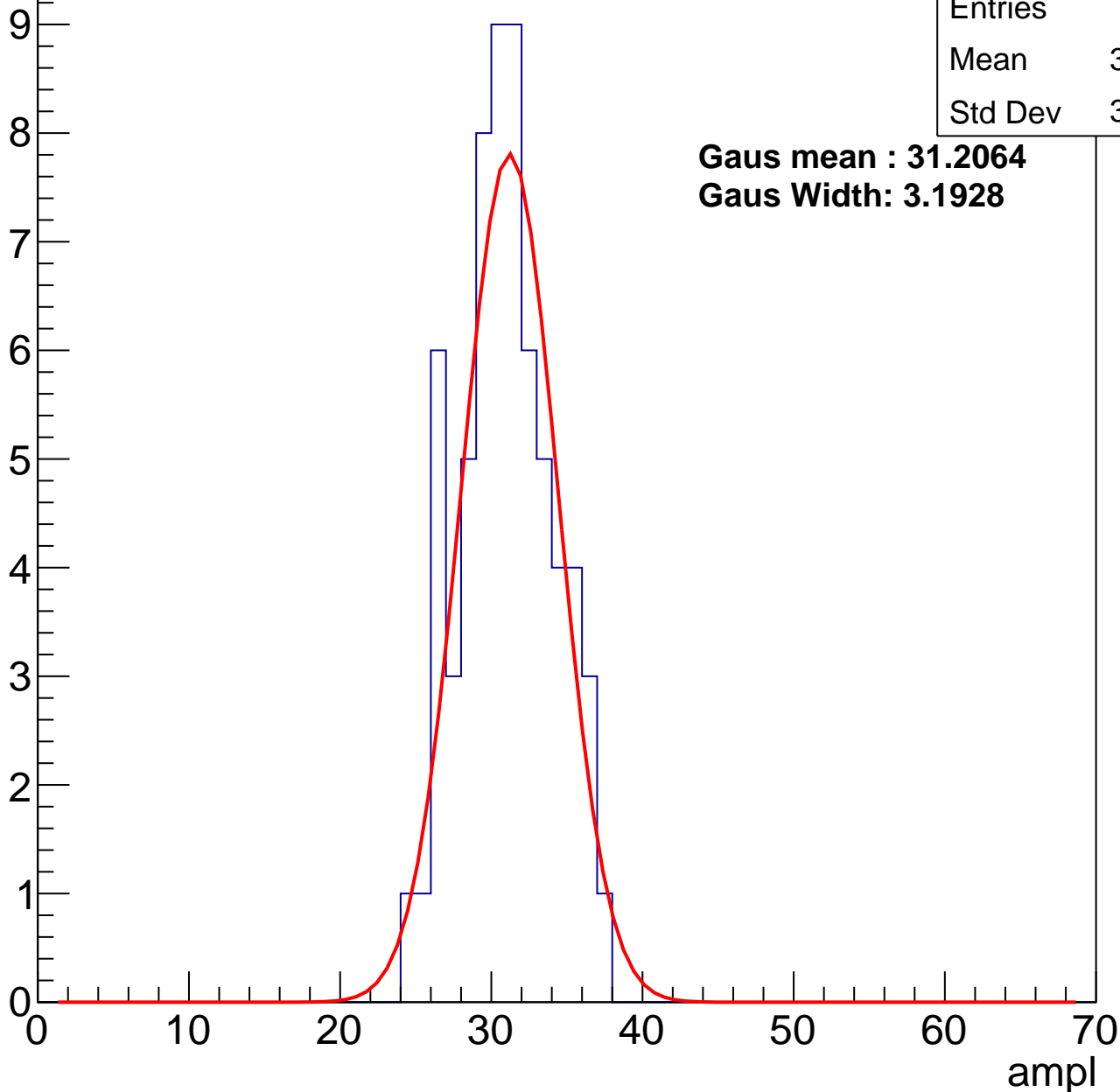
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 30.54 |
| Std Dev | 3.018 |

**Gaus mean : 31.2064**

**Gaus Width: 3.1928**



# B0L002S, U1-ch71, adc1

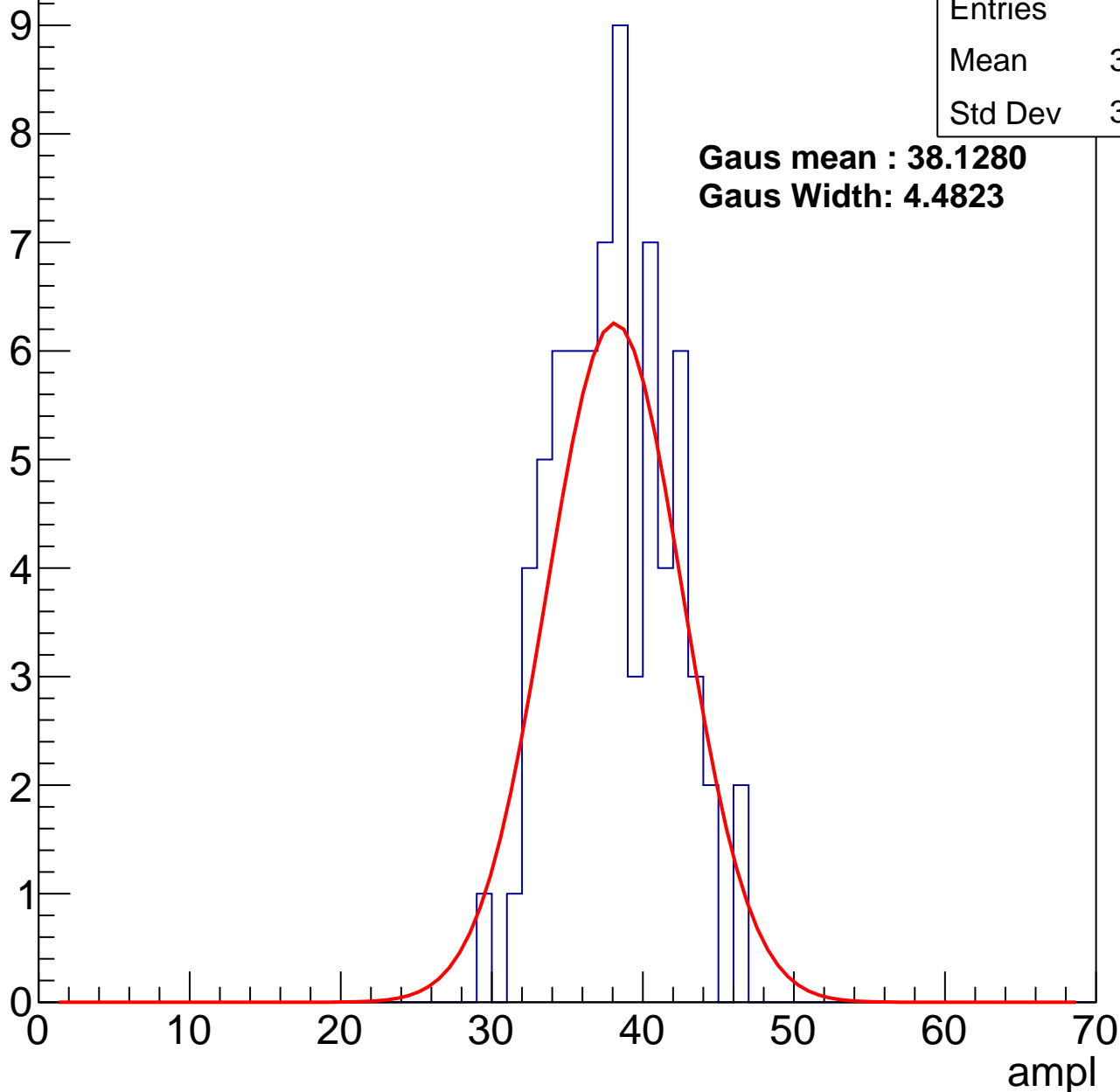
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 37.58 |
| Std Dev | 3.737 |

**Gaus mean : 38.1280**

**Gaus Width: 4.4823**

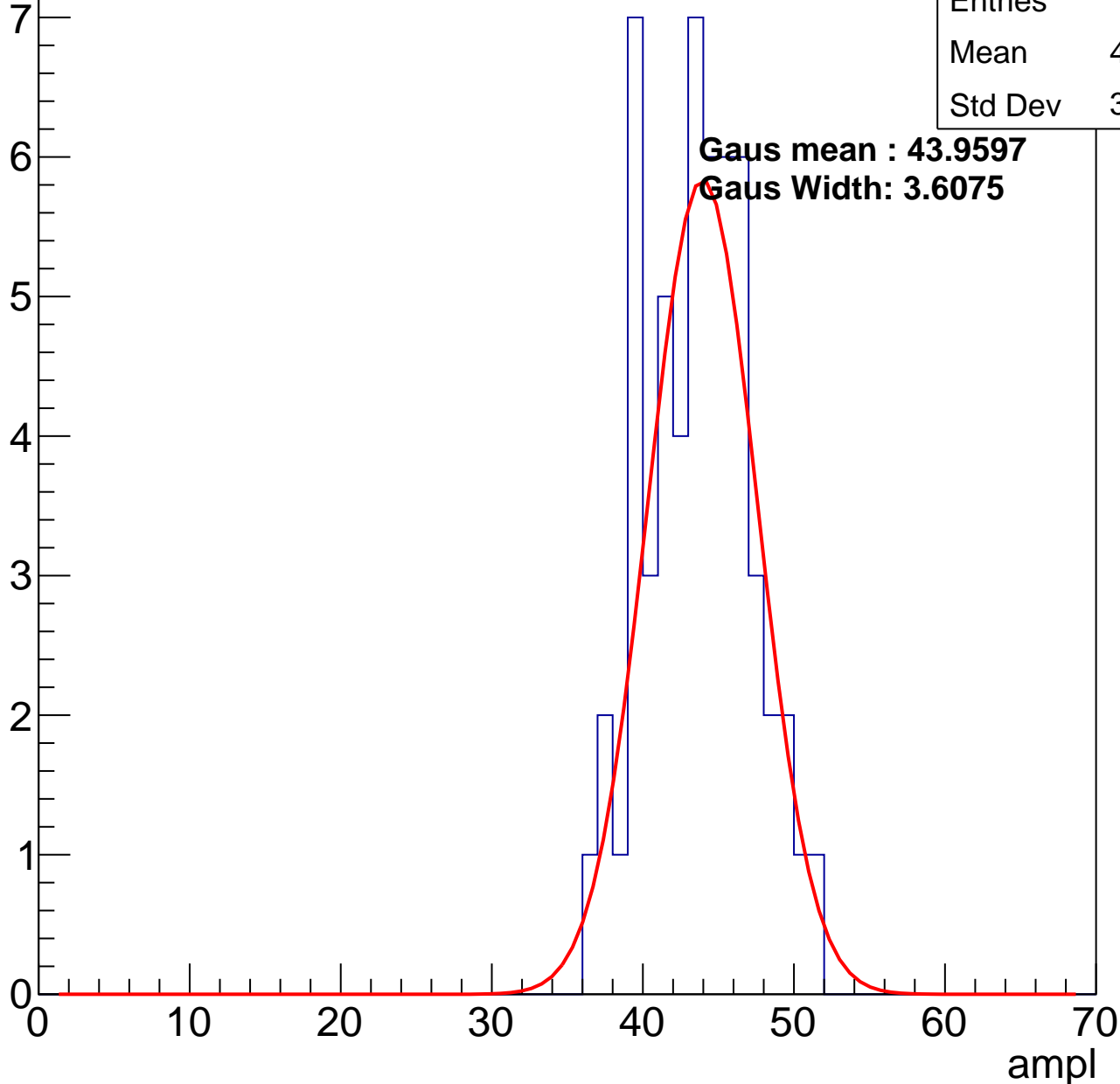


# B0L002S, U1-ch71, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 43.18 |
| Std Dev | 3.439 |

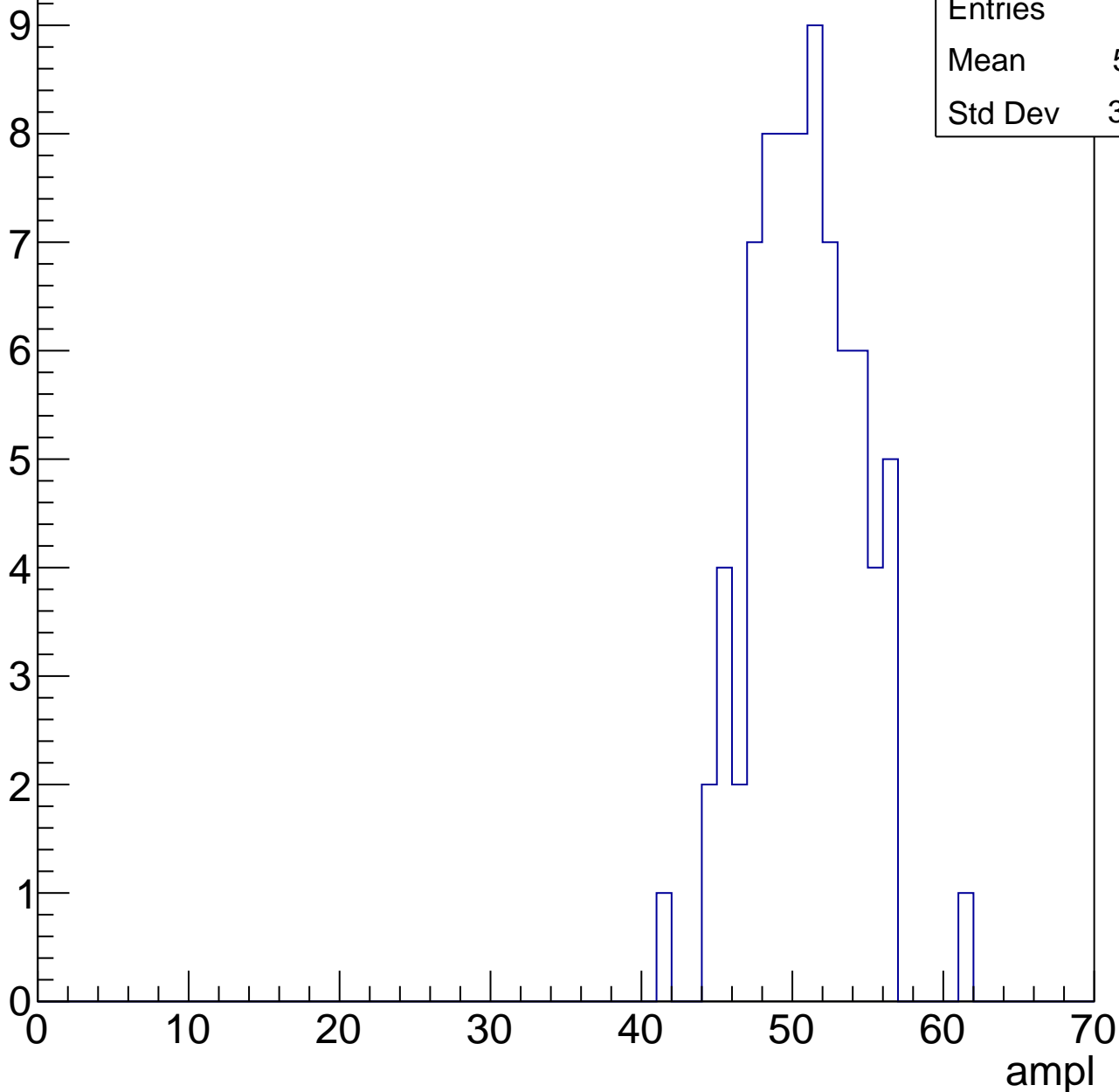


# B0L002S, U1-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

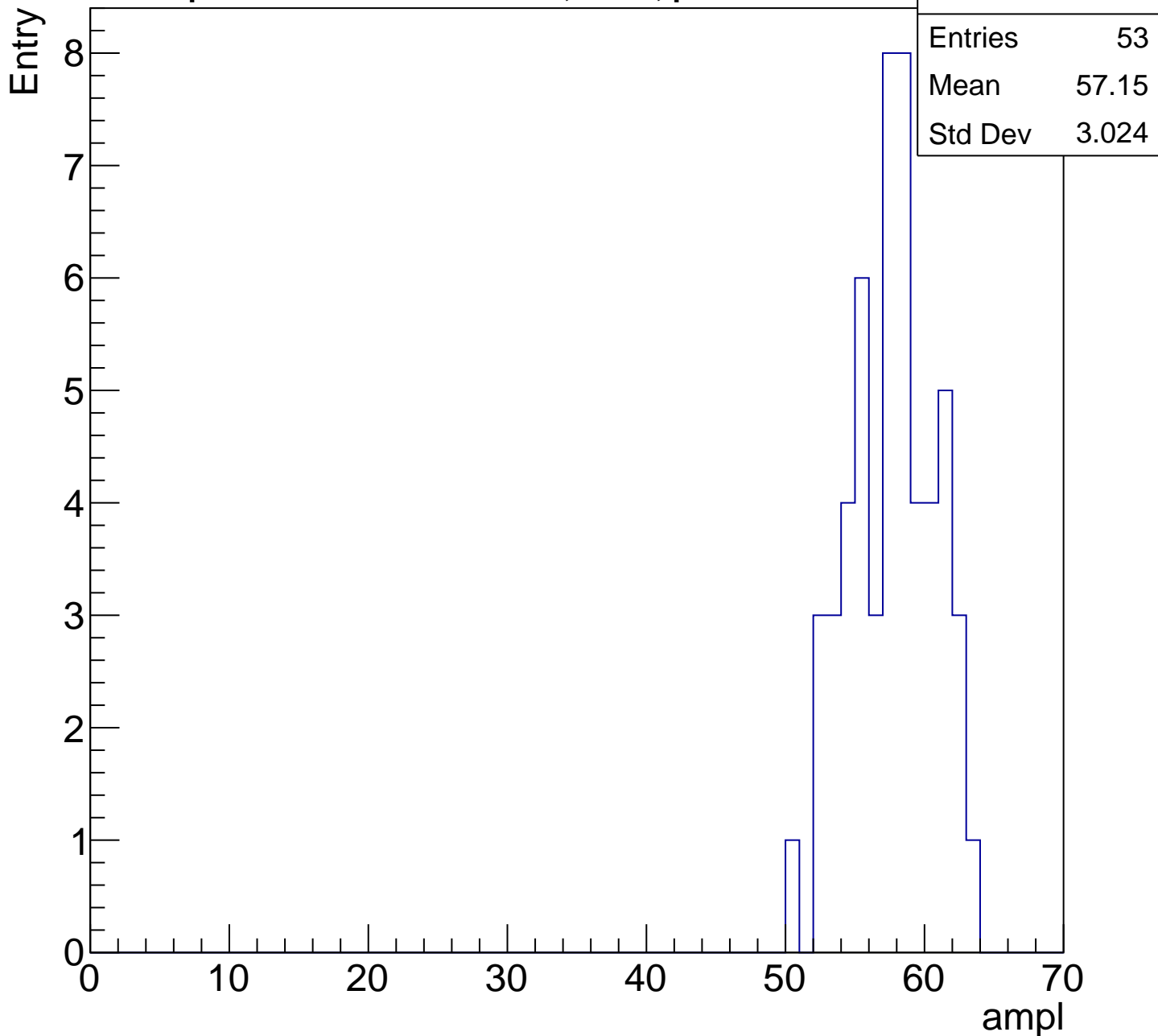
Entry

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 50.41 |
| Std Dev | 3.513 |



# B0L002S, U1-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

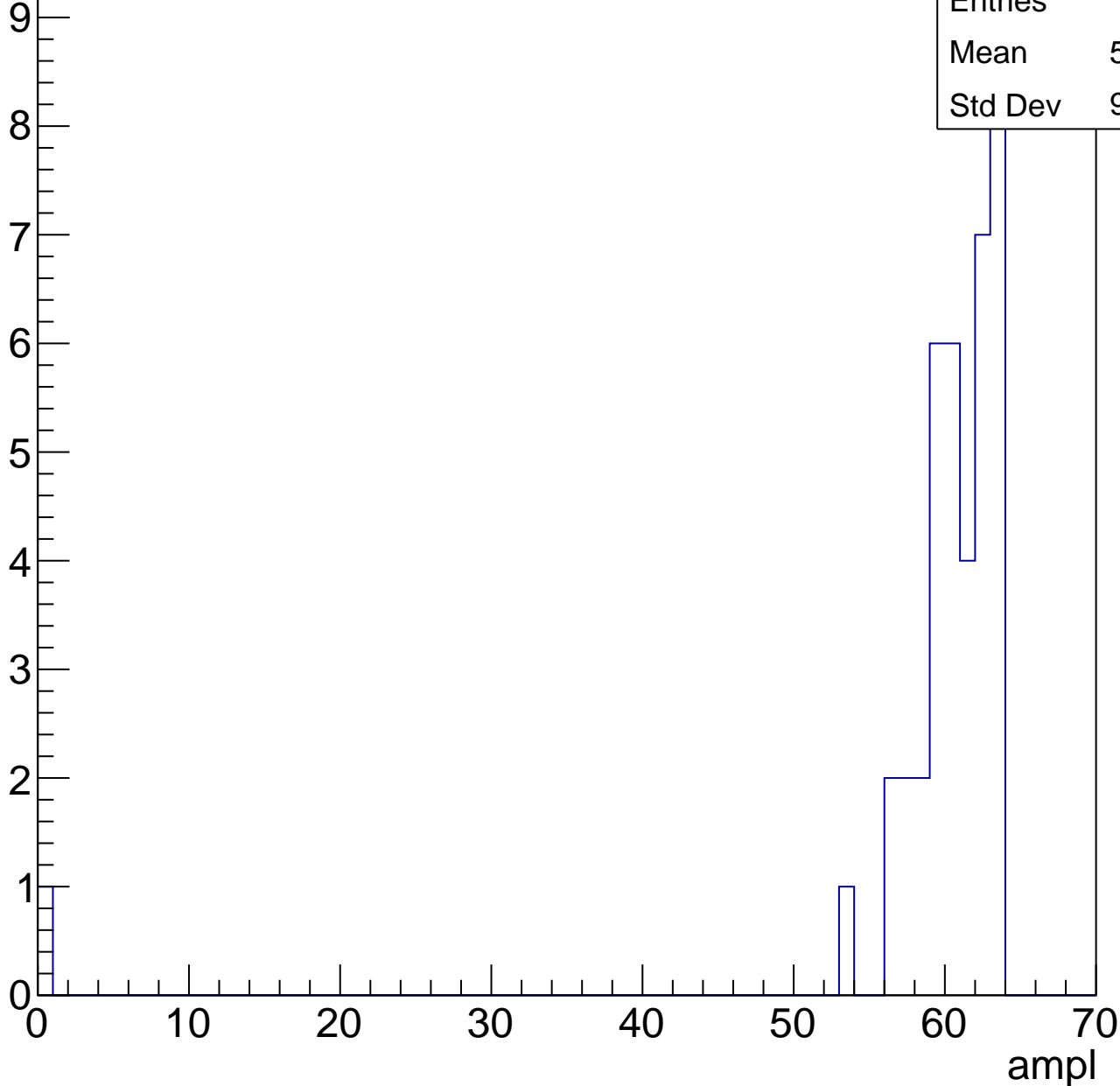


# B0L002S, U1-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 40    |
| Mean    | 58.85 |
| Std Dev | 9.712 |



# B0L002S, U1-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch72, adc0

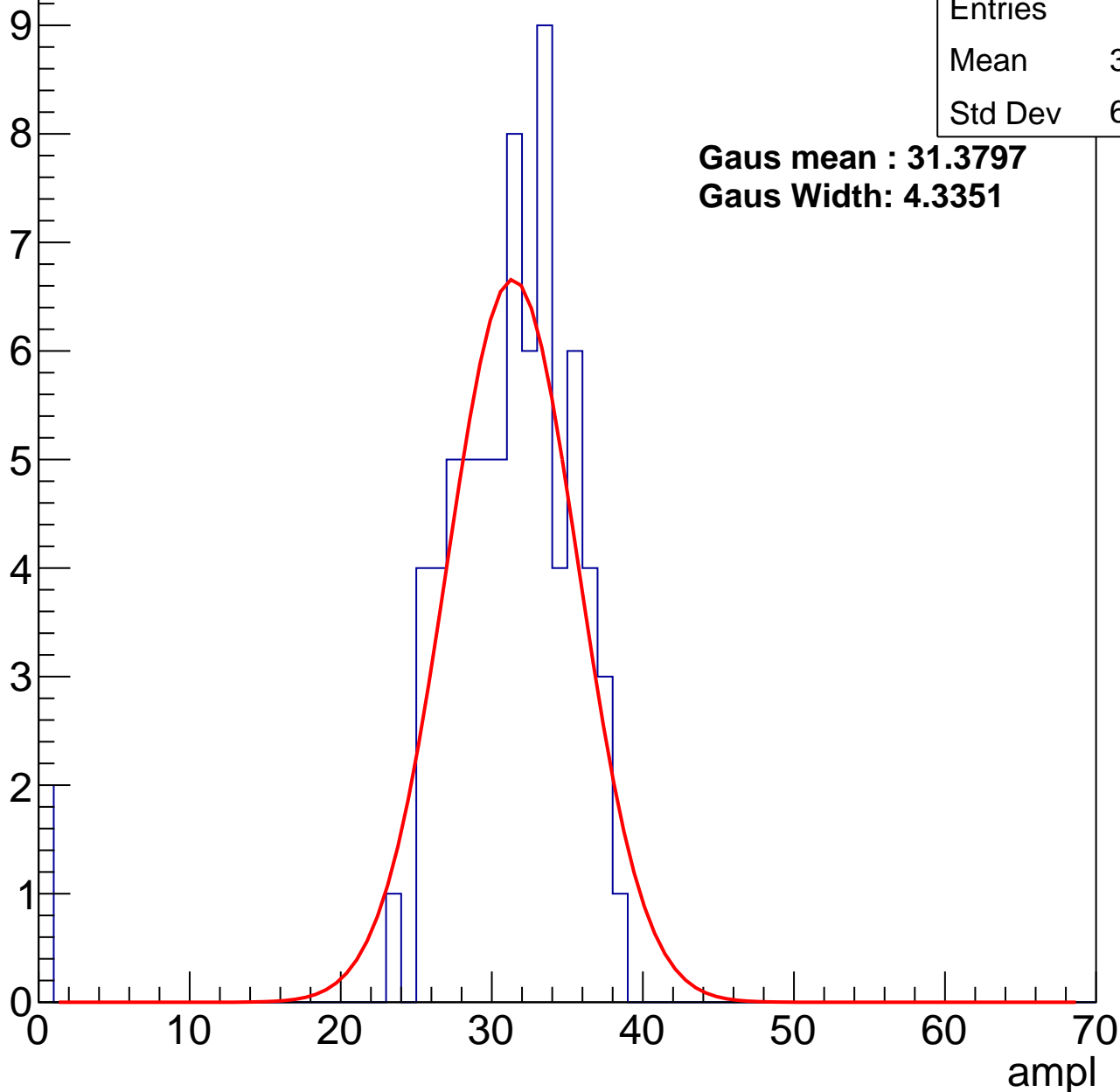
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 30.18 |
| Std Dev | 6.194 |

**Gaus mean : 31.3797**

**Gaus Width: 4.3351**



# B0L002S, U1-ch72, adc1

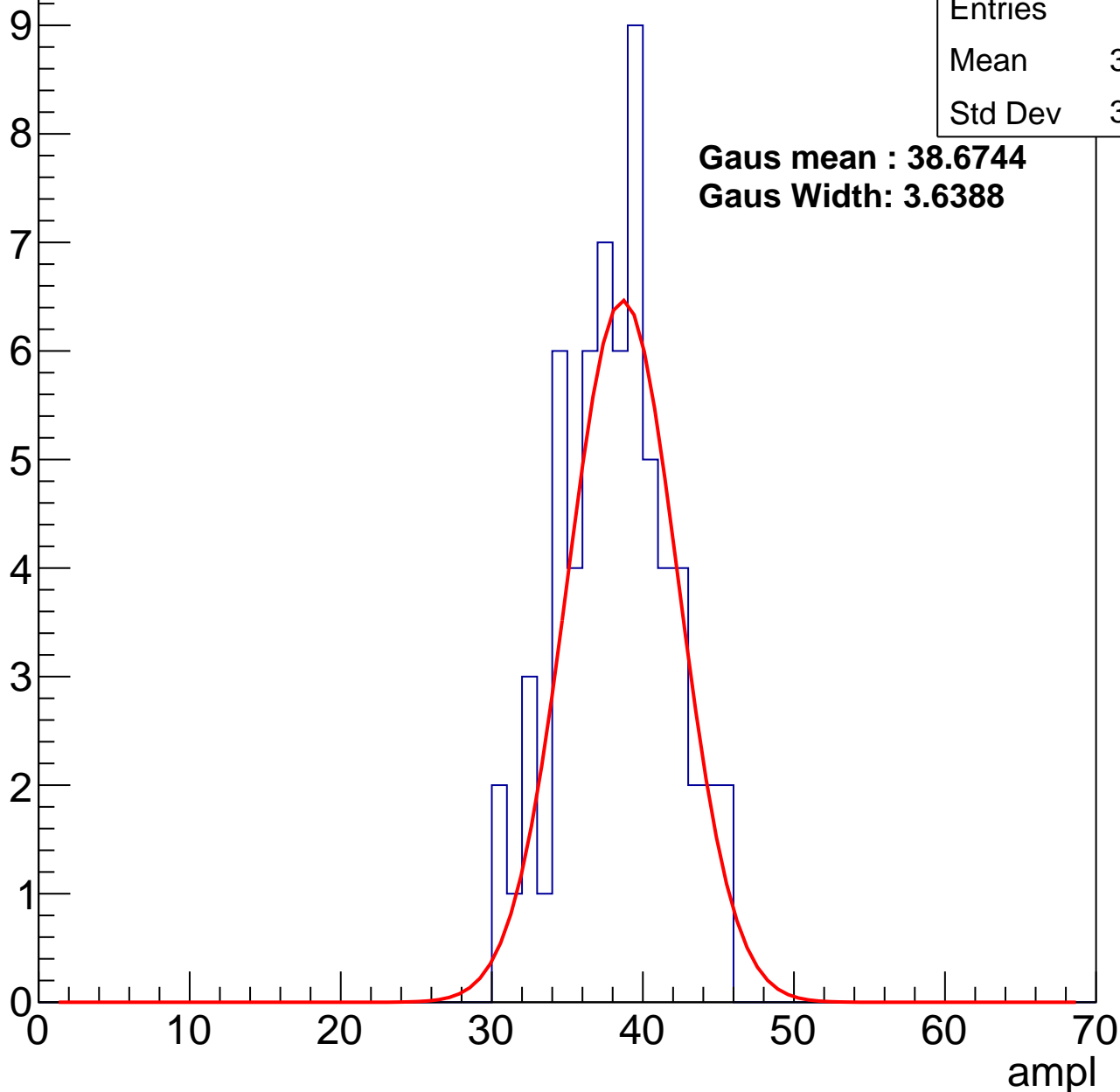
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 37.72 |
| Std Dev | 3.573 |

**Gaus mean : 38.6744**

**Gaus Width: 3.6388**



# B0L002S, U1-ch72, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 44.41 |
| Std Dev | 3.209 |

**Gaus mean : 44.9169**

**Gaus Width: 3.2945**

Entry

10

8

6

4

2

0

0

10

20

30

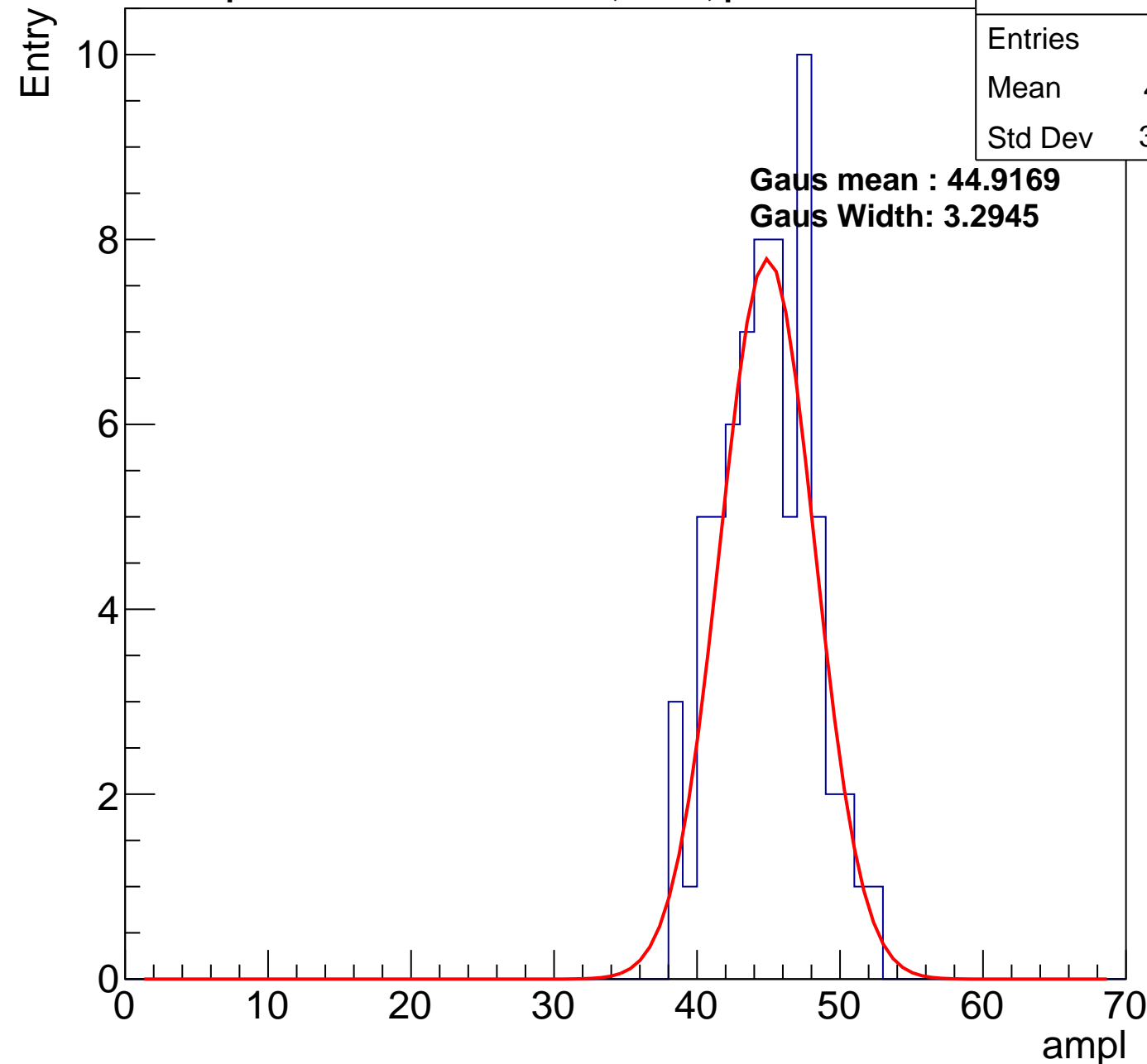
40

50

60

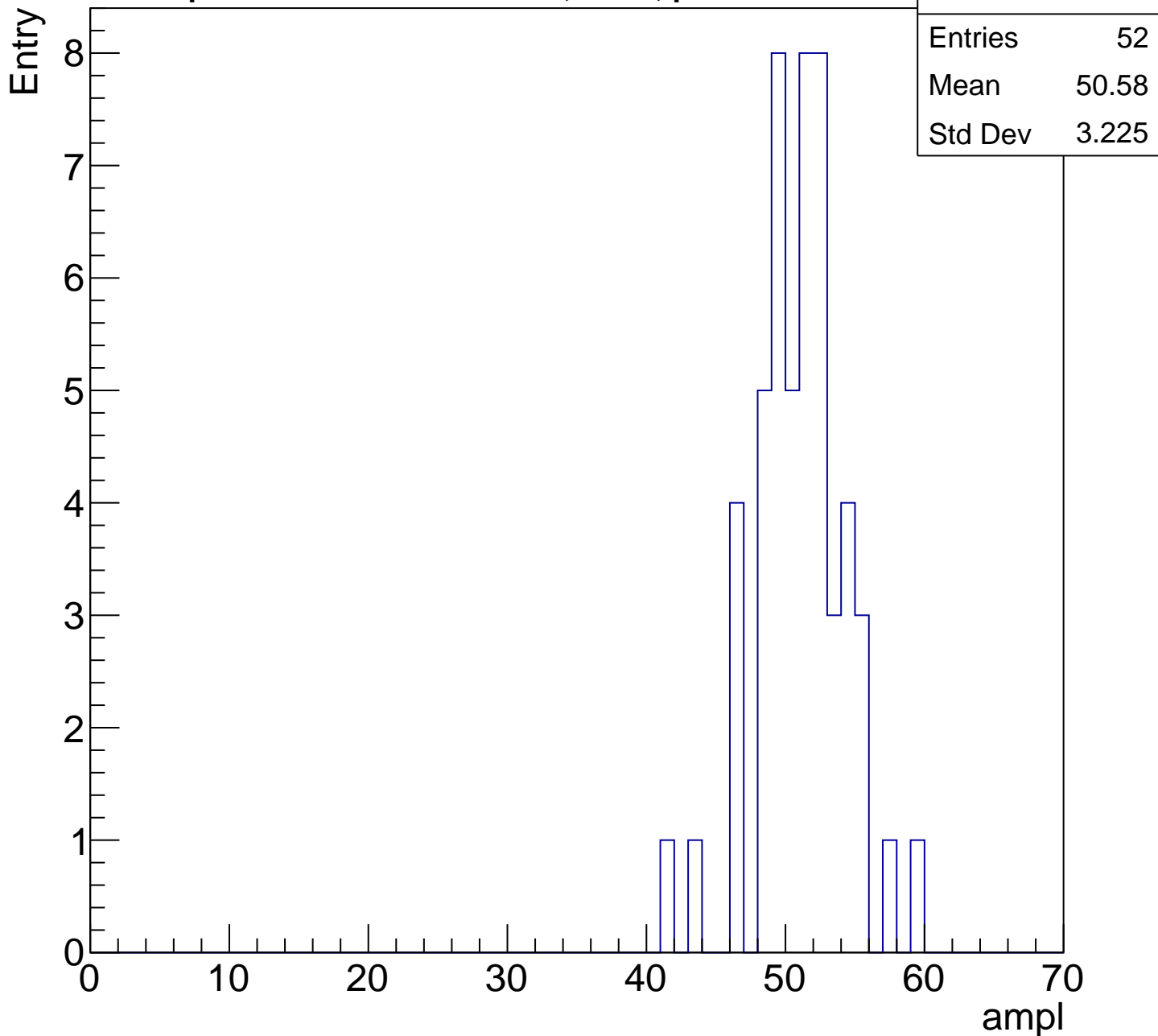
70

ampl



# B0L002S, U1-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

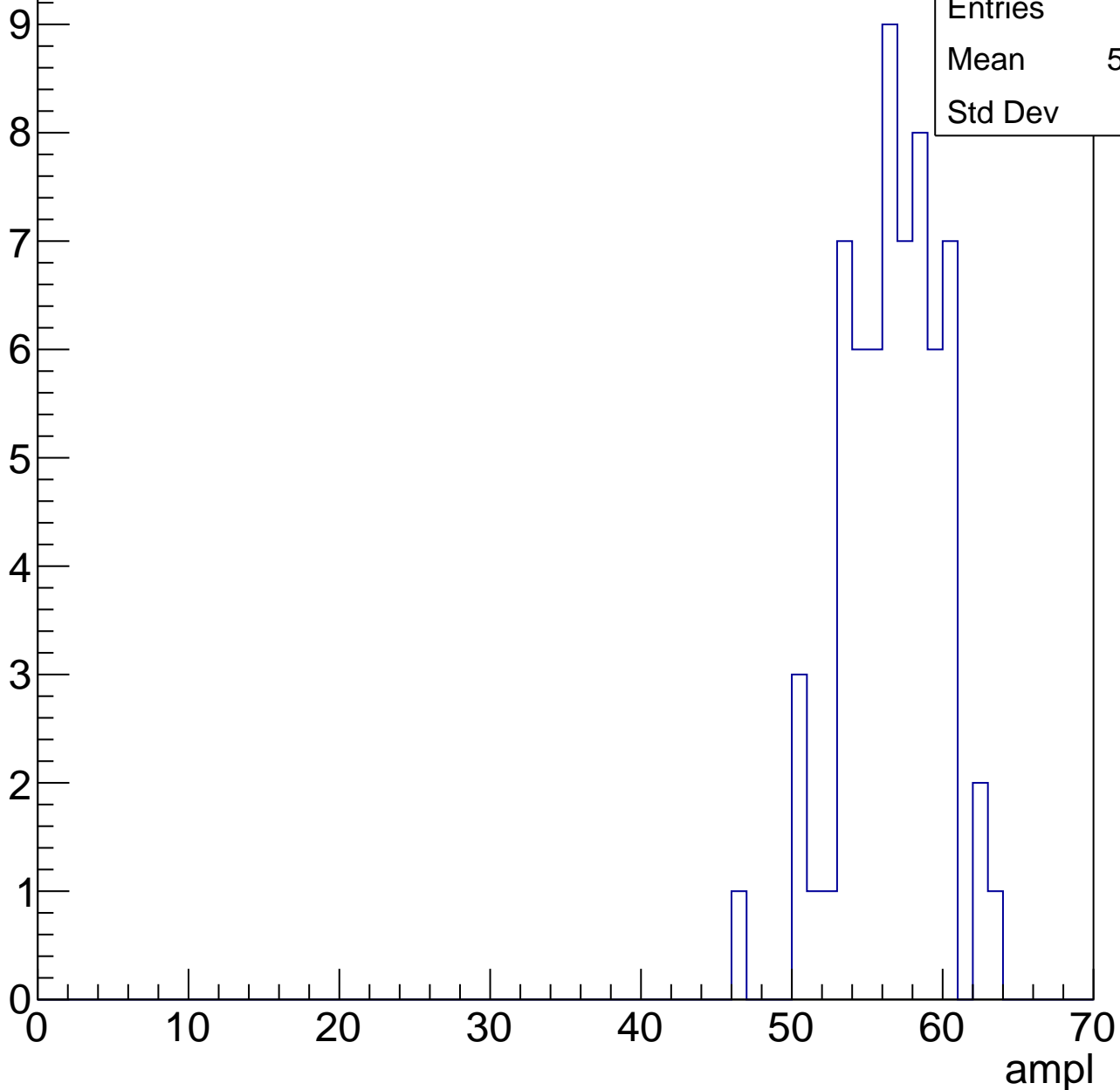


# B0L002S, U1-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 56.18 |
| Std Dev | 3.2   |



# B0L002S, U1-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 33    |
| Mean    | 58.73 |
| Std Dev | 10.61 |

ampl

0

10

20

30

40

50

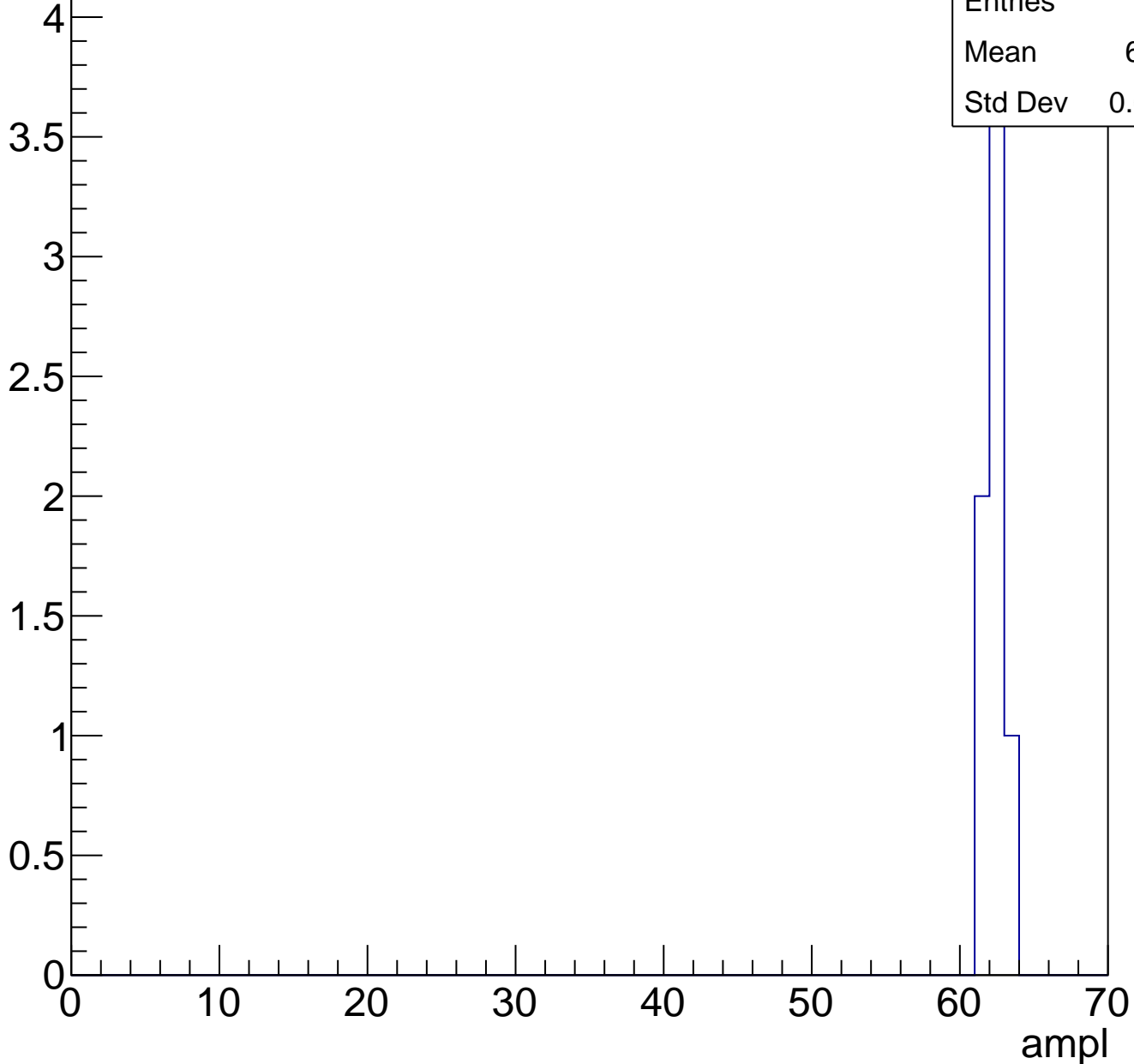
60

70

# B0L002S, U1-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 31.86 |
| Std Dev | 3.563 |

**Gaus mean : 32.4253**

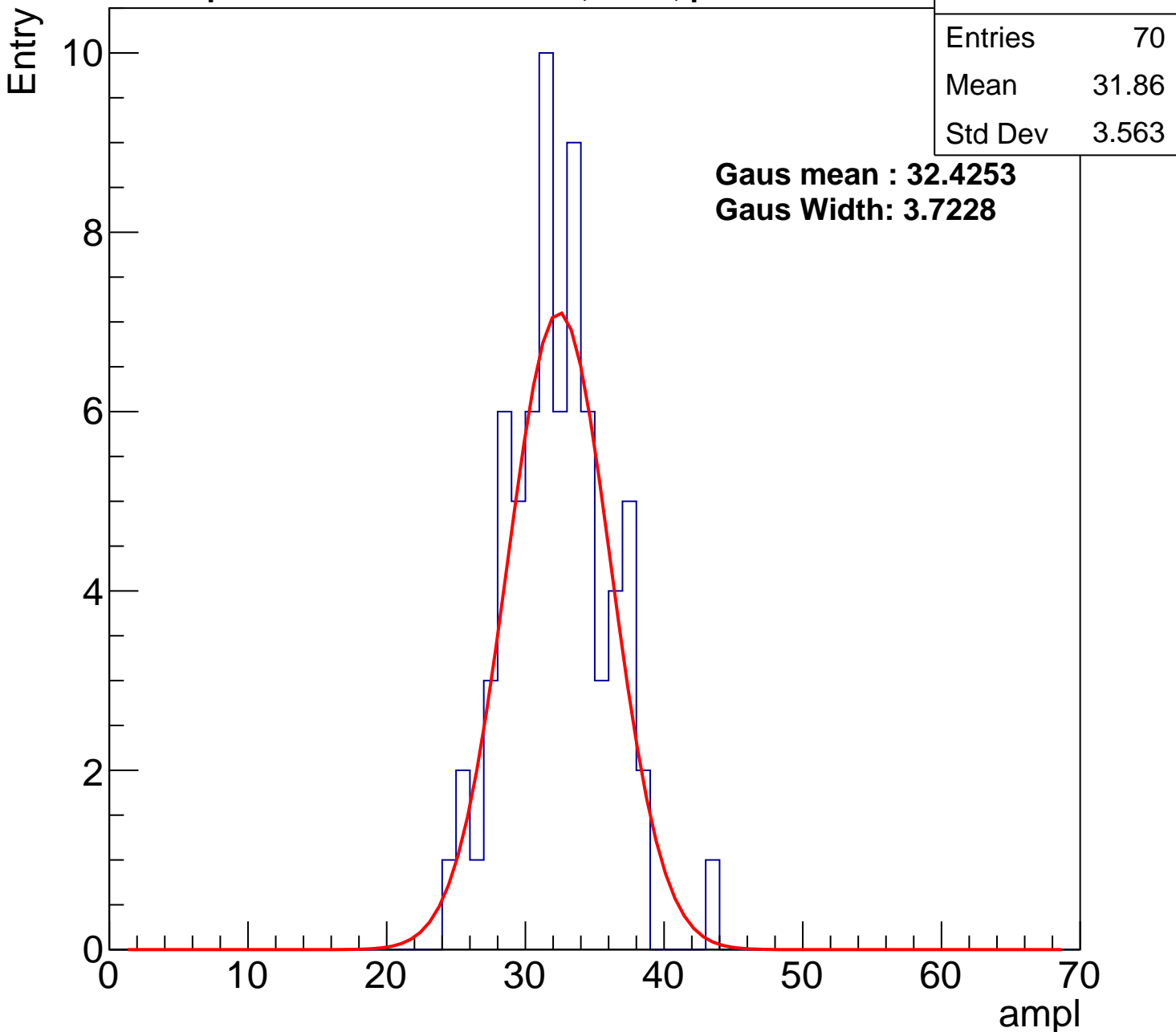
**Gaus Width: 3.7228**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch73, adc1

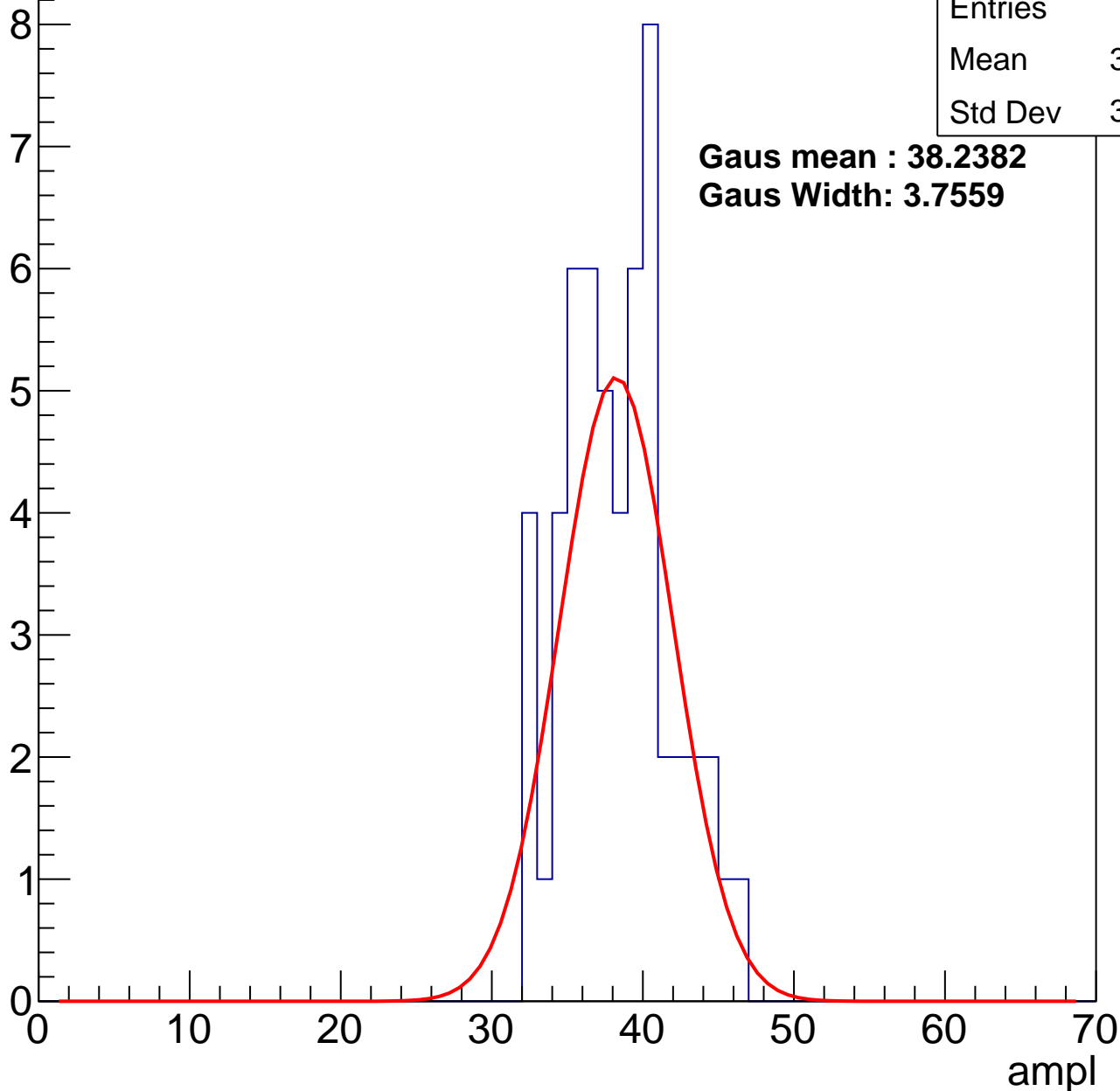
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 37.87 |
| Std Dev | 3.438 |

**Gaus mean : 38.2382**

**Gaus Width: 3.7559**



# B0L002S, U1-ch73, adc2

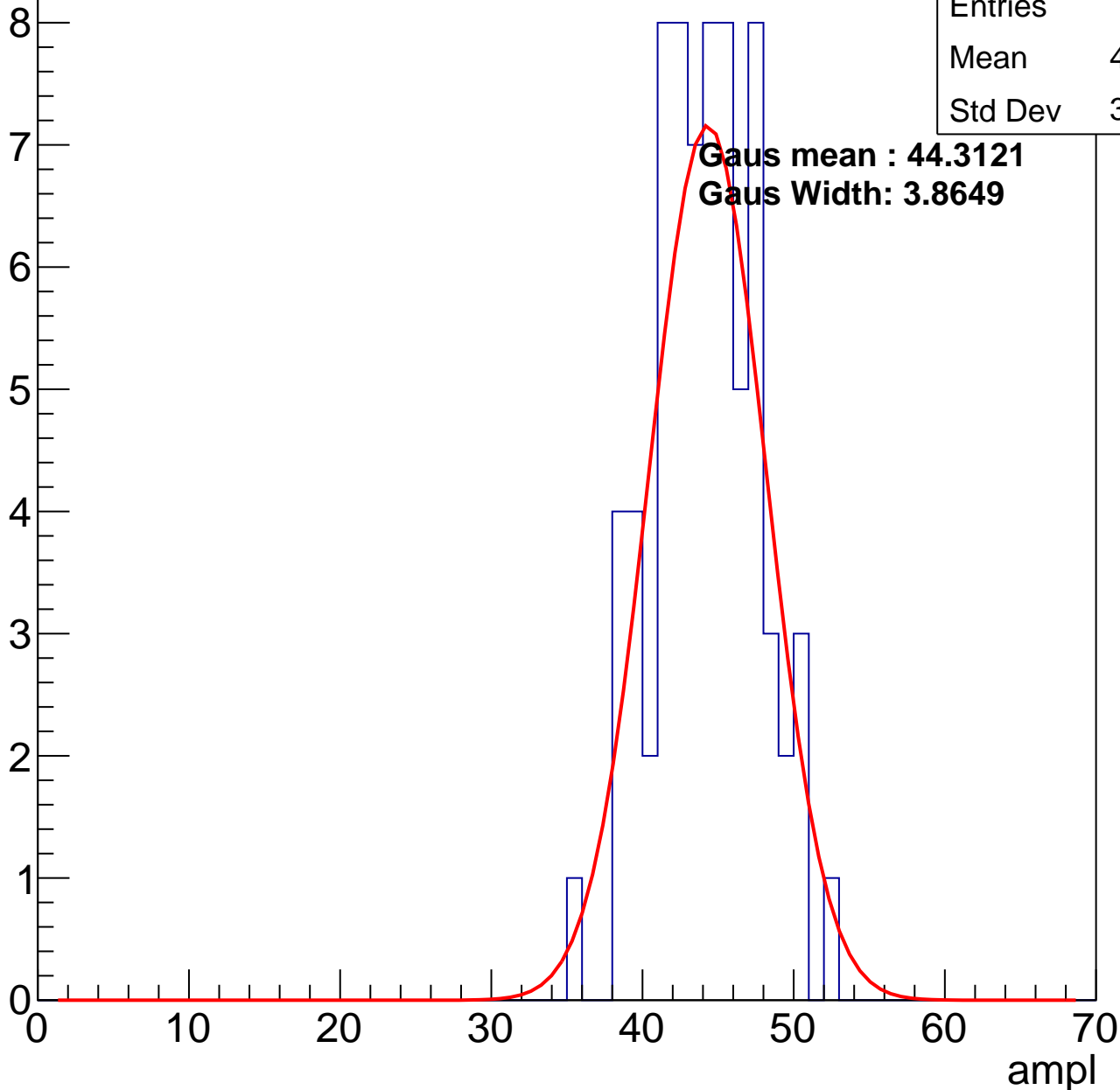
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 43.75 |
| Std Dev | 3.402 |

**Gaus mean : 44.3121**

**Gaus Width: 3.8649**

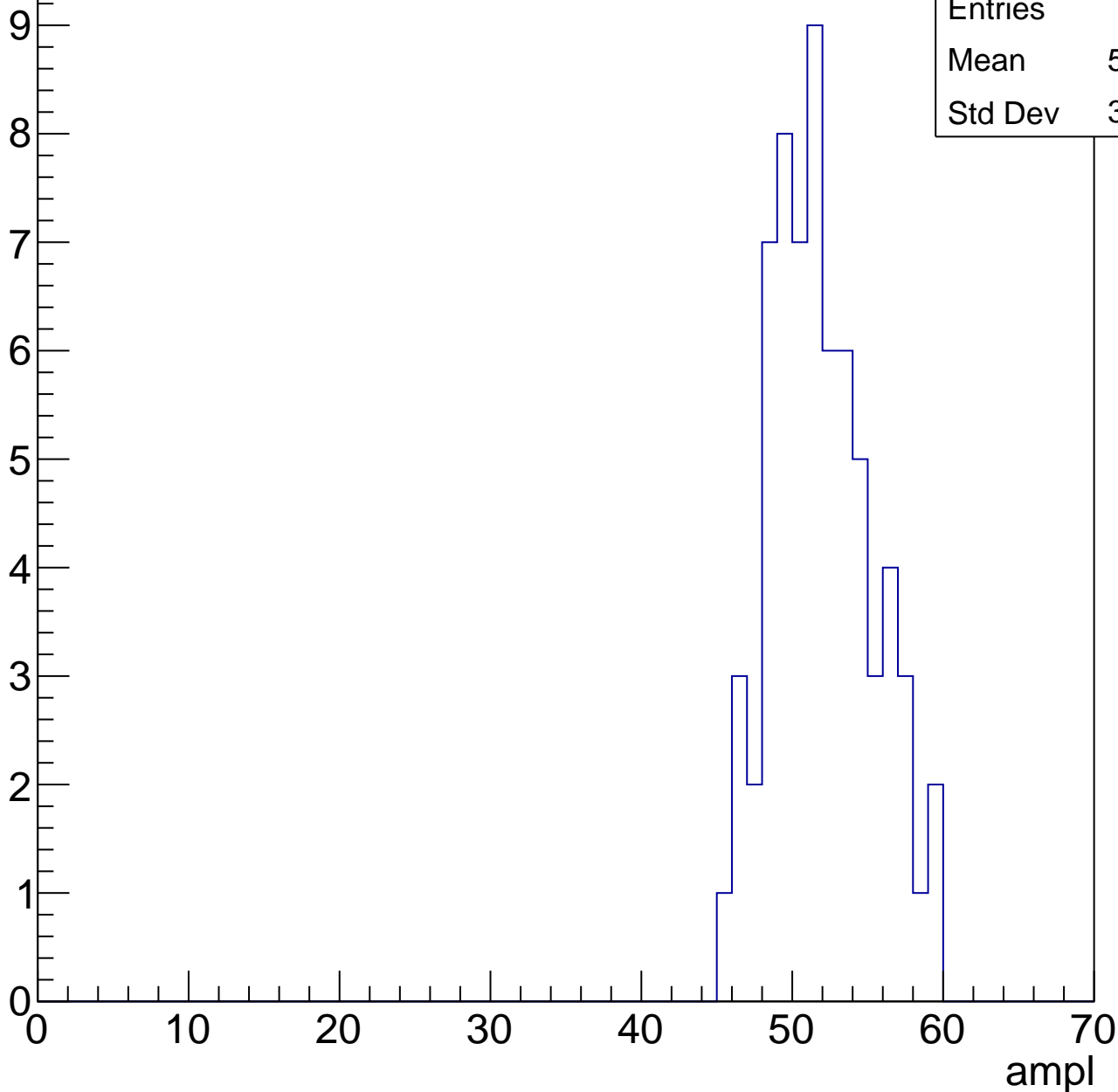


# B0L002S, U1-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 51.49 |
| Std Dev | 3.316 |

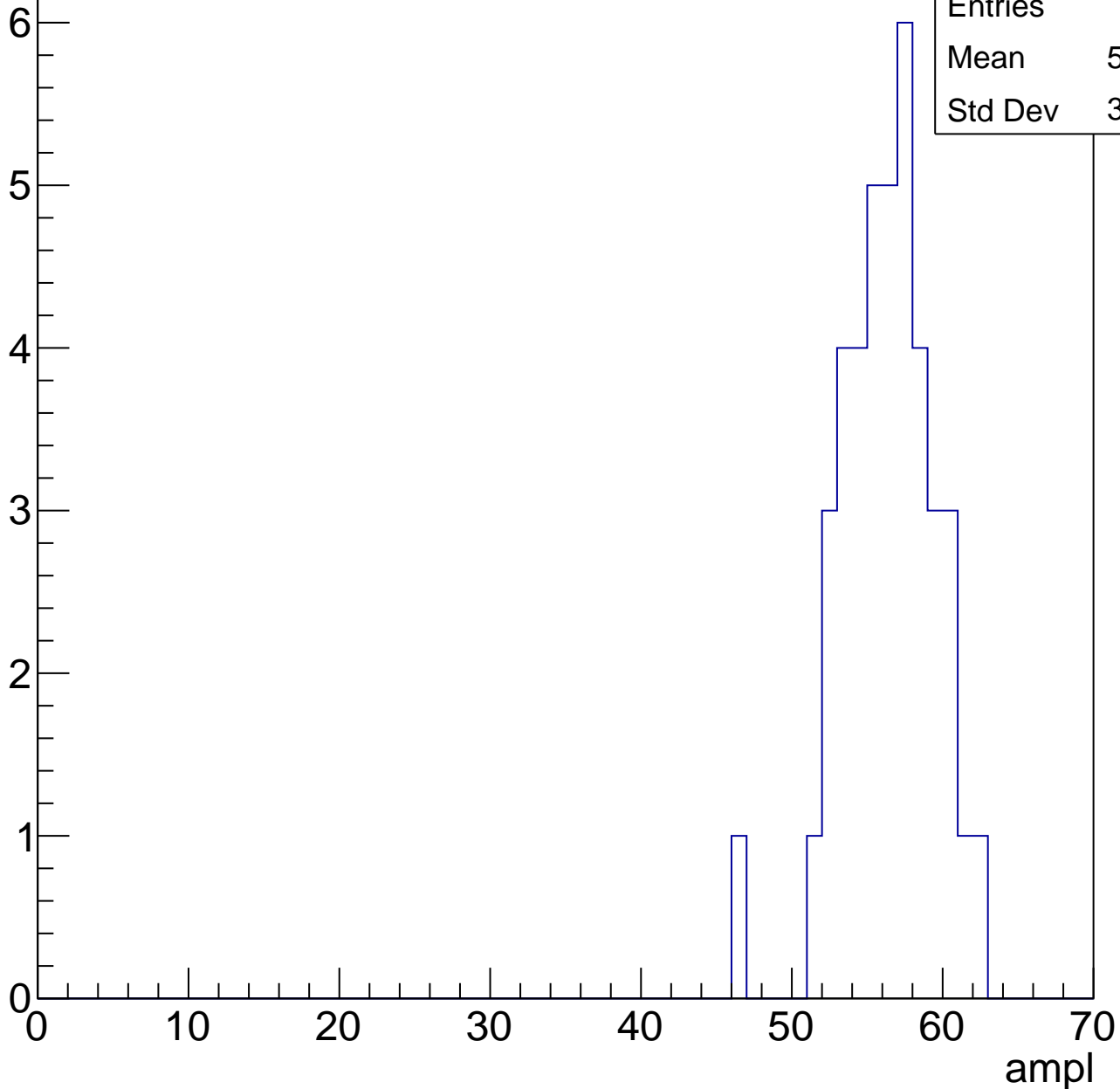


# B0L002S, U1-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

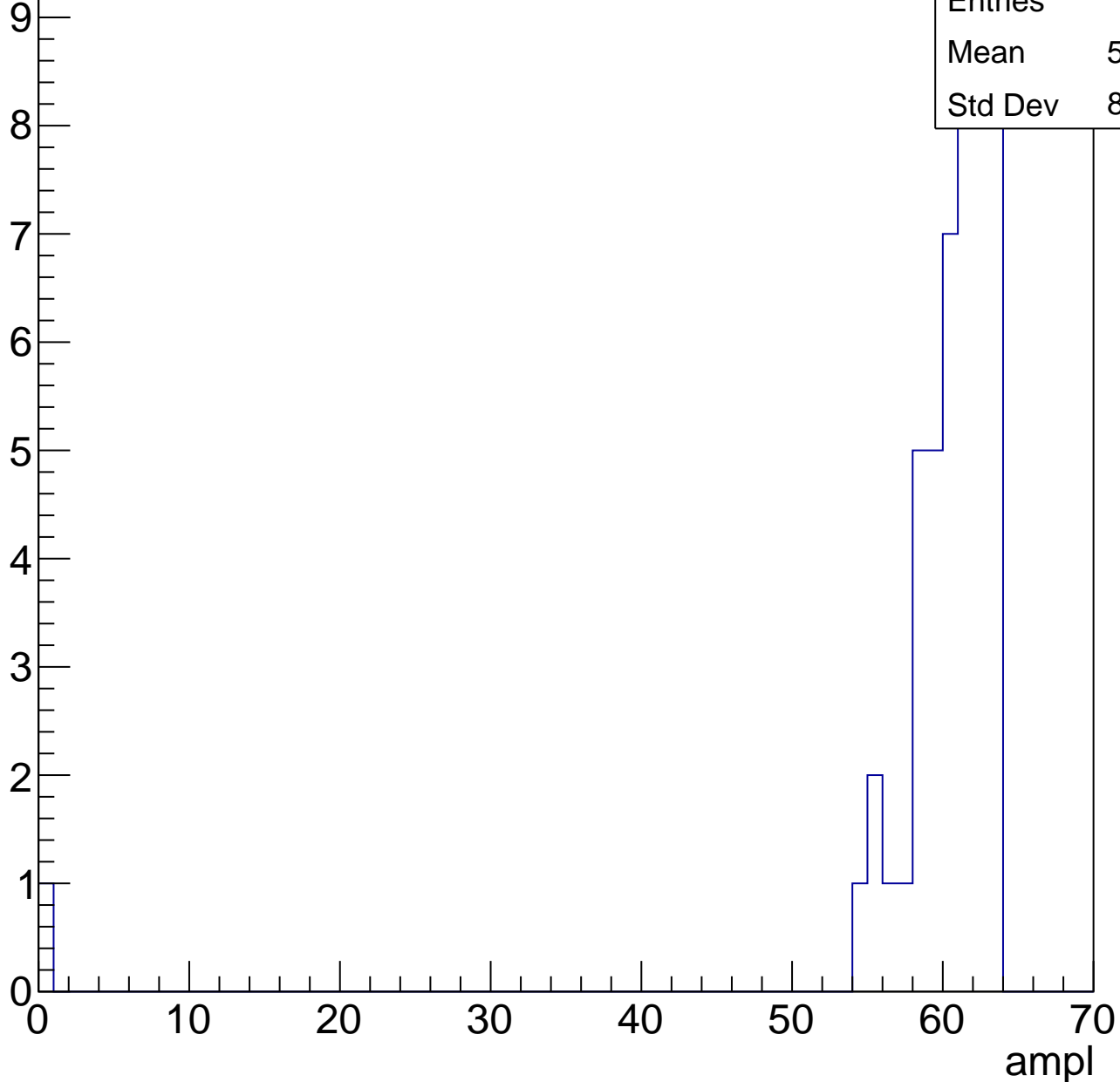
|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 55.85 |
| Std Dev | 3.073 |



# B0L002S, U1-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 28.74 |
| Std Dev | 4.687 |

**Gaus mean : 30.2825**

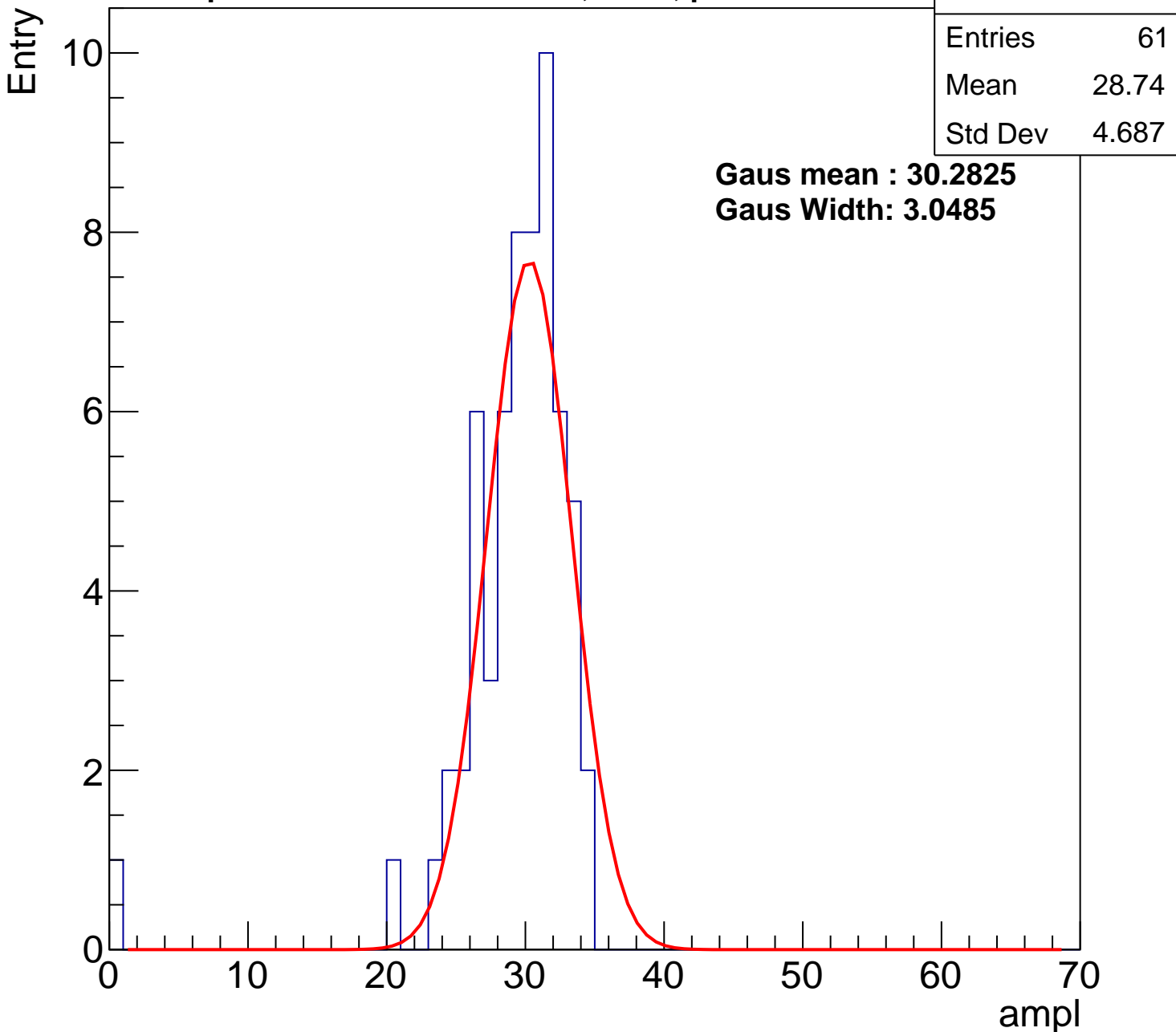
**Gaus Width: 3.0485**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch74, adc1

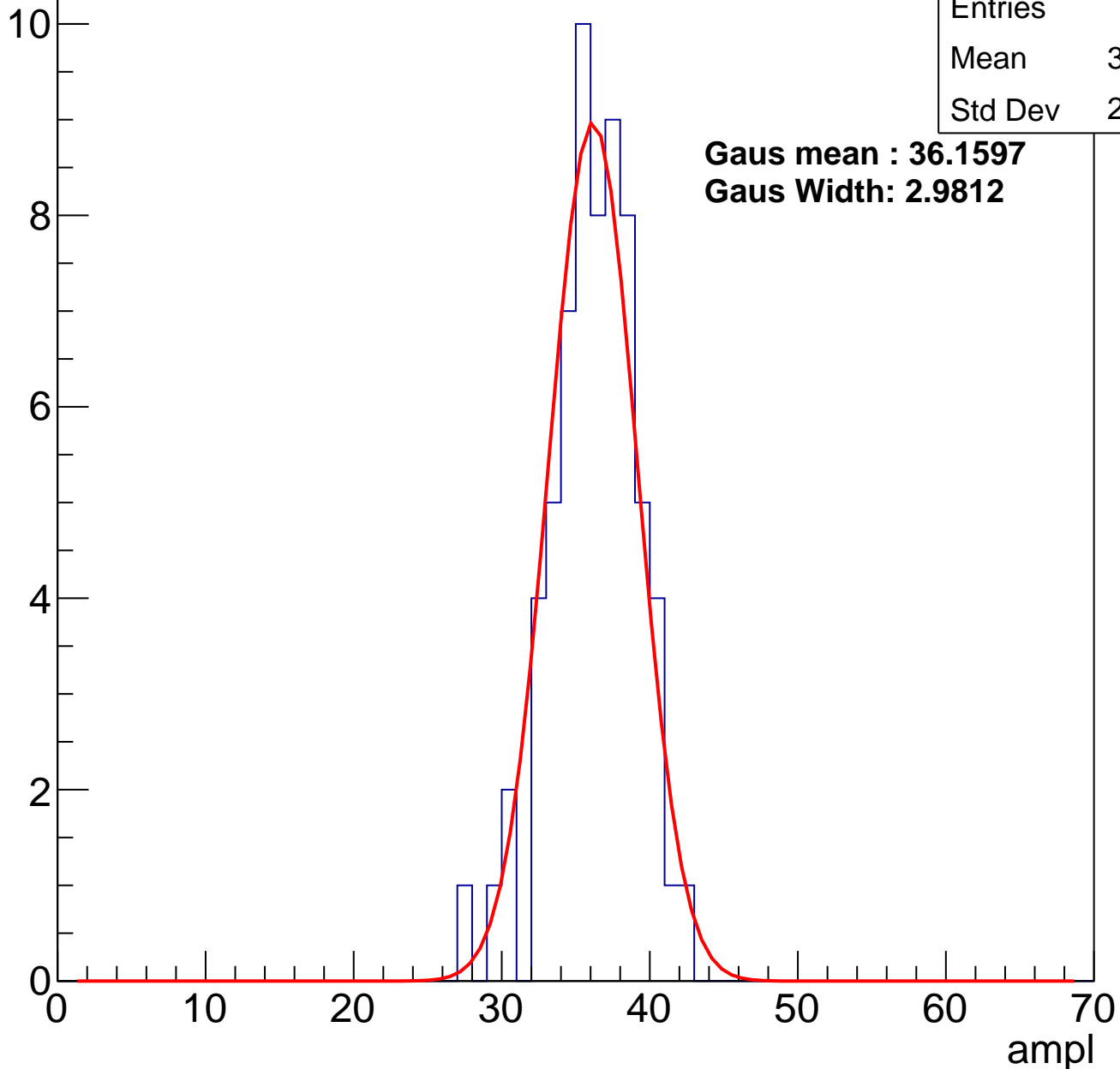
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 35.76 |
| Std Dev | 2.903 |

**Gaus mean : 36.1597**

**Gaus Width: 2.9812**

Entry



# B0L002S, U1-ch74, adc2

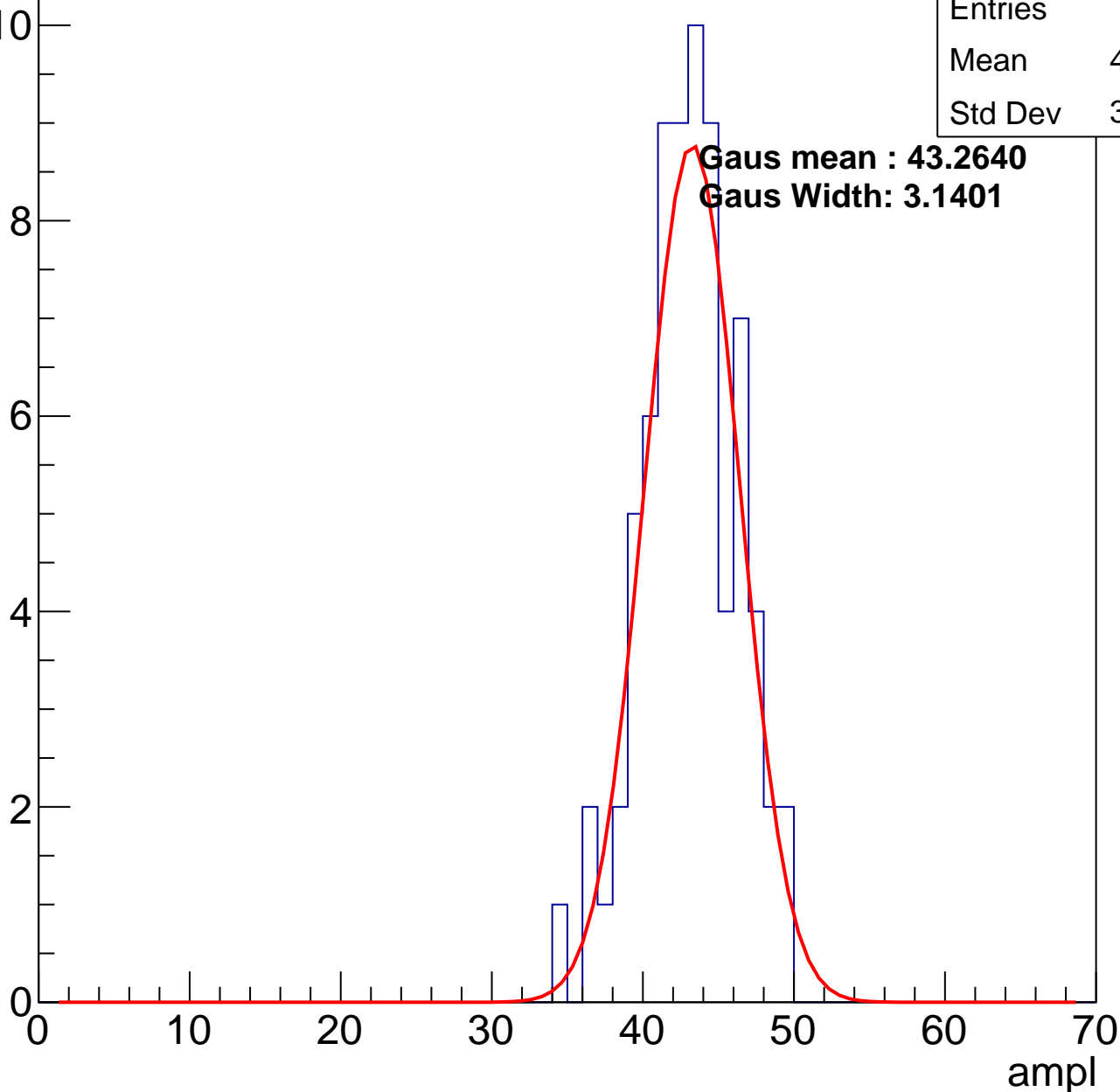
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 42.62 |
| Std Dev | 3.113 |

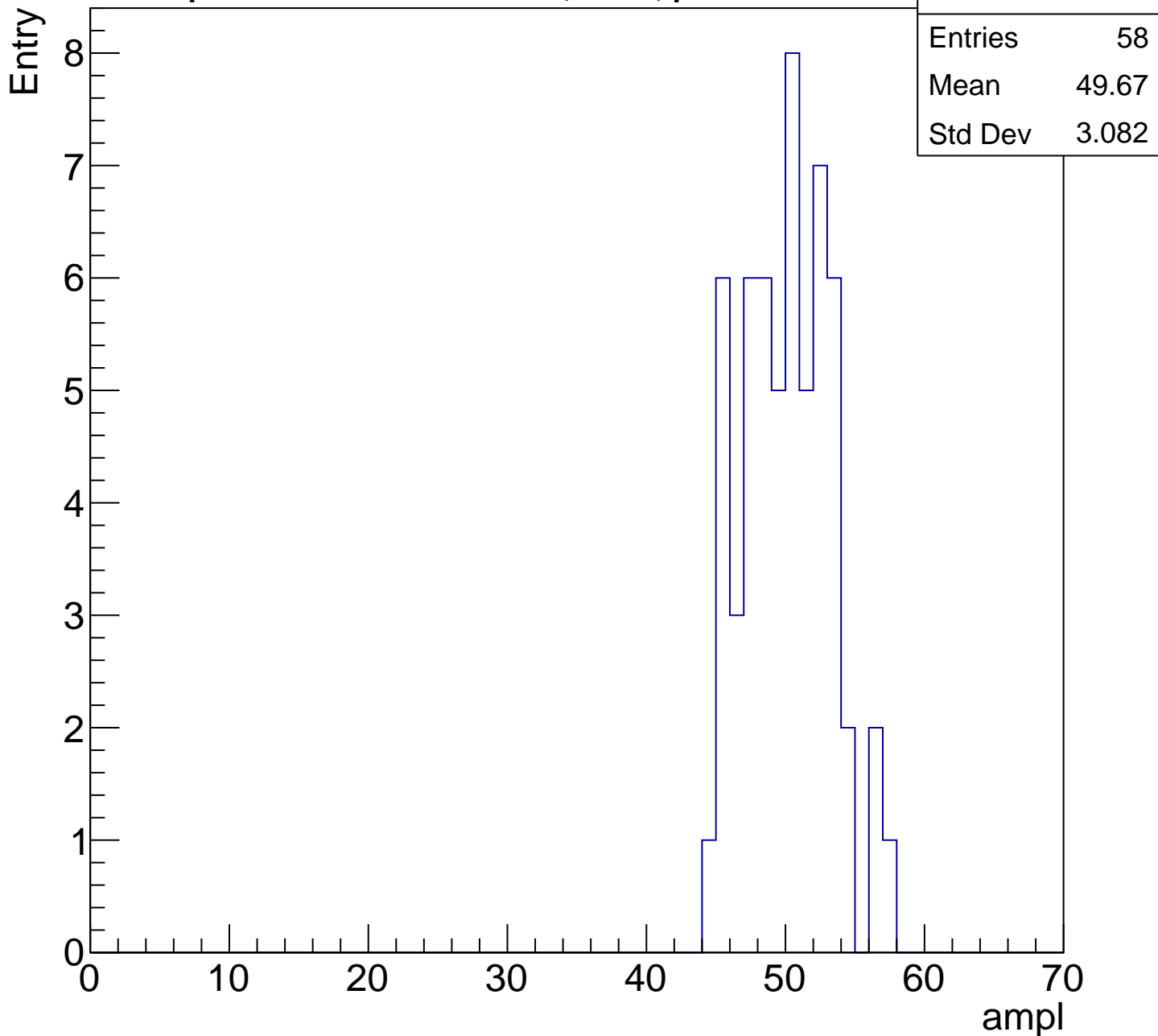
**Gaus mean : 43.2640**

**Gaus Width: 3.1401**



# B0L002S, U1-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

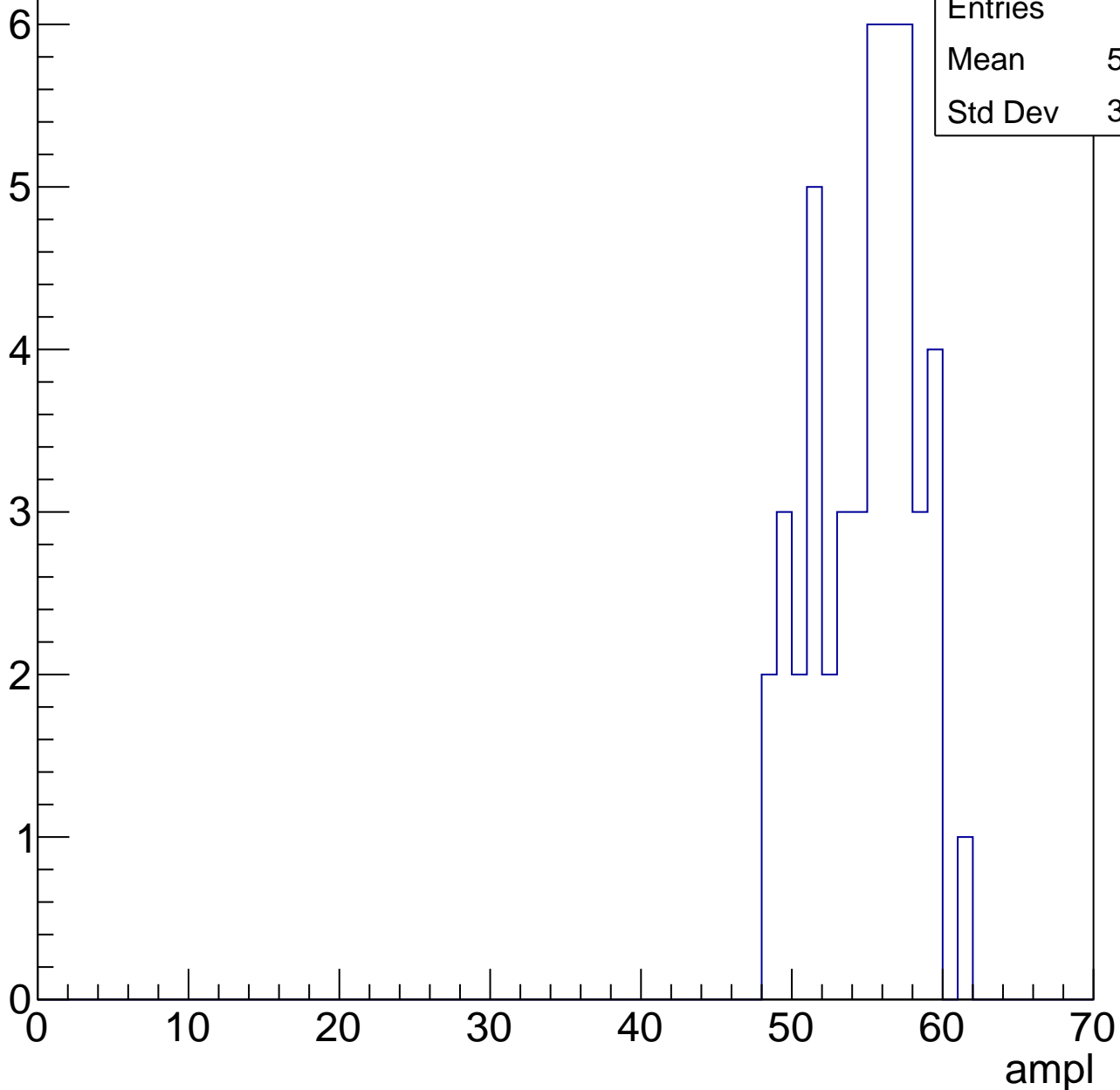


# B0L002S, U1-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

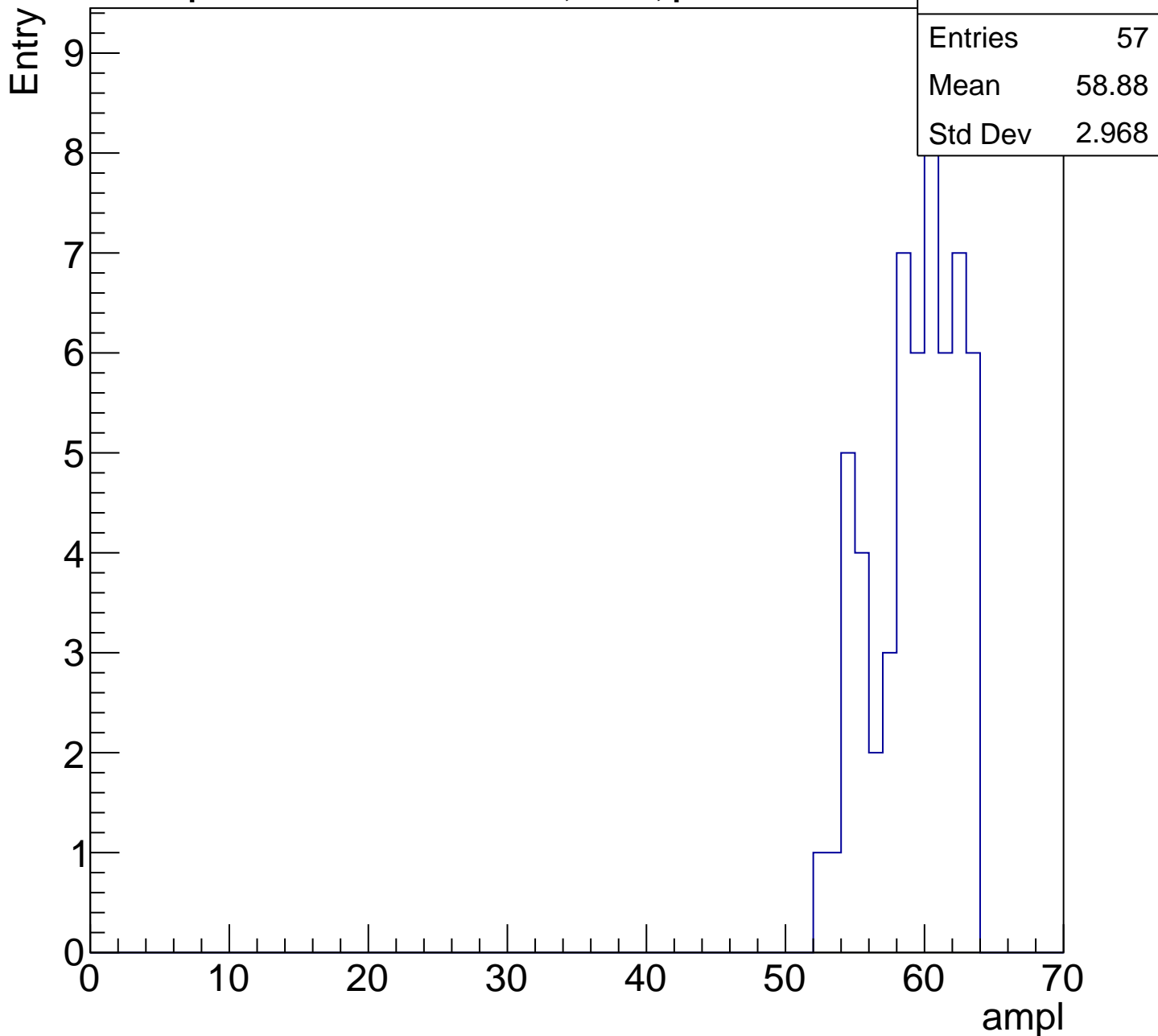
Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 54.39 |
| Std Dev | 3.326 |



# B0L002S, U1-ch74, adc5

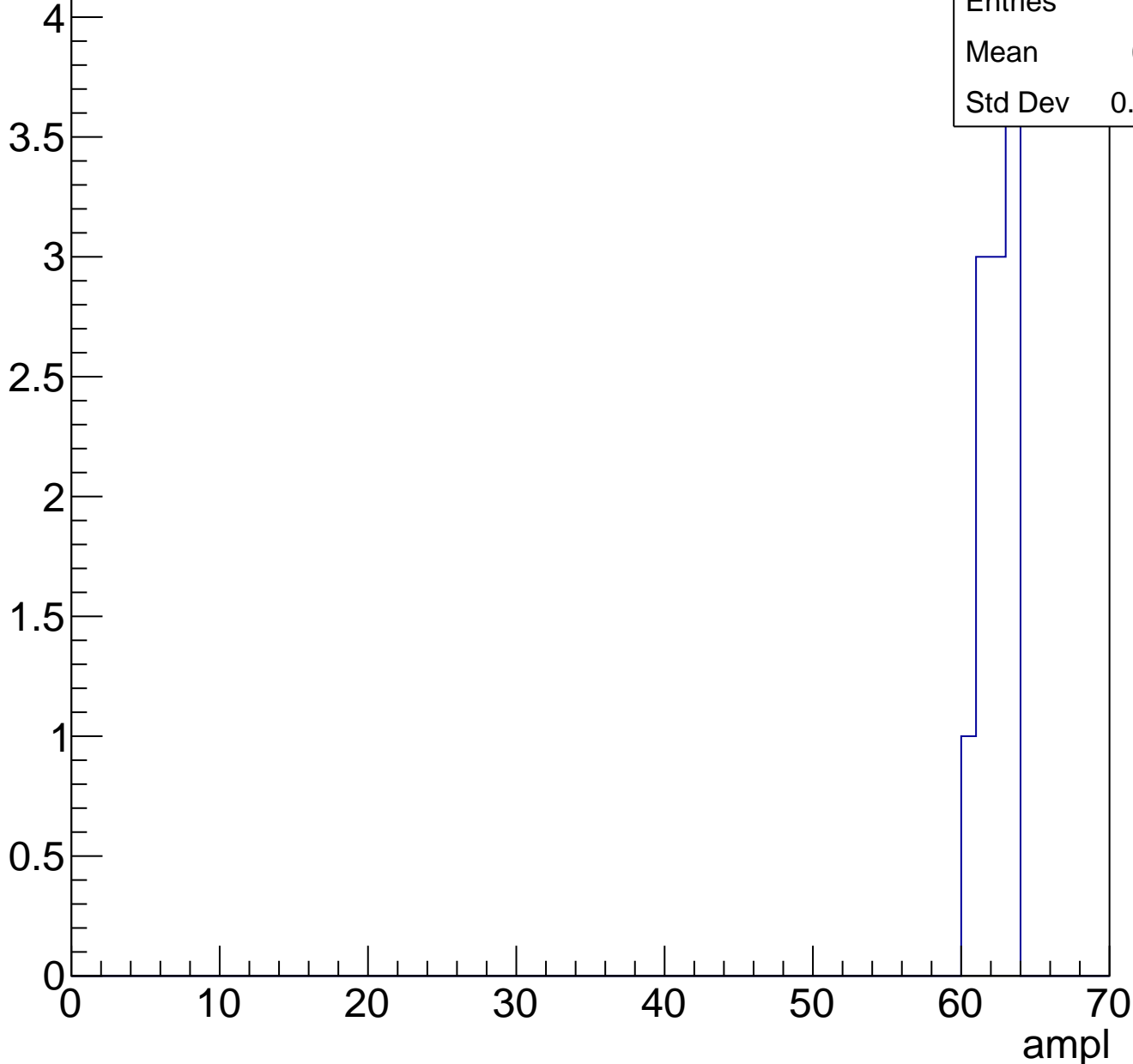
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |        |
|---------|--------|
| Entries | 11     |
| Mean    | 61.91  |
| Std Dev | 0.9959 |



# B0L002S, U1-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch75, adc0

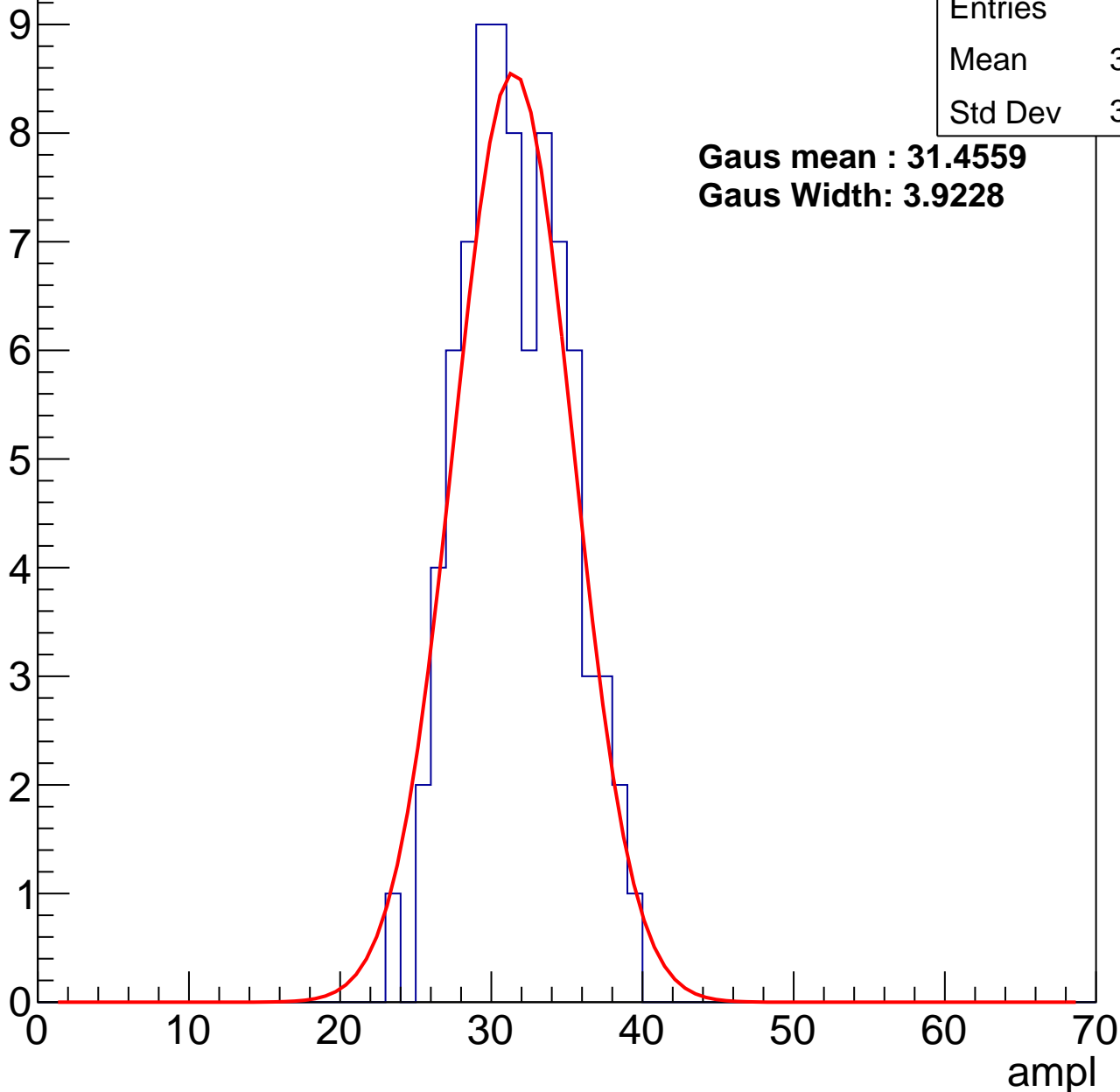
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 31.12 |
| Std Dev | 3.458 |

**Gaus mean : 31.4559**

**Gaus Width: 3.9228**



# B0L002S, U1-ch75, adc1

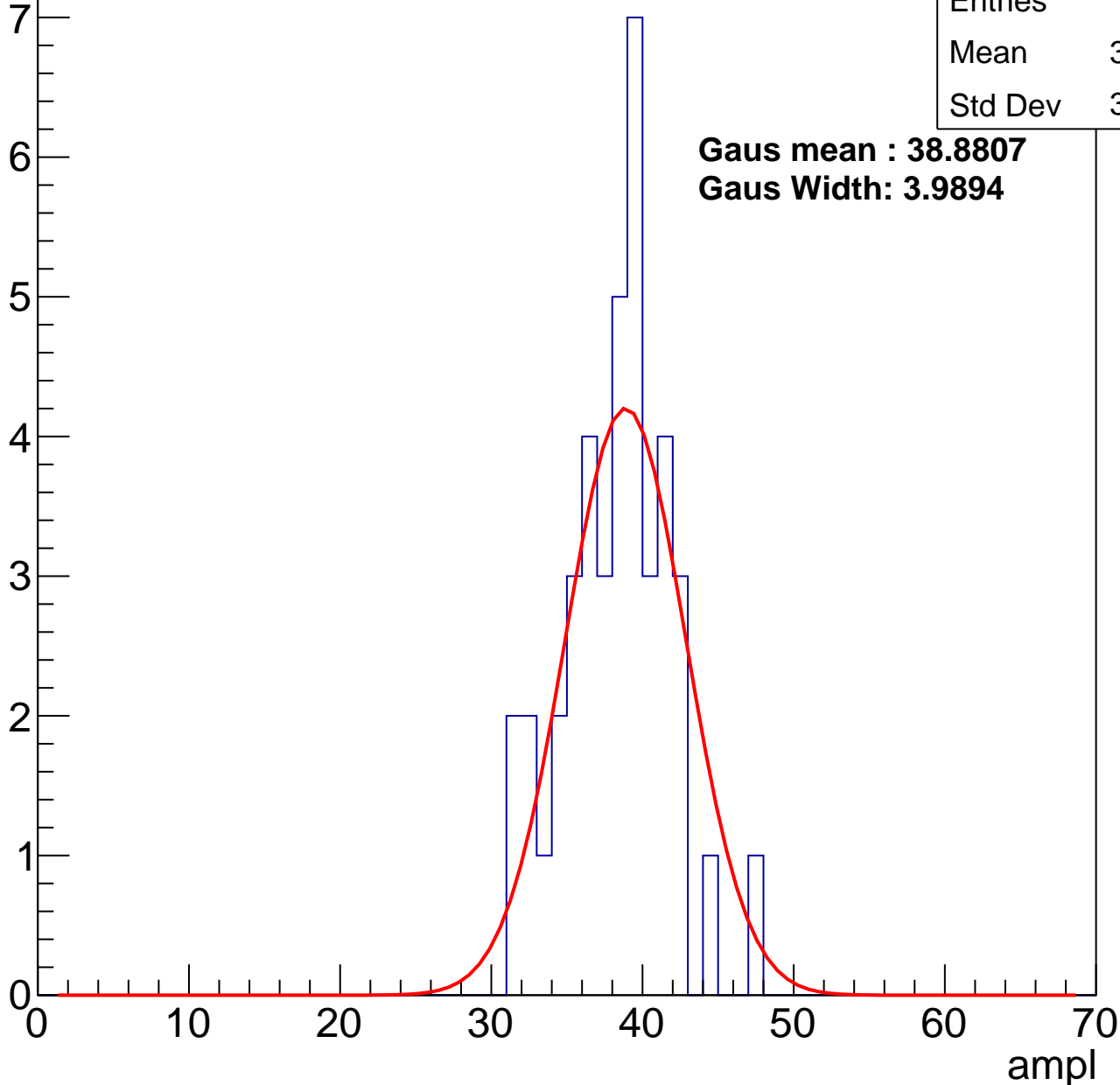
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 37.83 |
| Std Dev | 3.463 |

**Gaus mean : 38.8807**

**Gaus Width: 3.9894**



# B0L002S, U1-ch75, adc2

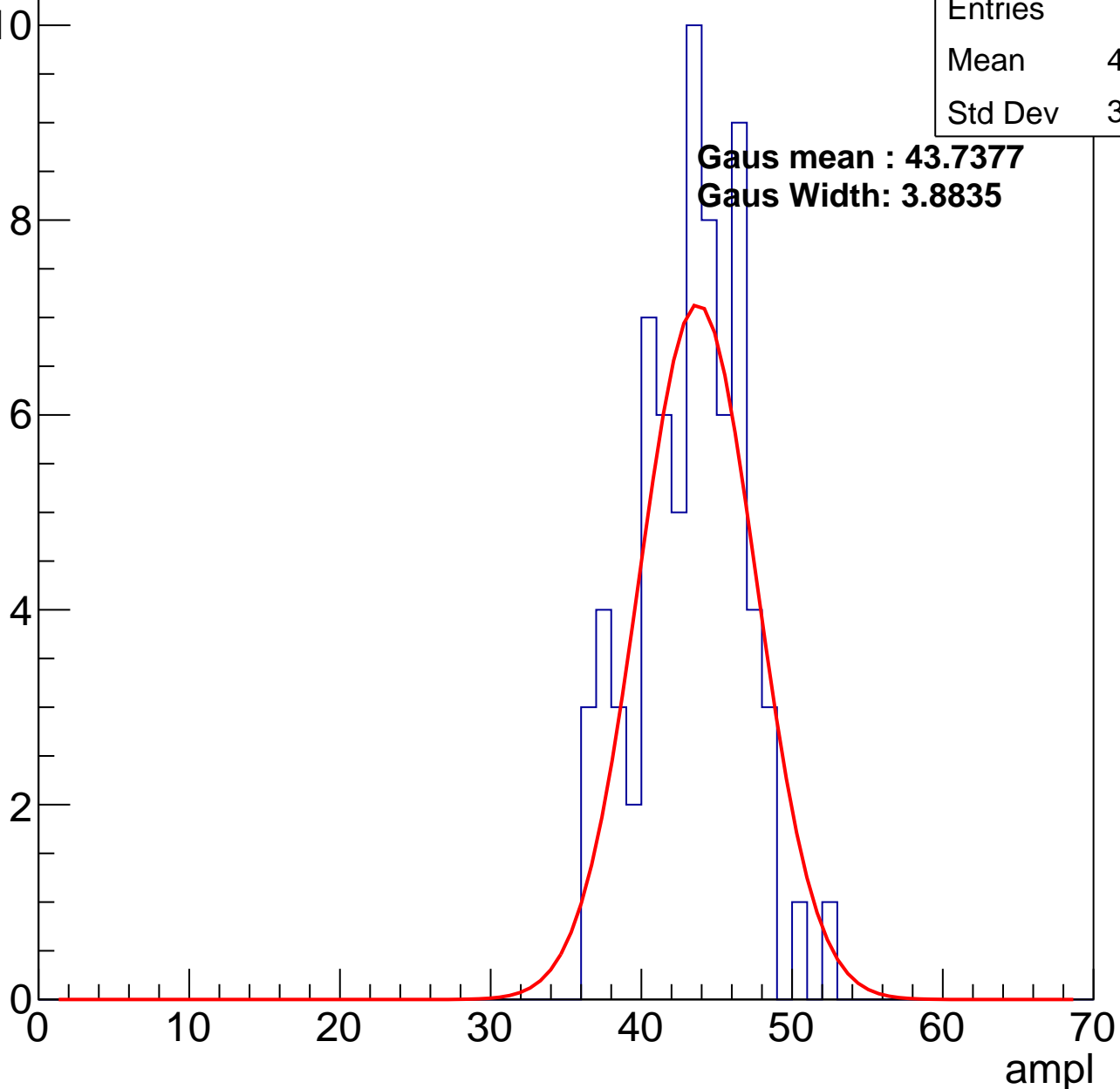
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 42.83 |
| Std Dev | 3.476 |

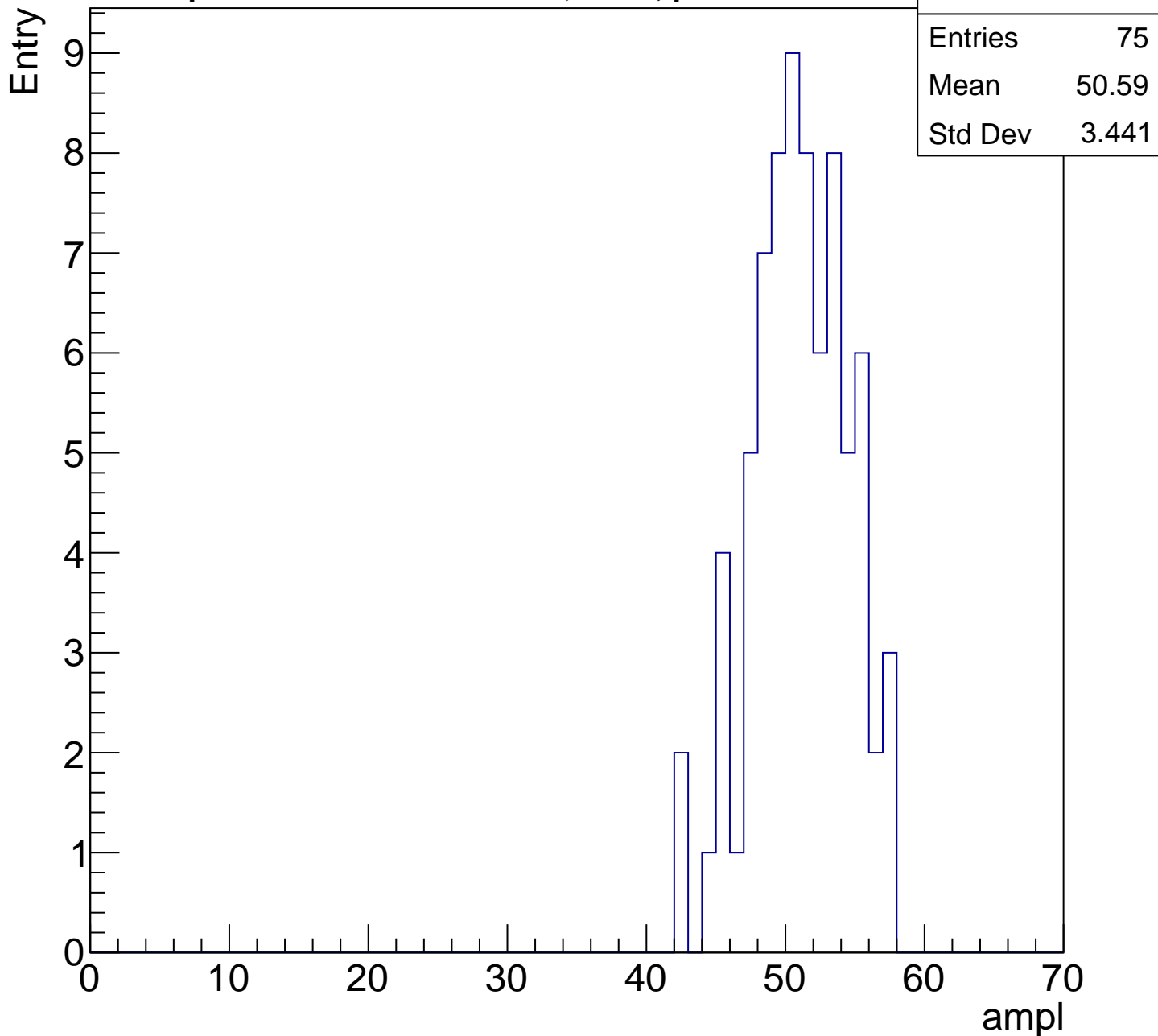
**Gaus mean : 43.7377**

**Gaus Width: 3.8835**



# B0L002S, U1-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

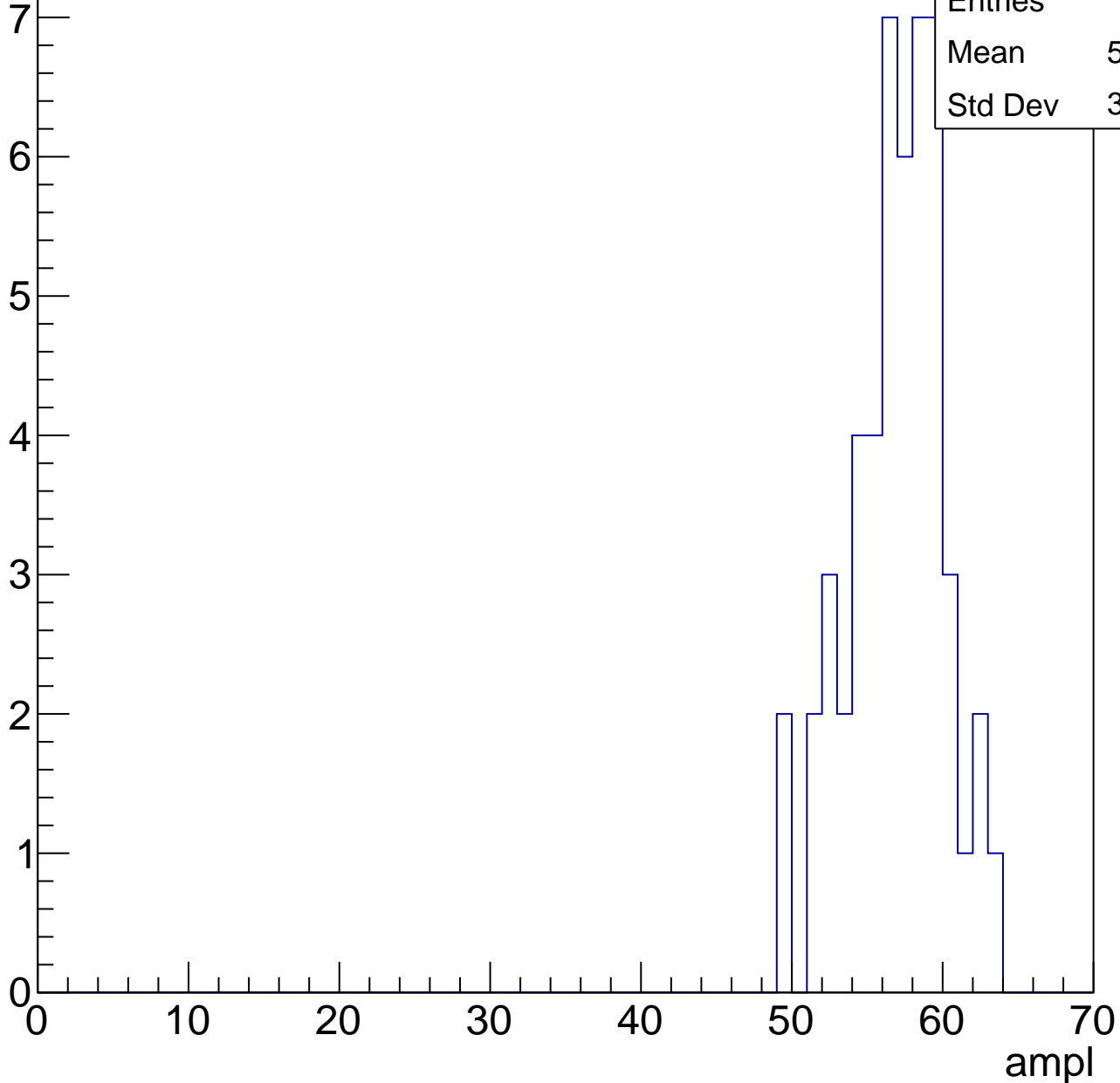


# B0L002S, U1-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 56.45 |
| Std Dev | 3.177 |

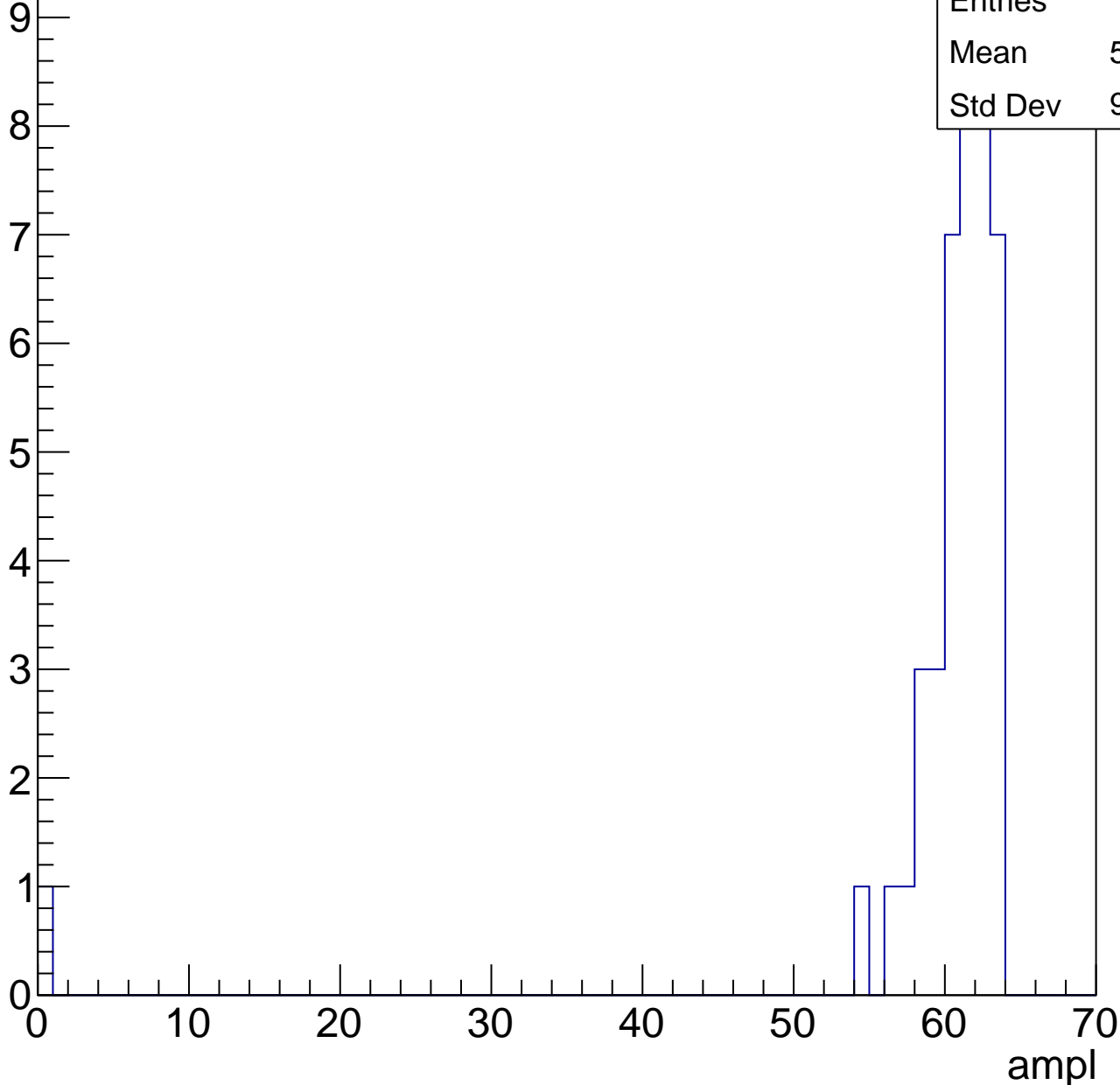


# B0L002S, U1-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 59.12 |
| Std Dev | 9.562 |



# B0L002S, U1-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 63 |
| Std Dev | 0  |

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch76, adc0

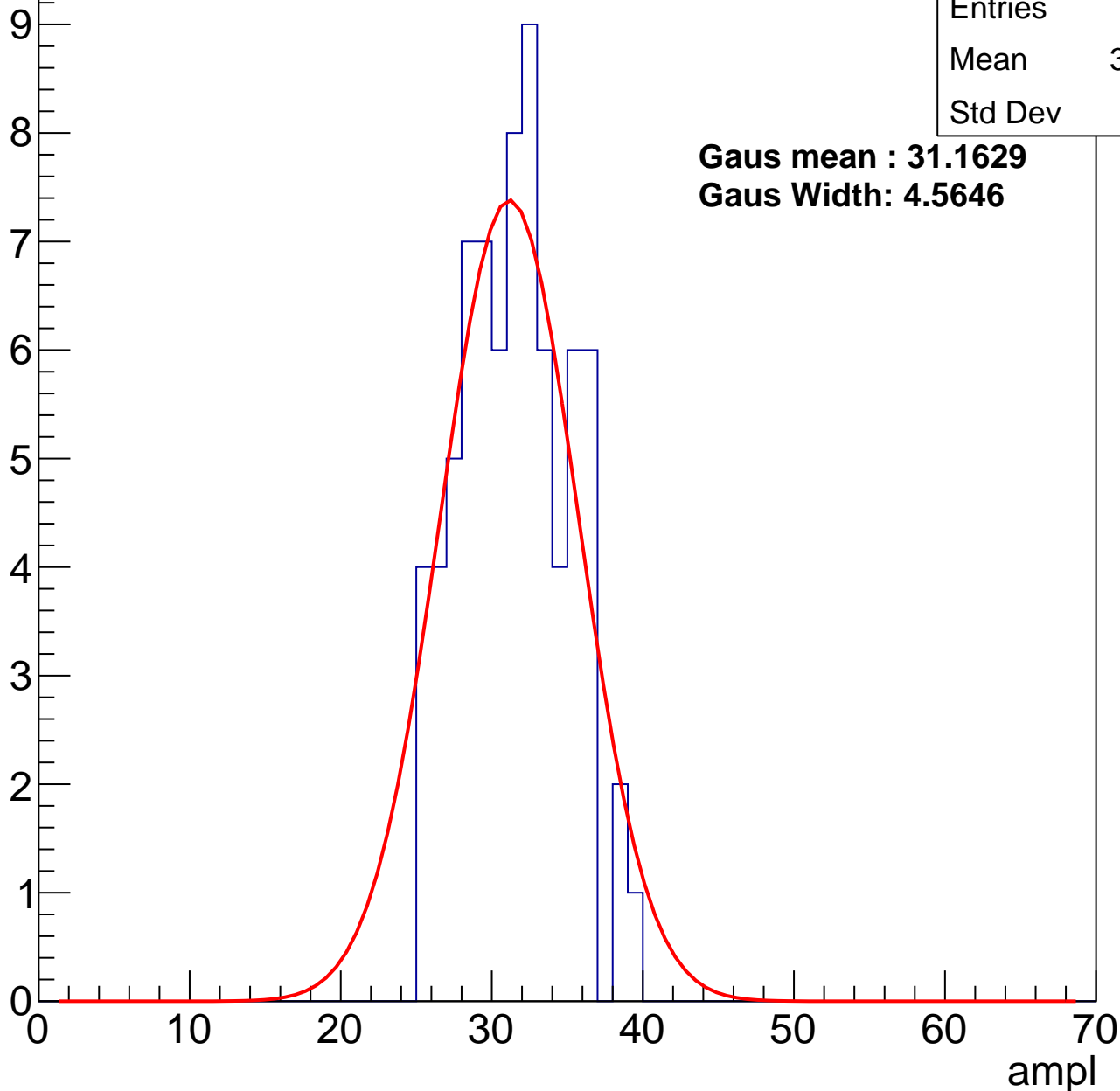
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 31.05 |
| Std Dev | 3.46  |

**Gaus mean : 31.1629**

**Gaus Width: 4.5646**



# B0L002S, U1-ch76, adc1

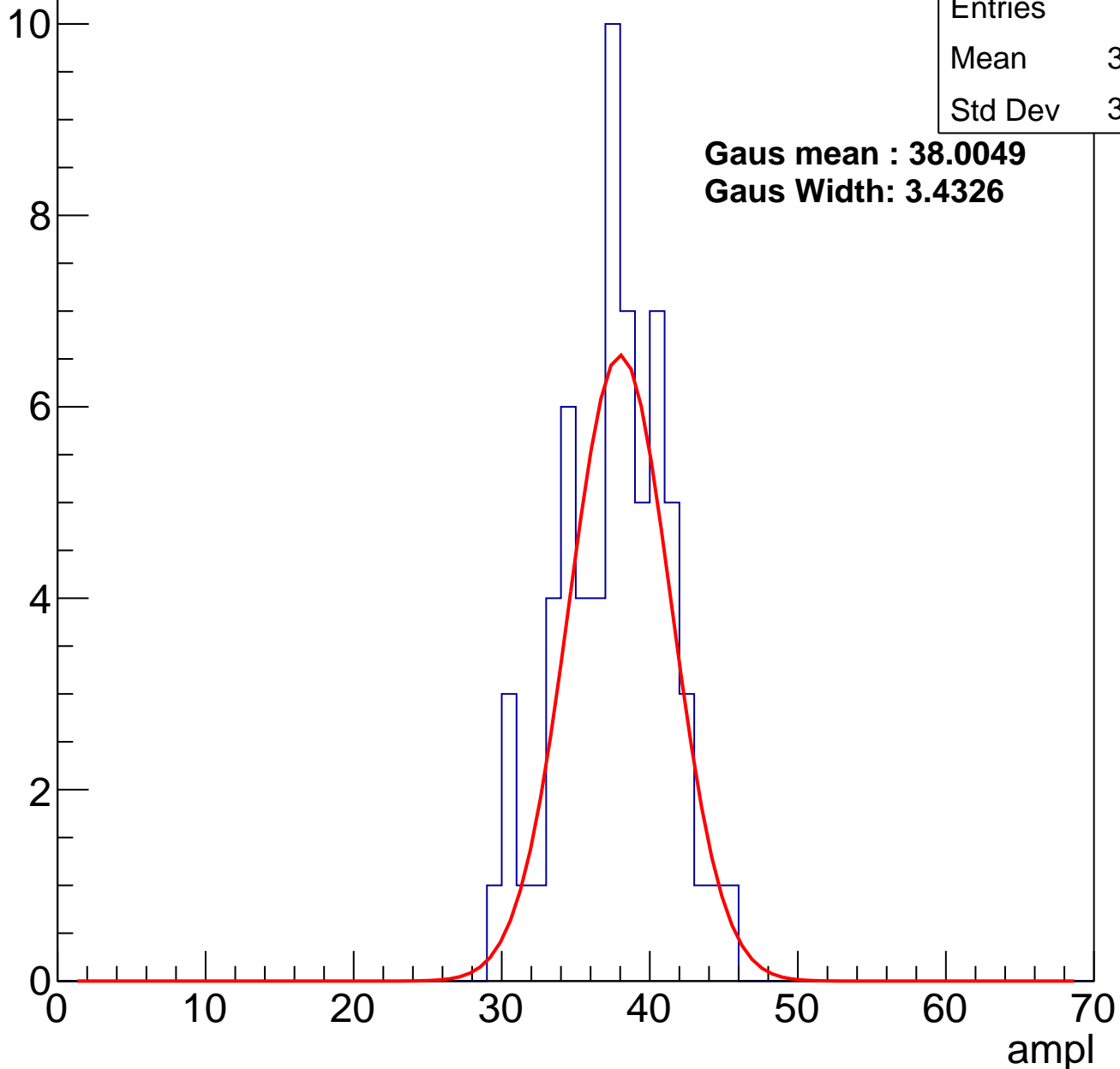
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 37.12 |
| Std Dev | 3.542 |

**Gaus mean : 38.0049**

**Gaus Width: 3.4326**

Entry



# B0L002S, U1-ch76, adc2

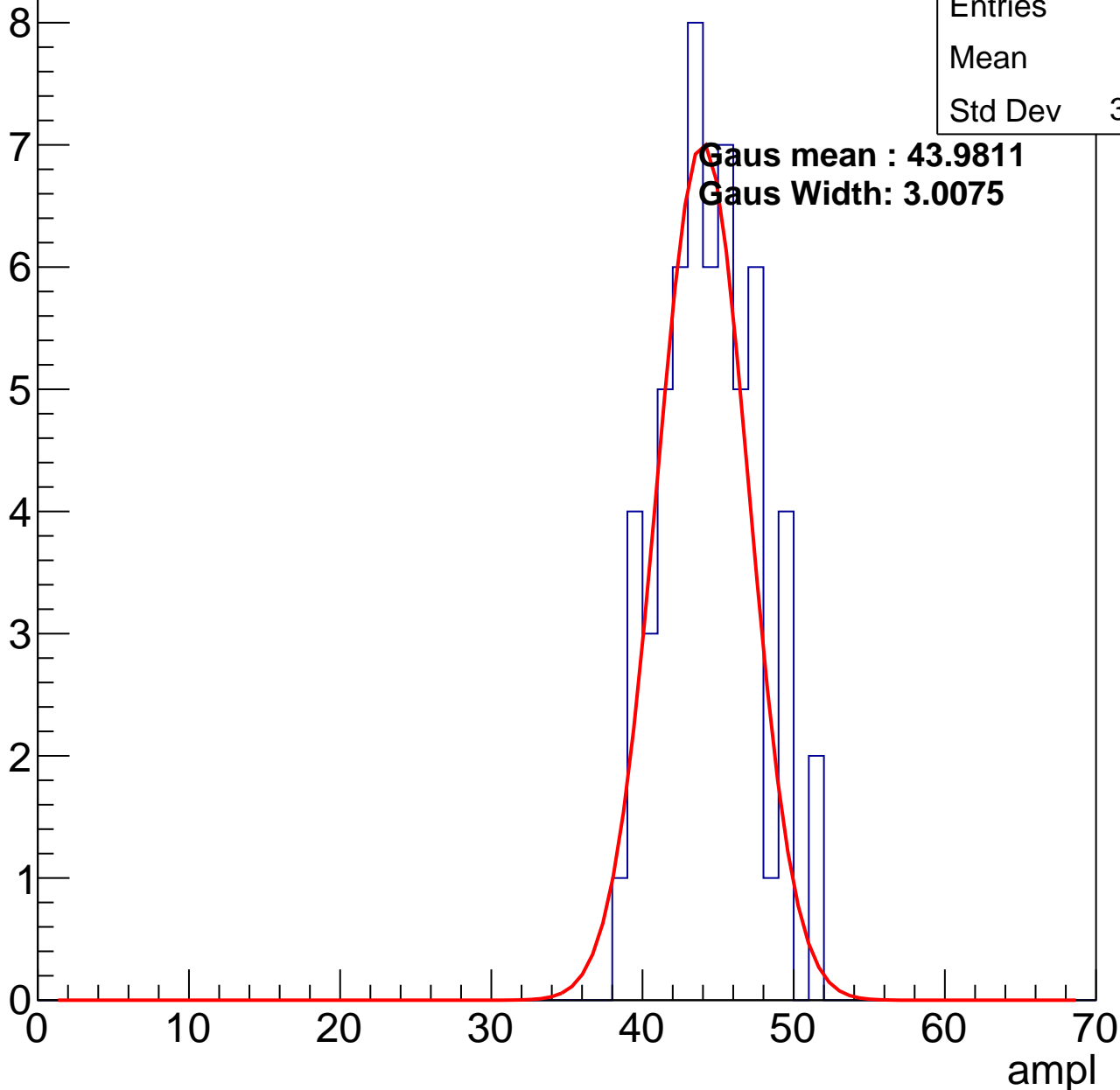
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 44    |
| Std Dev | 3.096 |

Gaus mean : 43.9811

Gaus Width: 3.0075

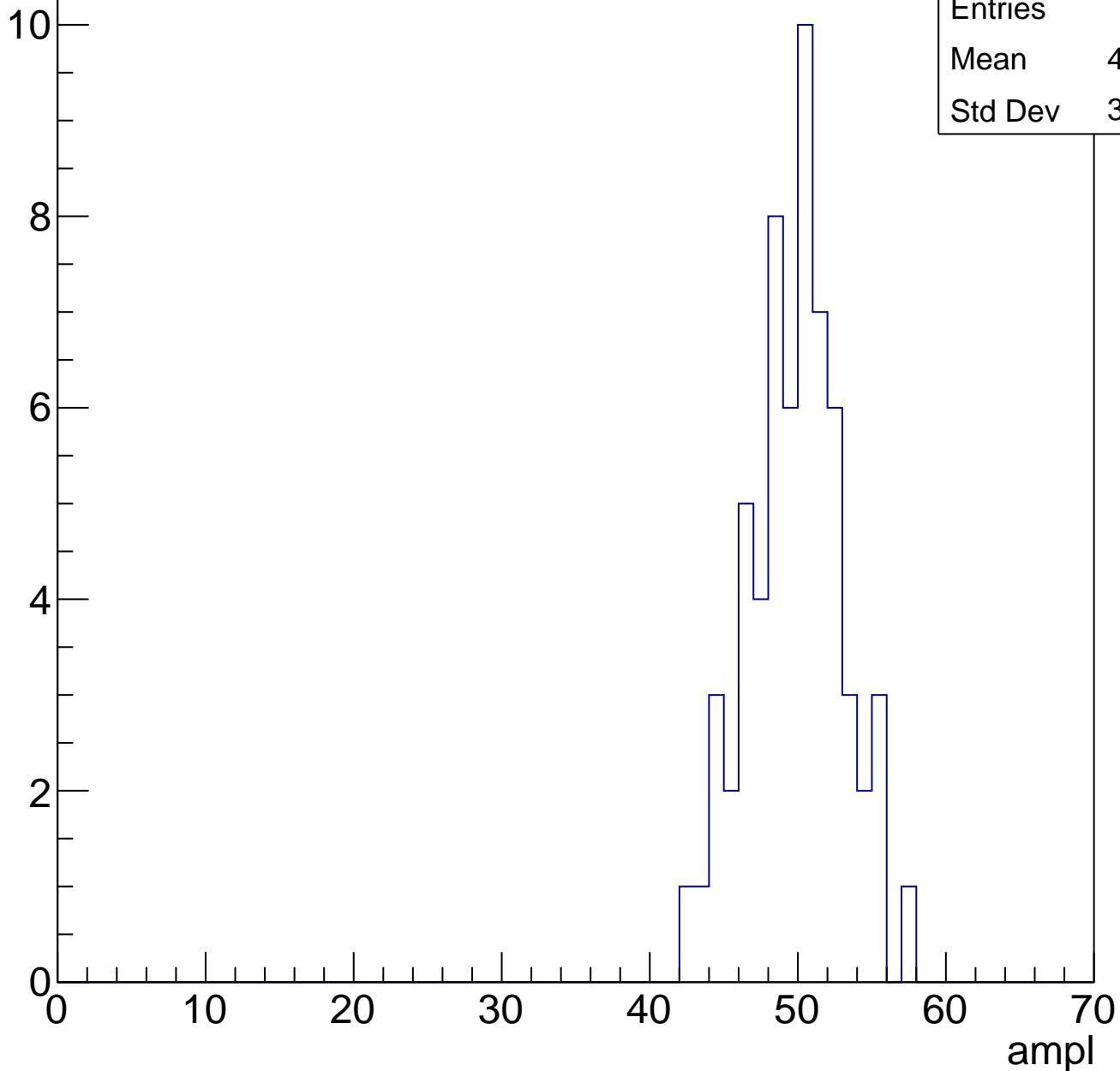


# B0L002S, U1-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 49.37 |
| Std Dev | 3.148 |

Entry

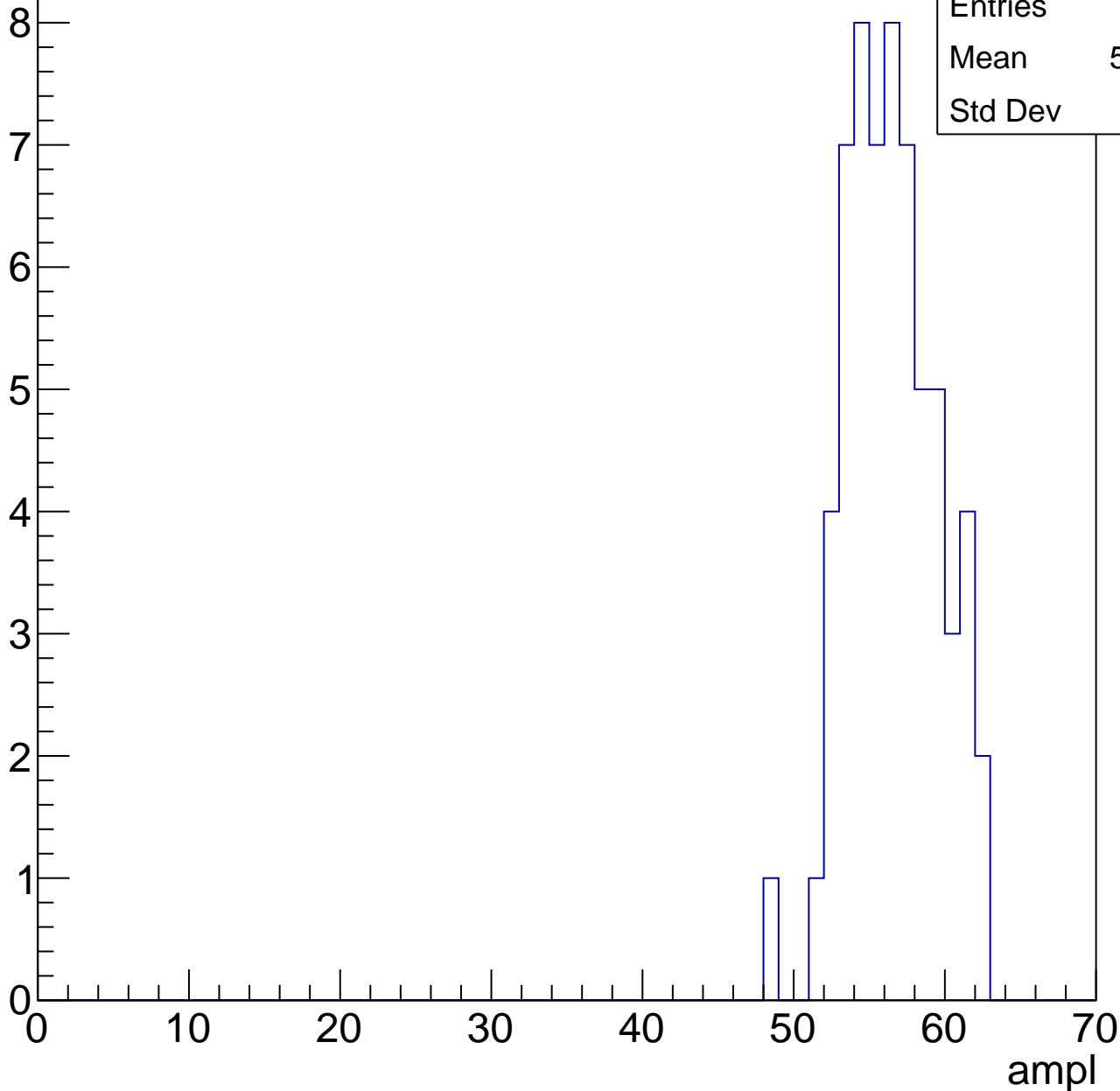


# B0L002S, U1-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 56.05 |
| Std Dev | 2.97  |

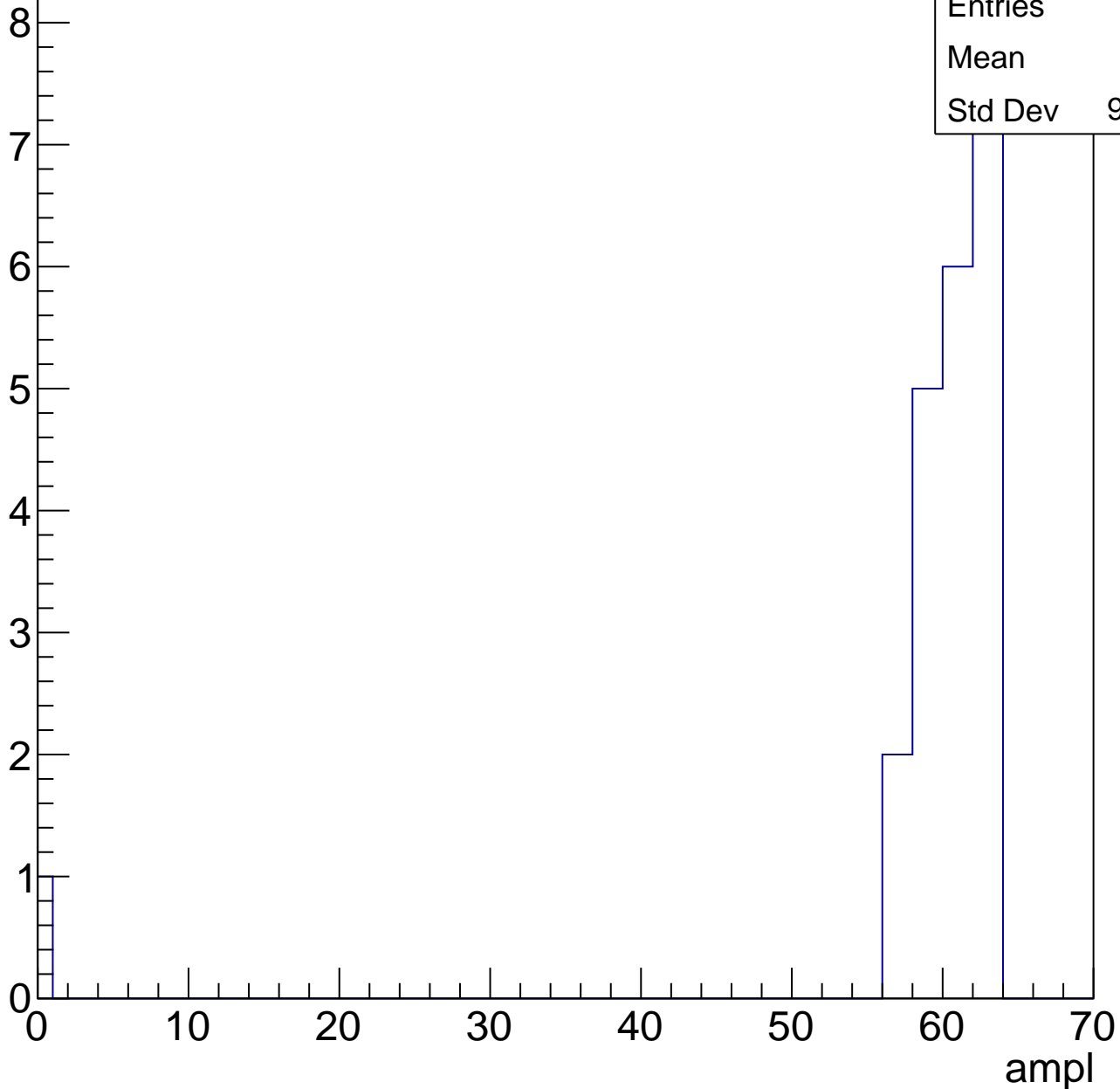


# B0L002S, U1-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 59    |
| Std Dev | 9.329 |



# B0L002S, U1-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

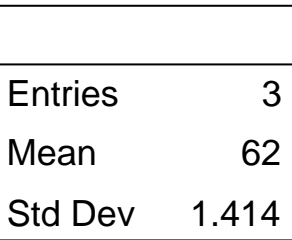
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |       |
|---------|-------|
| Entries | 3     |
| Mean    | 62    |
| Std Dev | 1.414 |

ampl

0 10 20 30 40 50 60 70





# B0L002S, U1-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch77, adc0

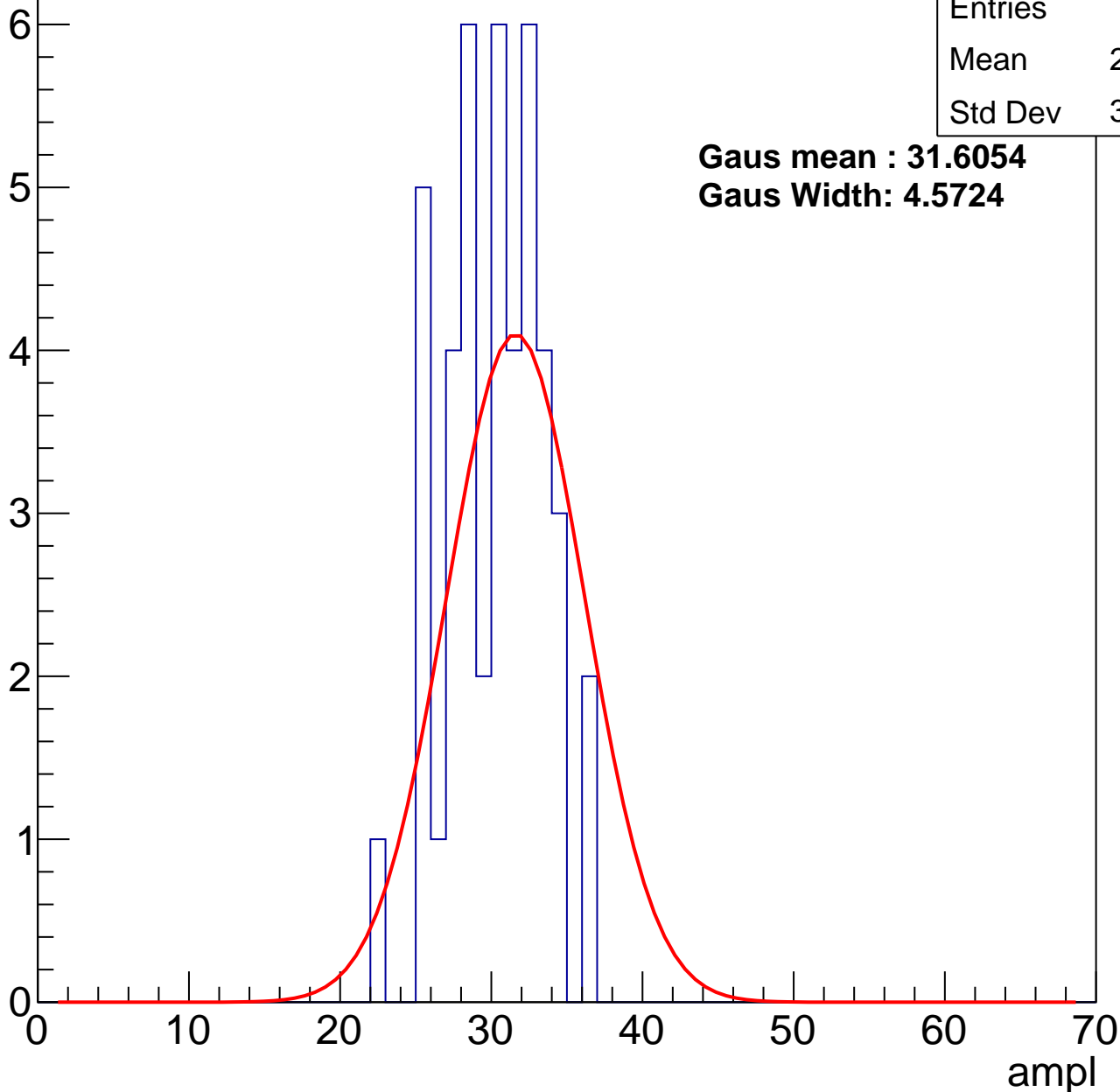
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 29.75 |
| Std Dev | 3.192 |

**Gaus mean : 31.6054**

**Gaus Width: 4.5724**



# B0L002S, U1-ch77, adc1

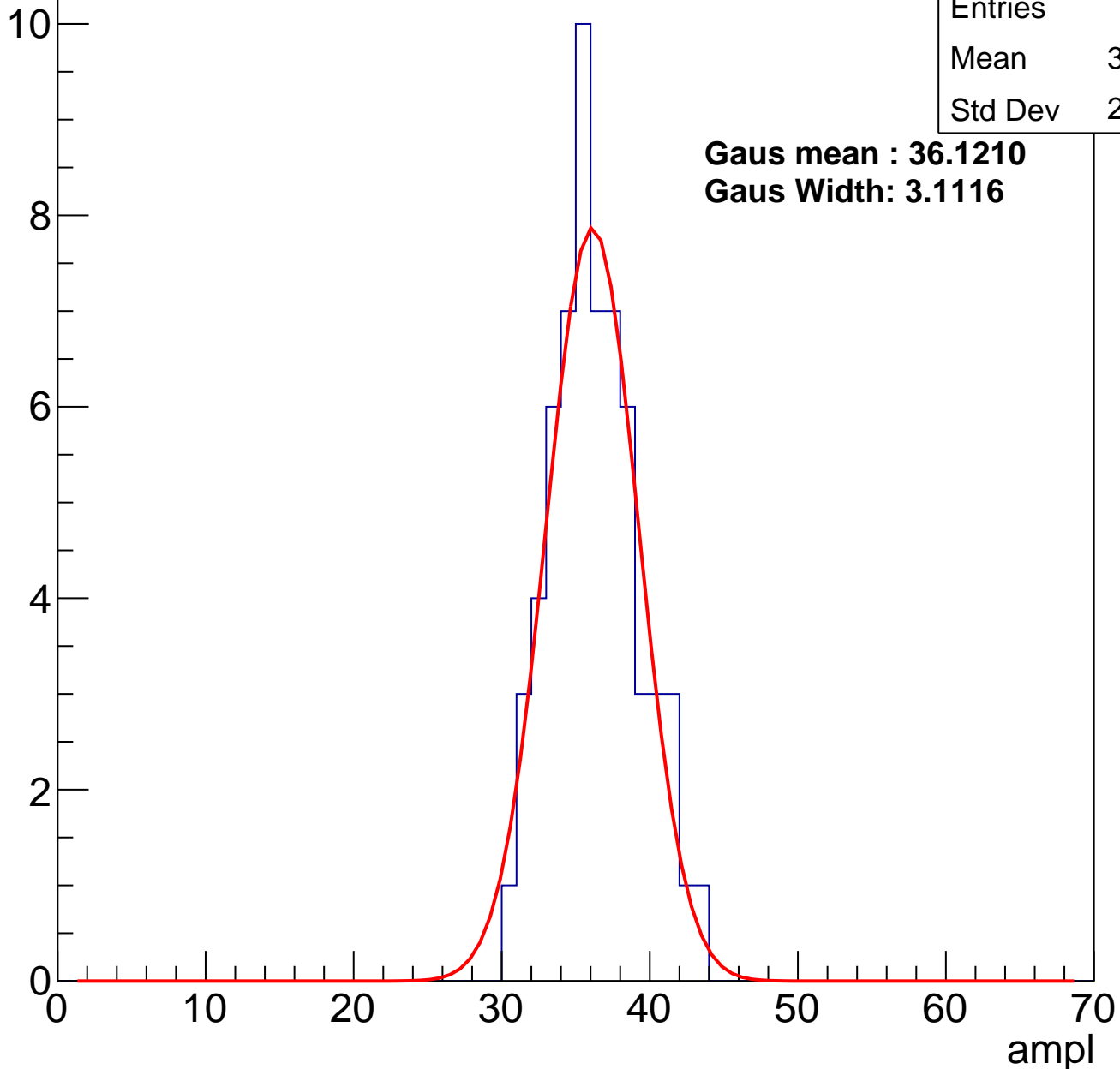
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 35.82 |
| Std Dev | 2.927 |

**Gaus mean : 36.1210**

**Gaus Width: 3.1116**

Entry



# B0L002S, U1-ch77, adc2

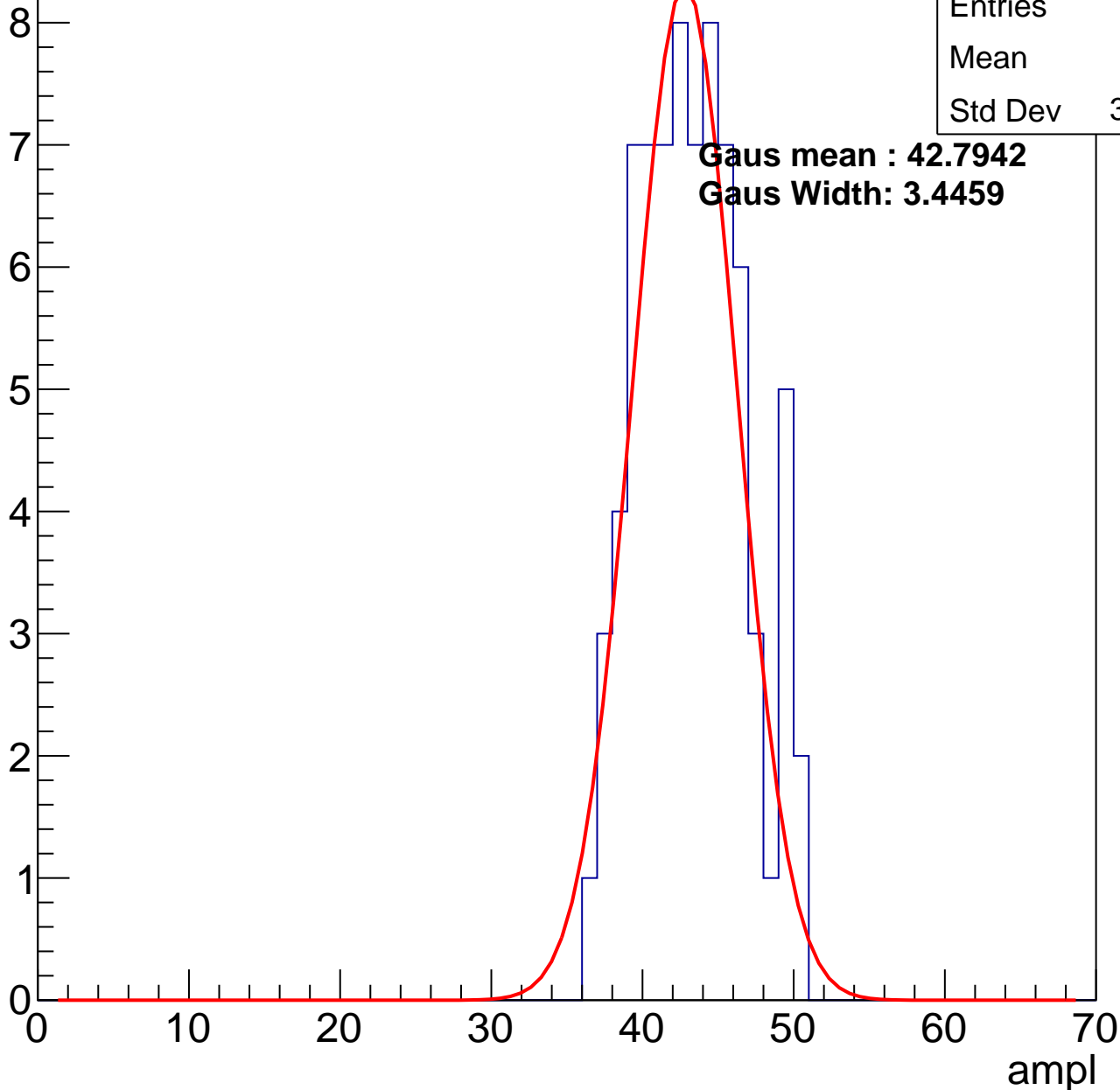
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 42.8  |
| Std Dev | 3.453 |

**Gaus mean : 42.7942**

**Gaus Width: 3.4459**

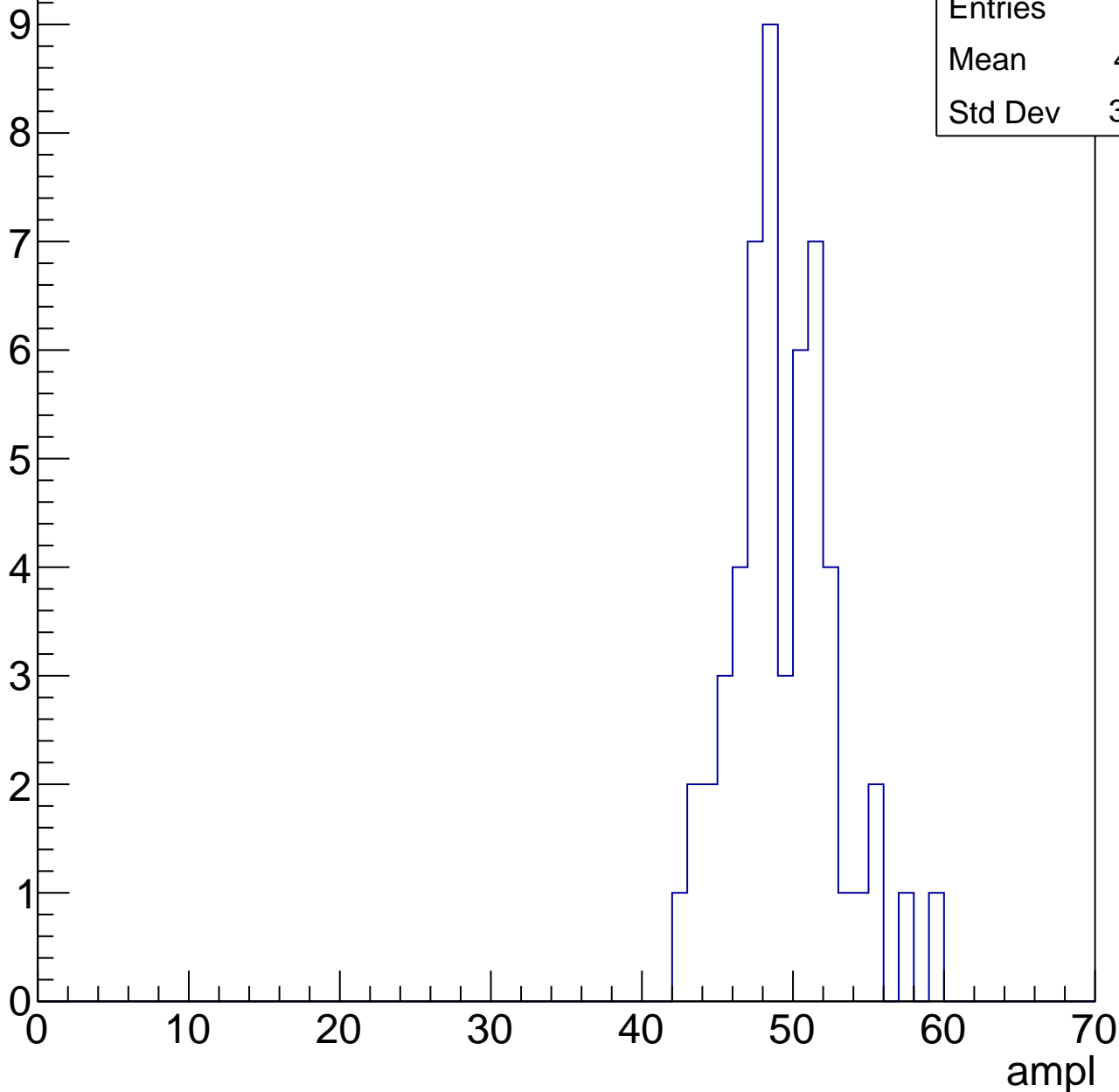


# B0L002S, U1-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 48.91 |
| Std Dev | 3.439 |

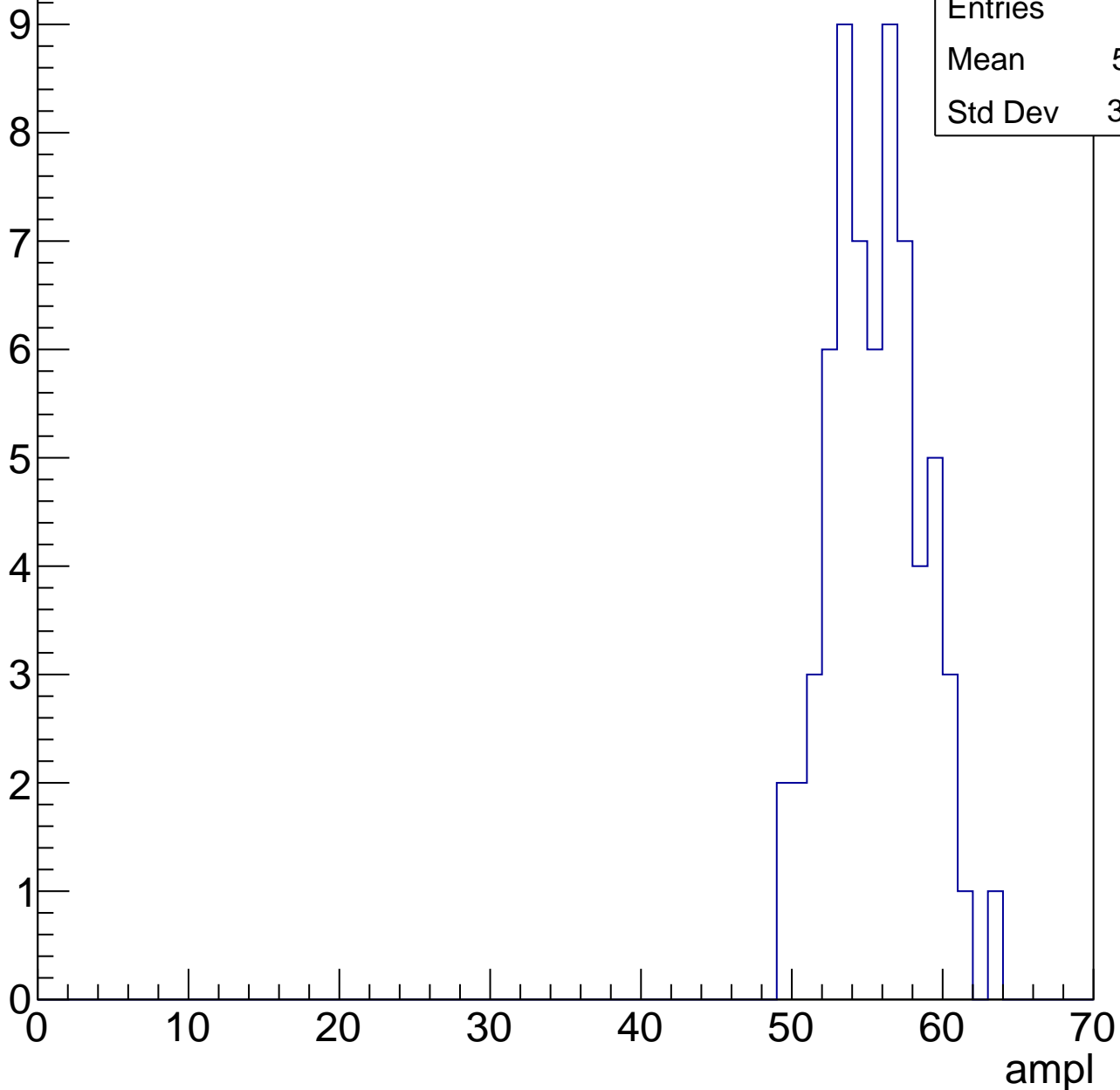


# B0L002S, U1-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 55.11 |
| Std Dev | 3.024 |

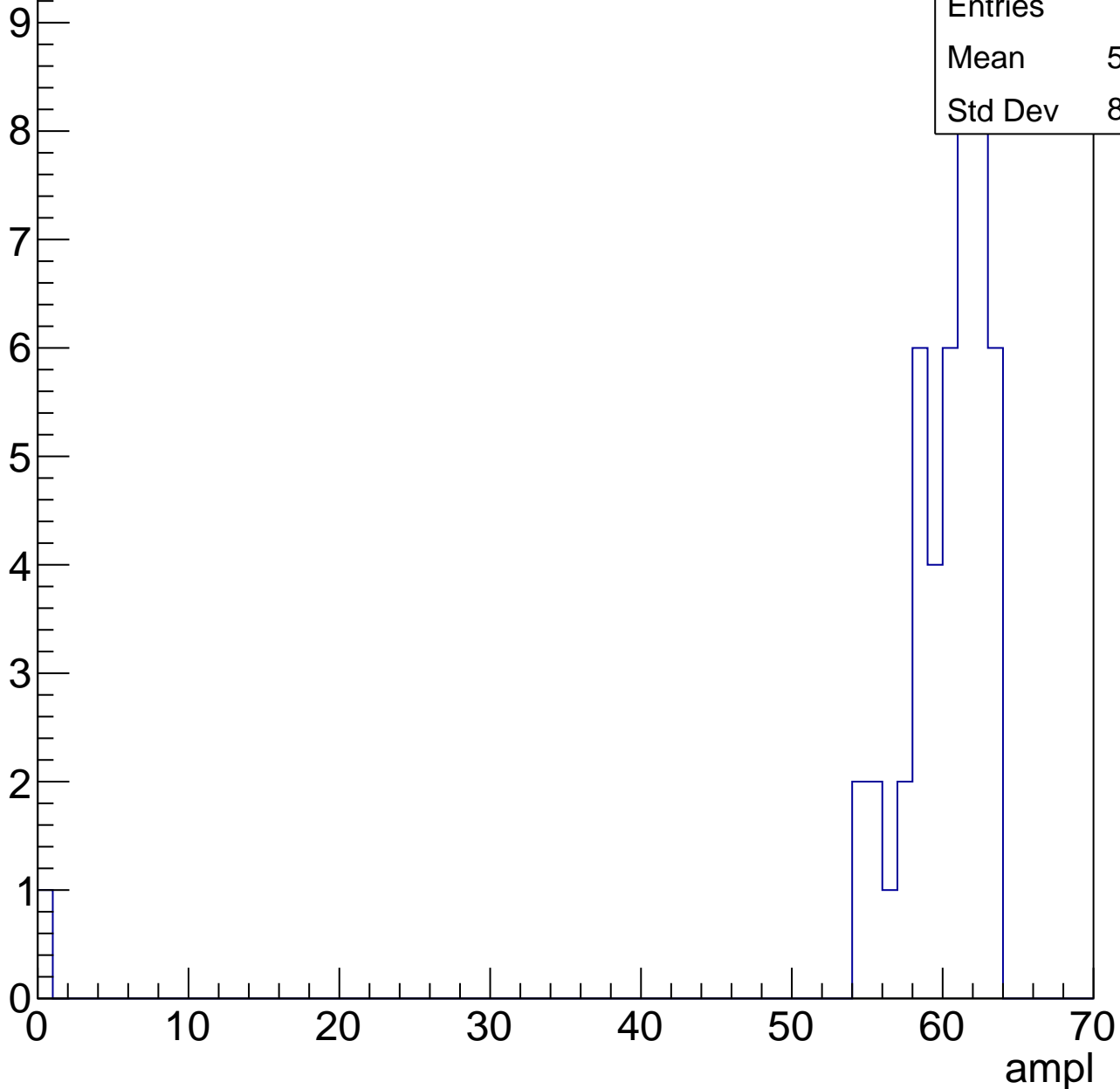


# B0L002S, U1-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 58.64 |
| Std Dev | 8.986 |



# B0L002S, U1-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch78, adc0

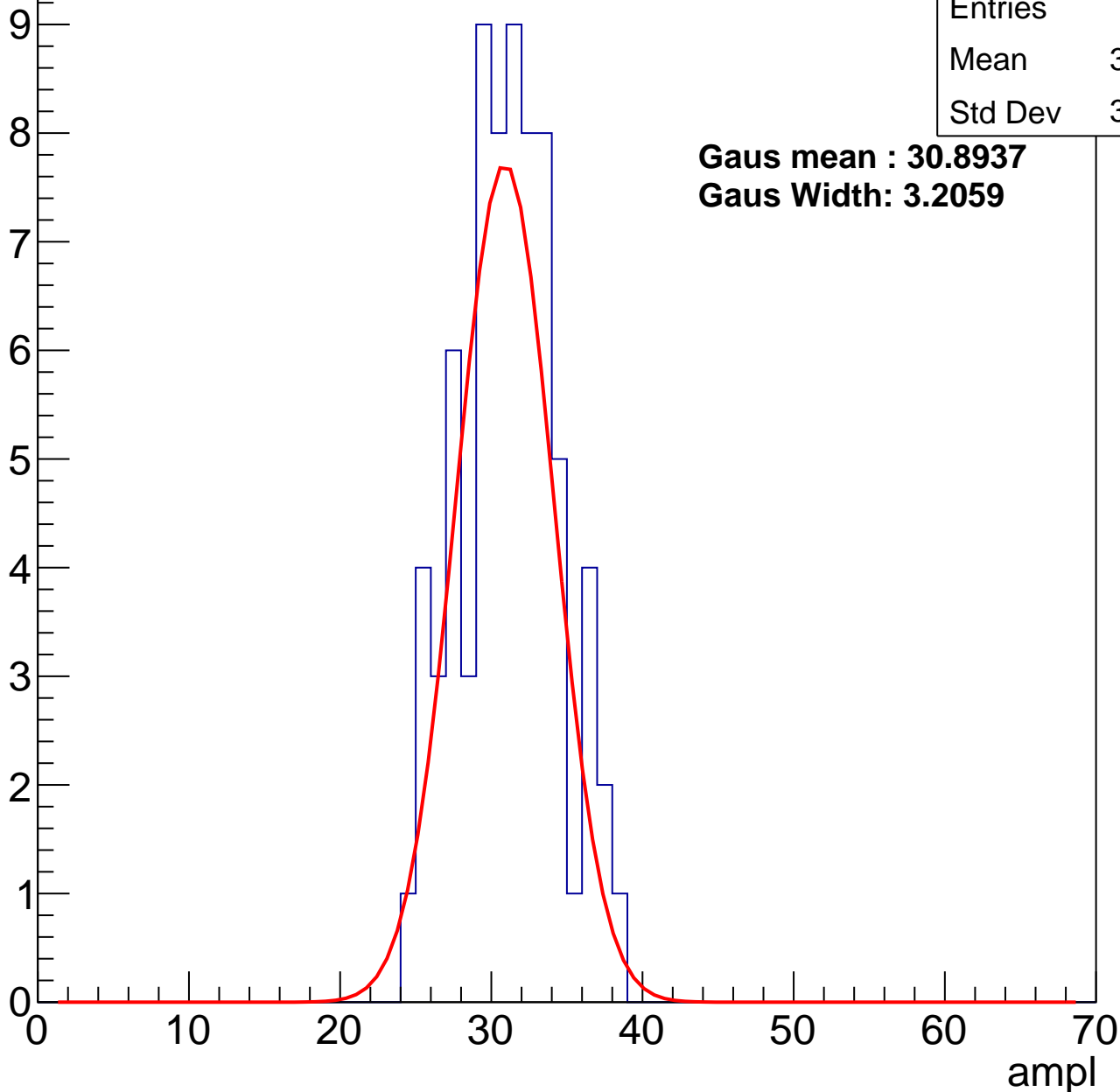
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 30.68 |
| Std Dev | 3.227 |

**Gaus mean : 30.8937**

**Gaus Width: 3.2059**



# B0L002S, U1-ch78, adc1

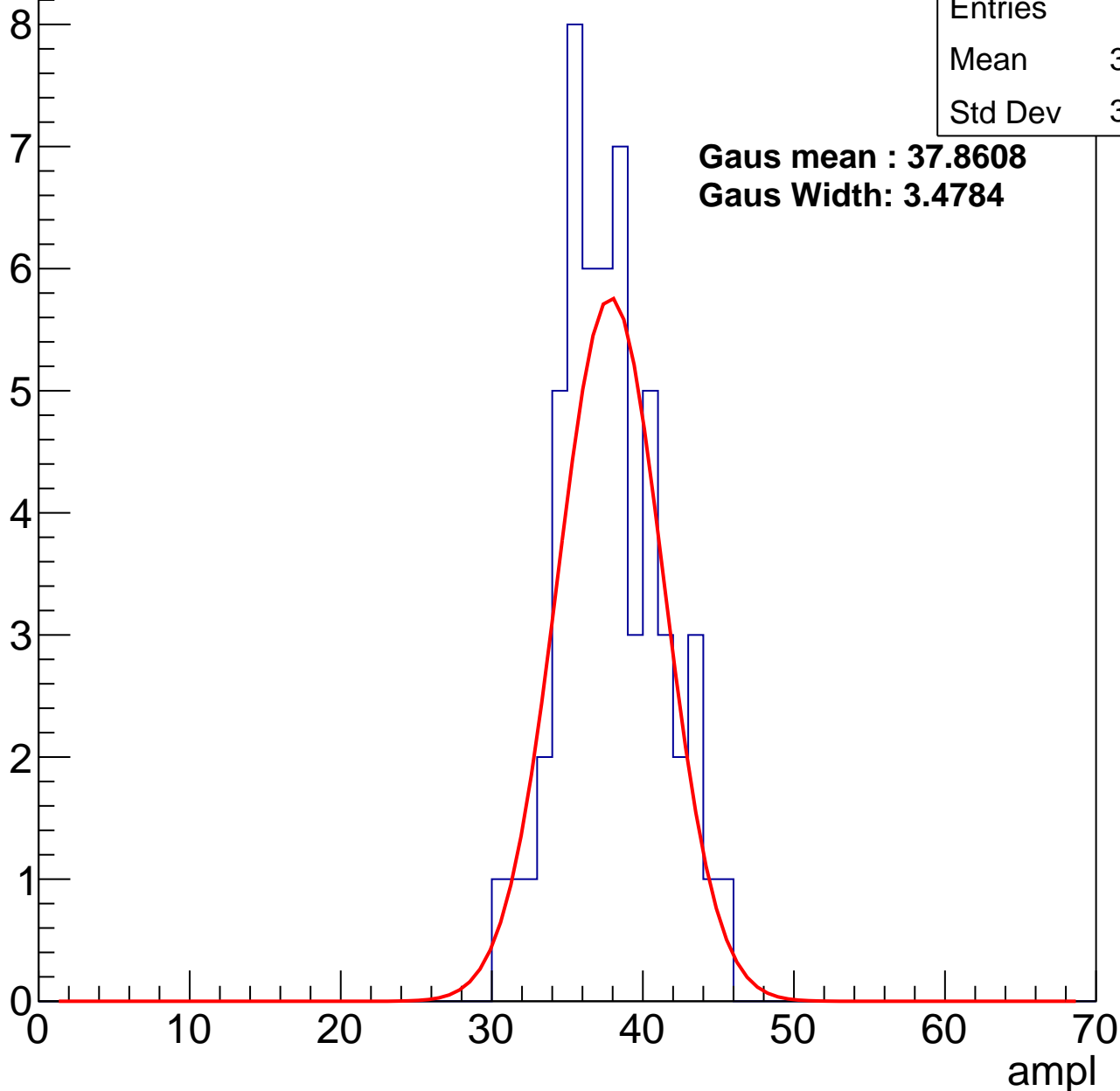
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 37.36 |
| Std Dev | 3.305 |

**Gaus mean : 37.8608**

**Gaus Width: 3.4784**



# B0L002S, U1-ch78, adc2

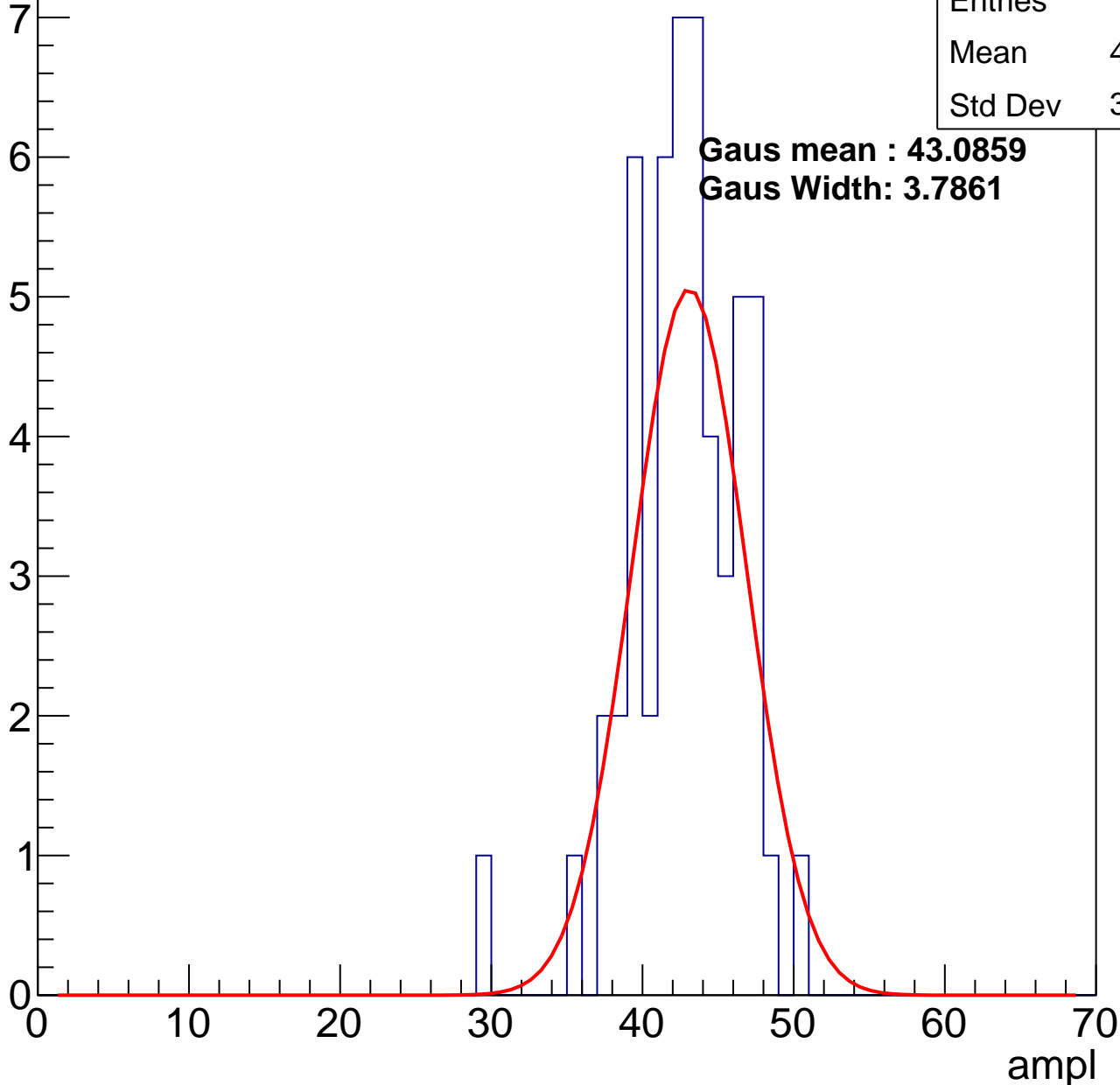
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 42.32 |
| Std Dev | 3.679 |

**Gaus mean : 43.0859**

**Gaus Width: 3.7861**

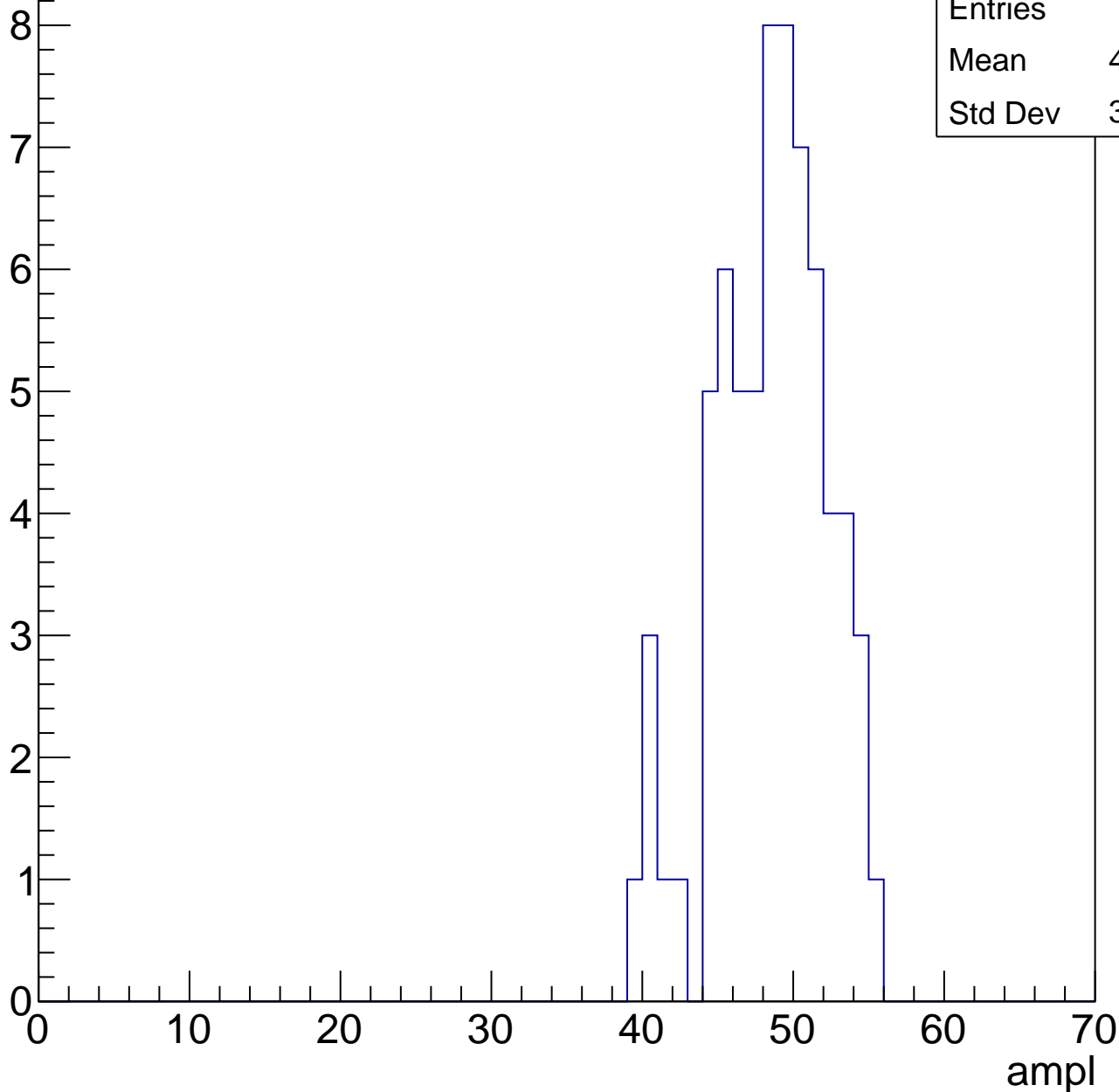


# B0L002S, U1-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

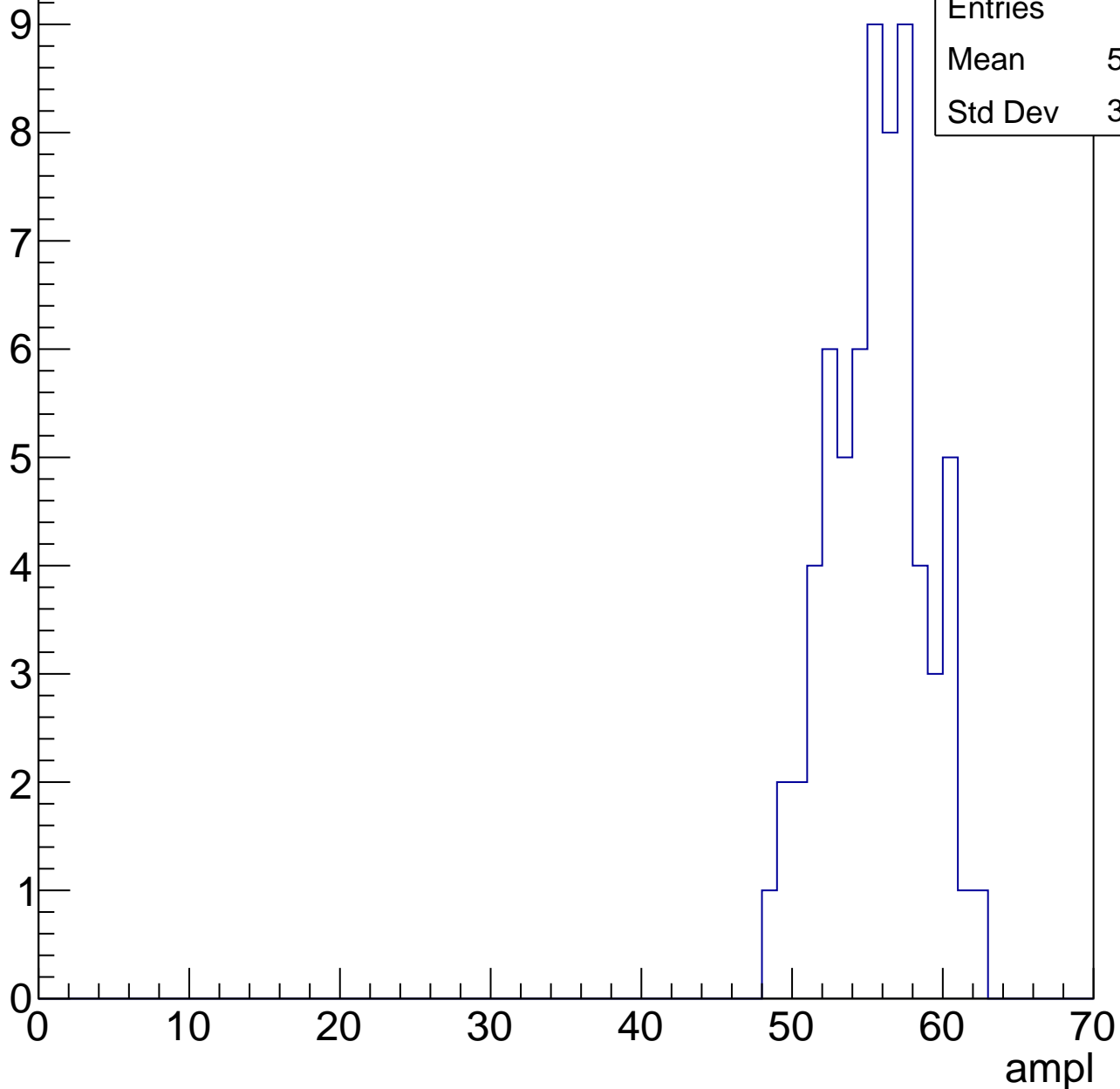
|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 48.03 |
| Std Dev | 3.694 |



# B0L002S, U1-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

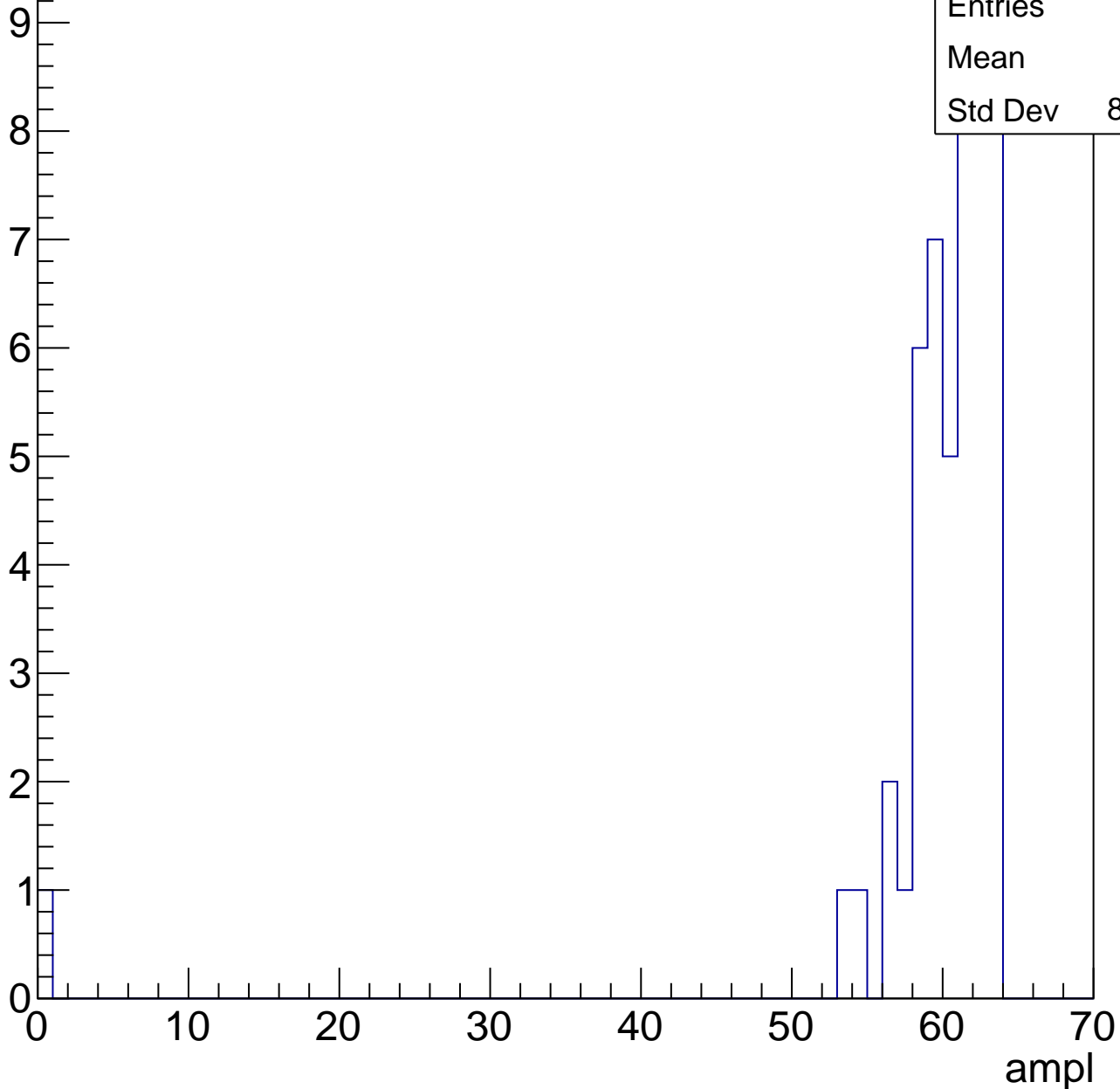


# B0L002S, U1-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 58.9  |
| Std Dev | 8.818 |



# B0L002S, U1-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch79, adc0

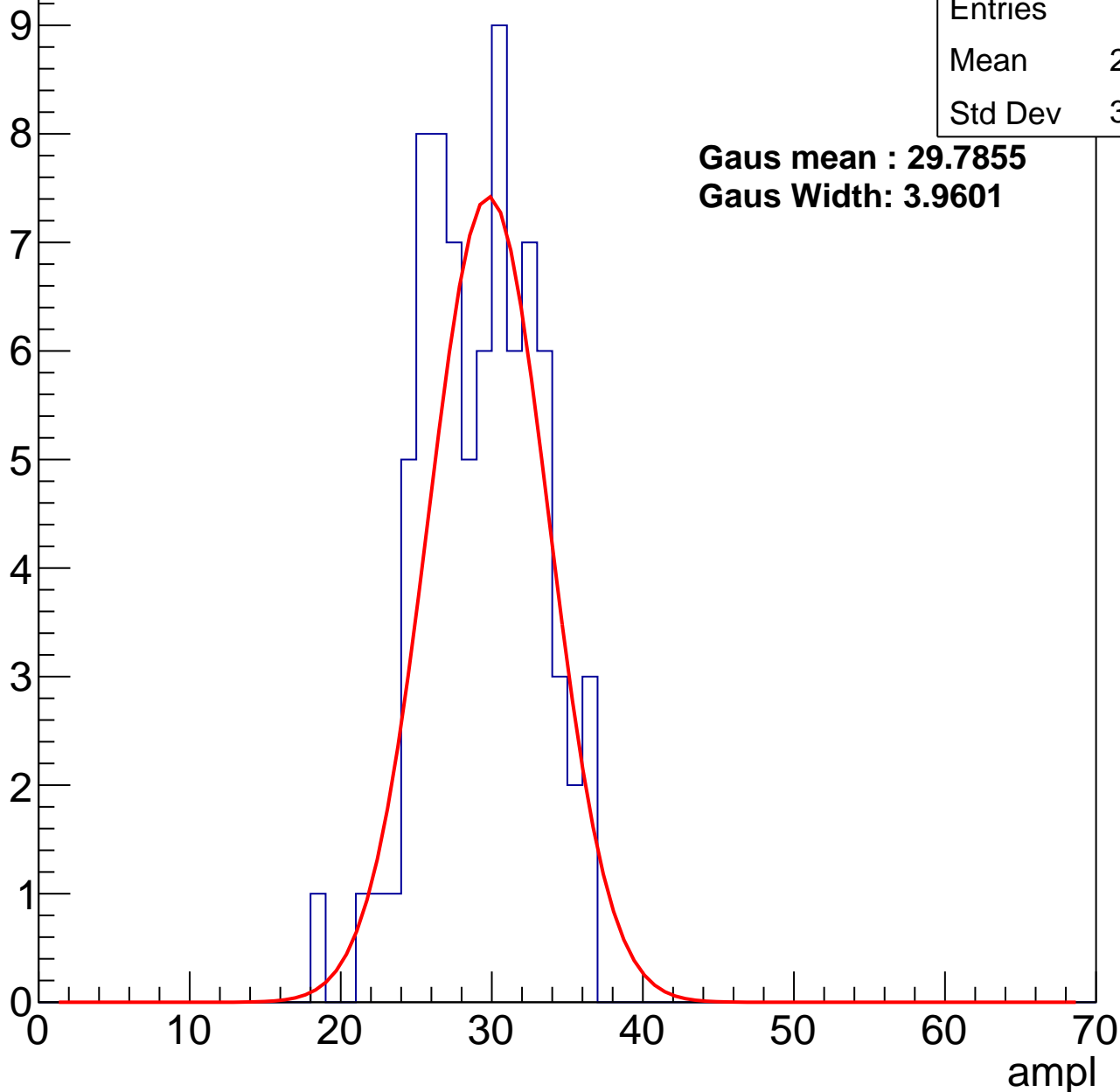
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 28.77 |
| Std Dev | 3.748 |

**Gaus mean : 29.7855**

**Gaus Width: 3.9601**



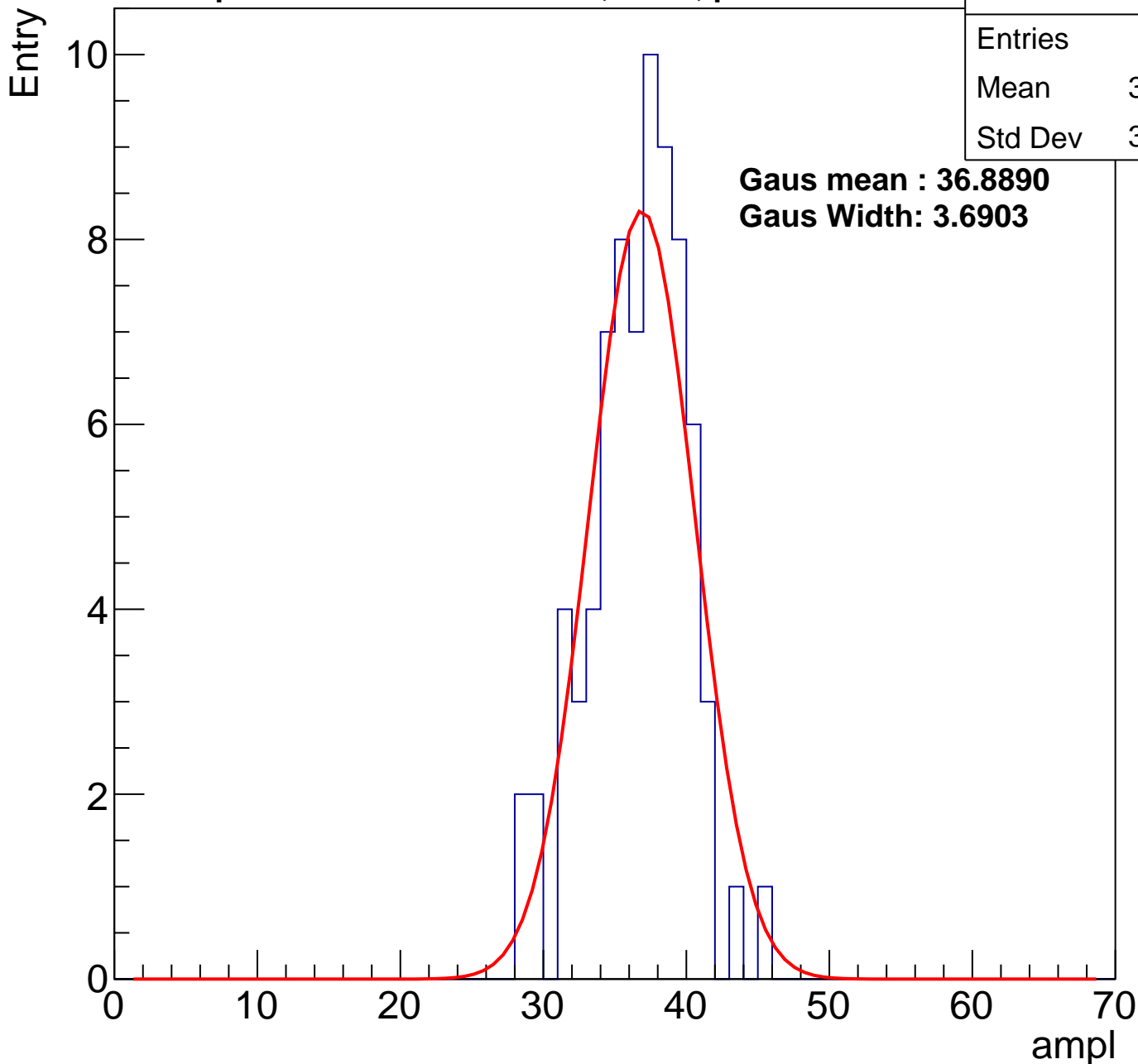
# B0L002S, U1-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 36.15 |
| Std Dev | 3.393 |

**Gaus mean : 36.8890**

**Gaus Width: 3.6903**



# B0L002S, U1-ch79, adc2

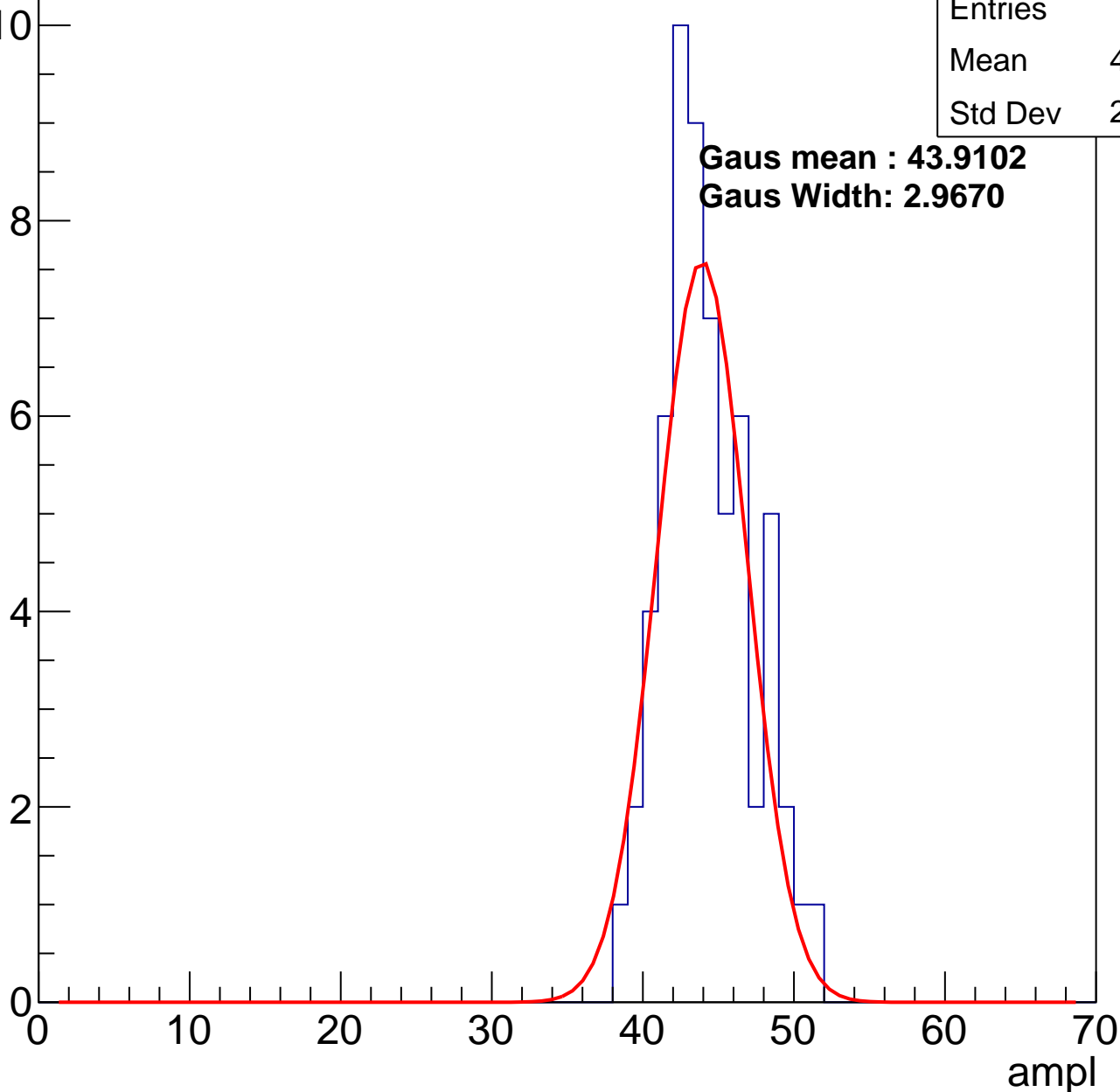
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 43.79 |
| Std Dev | 2.898 |

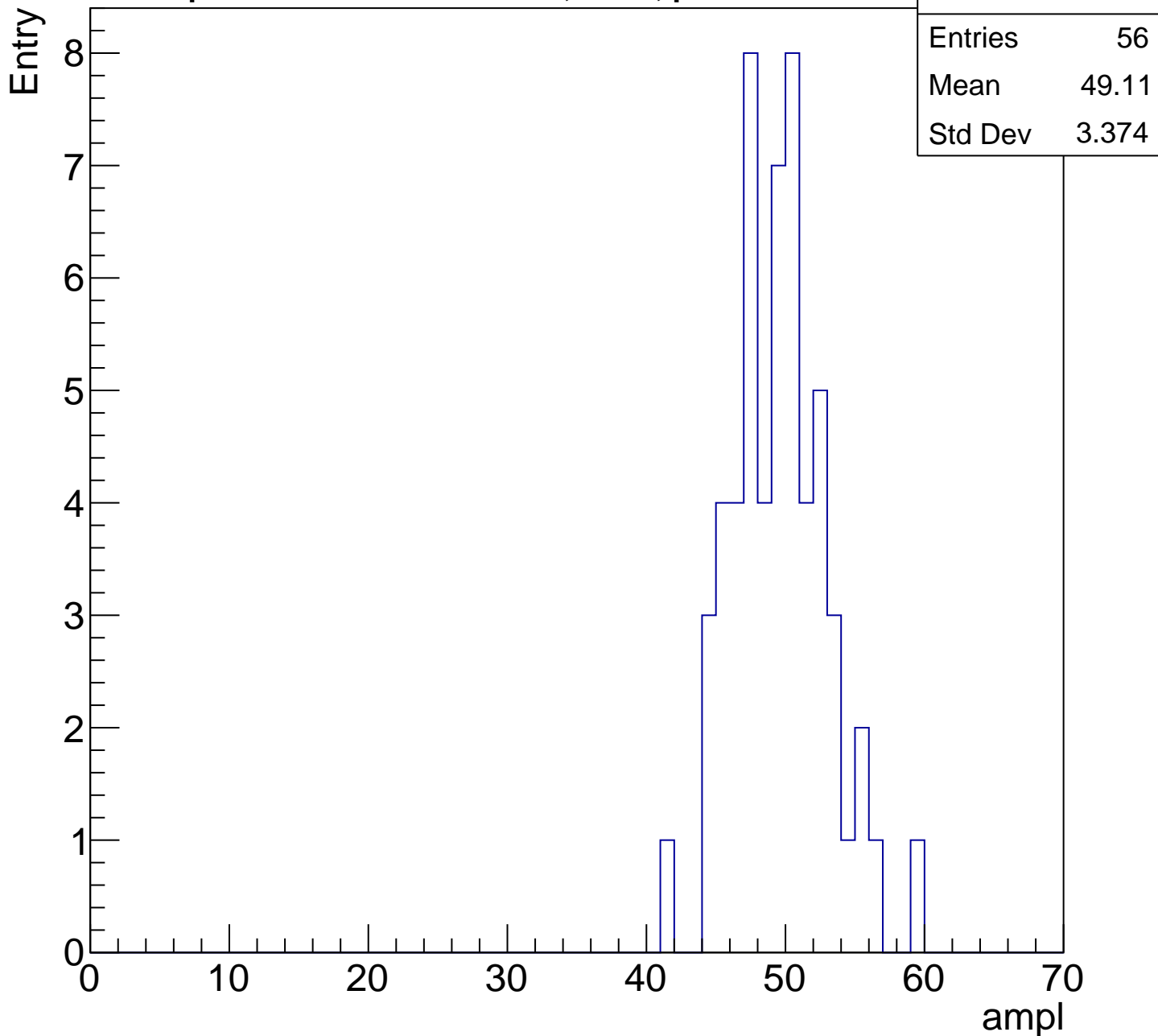
**Gaus mean : 43.9102**

**Gaus Width: 2.9670**



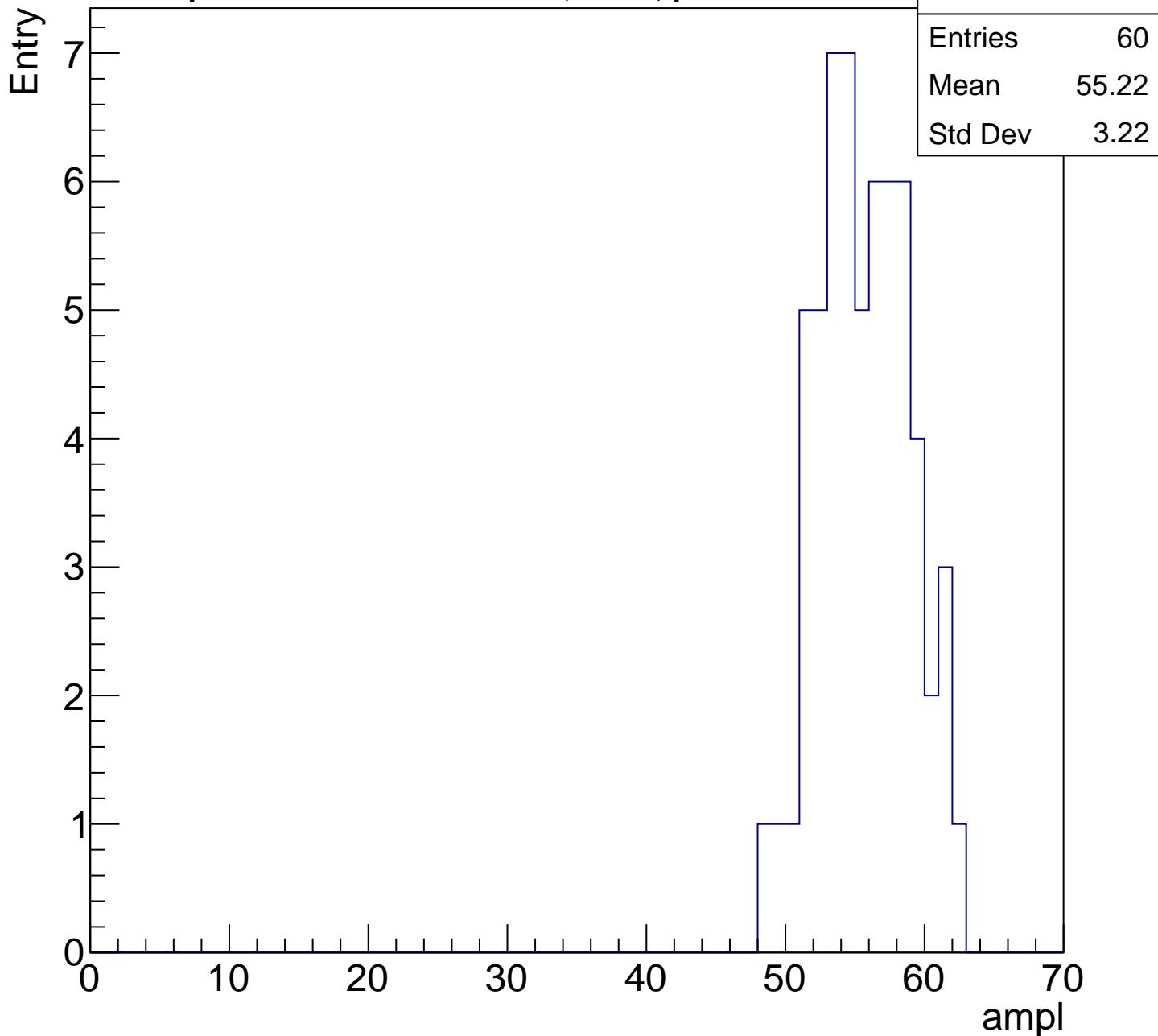
# B0L002S, U1-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

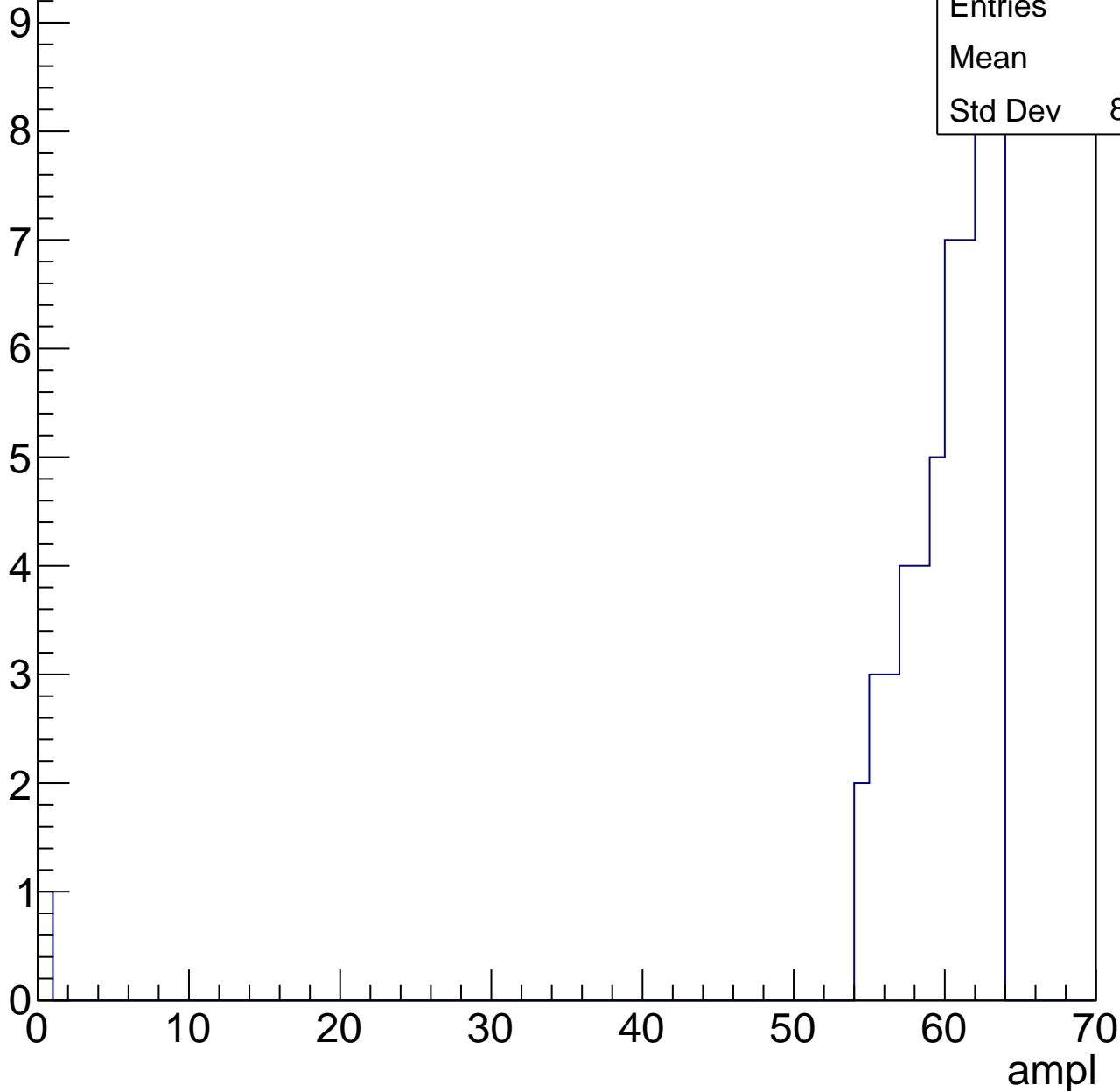


# B0L002S, U1-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 58.6  |
| Std Dev | 8.544 |



# B0L002S, U1-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch80, adc0

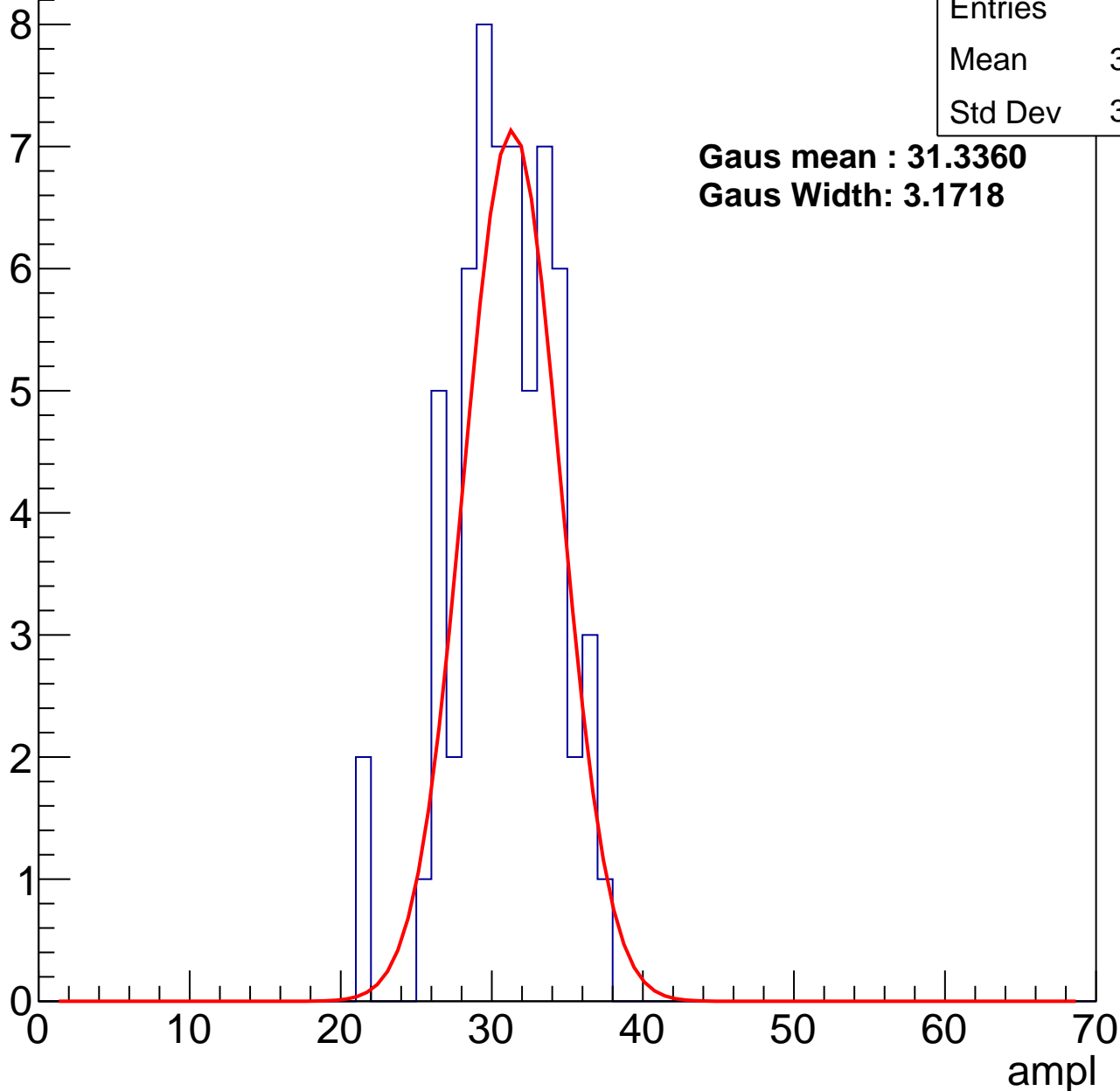
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 30.45 |
| Std Dev | 3.354 |

**Gaus mean : 31.3360**

**Gaus Width: 3.1718**



# B0L002S, U1-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 37.37 |
| Std Dev | 3.658 |

**Gaus mean : 37.8794**

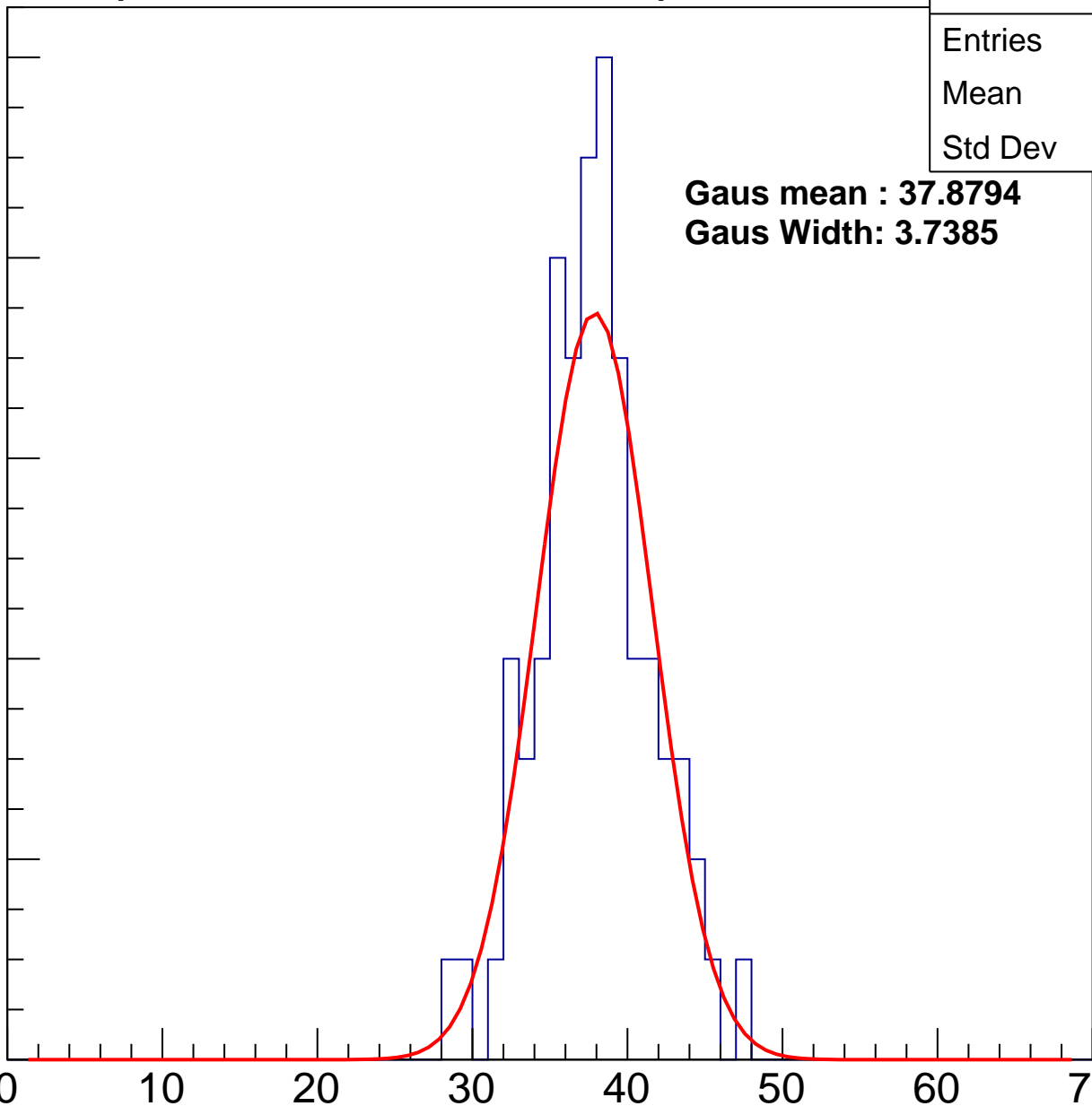
**Gaus Width: 3.7385**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

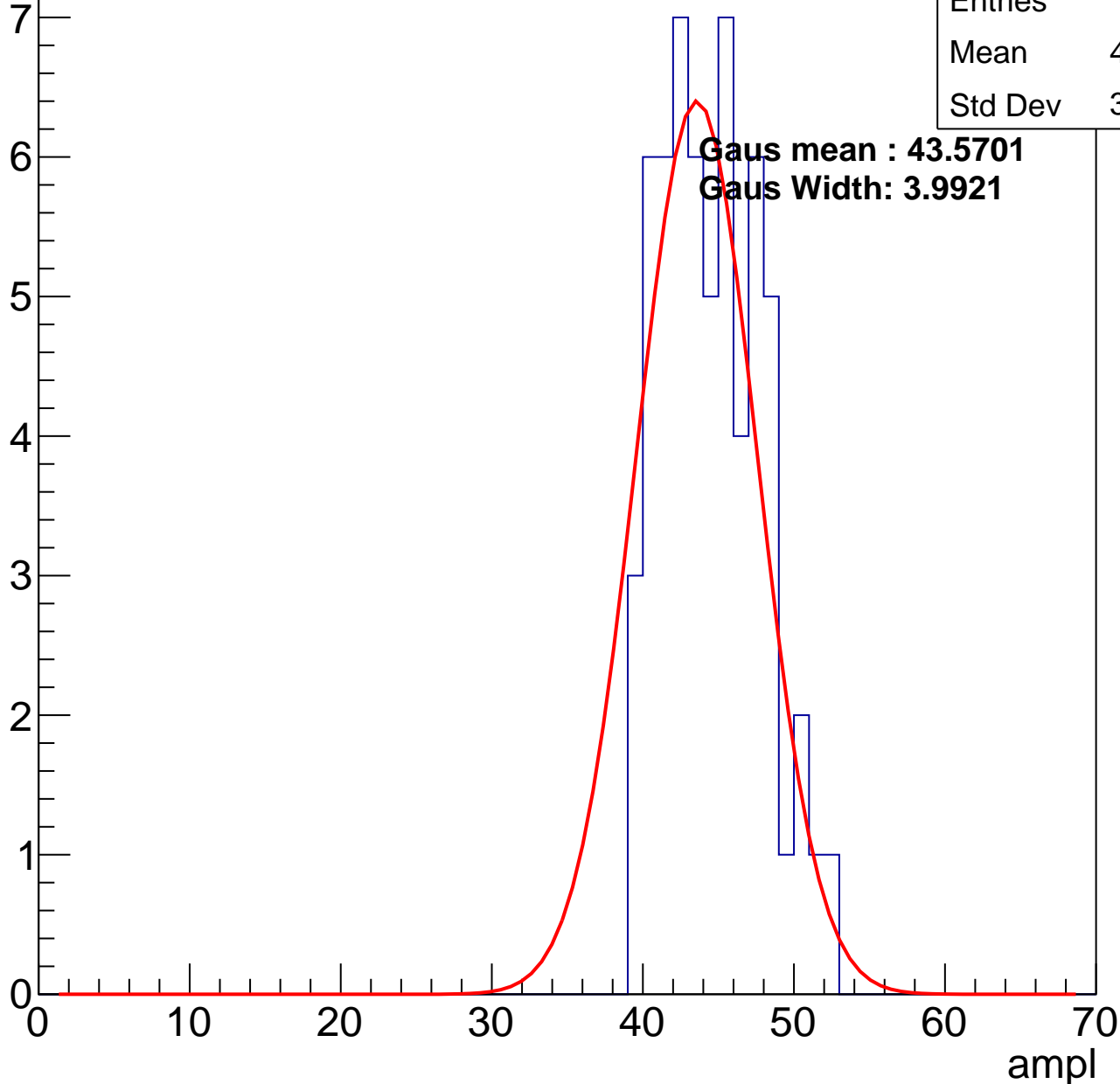


# B0L002S, U1-ch80, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 44.13 |
| Std Dev | 3.227 |

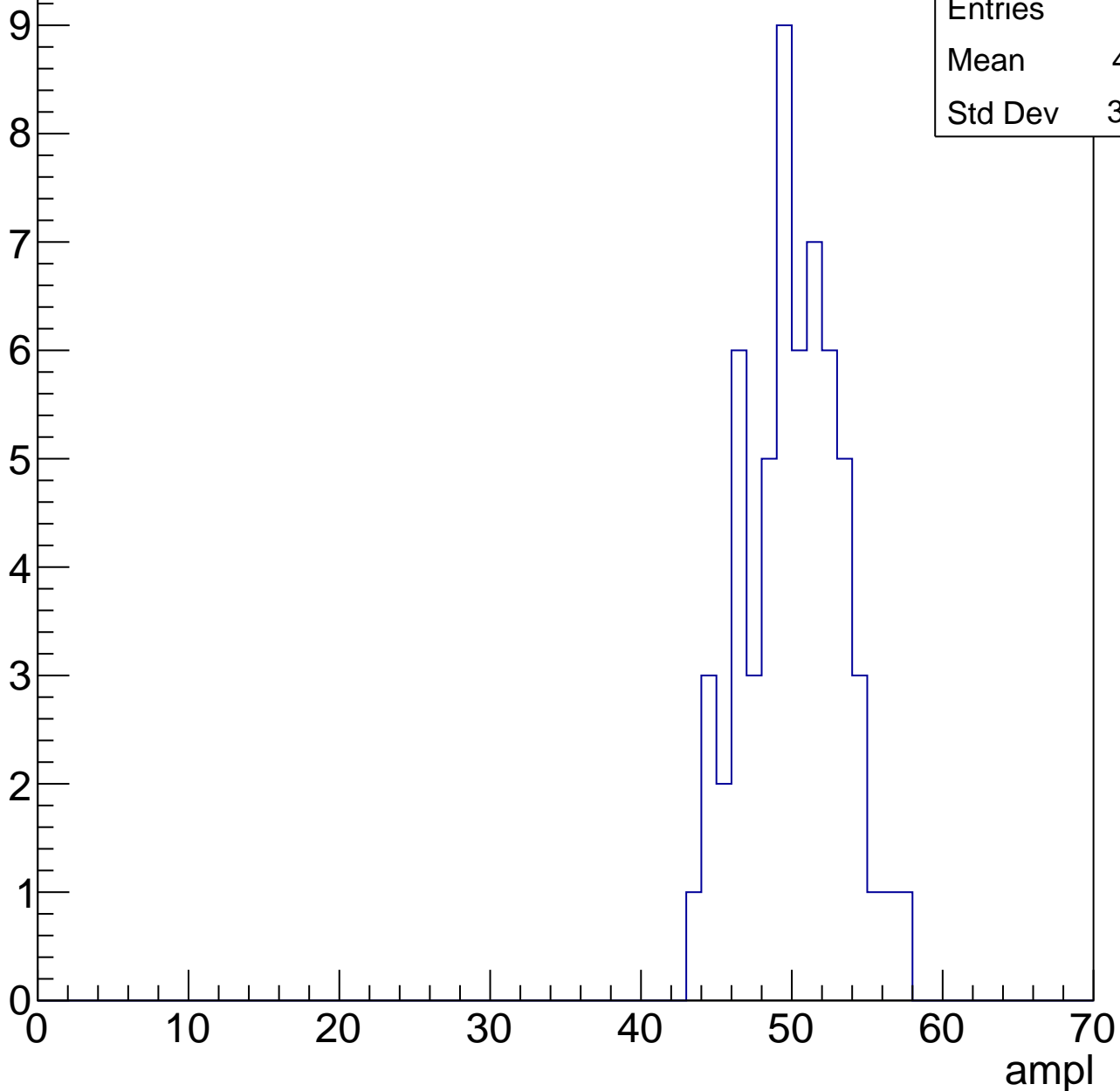


# B0L002S, U1-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 49.61 |
| Std Dev | 3.146 |

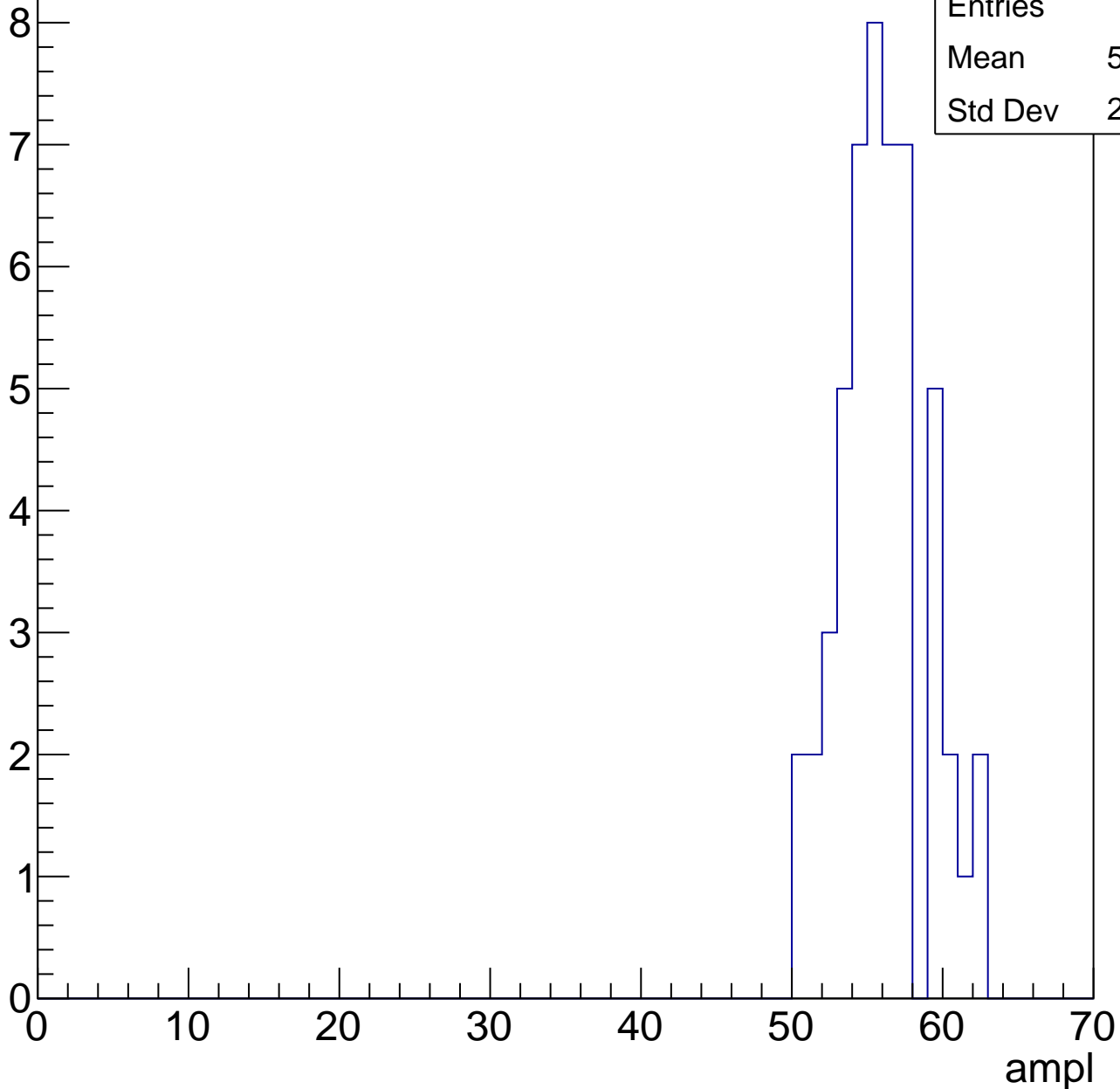


# B0L002S, U1-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 55.53 |
| Std Dev | 2.872 |

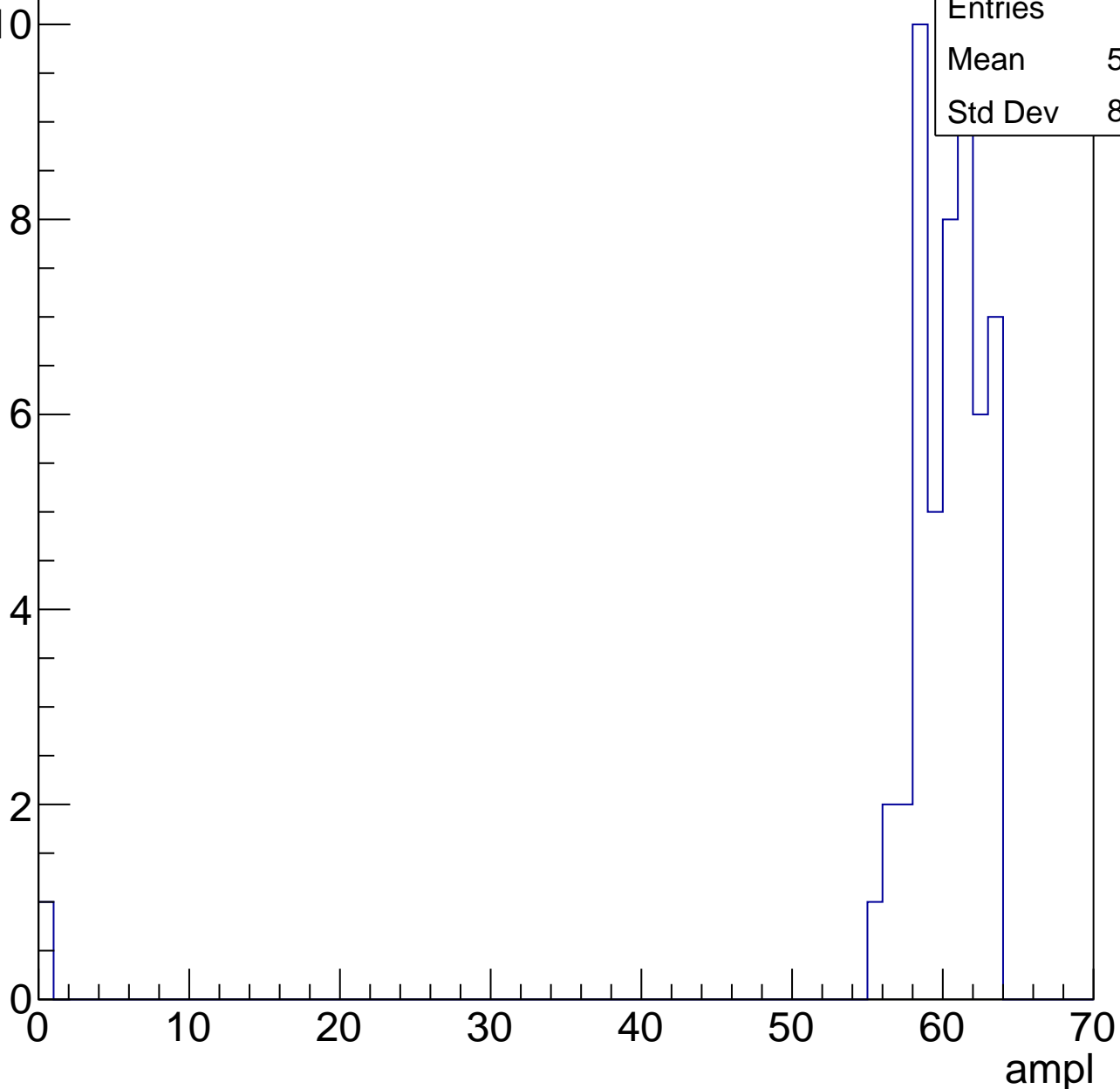


# B0L002S, U1-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 58.78 |
| Std Dev | 8.564 |



# B0L002S, U1-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

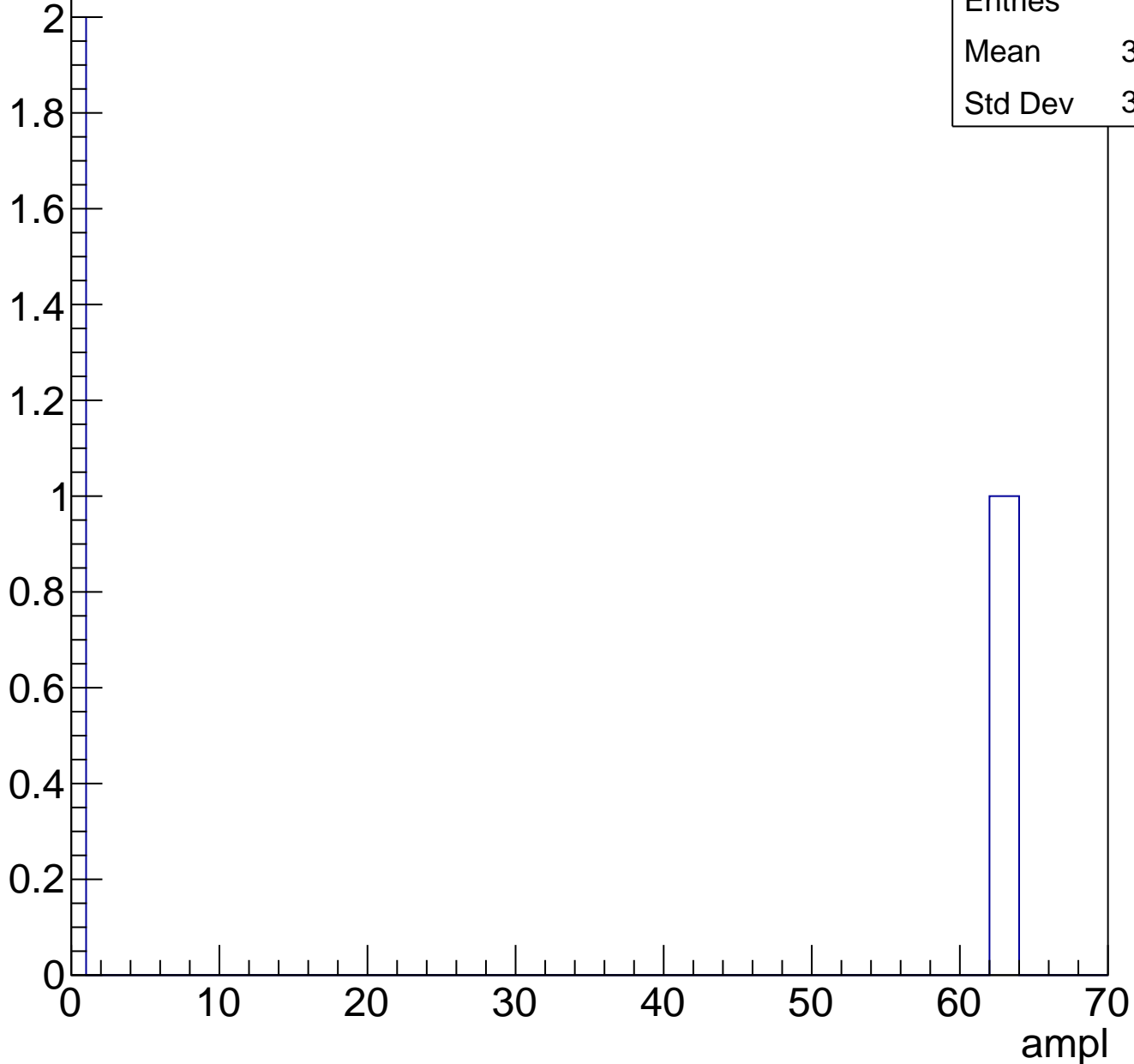




# B0L002S, U1-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch81, adc0

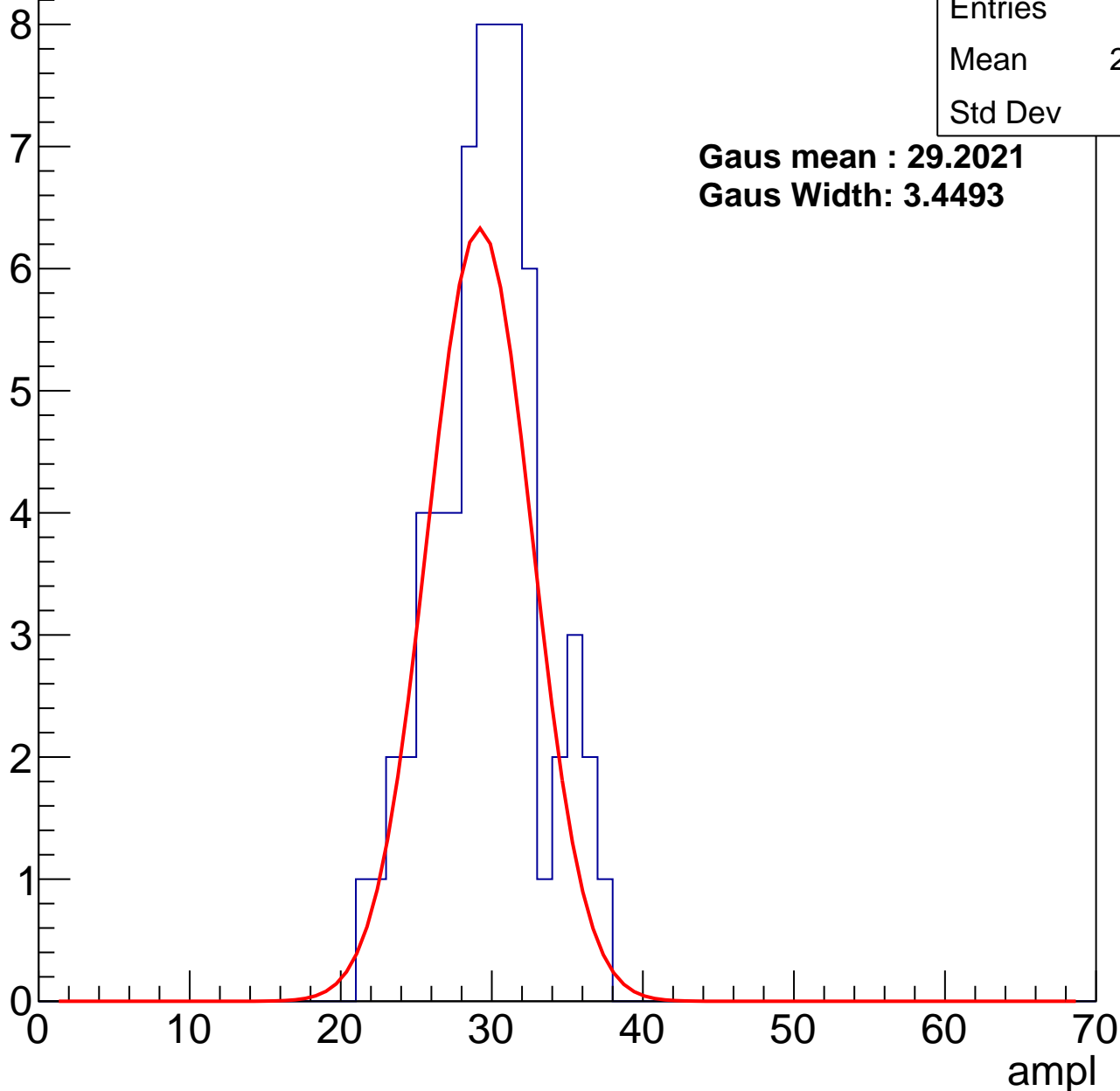
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 29.25 |
| Std Dev | 3.5   |

**Gaus mean : 29.2021**

**Gaus Width: 3.4493**



# B0L002S, U1-ch81, adc1

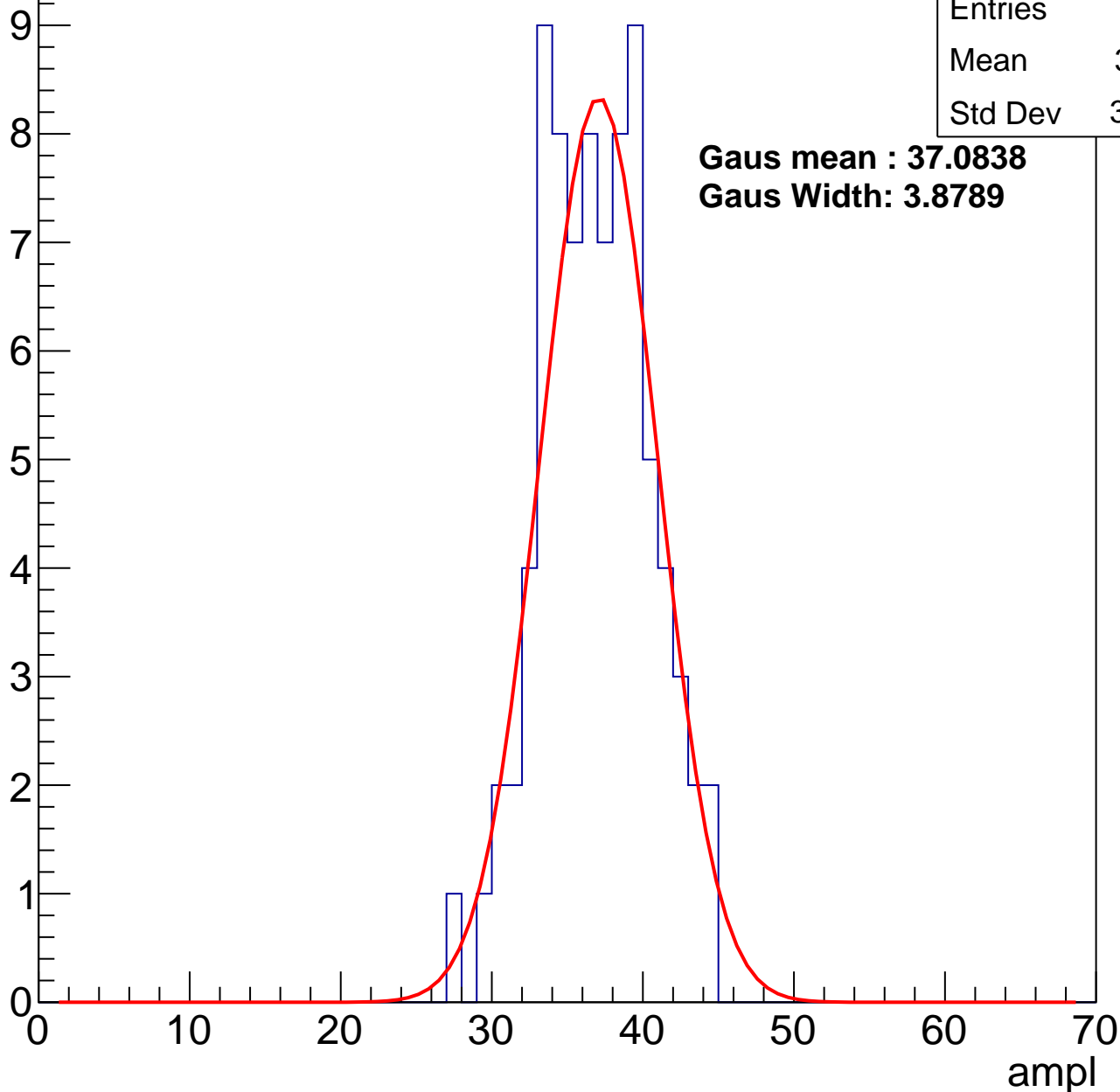
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 36.41 |
| Std Dev | 3.588 |

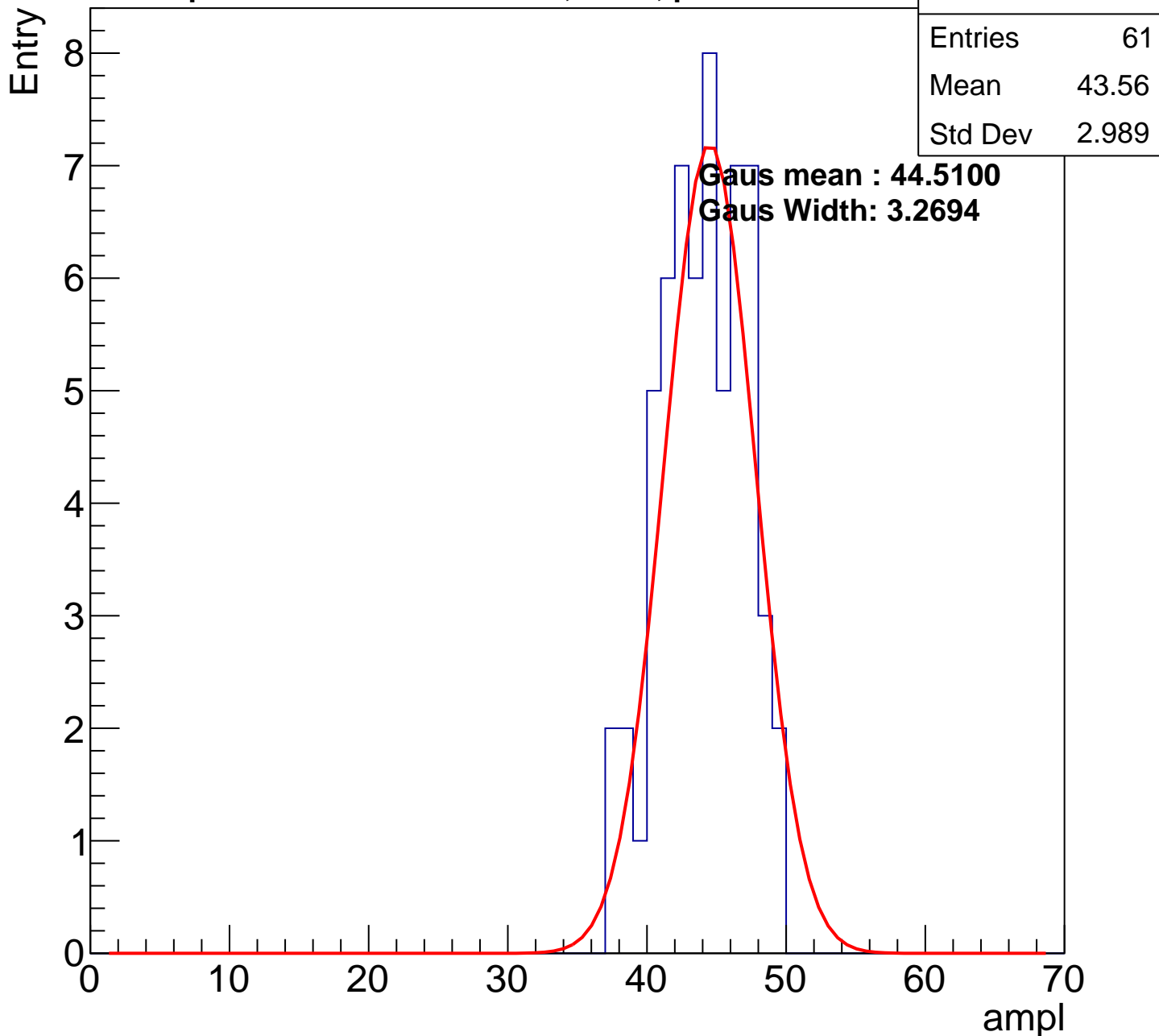
**Gaus mean : 37.0838**

**Gaus Width: 3.8789**



# B0L002S, U1-ch81, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

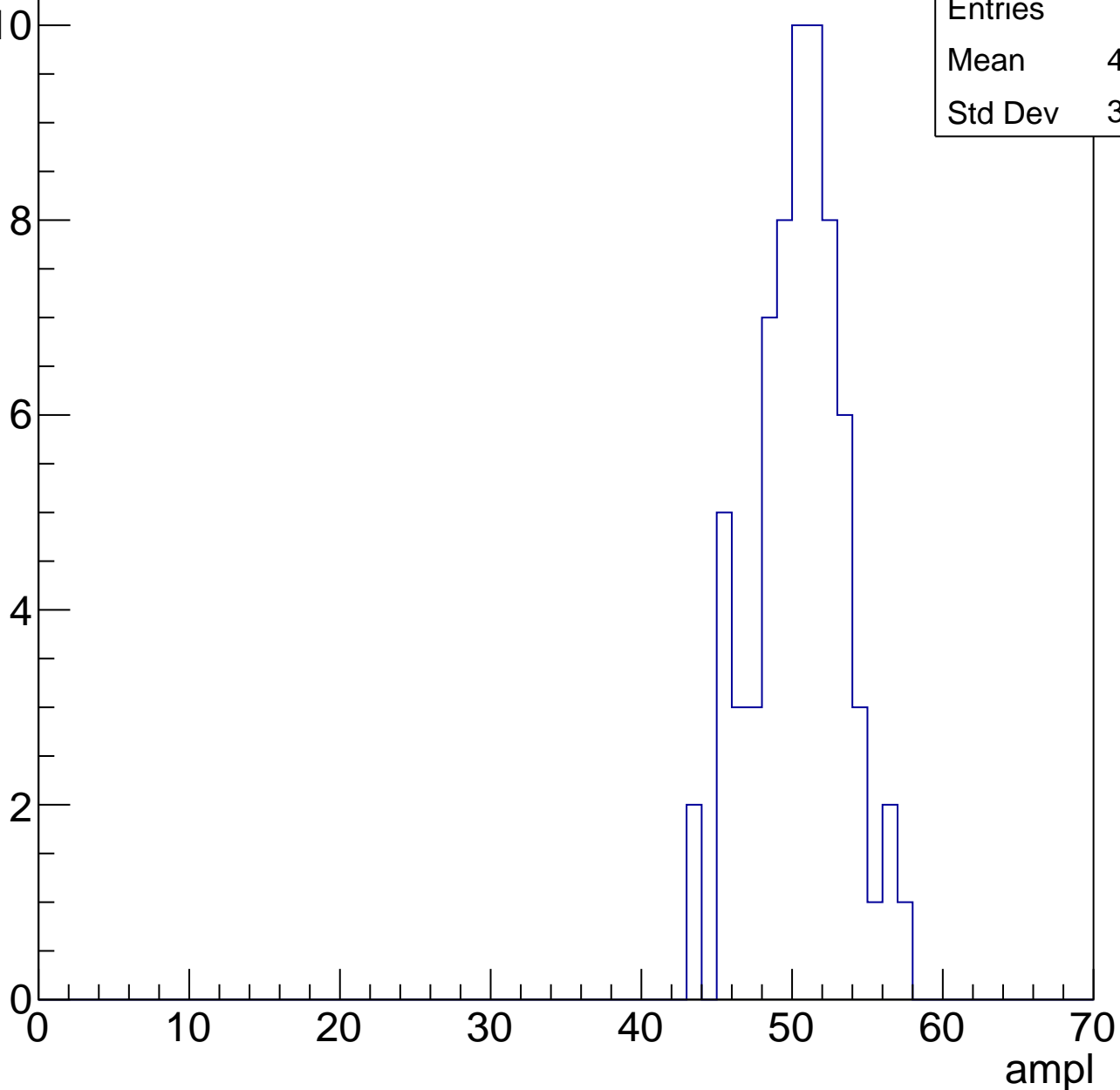


# B0L002S, U1-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

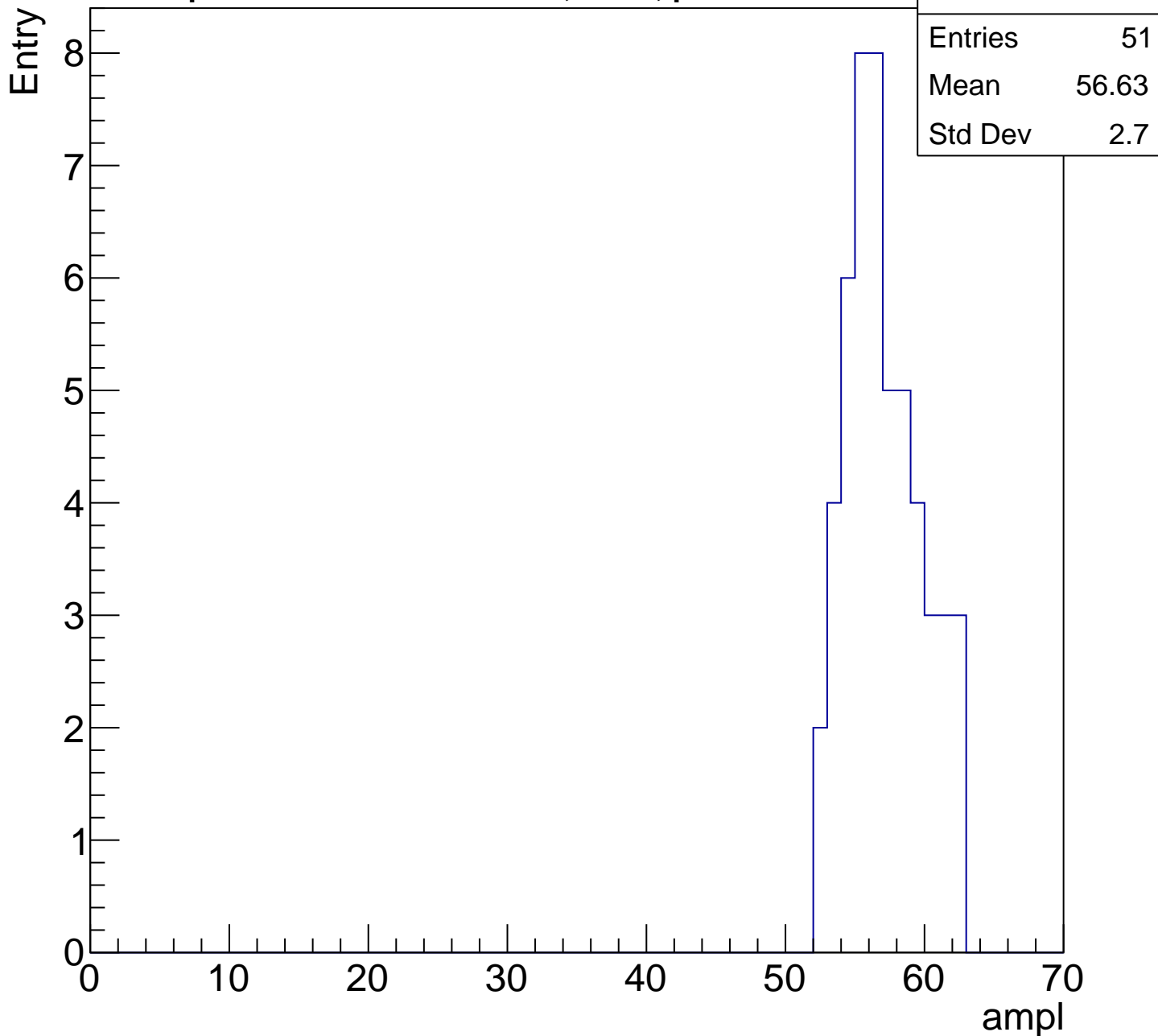
Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 49.97 |
| Std Dev | 3.007 |



# B0L002S, U1-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

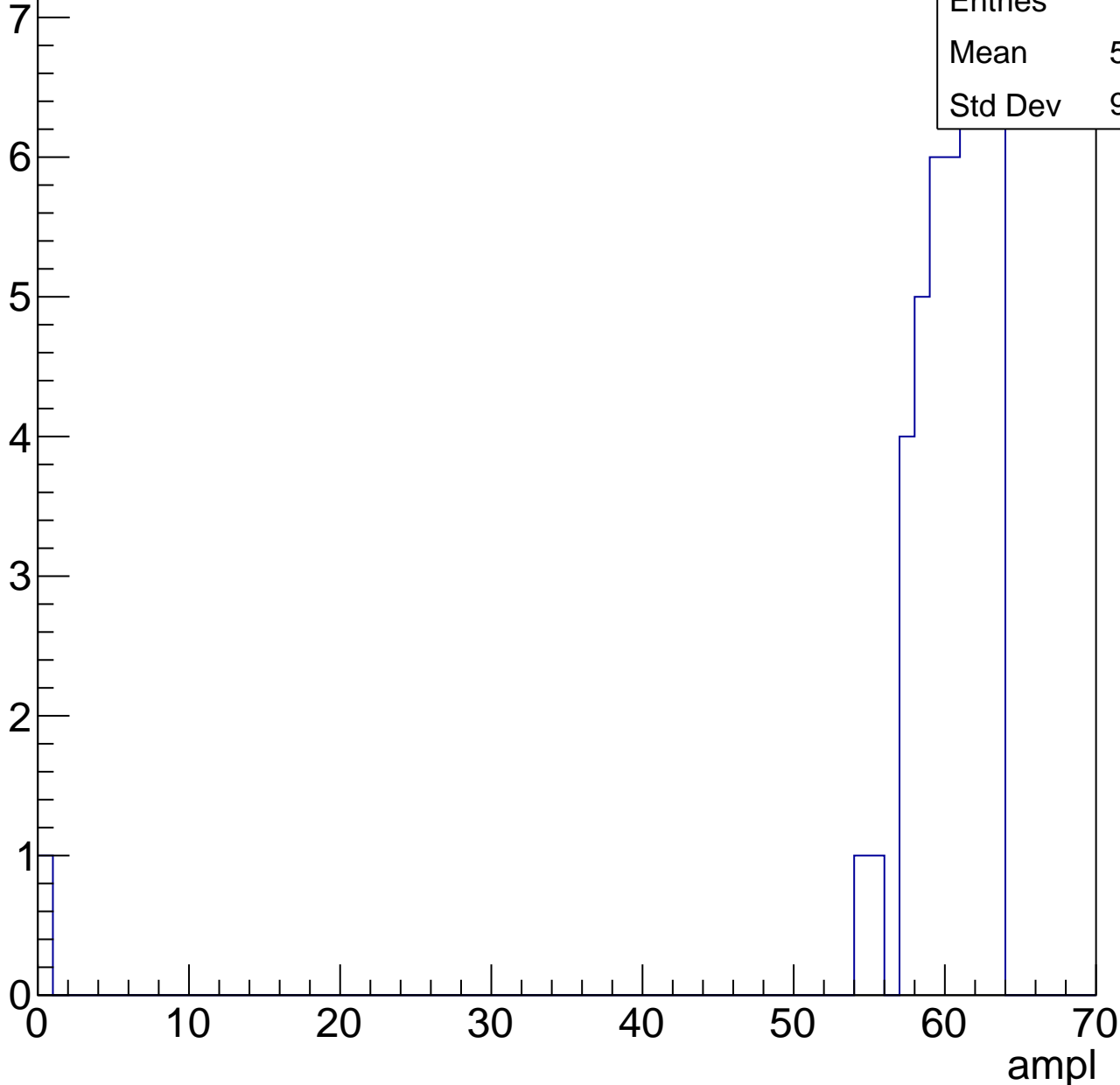


# B0L002S, U1-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

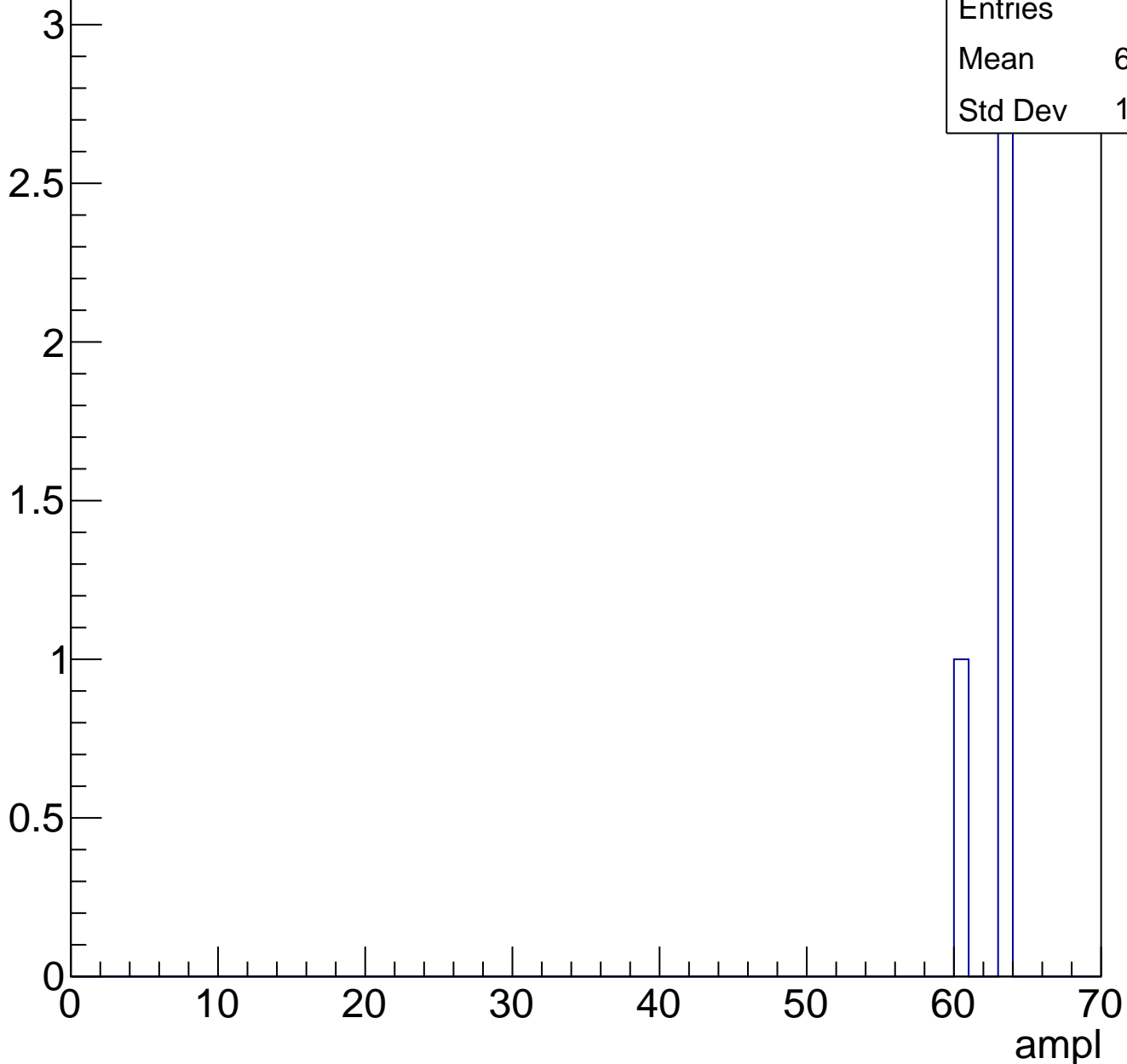
|         |       |
|---------|-------|
| Entries | 45    |
| Mean    | 58.73 |
| Std Dev | 9.127 |



# B0L002S, U1-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch82, adc0

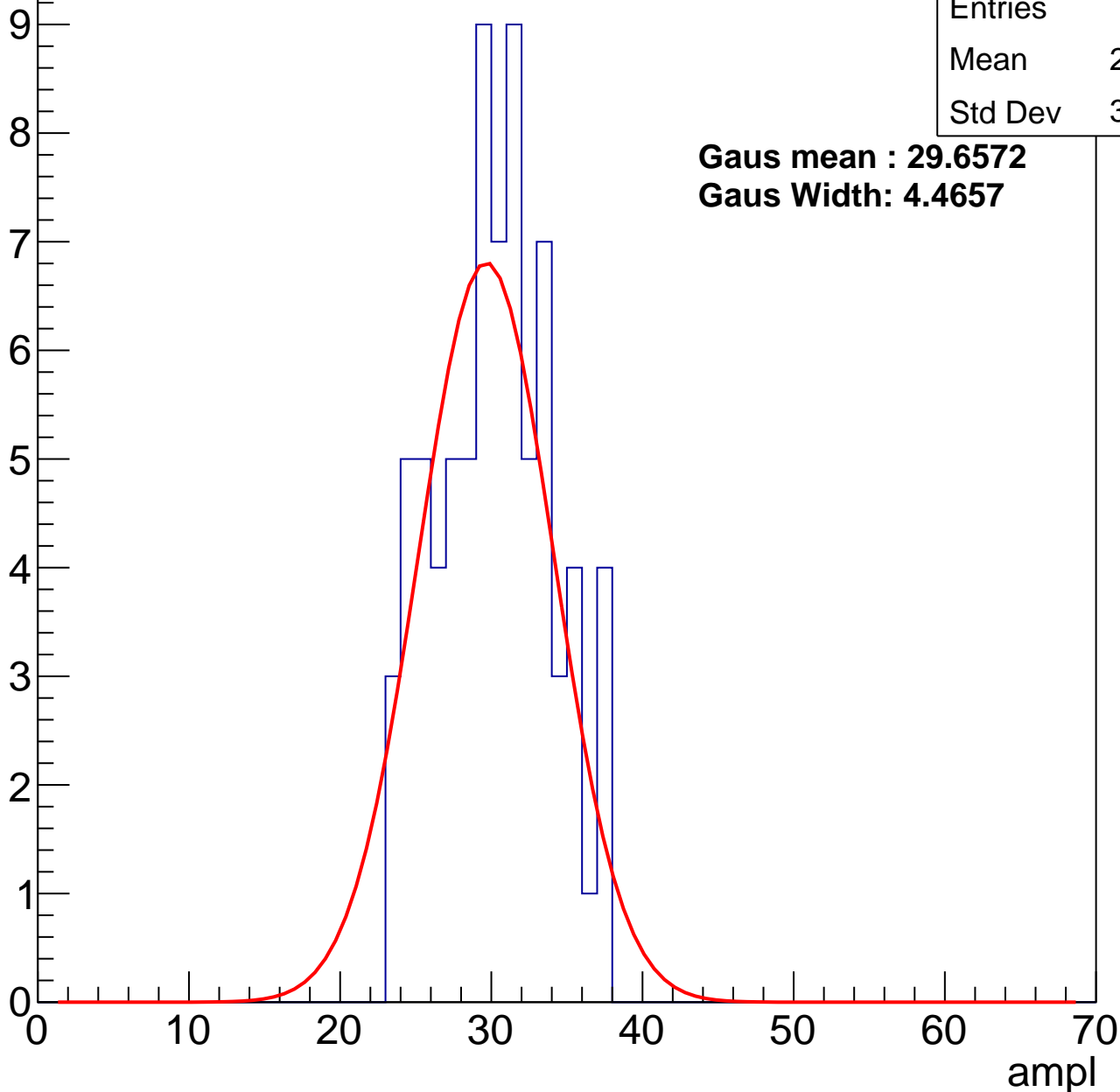
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 29.74 |
| Std Dev | 3.729 |

**Gaus mean : 29.6572**

**Gaus Width: 4.4657**



# B0L002S, U1-ch82, adc1

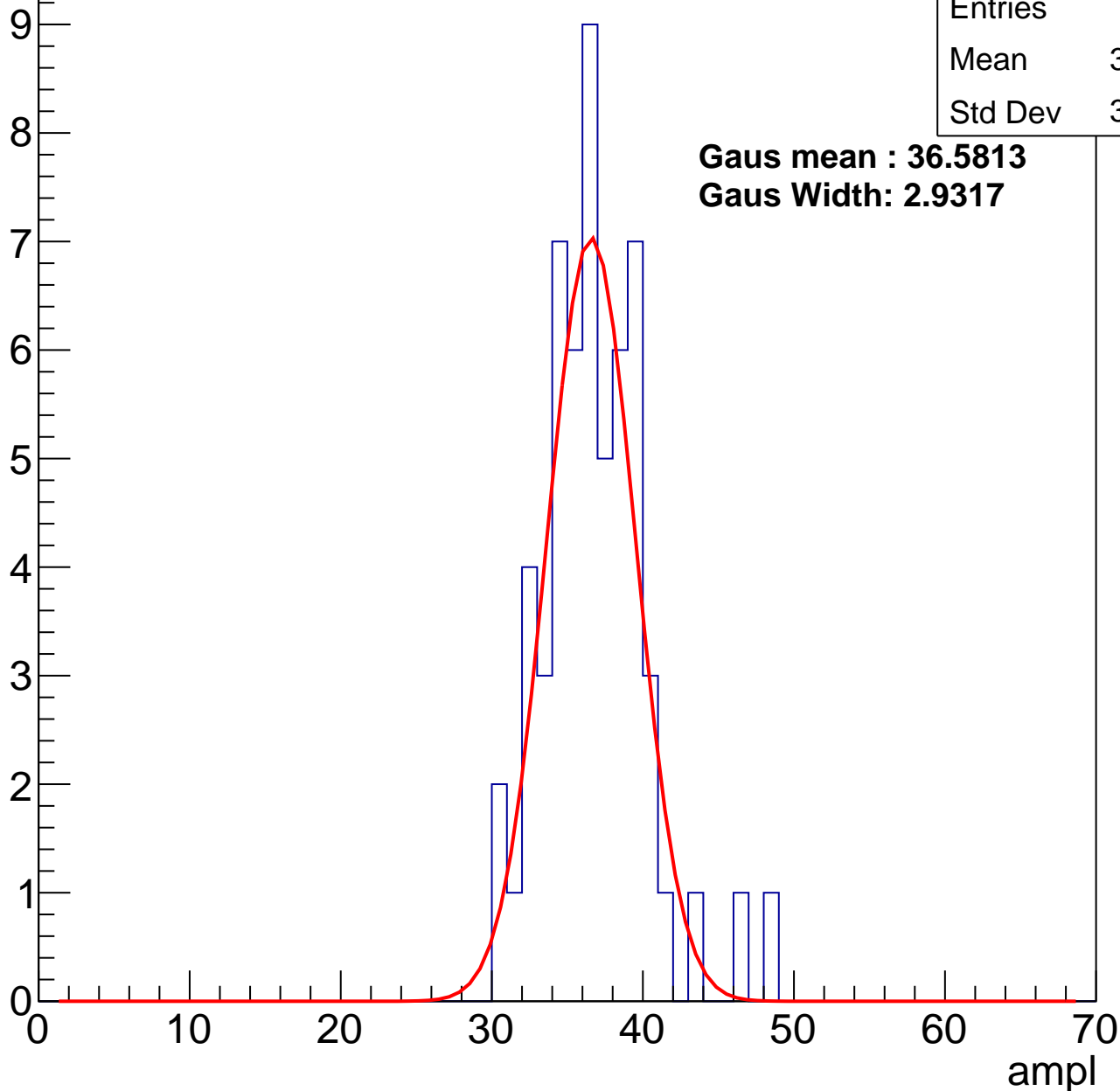
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 36.39 |
| Std Dev | 3.432 |

**Gaus mean : 36.5813**

**Gaus Width: 2.9317**



# B0L002S, U1-ch82, adc2

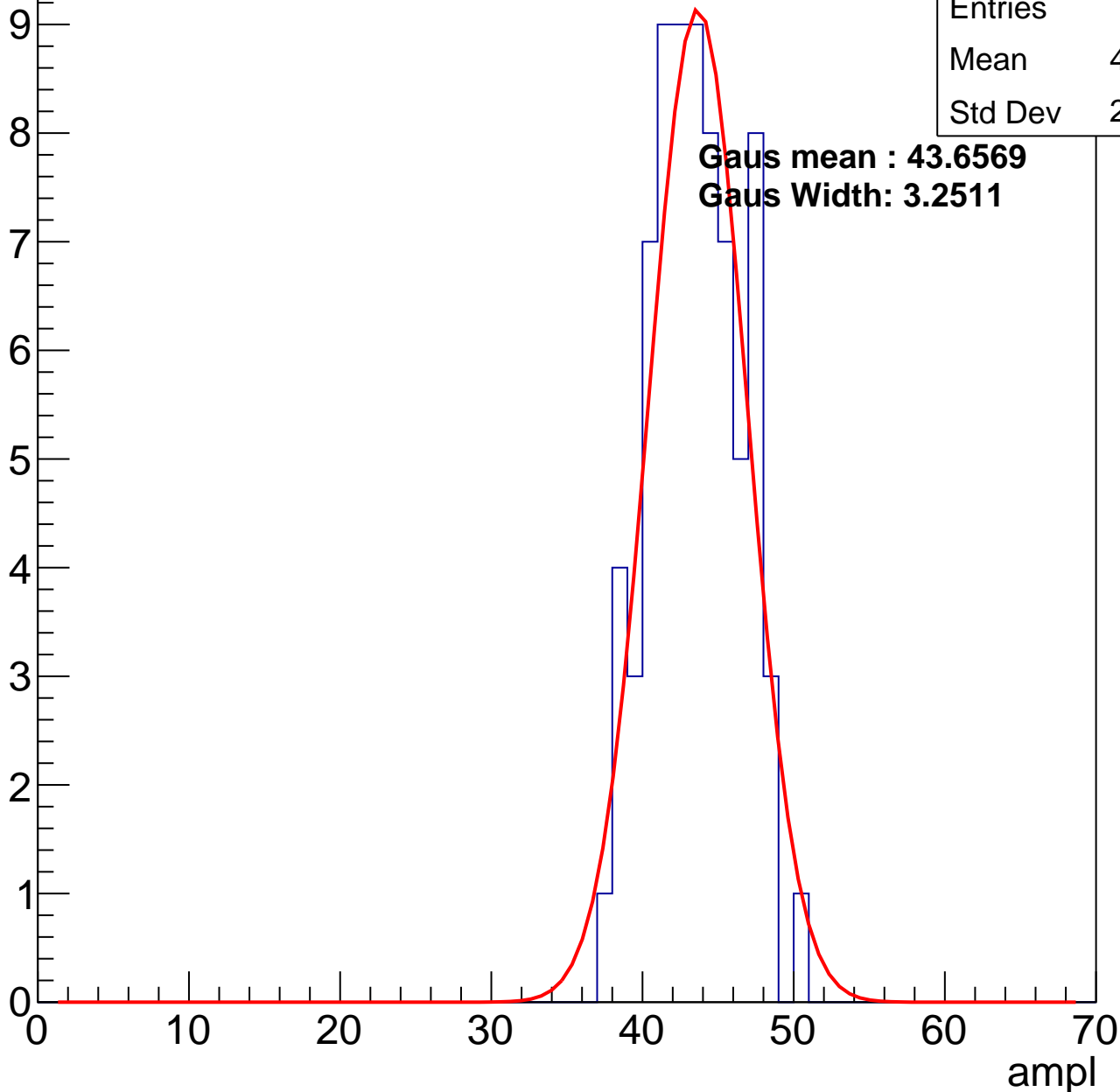
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 43.07 |
| Std Dev | 2.905 |

**Gaus mean : 43.6569**

**Gaus Width: 3.2511**

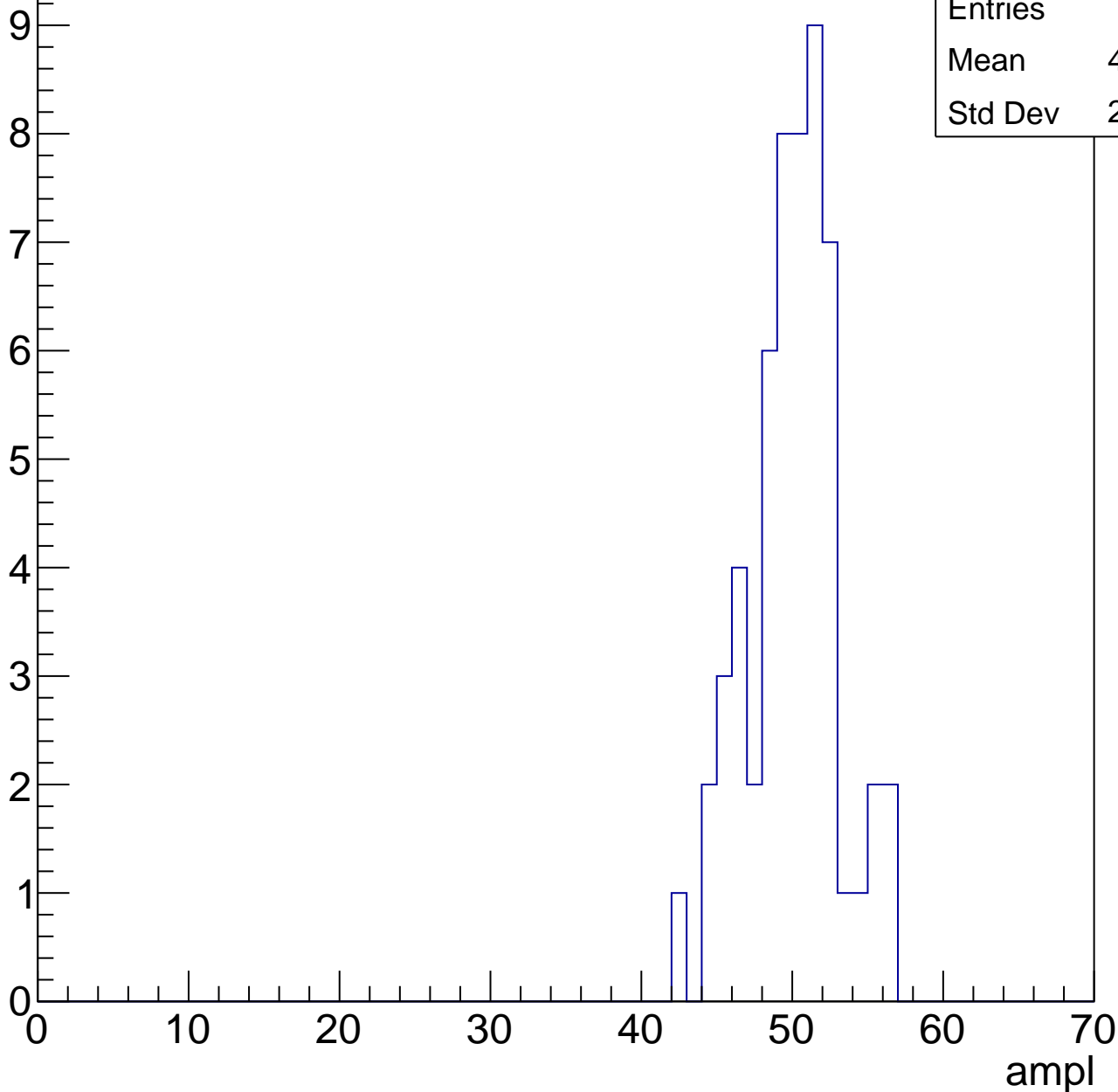


# B0L002S, U1-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

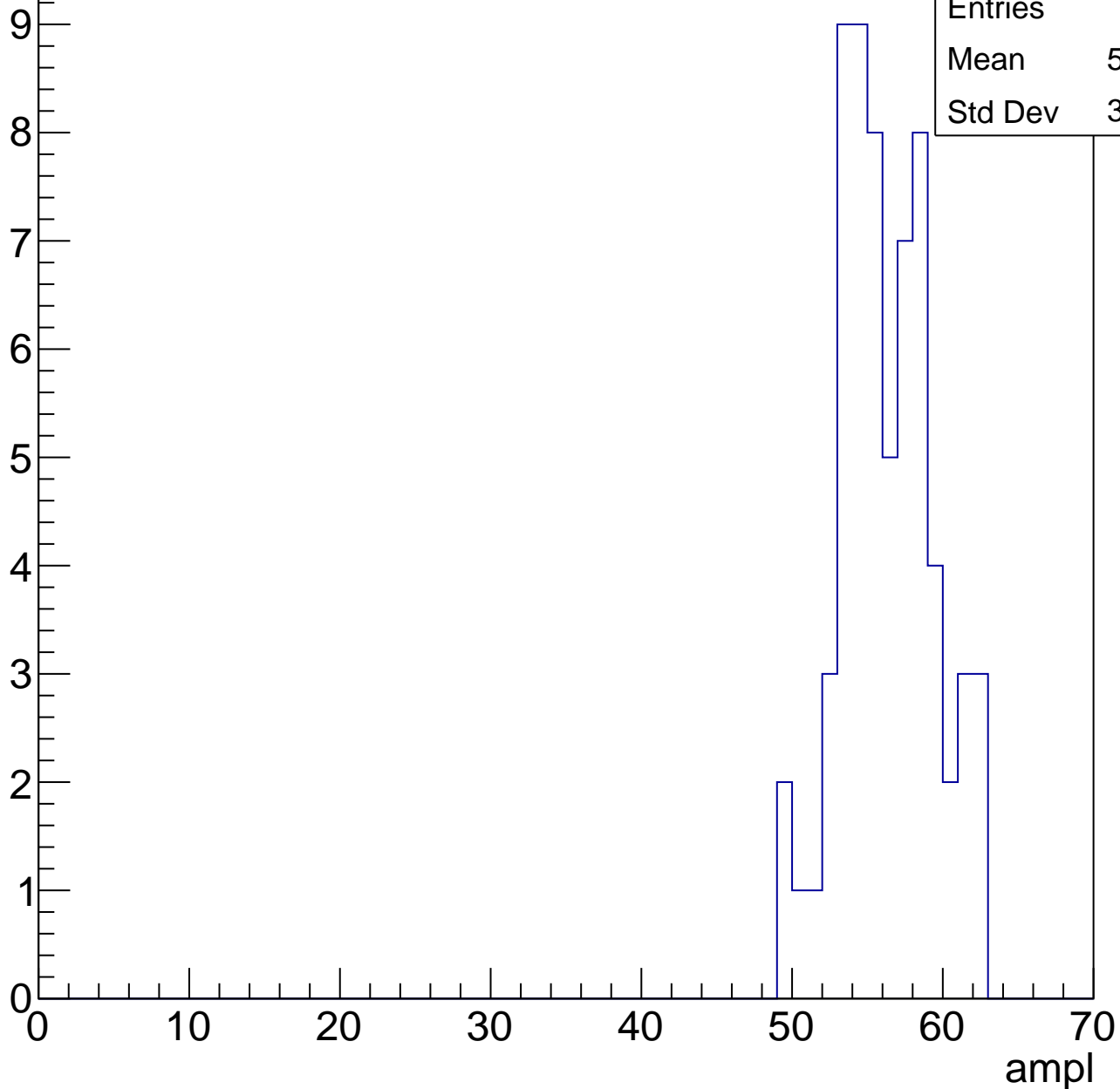
|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 49.55 |
| Std Dev | 2.982 |



# B0L002S, U1-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 55.78 |
| Std Dev | 3.086 |

# B0L002S, U1-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

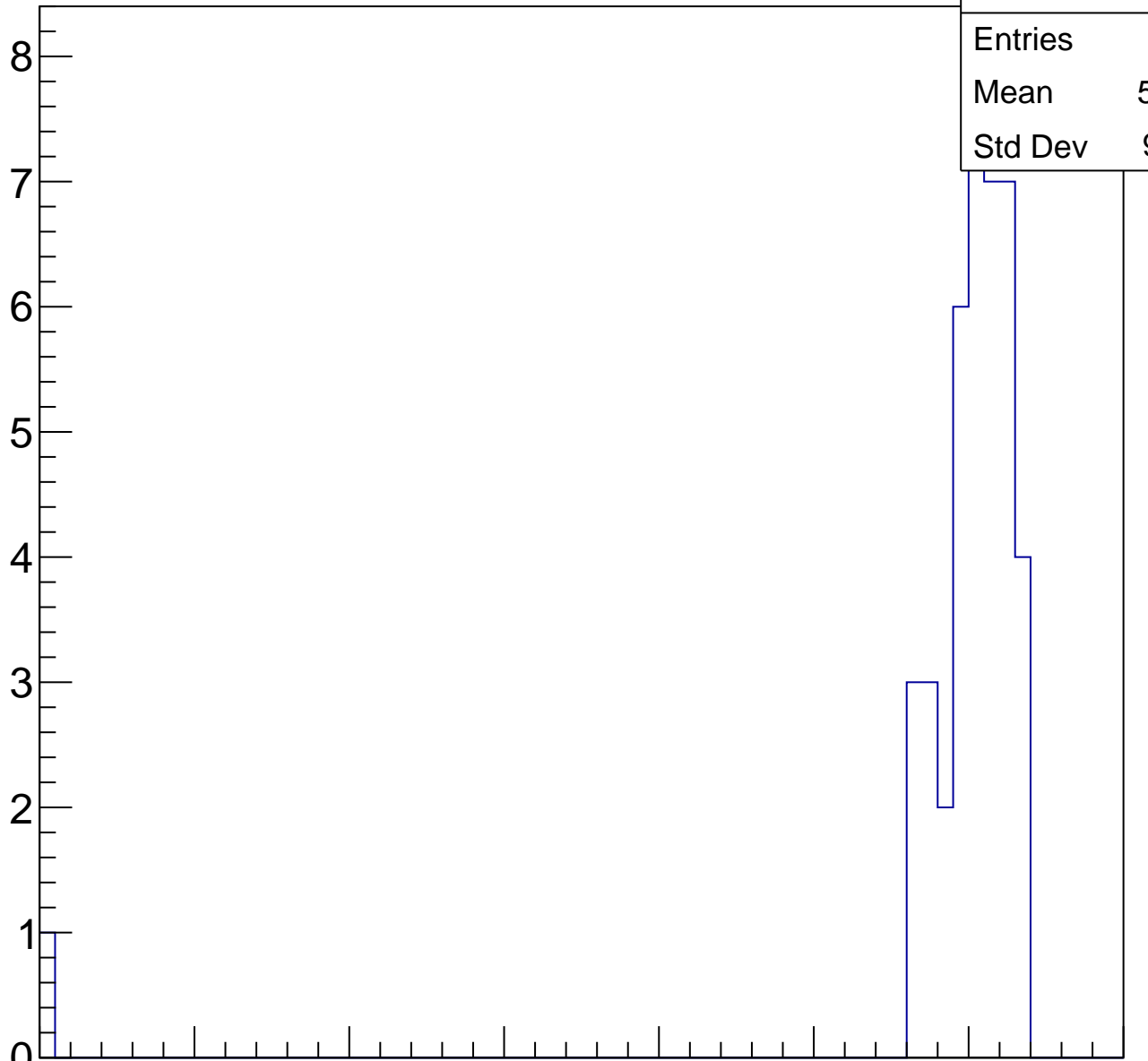
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 58.59 |
| Std Dev | 9.471 |

ampl

0 10 20 30 40 50 60 70

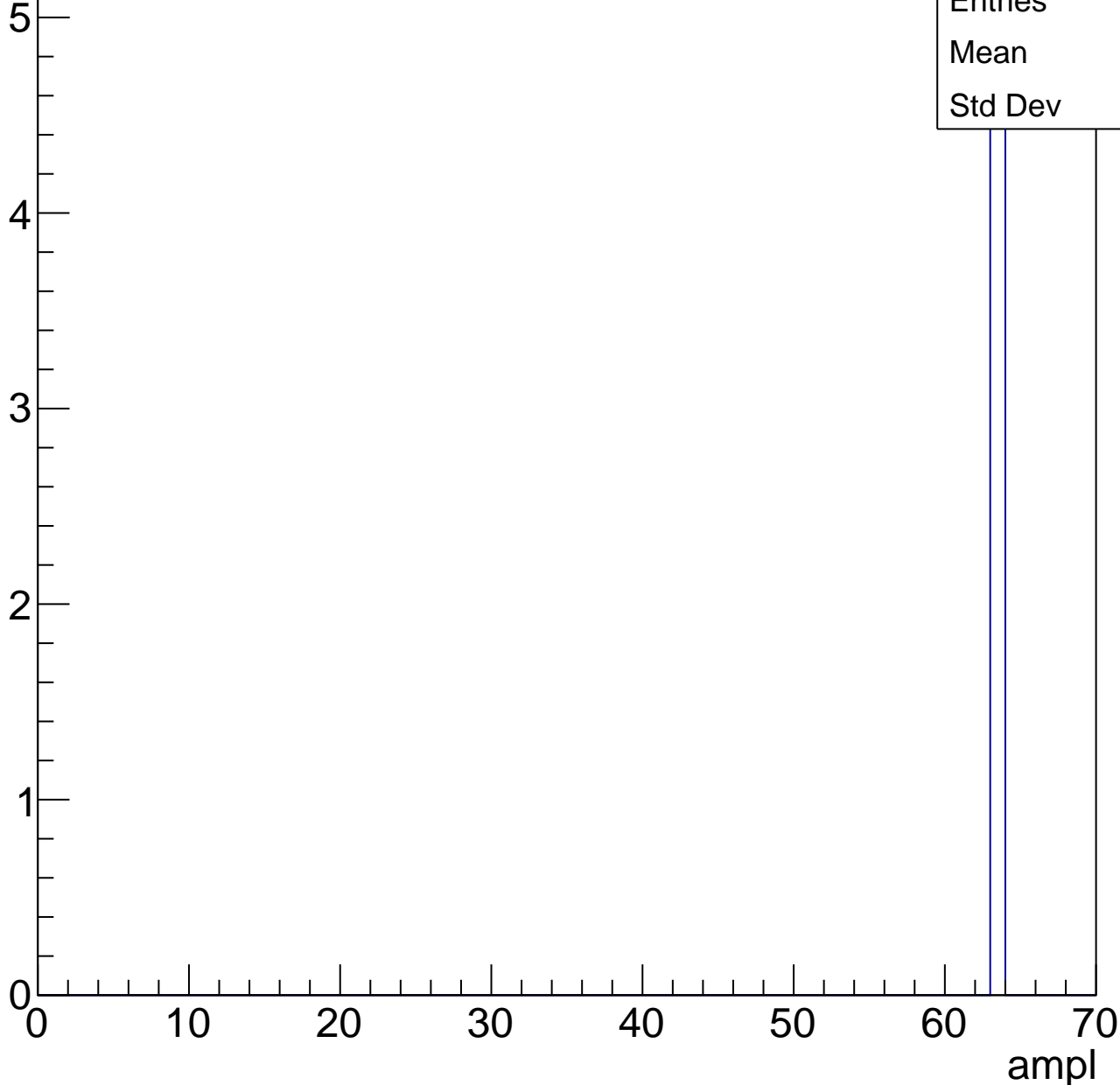


# B0L002S, U1-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |    |
|---------|----|
| Entries | 5  |
| Mean    | 63 |
| Std Dev | 0  |





# B0L002S, U1-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch83, adc0

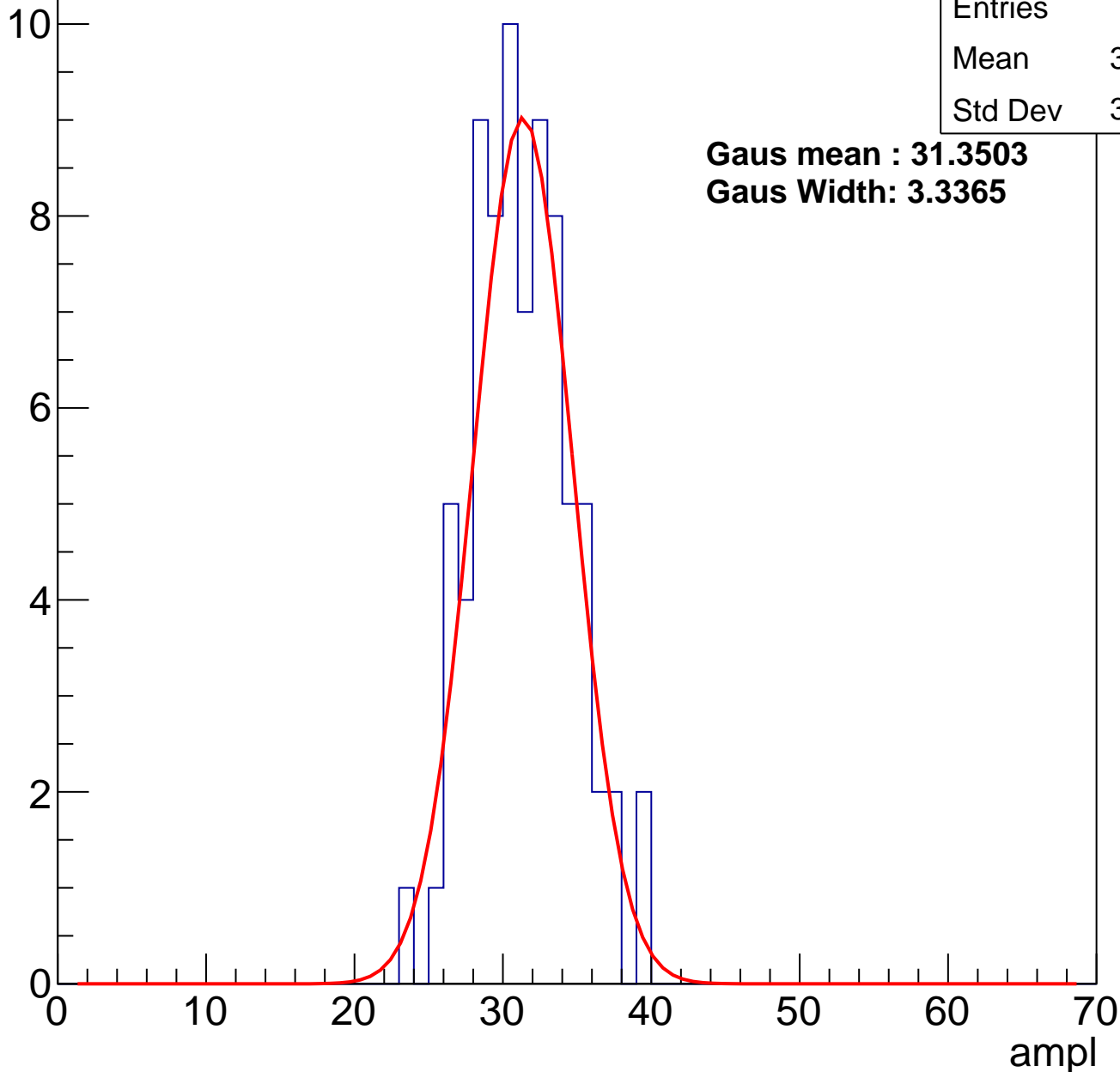
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 30.87 |
| Std Dev | 3.256 |

**Gaus mean : 31.3503**

**Gaus Width: 3.3365**

Entry



# B0L002S, U1-ch83, adc1

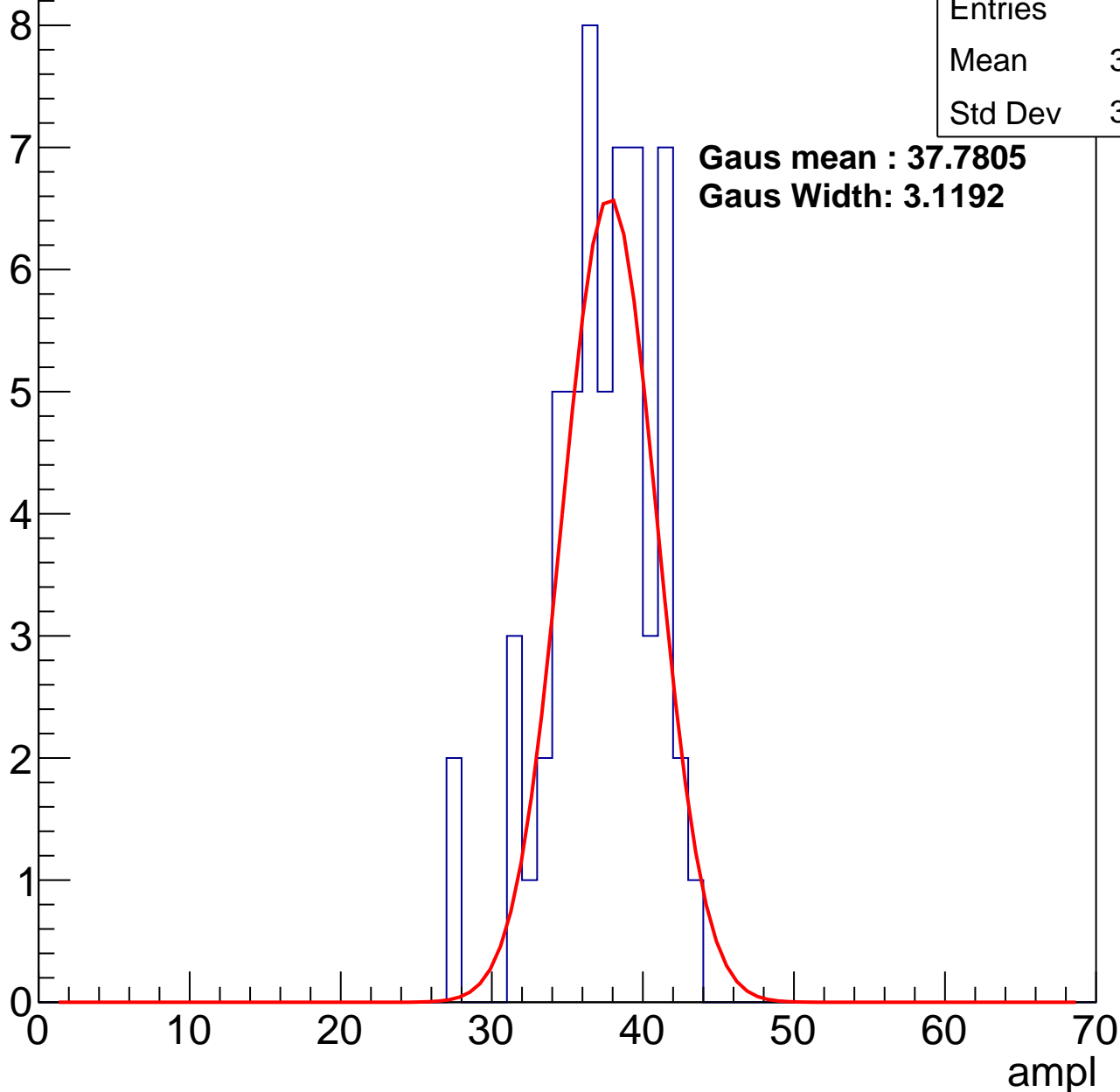
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 36.83 |
| Std Dev | 3.465 |

**Gaus mean : 37.7805**

**Gaus Width: 3.1192**



# B0L002S, U1-ch83, adc2

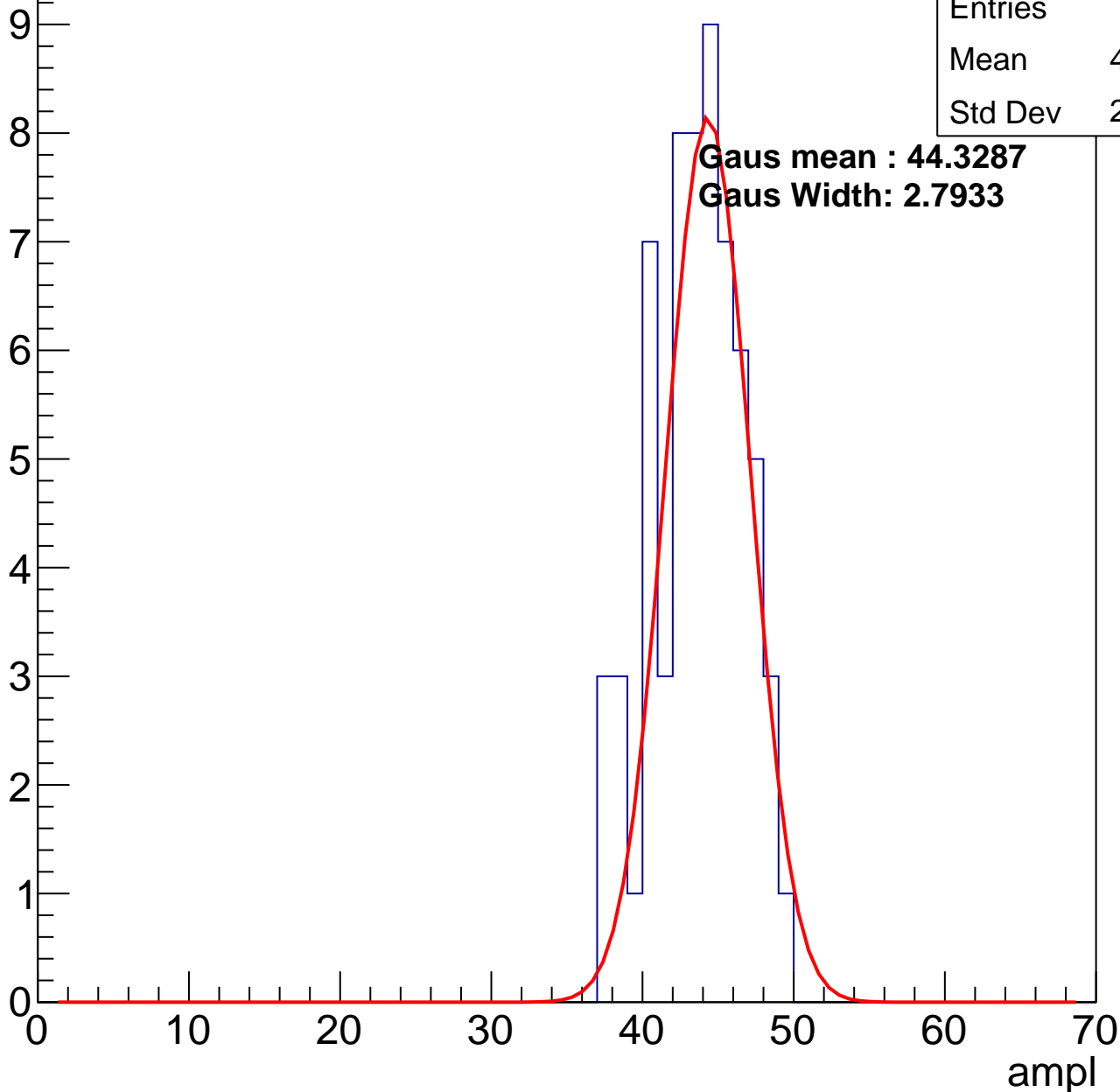
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 43.16 |
| Std Dev | 2.964 |

**Gaus mean : 44.3287**

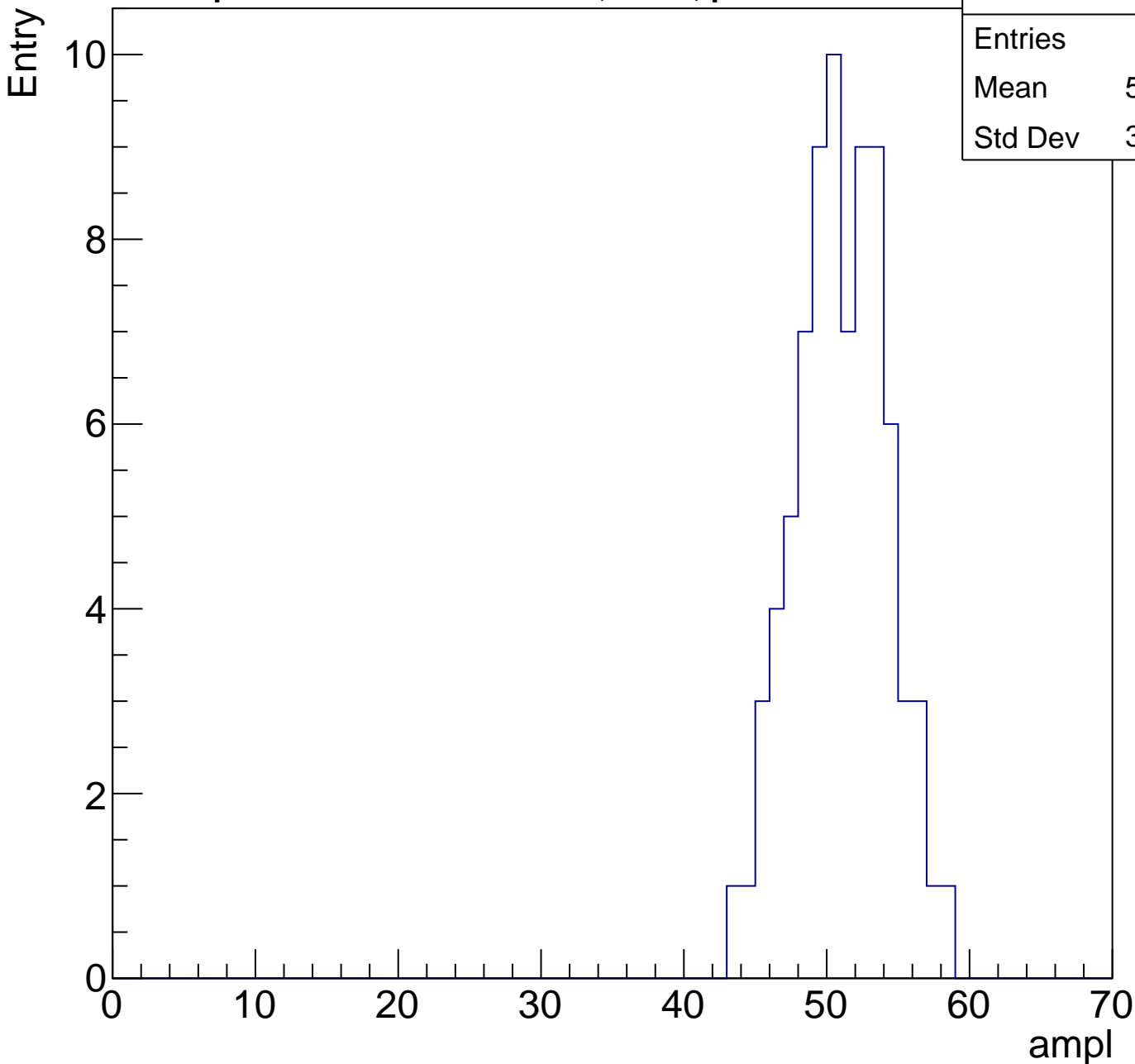
**Gaus Width: 2.7933**



# B0L002S, U1-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

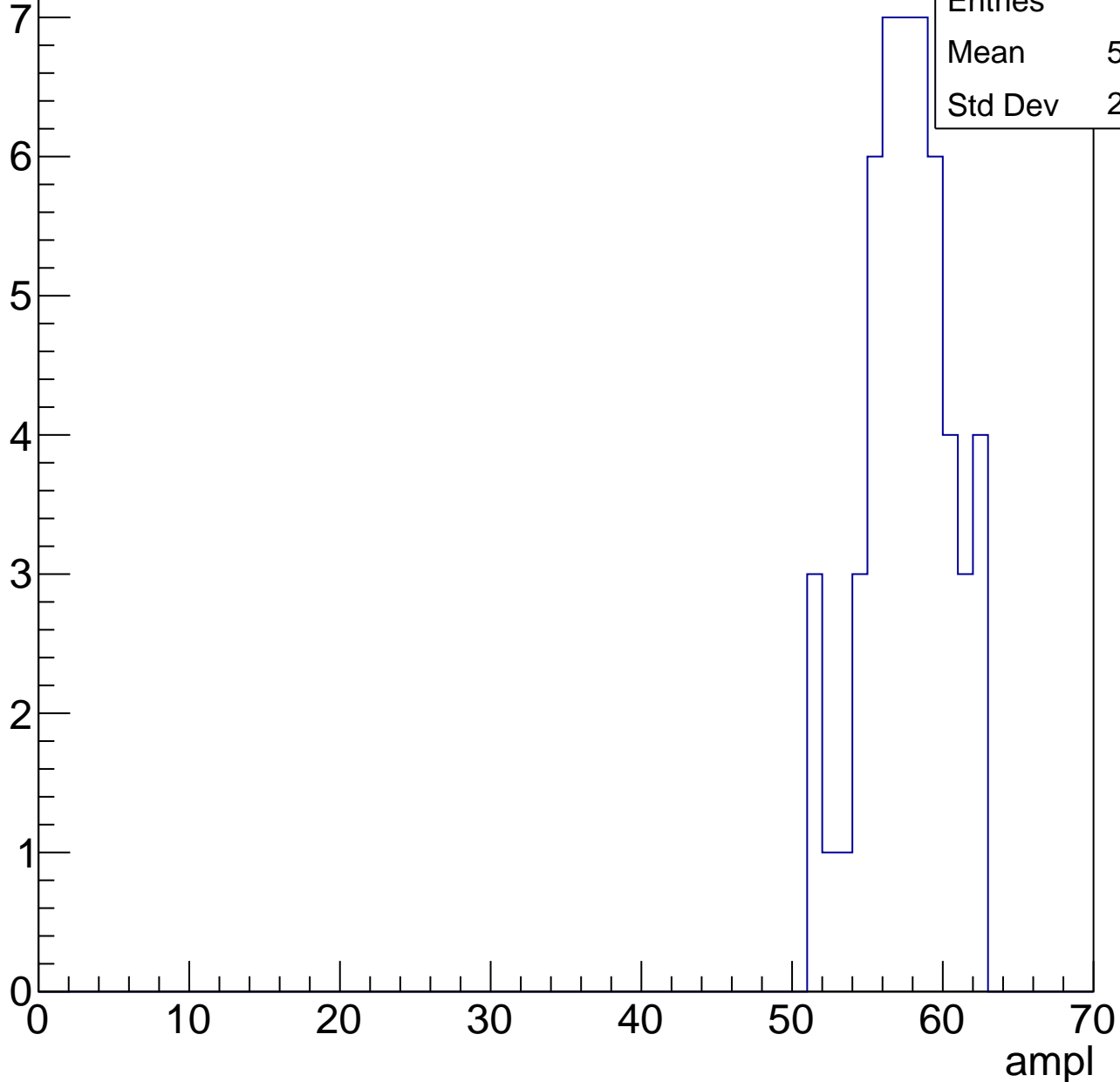
|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 50.53 |
| Std Dev | 3.182 |



# B0L002S, U1-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



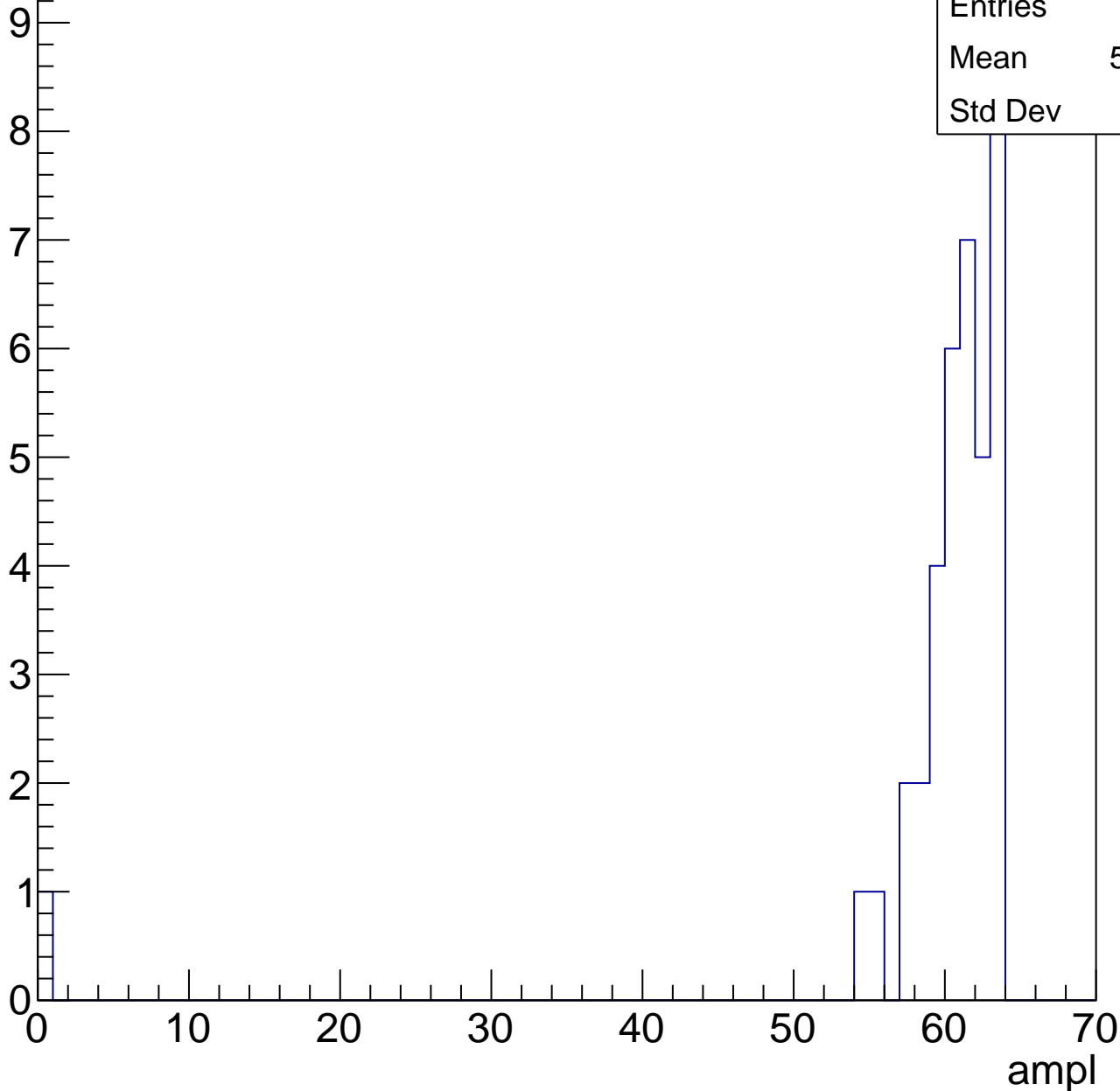
|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 57.15 |
| Std Dev | 2.845 |

# B0L002S, U1-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 58.92 |
| Std Dev | 9.94  |



# B0L002S, U1-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

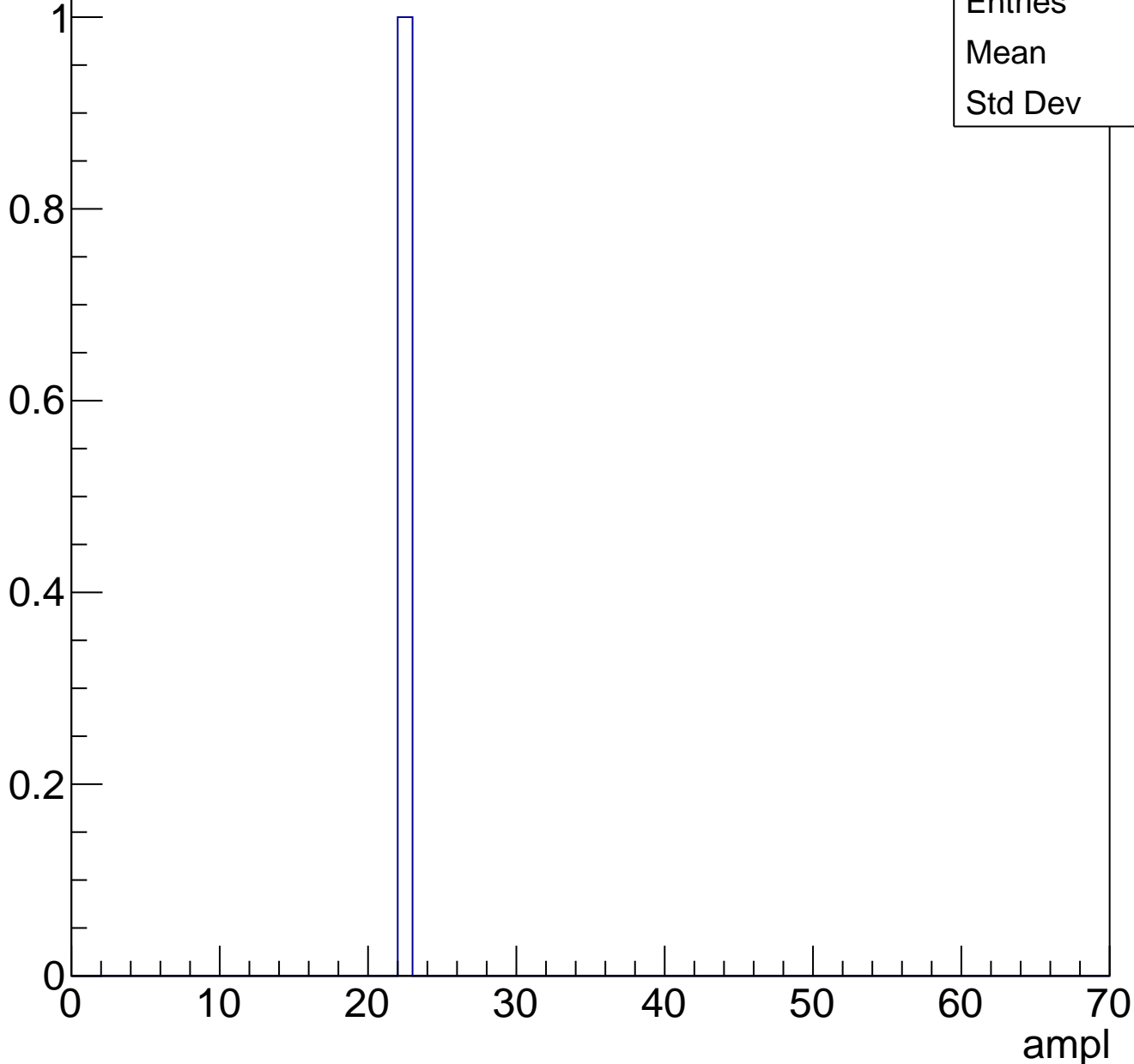




# B0L002S, U1-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch84, adc0

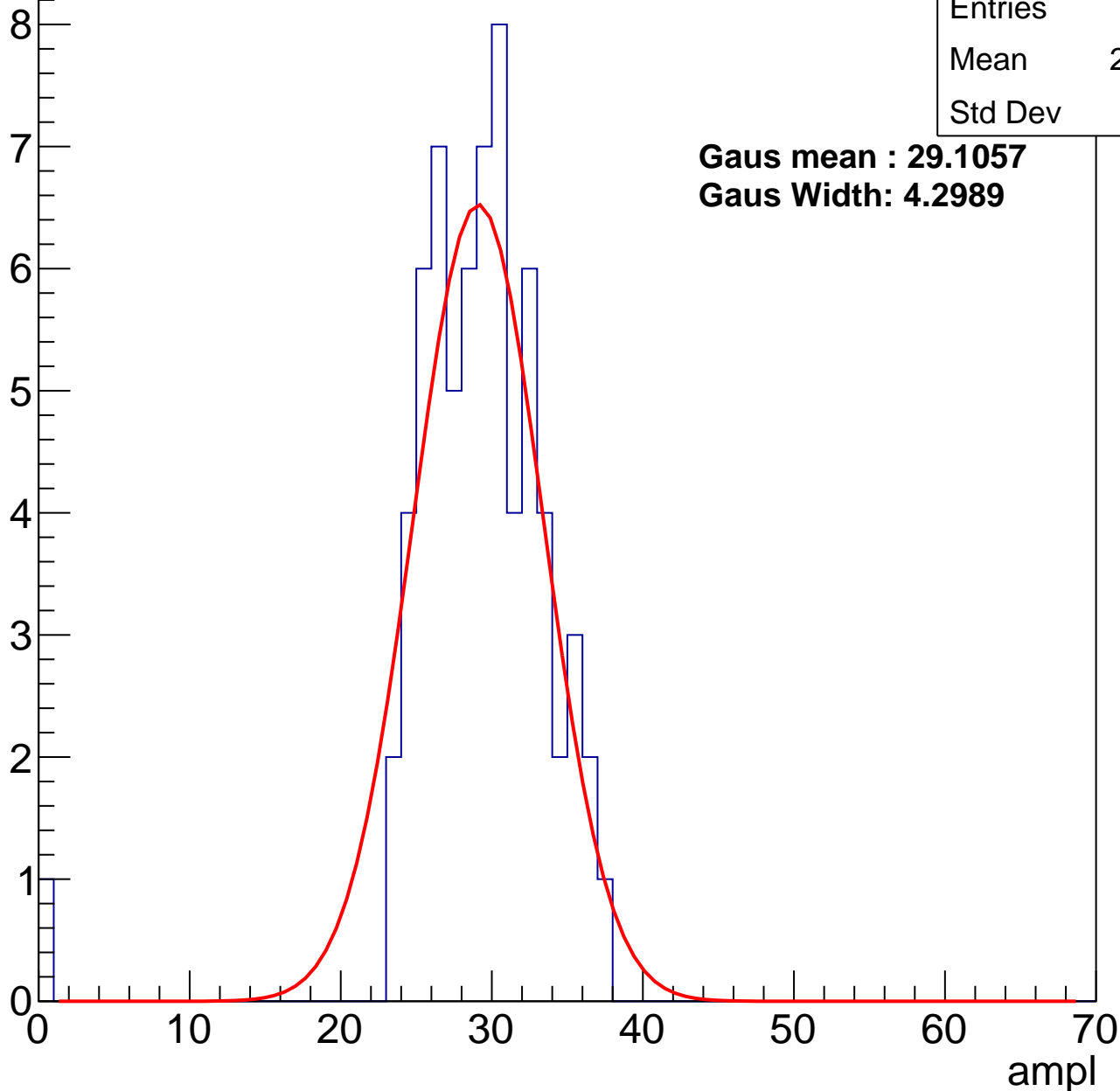
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 28.68 |
| Std Dev | 4.93  |

**Gaus mean : 29.1057**

**Gaus Width: 4.2989**



# B0L002S, U1-ch84, adc1

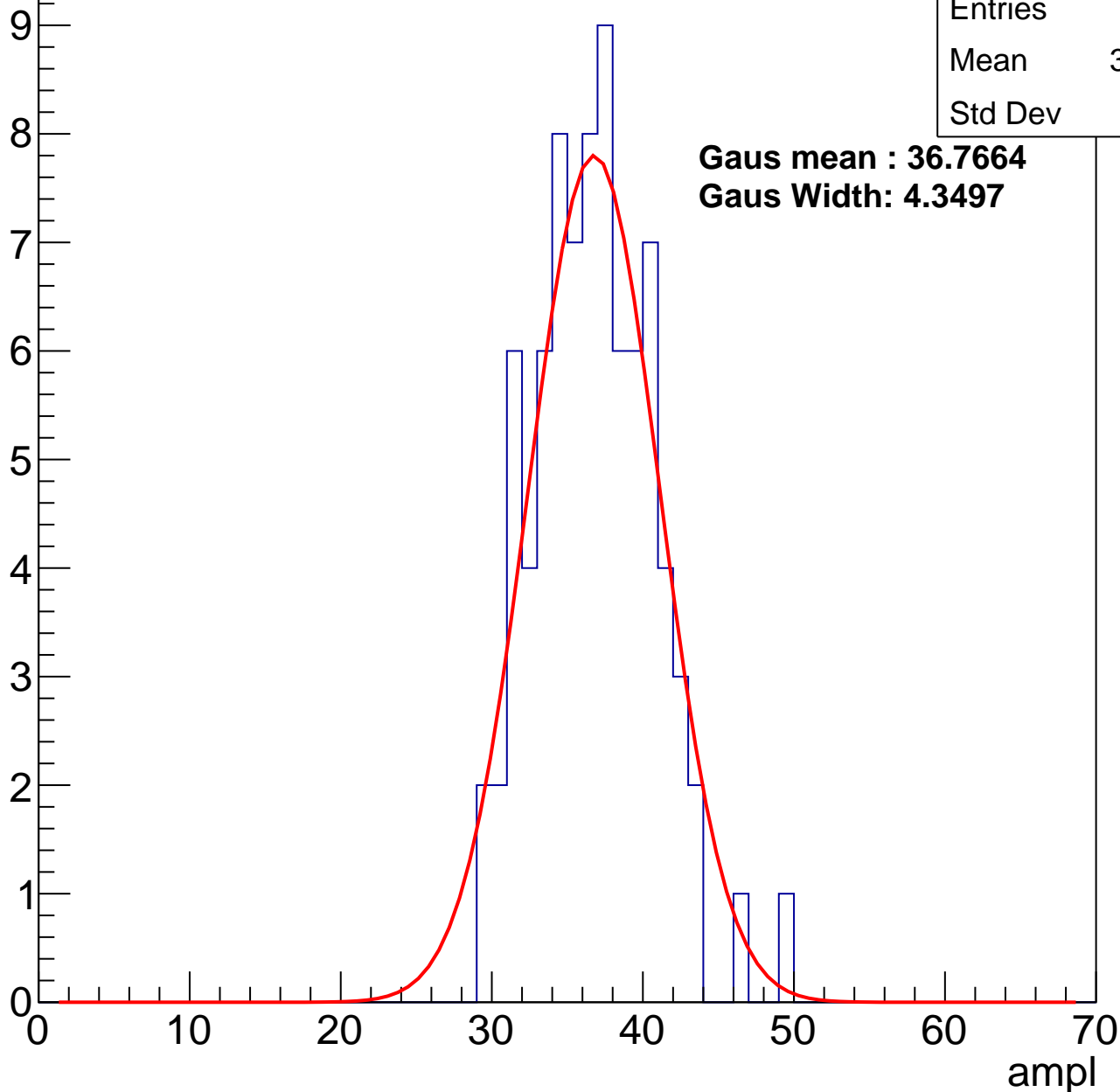
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 36.35 |
| Std Dev | 3.89  |

**Gaus mean : 36.7664**

**Gaus Width: 4.3497**



# B0L002S, U1-ch84, adc2

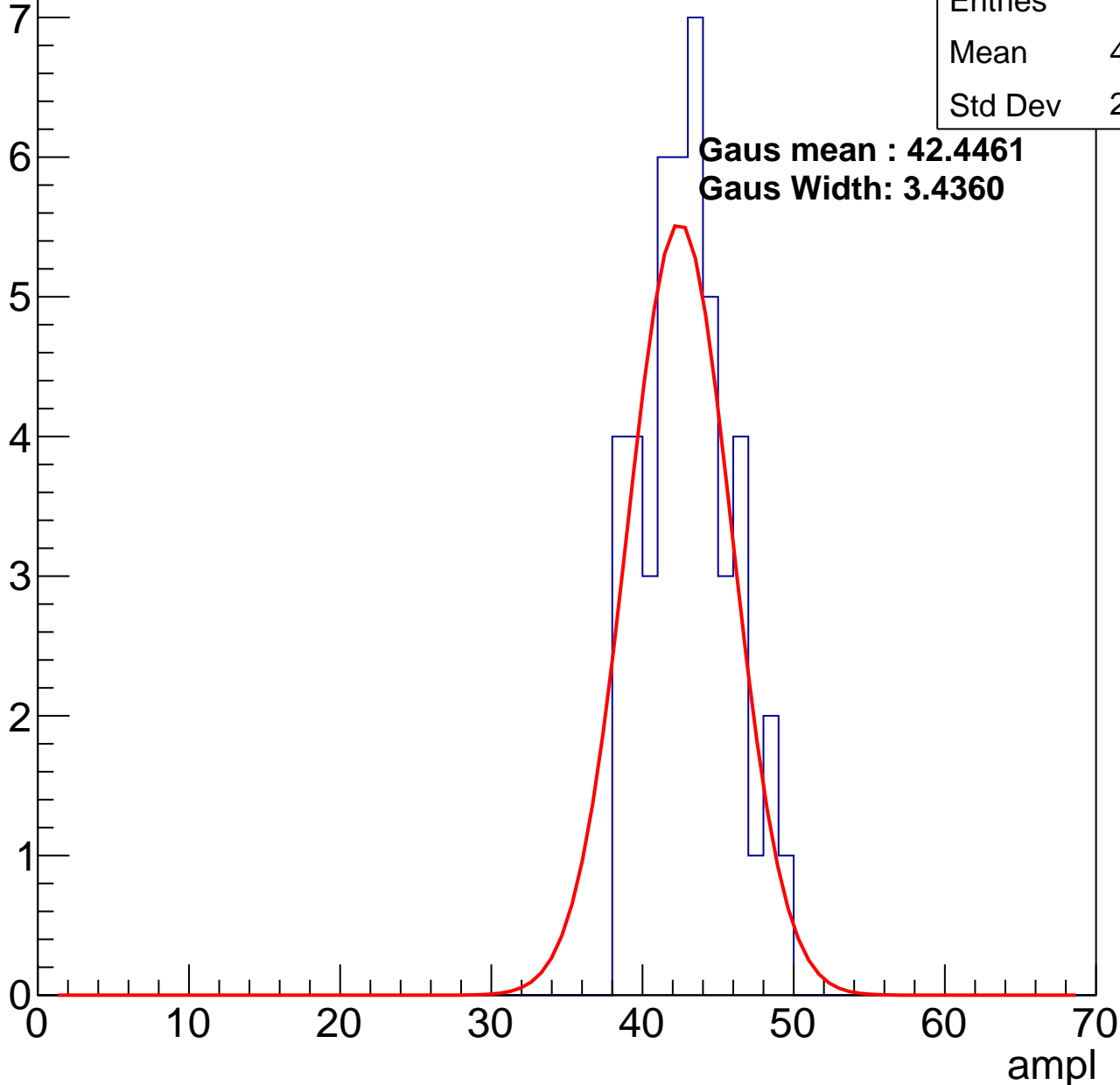
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 42.57 |
| Std Dev | 2.826 |

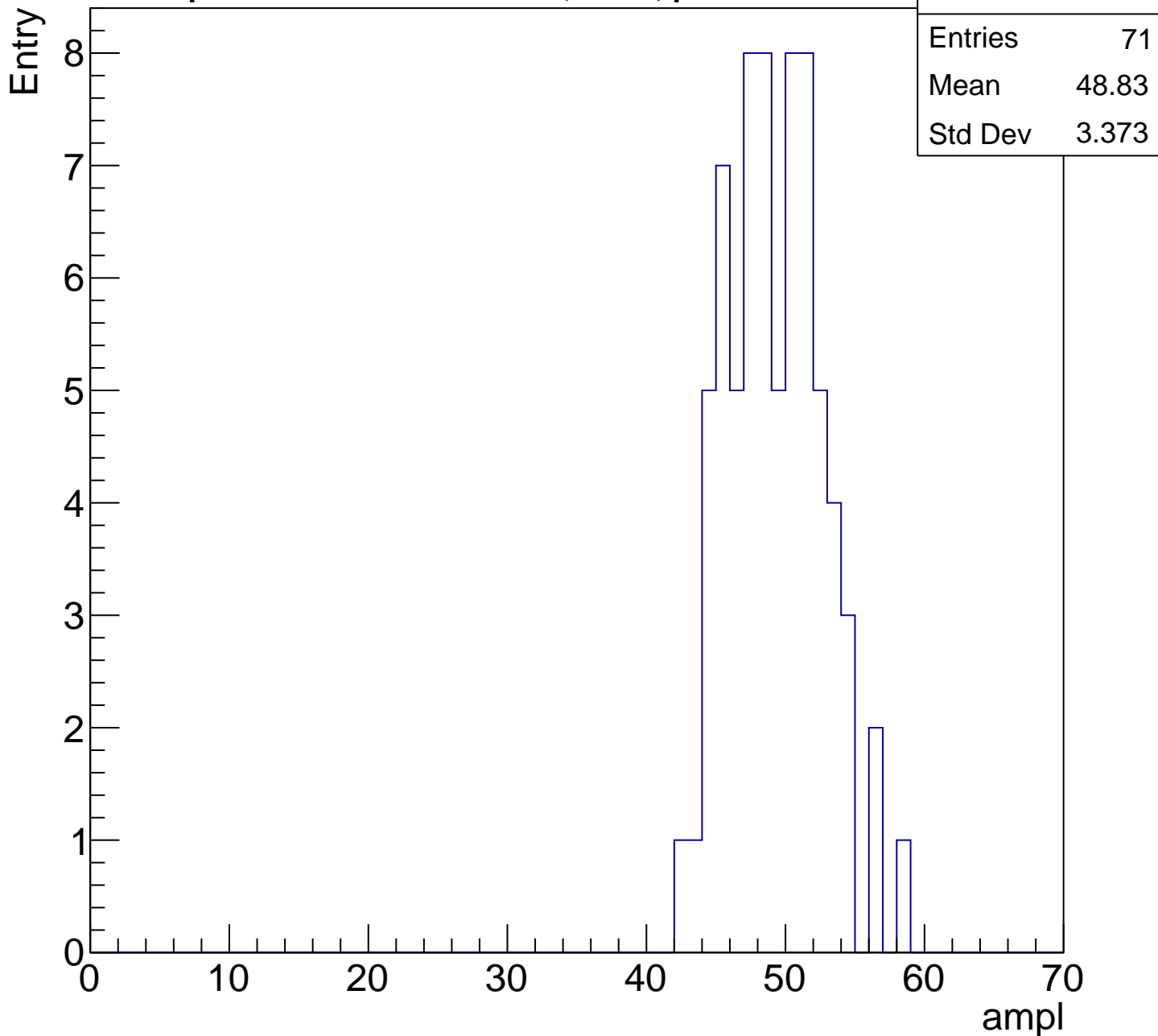
**Gaus mean : 42.4461**

**Gaus Width: 3.4360**



# B0L002S, U1-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

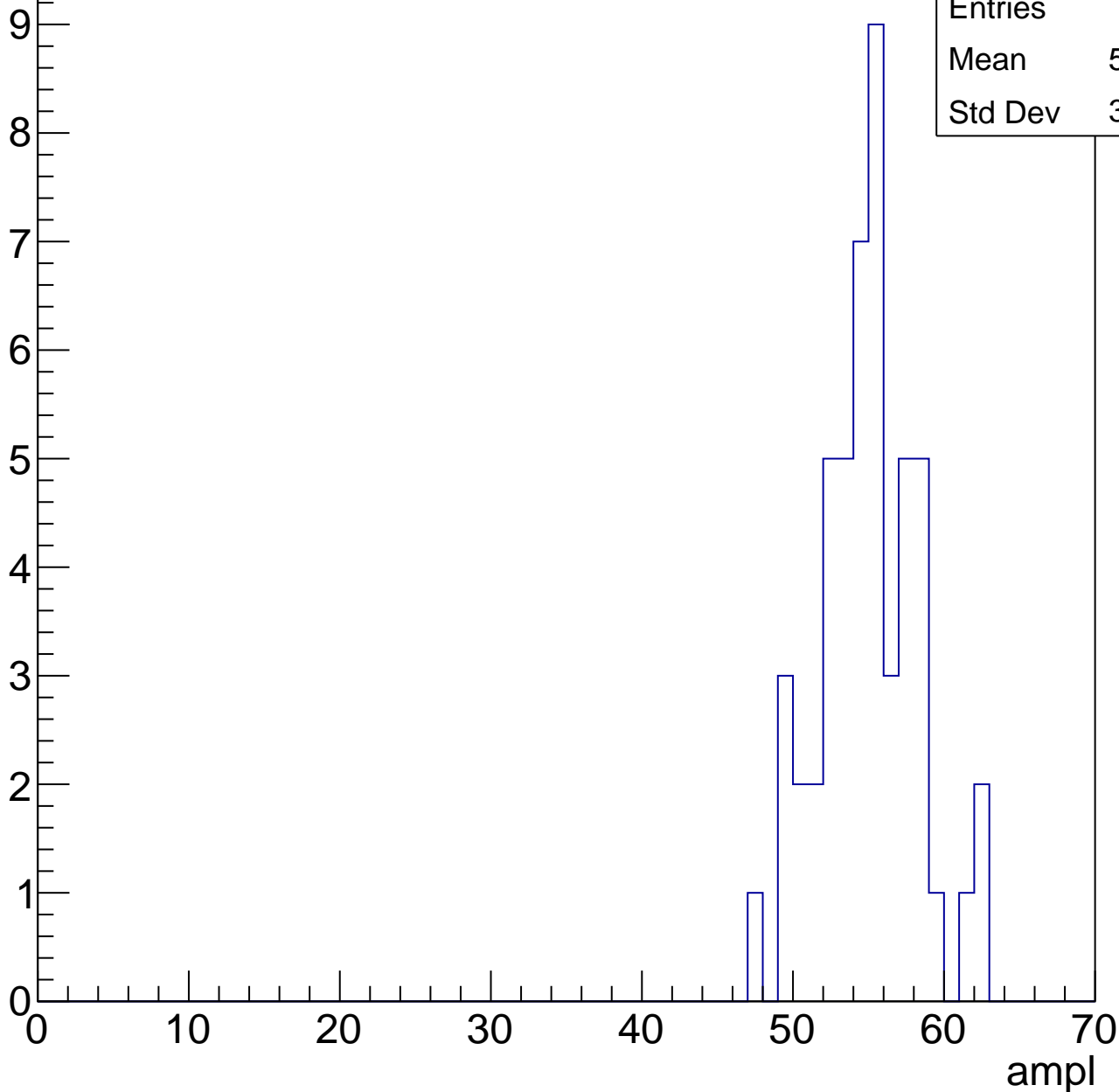


# B0L002S, U1-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 54.53 |
| Std Dev | 3.232 |



# B0L002S, U1-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

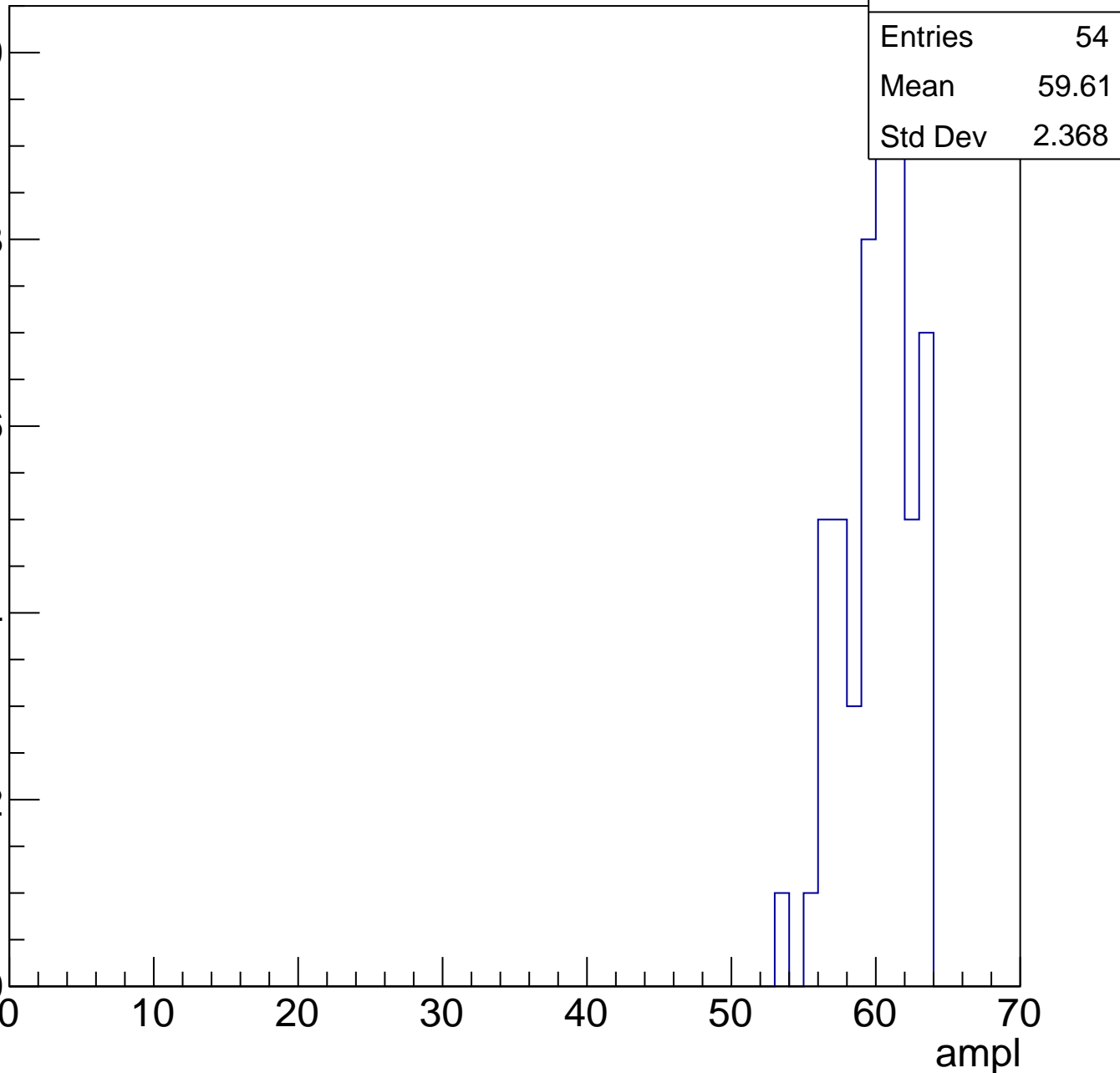
40

50

60

ampl

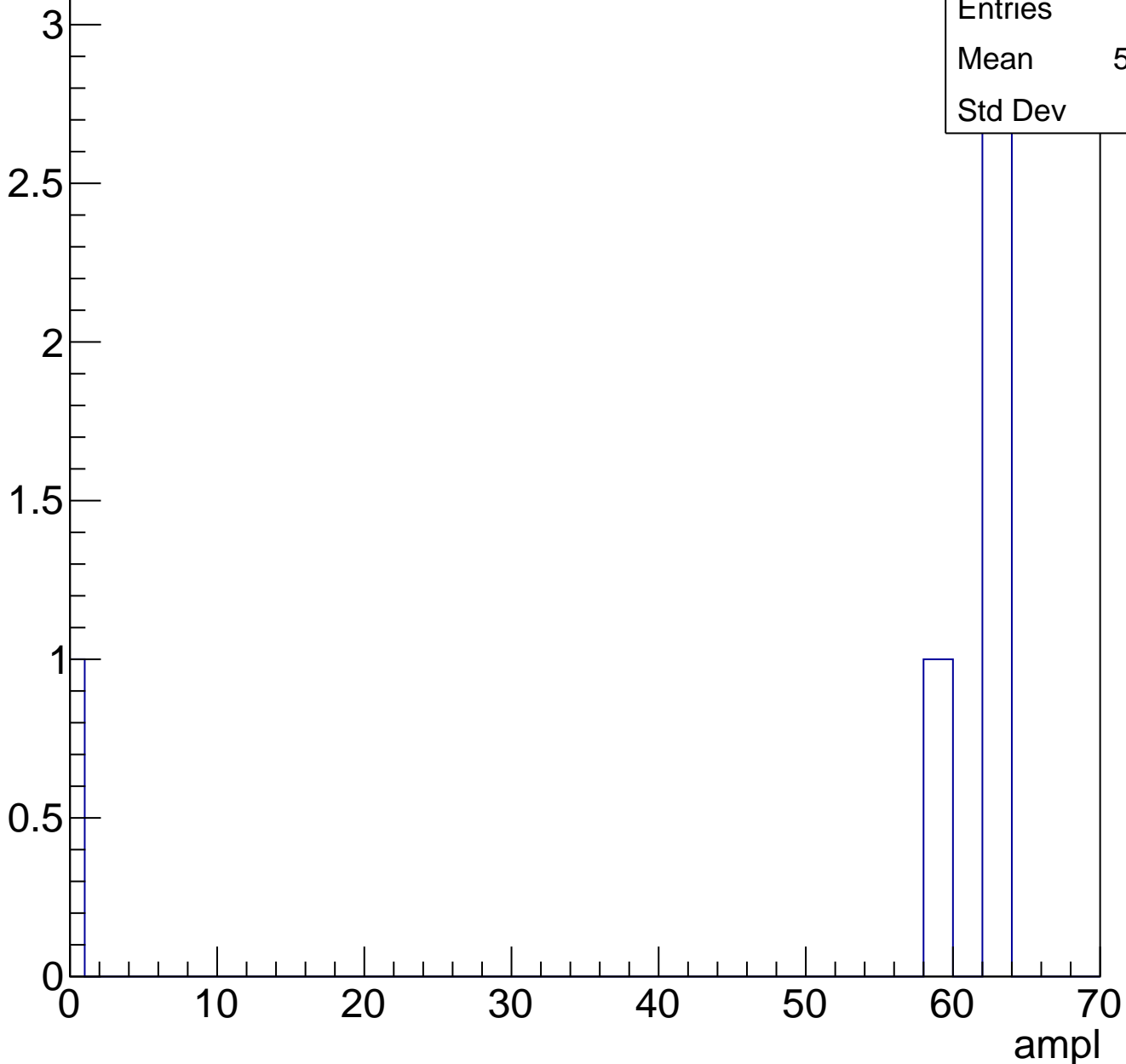
|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 59.61 |
| Std Dev | 2.368 |



# B0L002S, U1-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch85, adc0

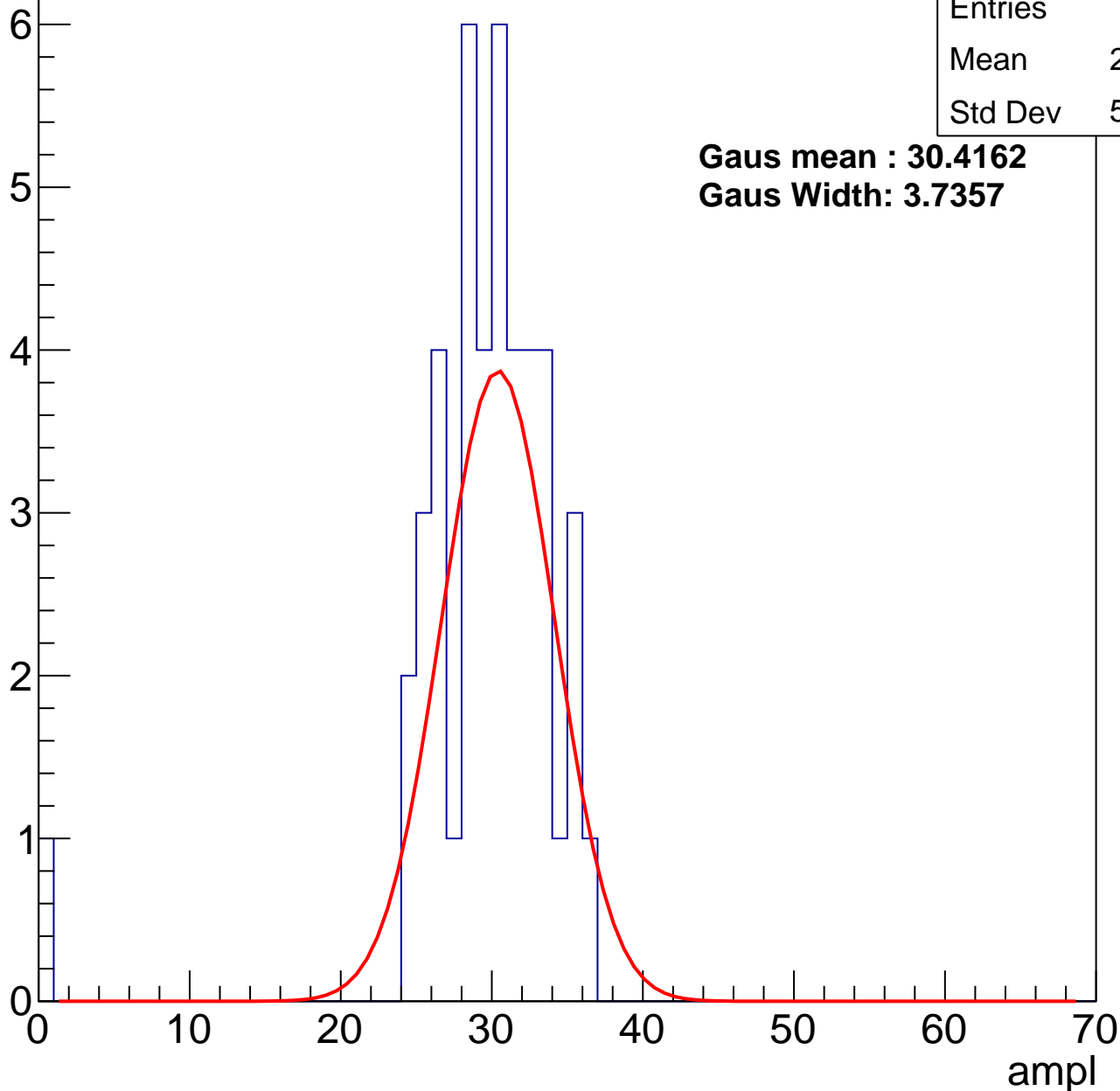
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 29.02 |
| Std Dev | 5.412 |

**Gaus mean : 30.4162**

**Gaus Width: 3.7357**



# B0L002S, U1-ch85, adc1

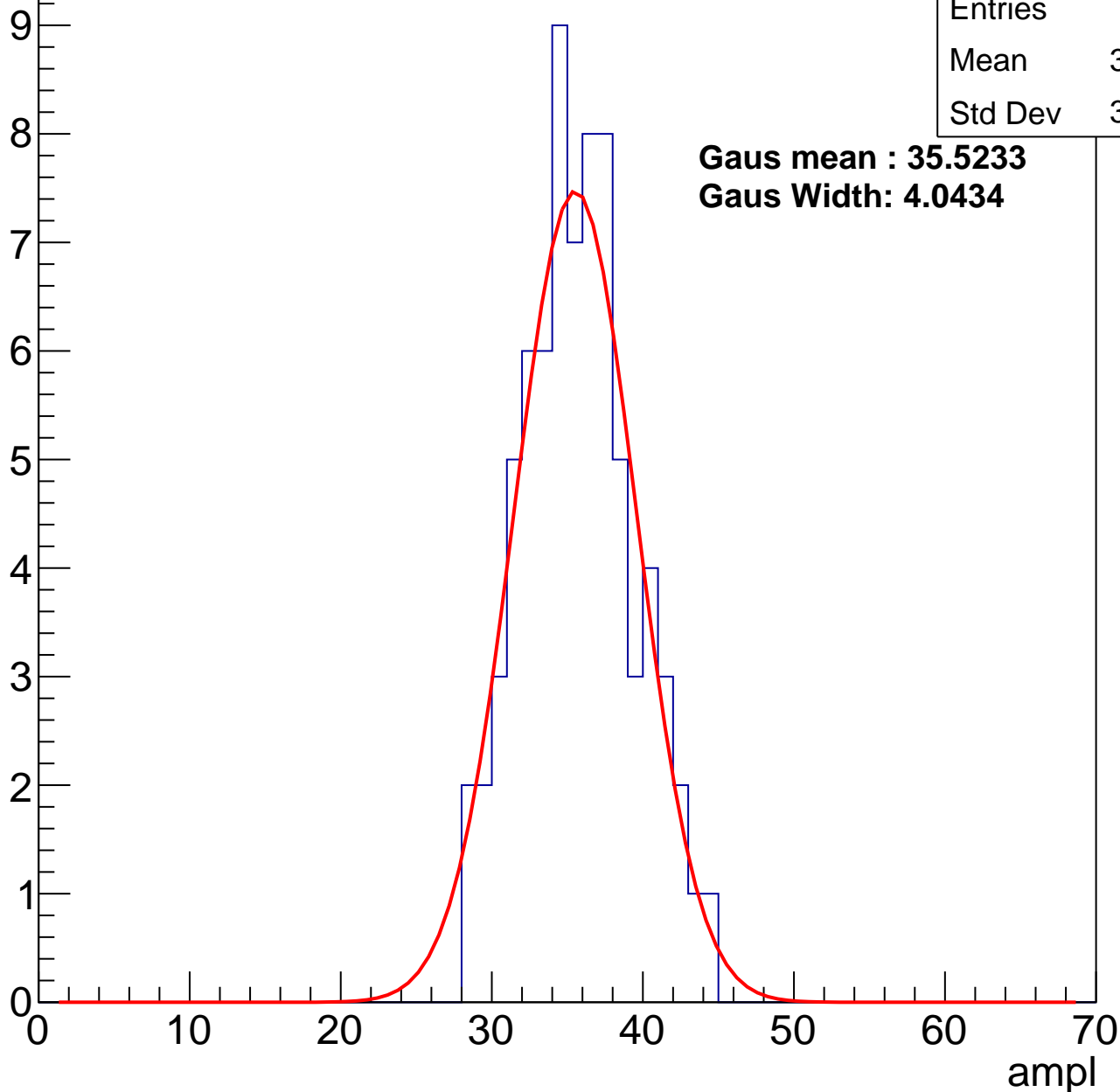
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 35.27 |
| Std Dev | 3.634 |

**Gaus mean : 35.5233**

**Gaus Width: 4.0434**



# B0L002S, U1-ch85, adc2

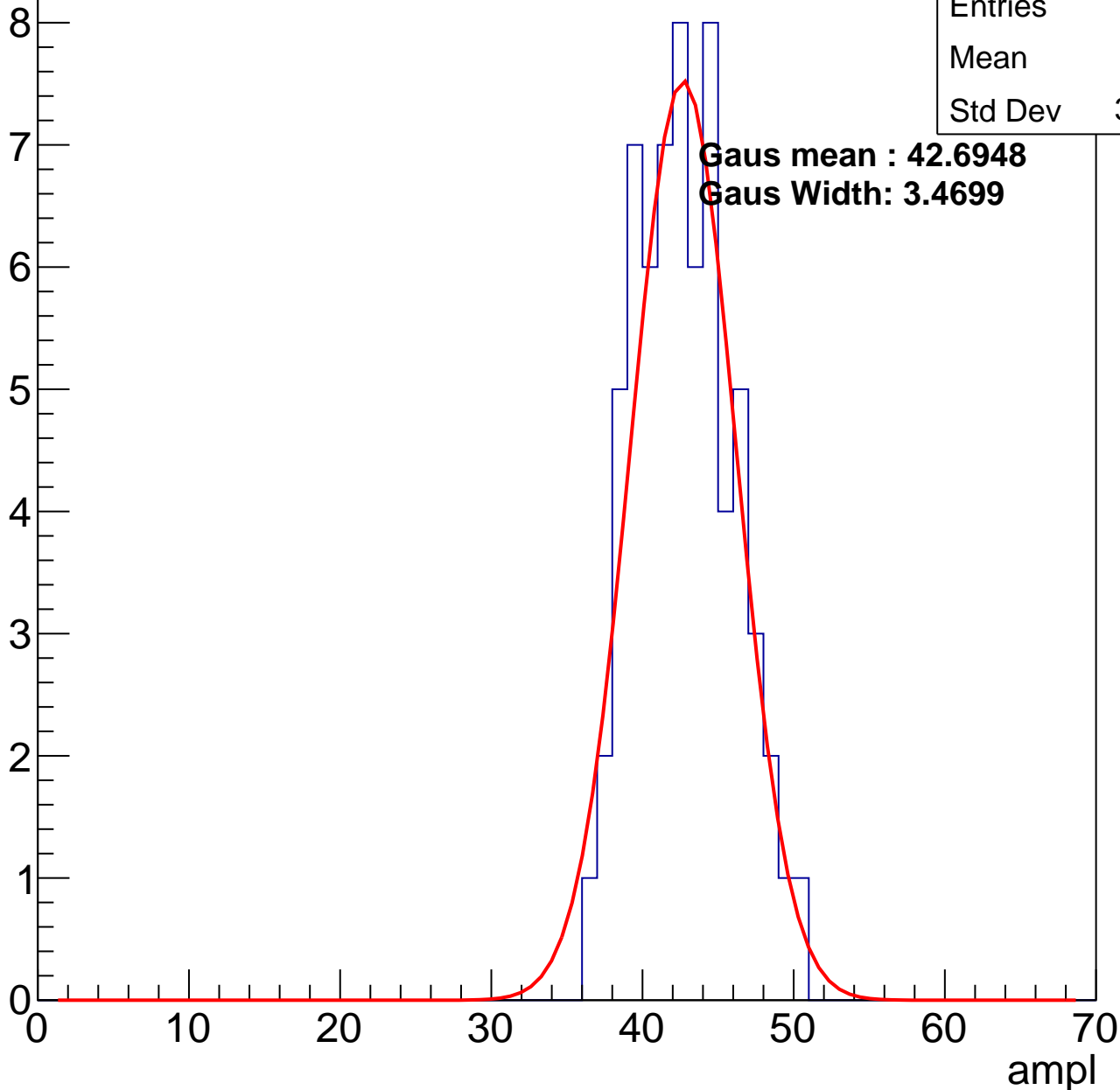
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 42.3  |
| Std Dev | 3.181 |

**Gaus mean : 42.6948**

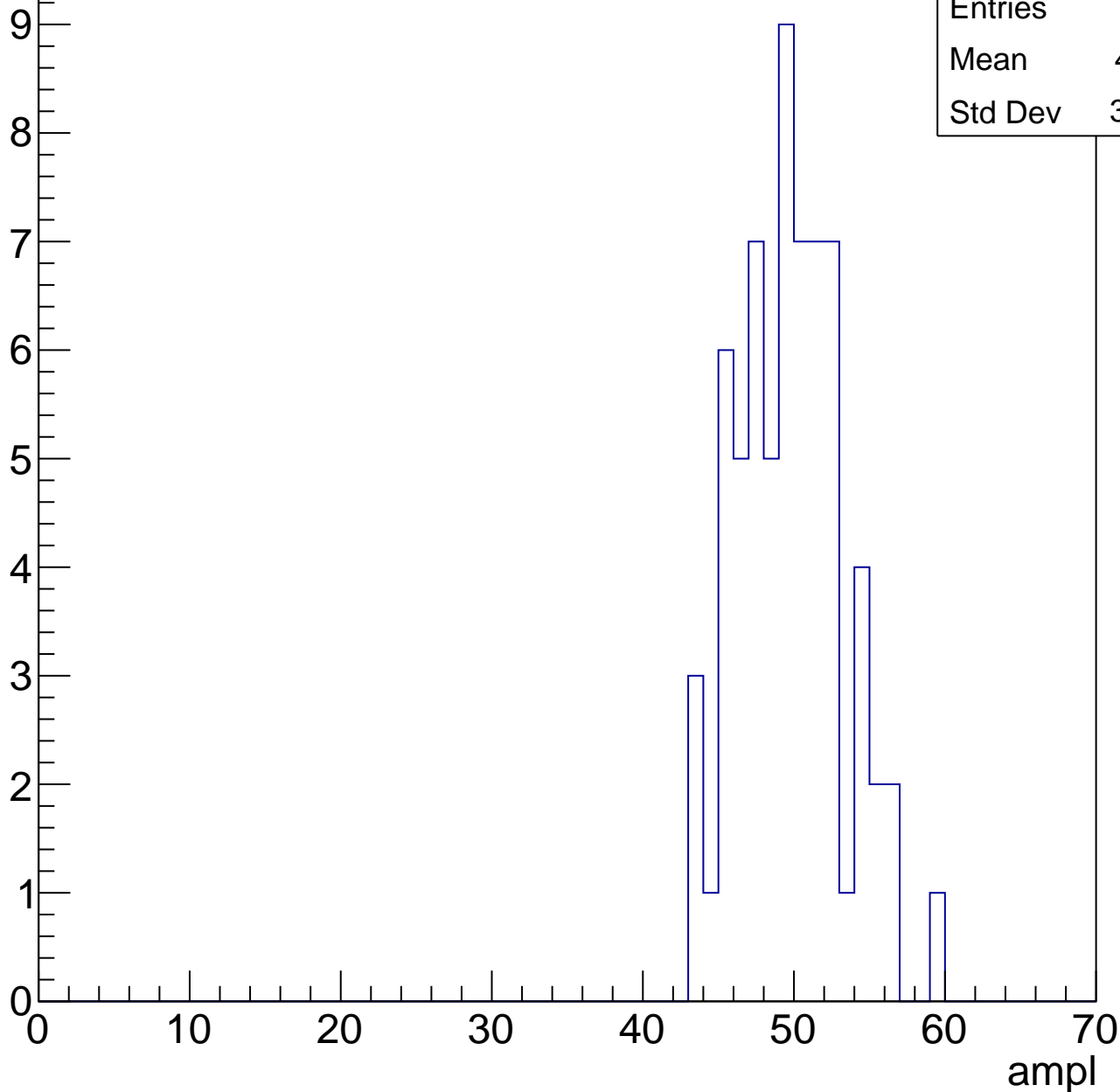
**Gaus Width: 3.4699**



# B0L002S, U1-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

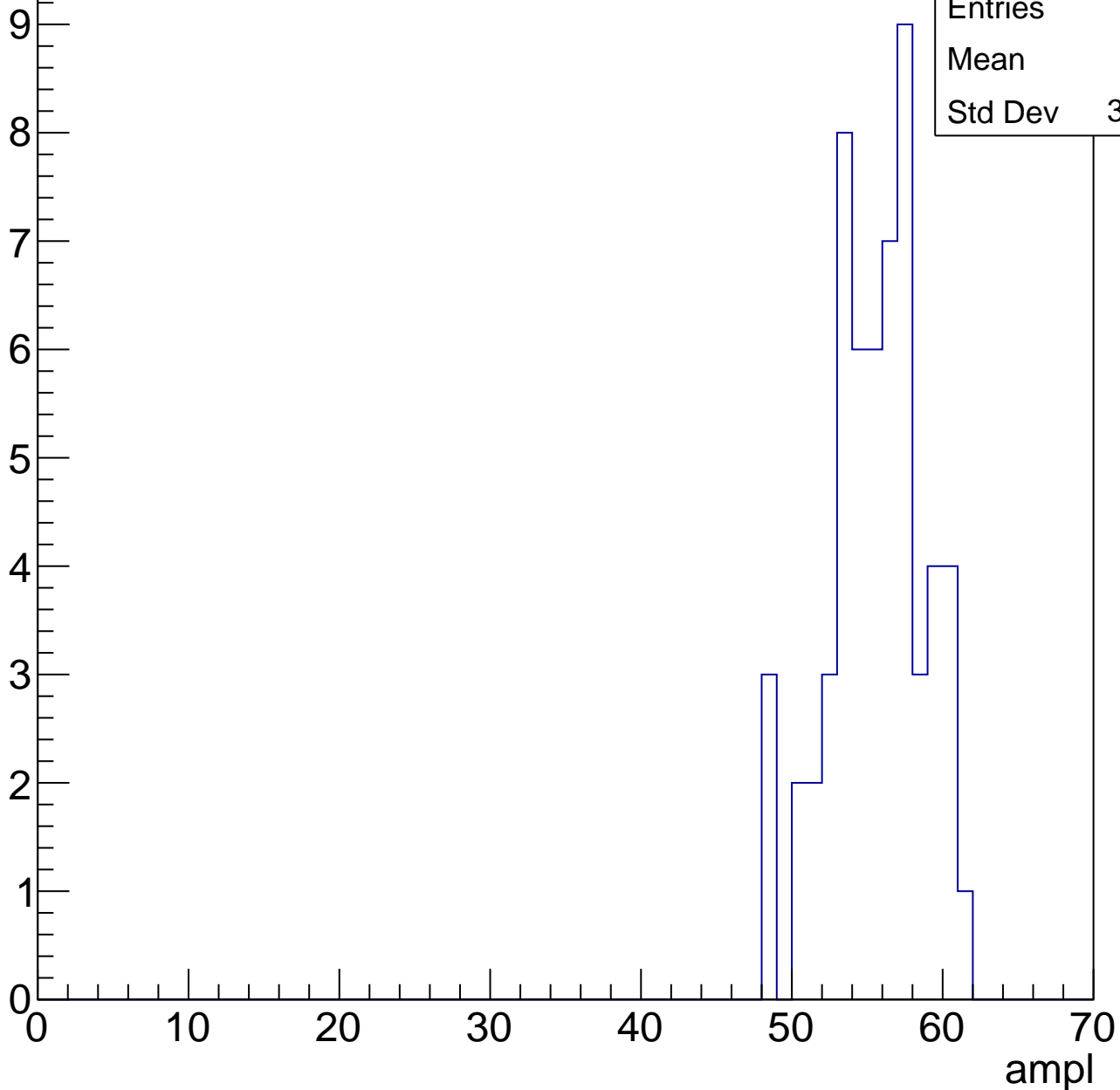


# B0L002S, U1-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 55.1  |
| Std Dev | 3.117 |

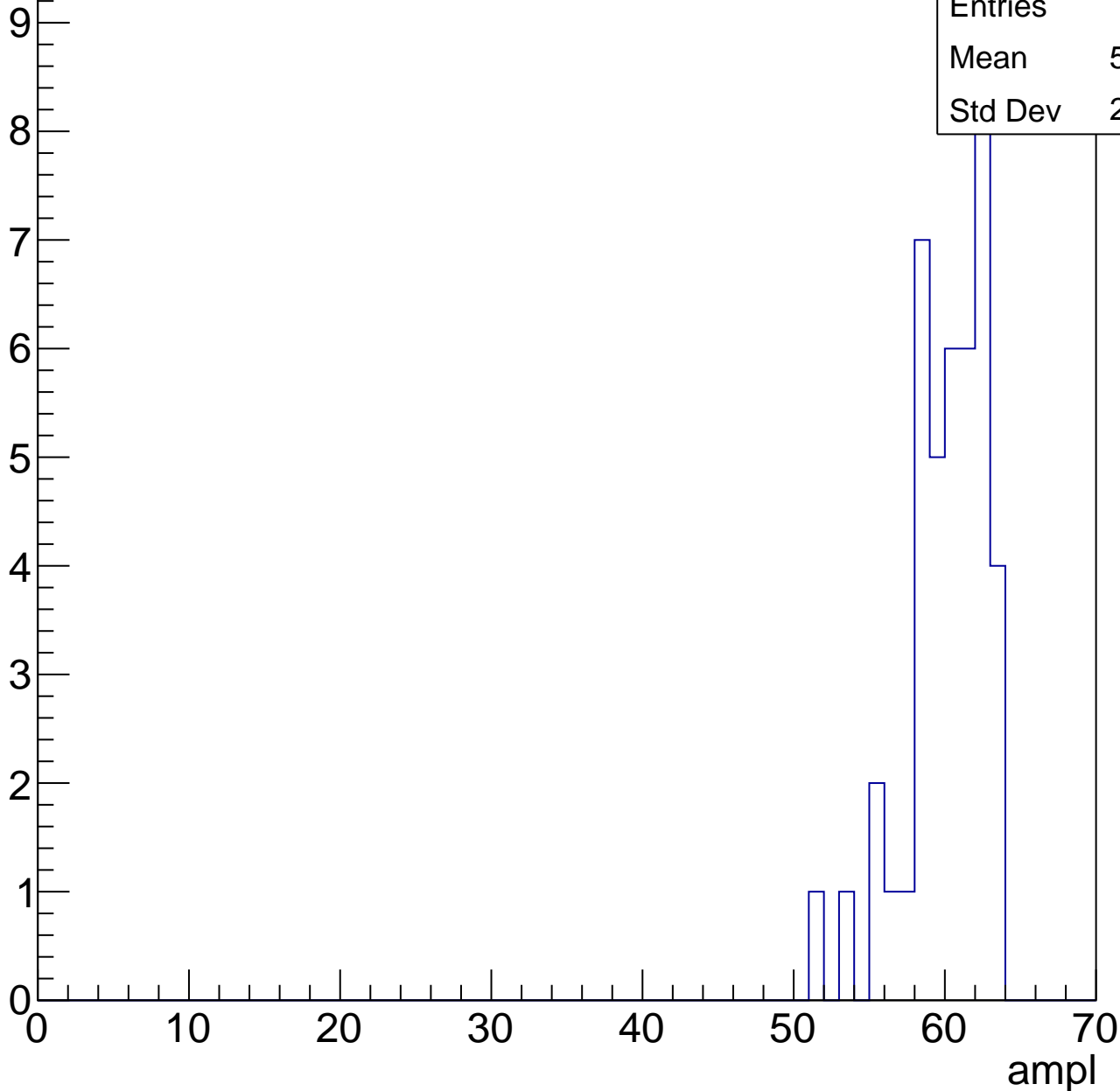


# B0L002S, U1-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 59.63 |
| Std Dev | 2.685 |

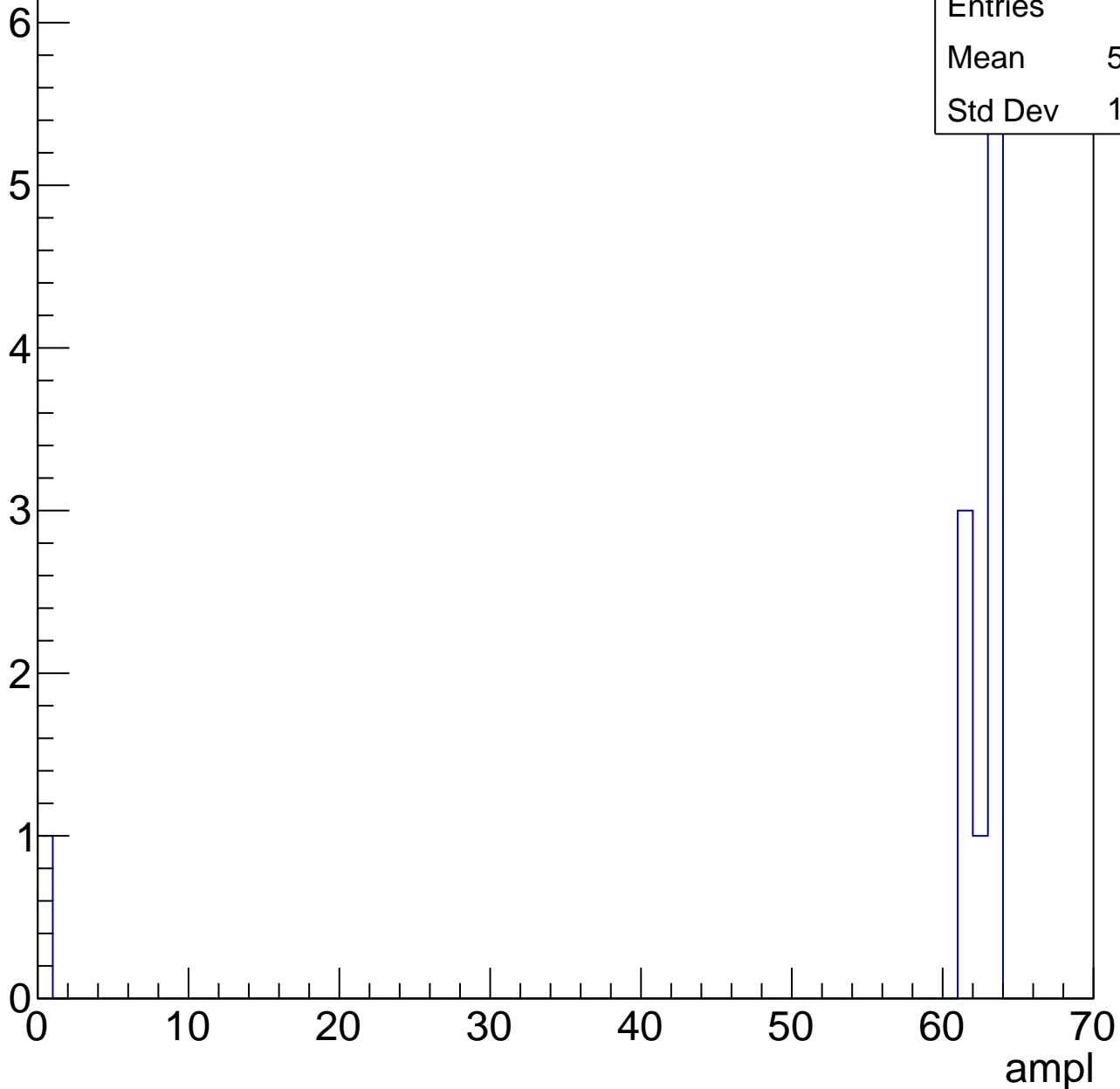


# B0L002S, U1-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 56.64 |
| Std Dev | 17.93 |





# B0L002S, U1-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 11.5 |
| Std Dev | 11.5 |

# B0L002S, U1-ch86, adc0

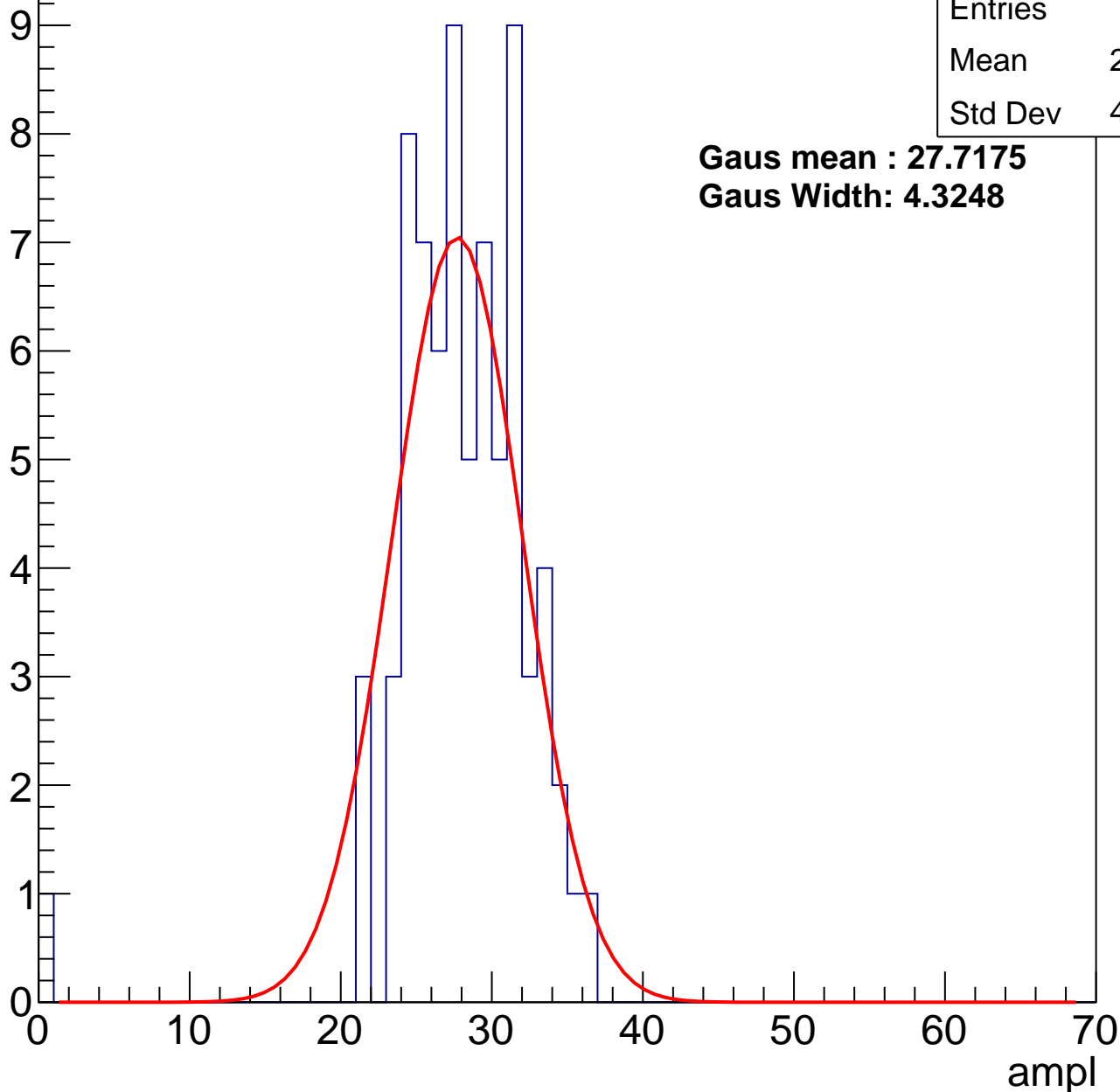
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 27.53 |
| Std Dev | 4.728 |

**Gaus mean : 27.7175**

**Gaus Width: 4.3248**



# B0L002S, U1-ch86, adc1

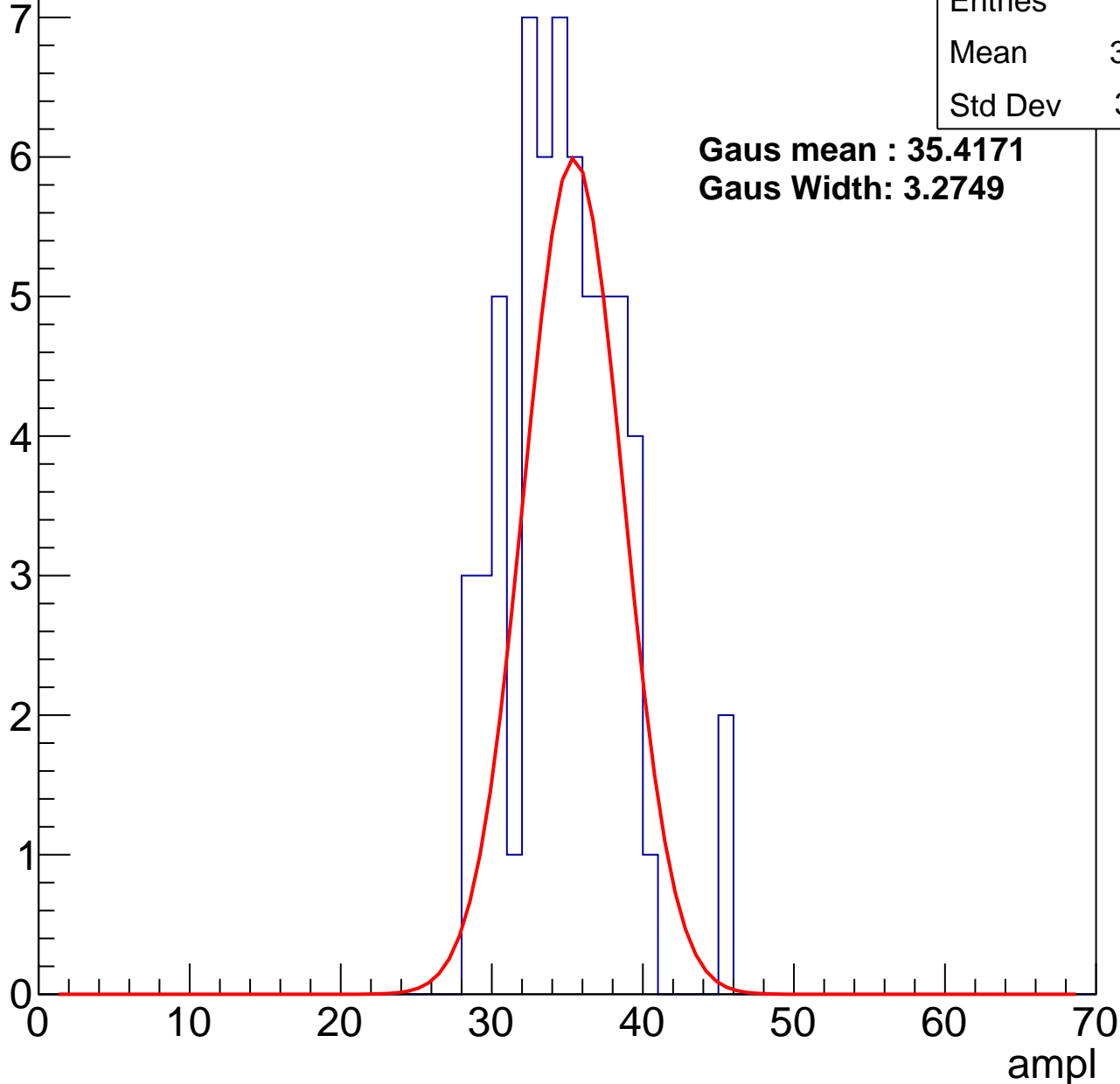
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 34.38 |
| Std Dev | 3.711 |

**Gaus mean : 35.4171**

**Gaus Width: 3.2749**



# B0L002S, U1-ch86, adc2

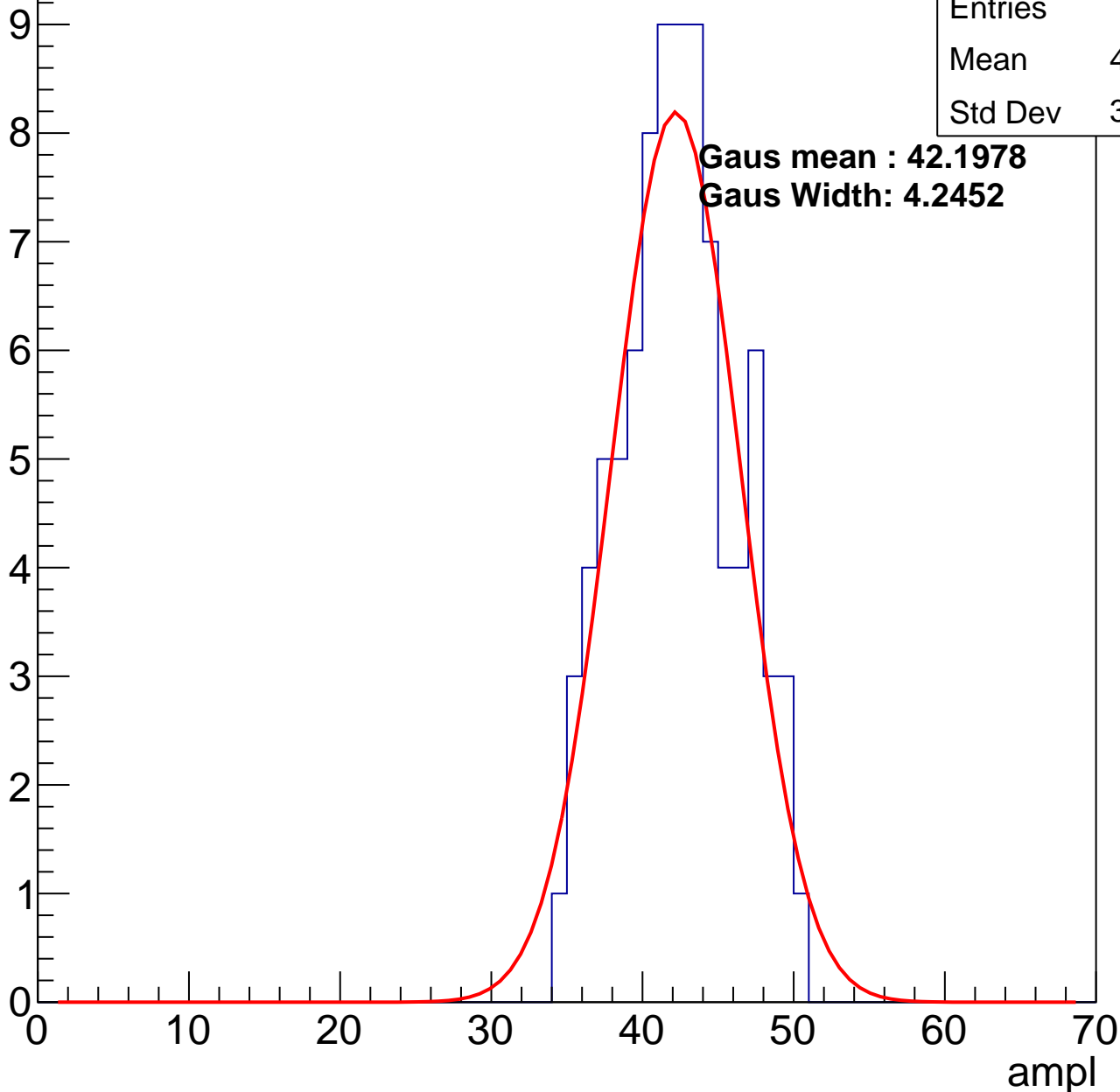
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 87    |
| Mean    | 41.85 |
| Std Dev | 3.804 |

**Gaus mean : 42.1978**

**Gaus Width: 4.2452**

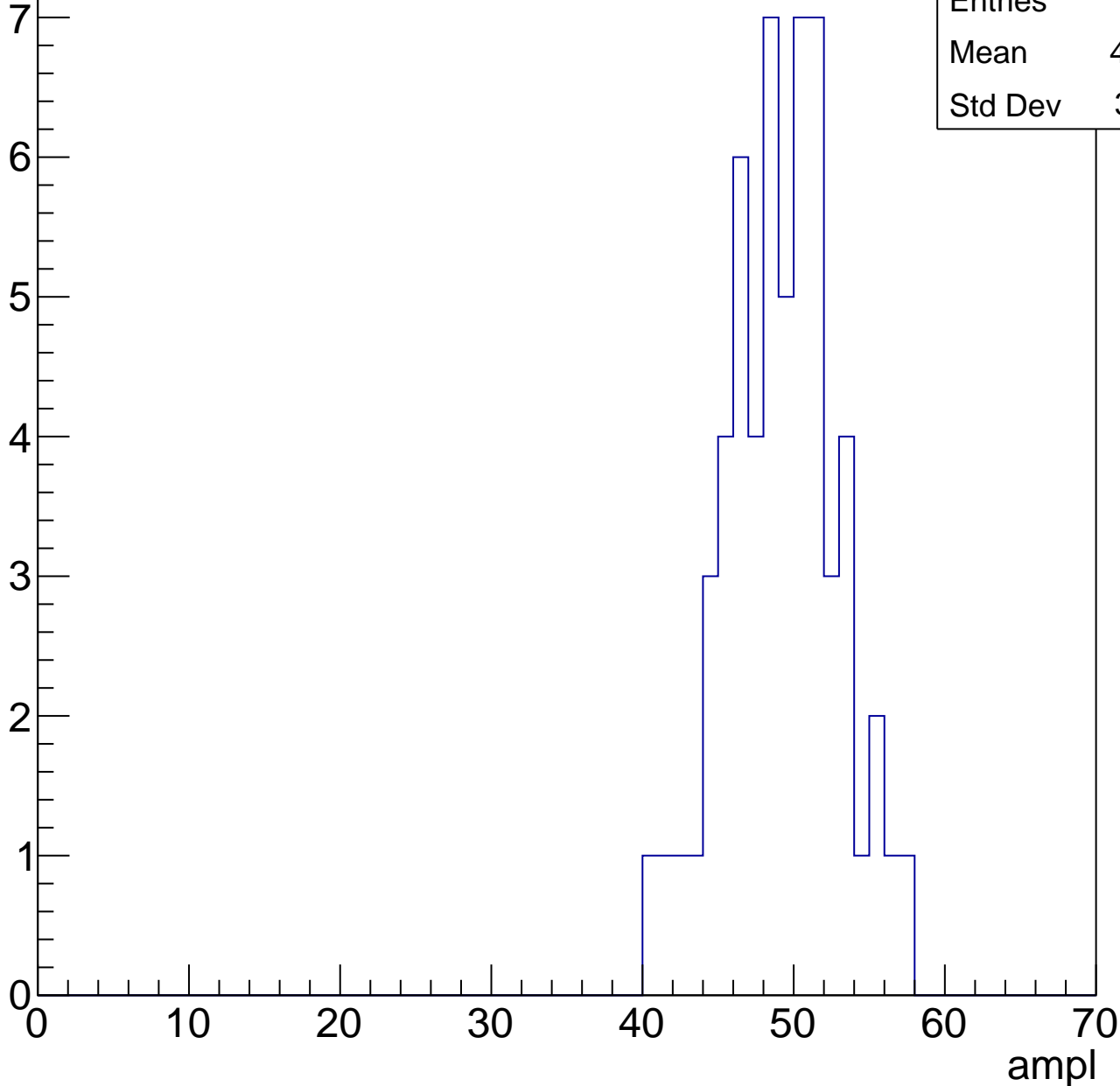


# B0L002S, U1-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

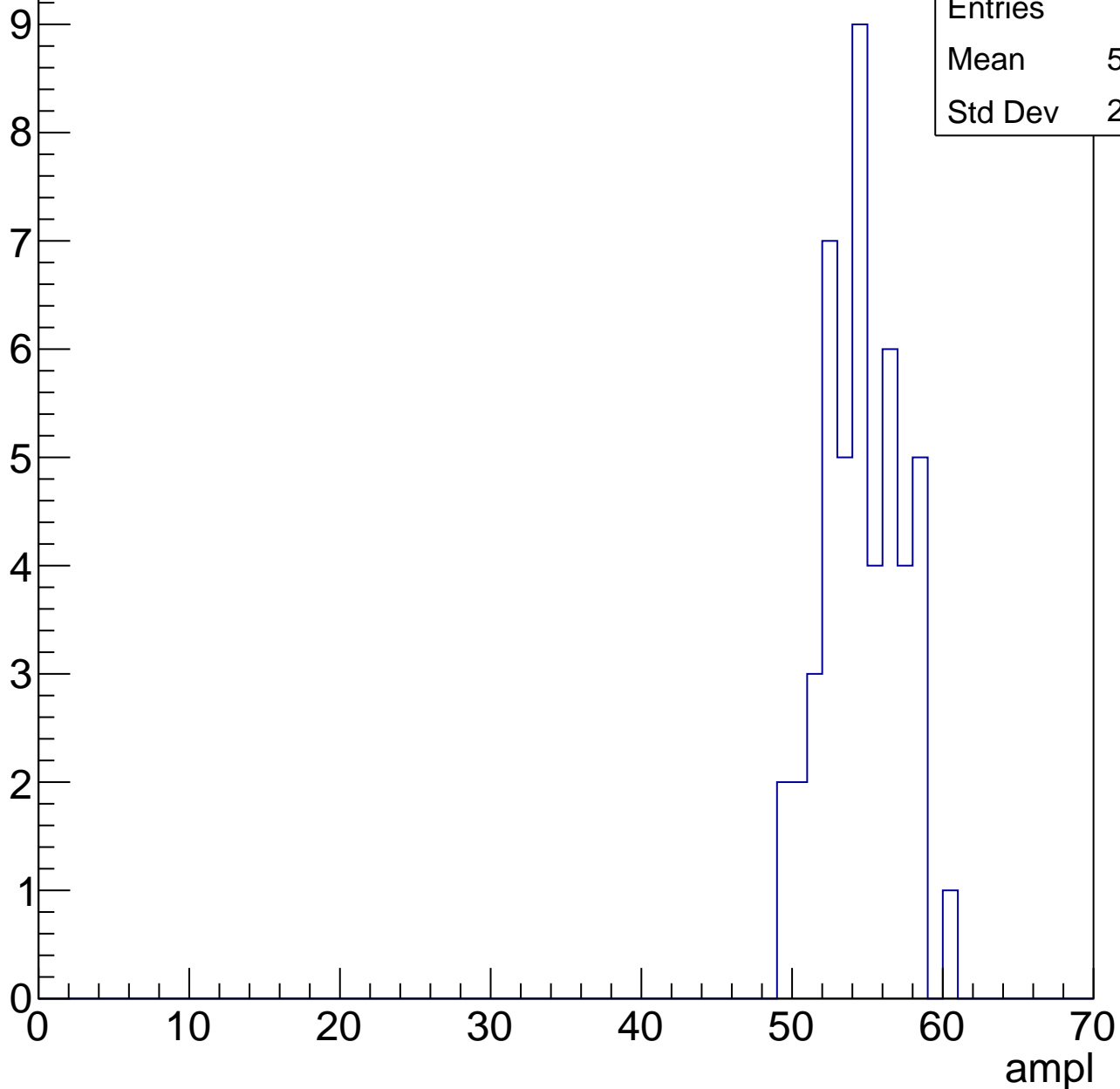
|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 48.73 |
| Std Dev | 3.631 |



# B0L002S, U1-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

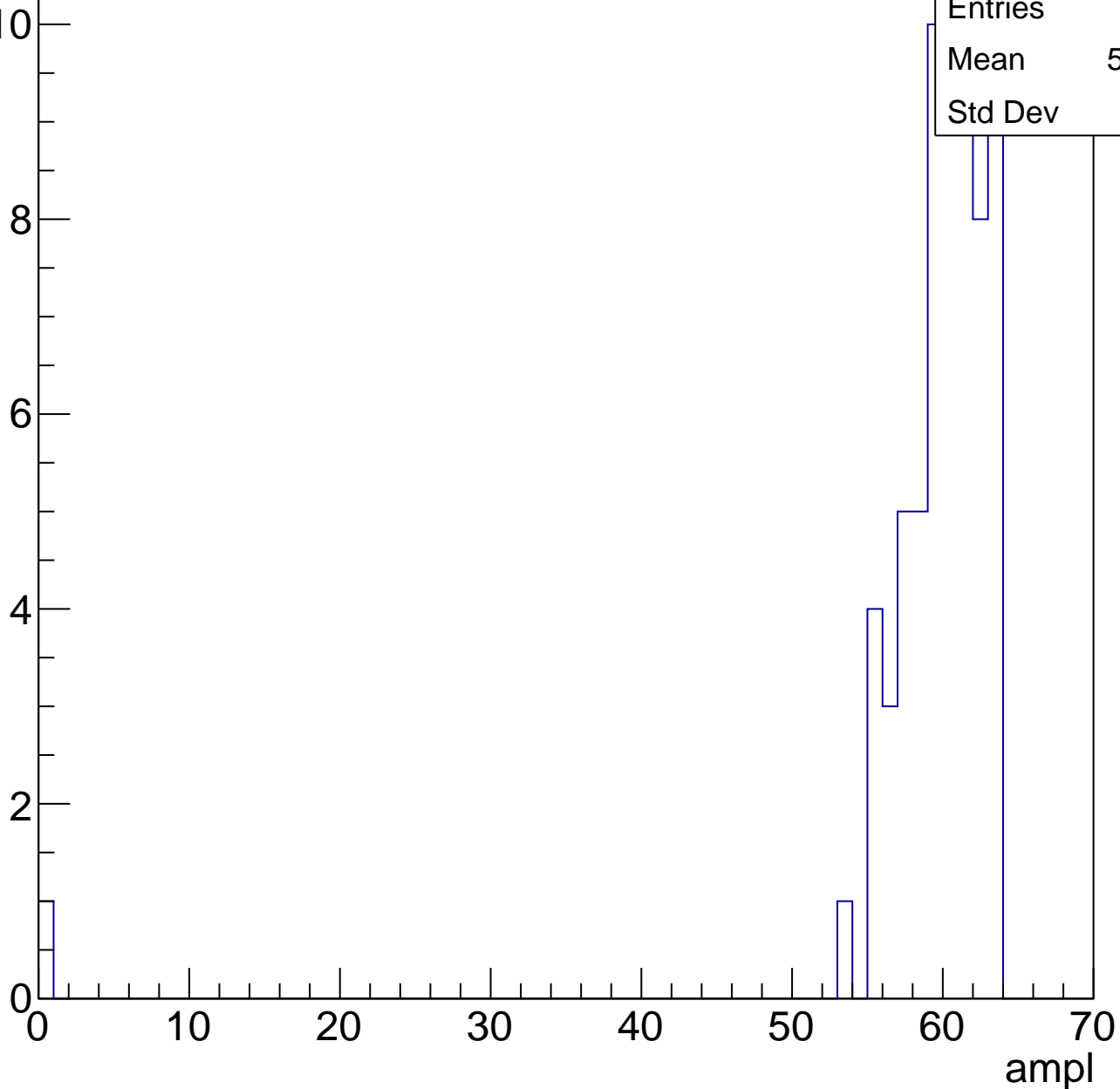


# B0L002S, U1-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

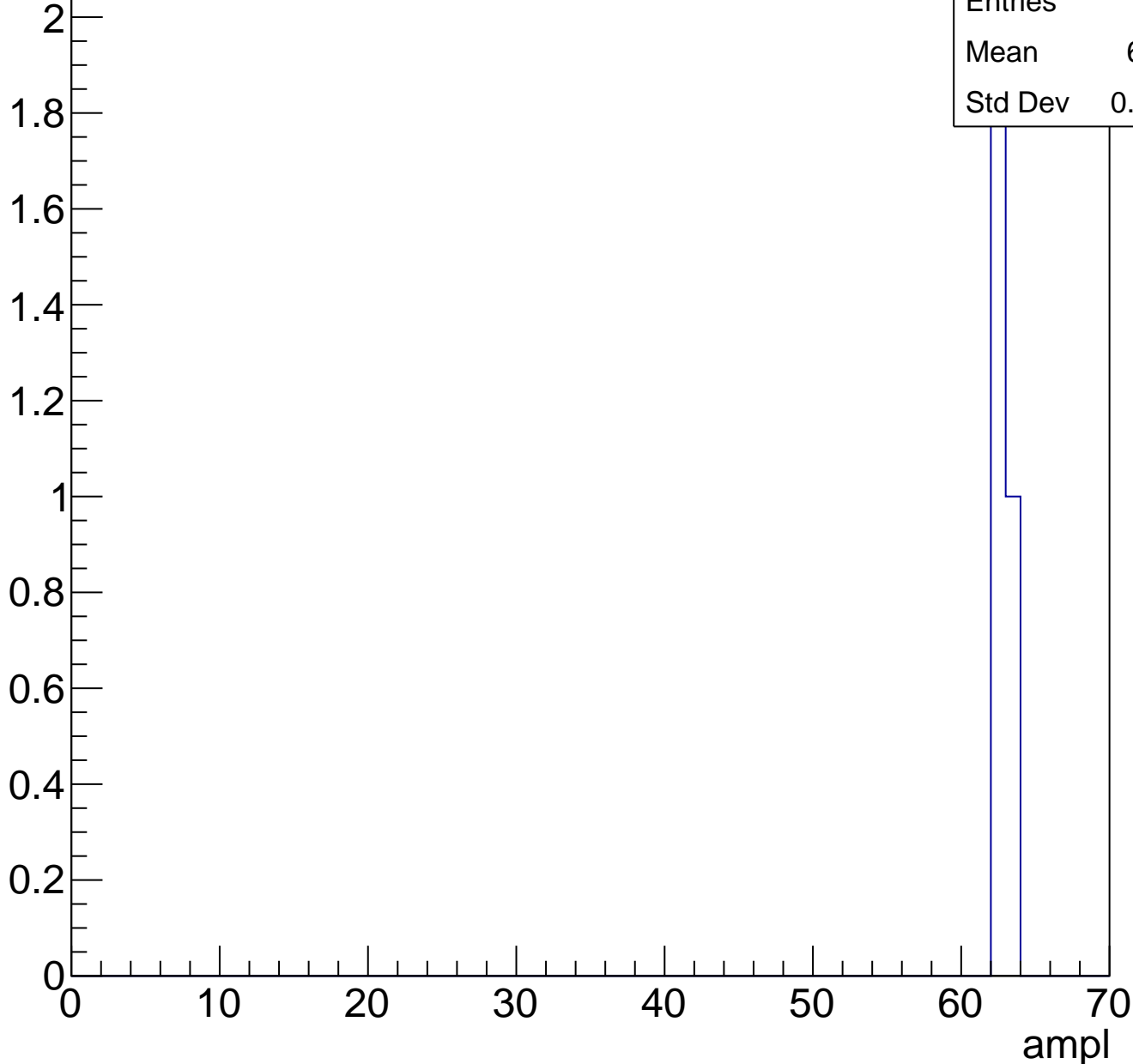
|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 58.75 |
| Std Dev | 7.74  |



# B0L002S, U1-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch87, adc0

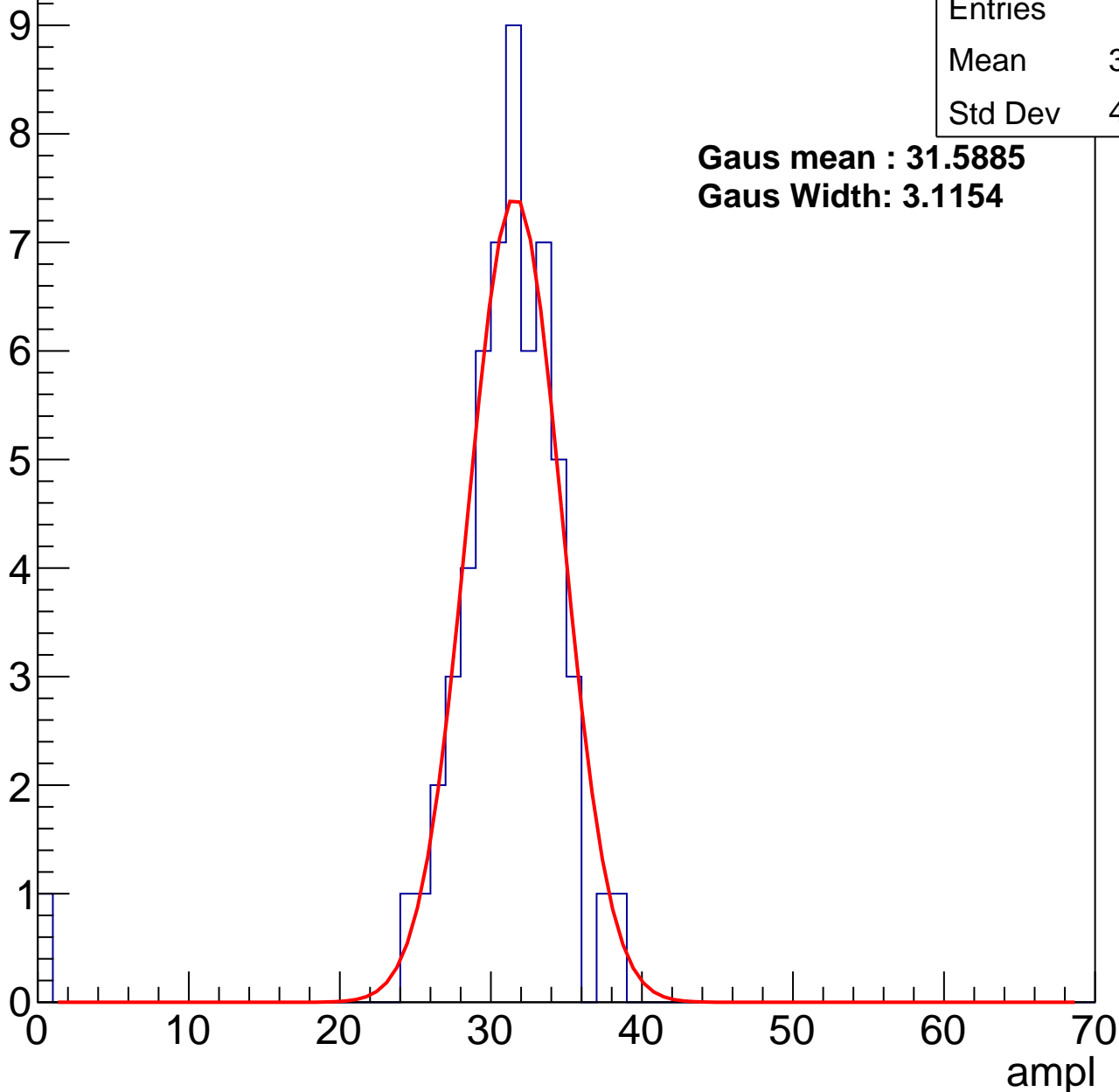
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 30.35 |
| Std Dev | 4.954 |

**Gaus mean : 31.5885**

**Gaus Width: 3.1154**



# B0L002S, U1-ch87, adc1

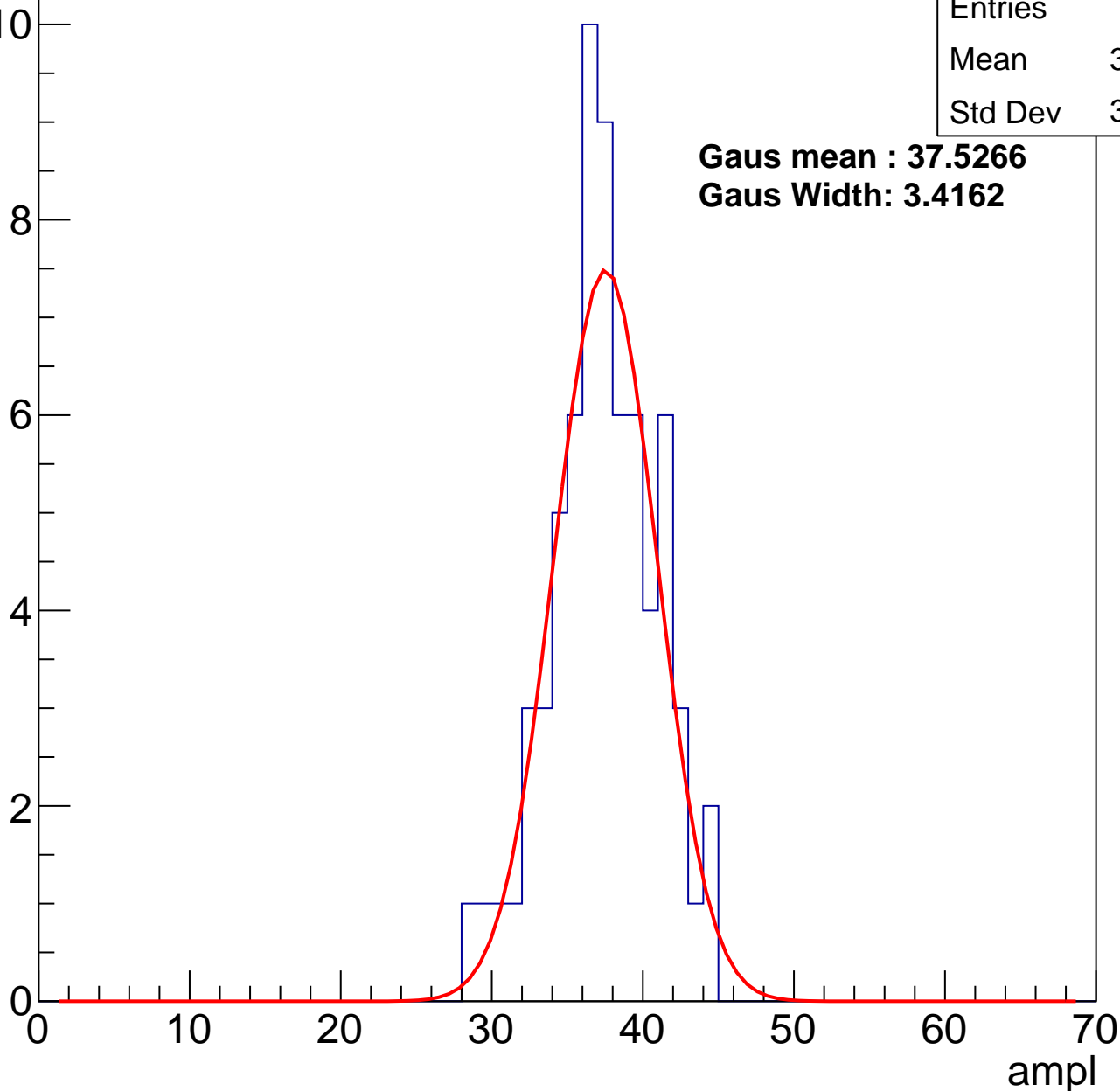
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 36.93 |
| Std Dev | 3.436 |

**Gaus mean : 37.5266**

**Gaus Width: 3.4162**



# B0L002S, U1-ch87, adc2

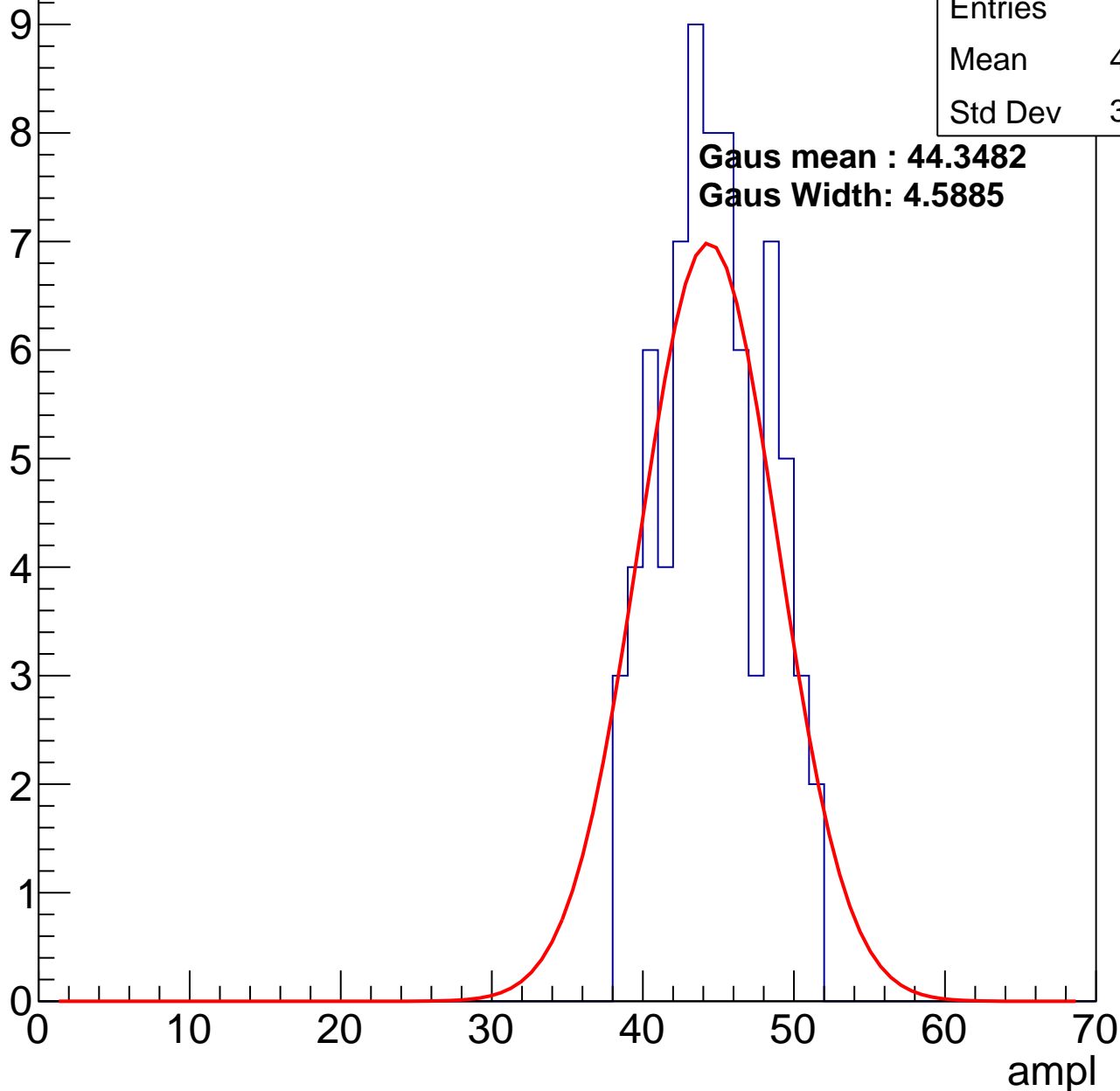
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 44.23 |
| Std Dev | 3.416 |

**Gaus mean : 44.3482**

**Gaus Width: 4.5885**

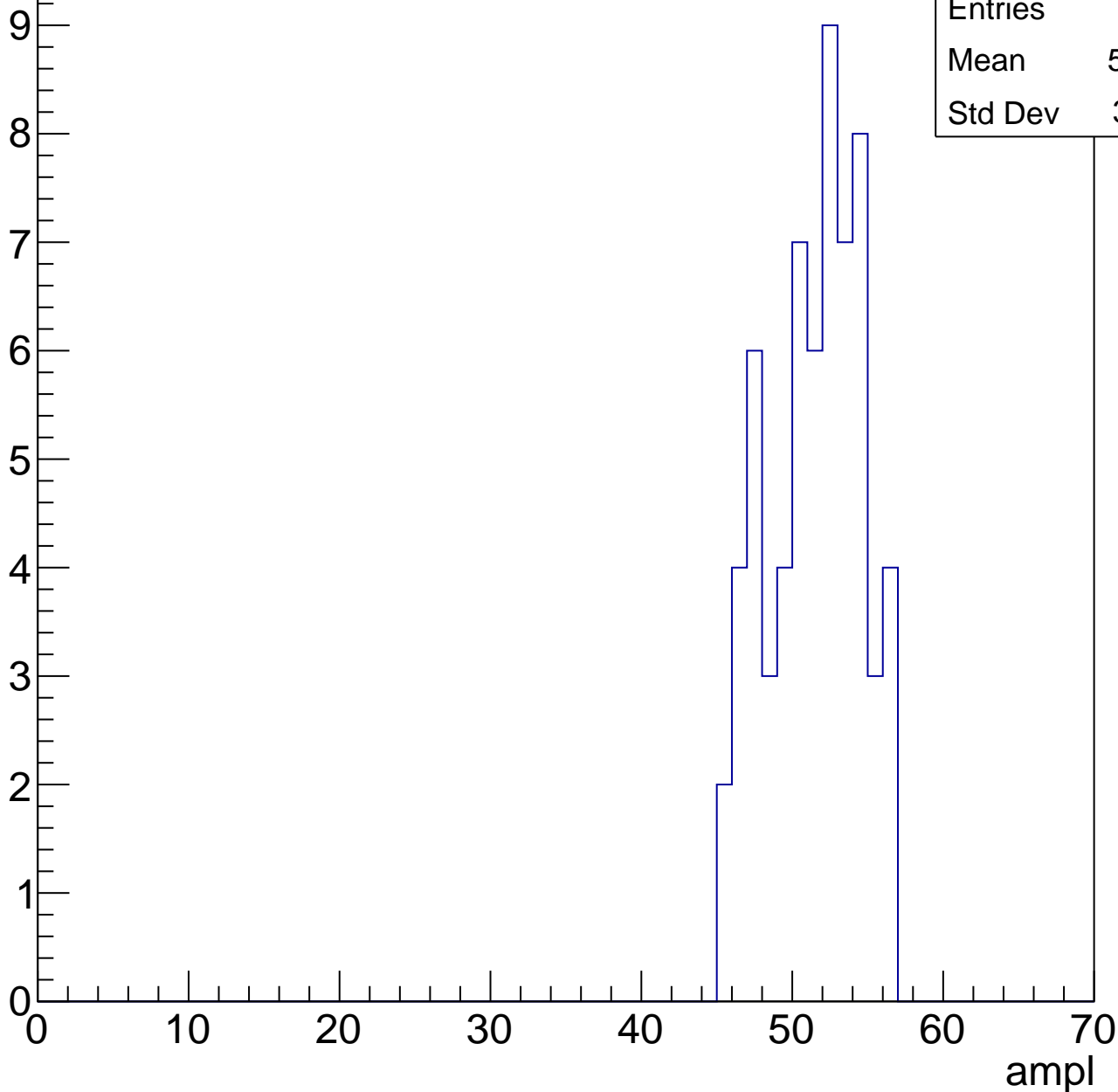


# B0L002S, U1-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 50.98 |
| Std Dev | 3.021 |

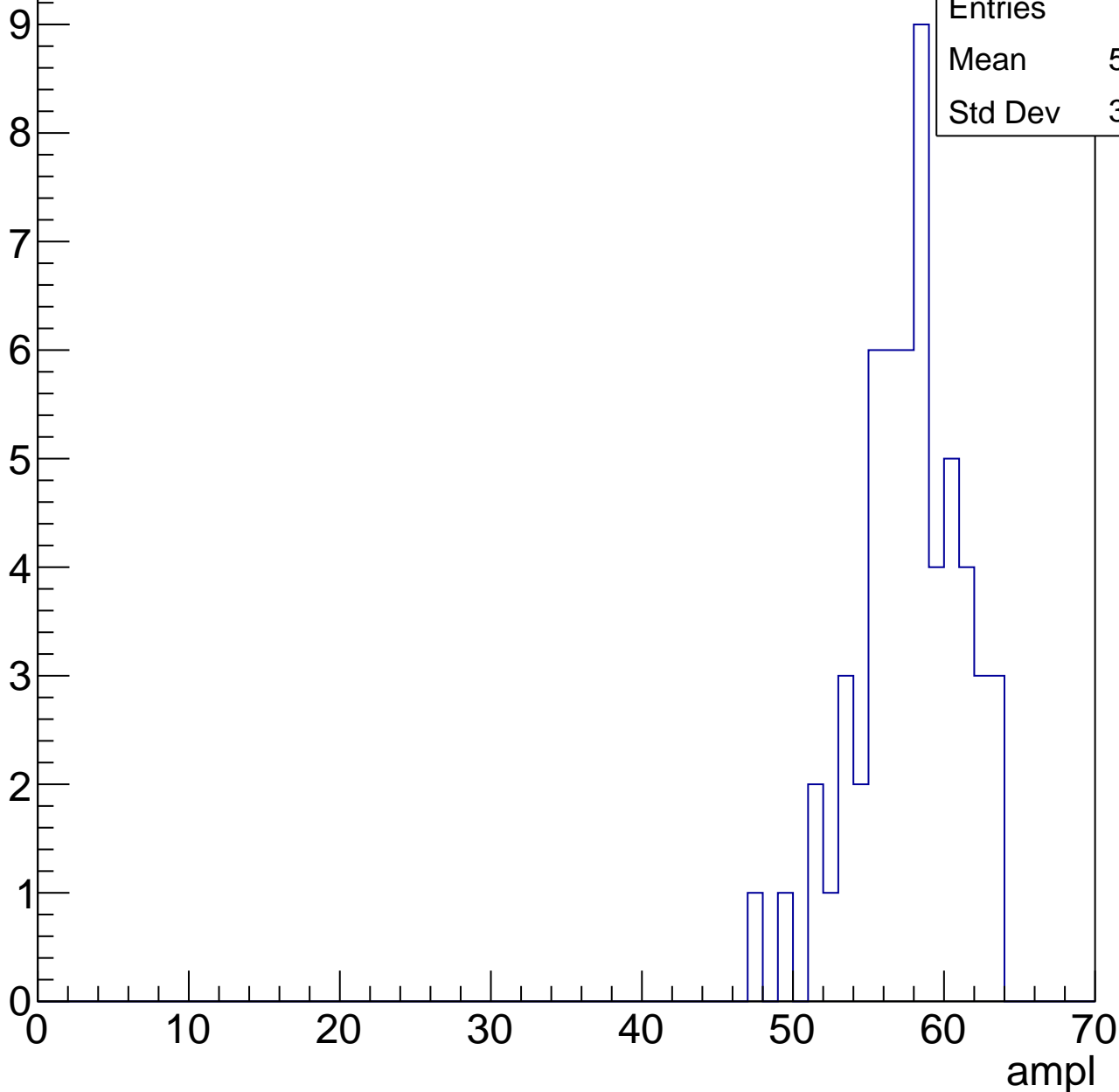


# B0L002S, U1-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 57.18 |
| Std Dev | 3.465 |

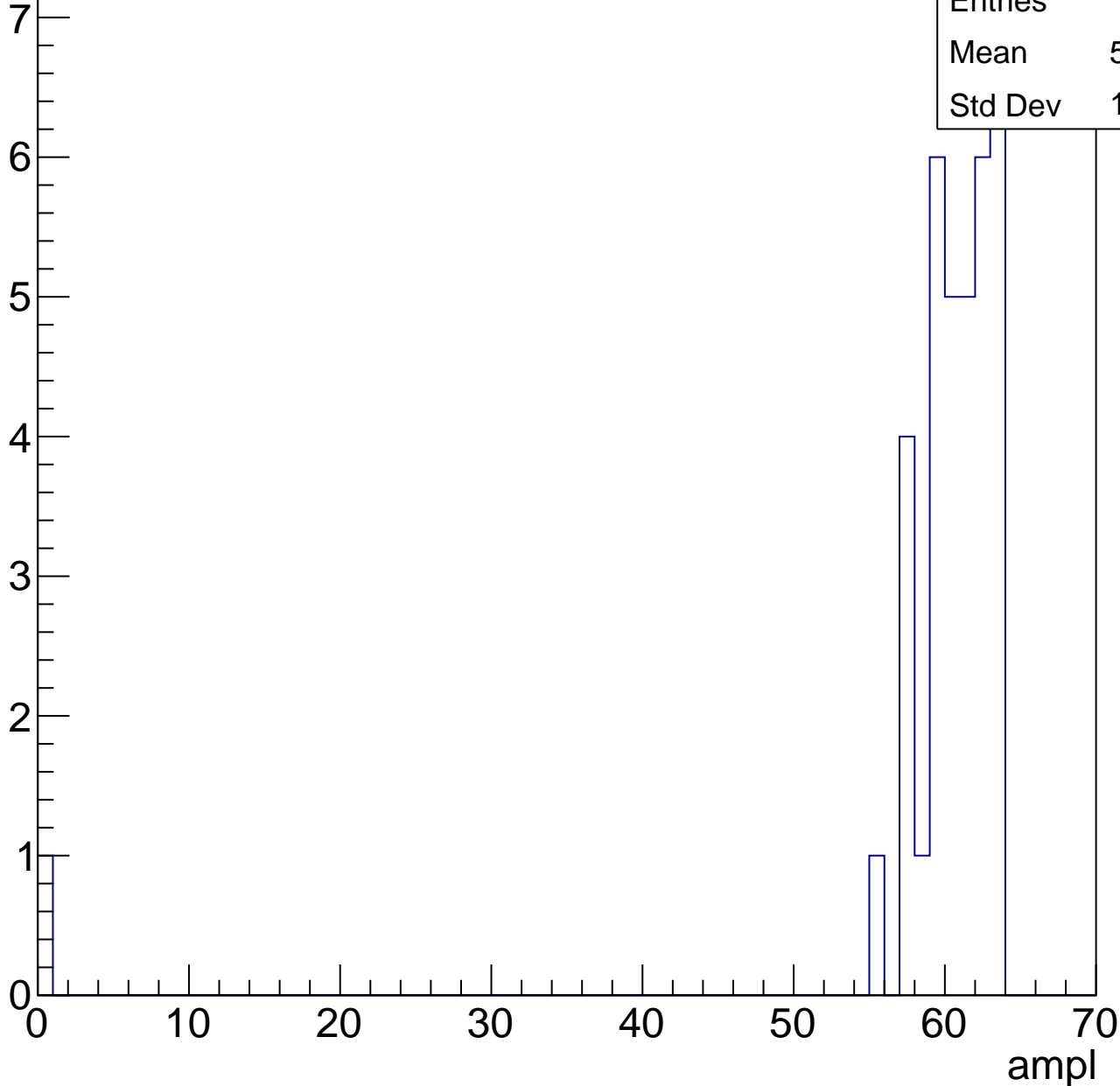


# B0L002S, U1-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 58.69 |
| Std Dev | 10.14 |



# B0L002S, U1-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch88, adc0

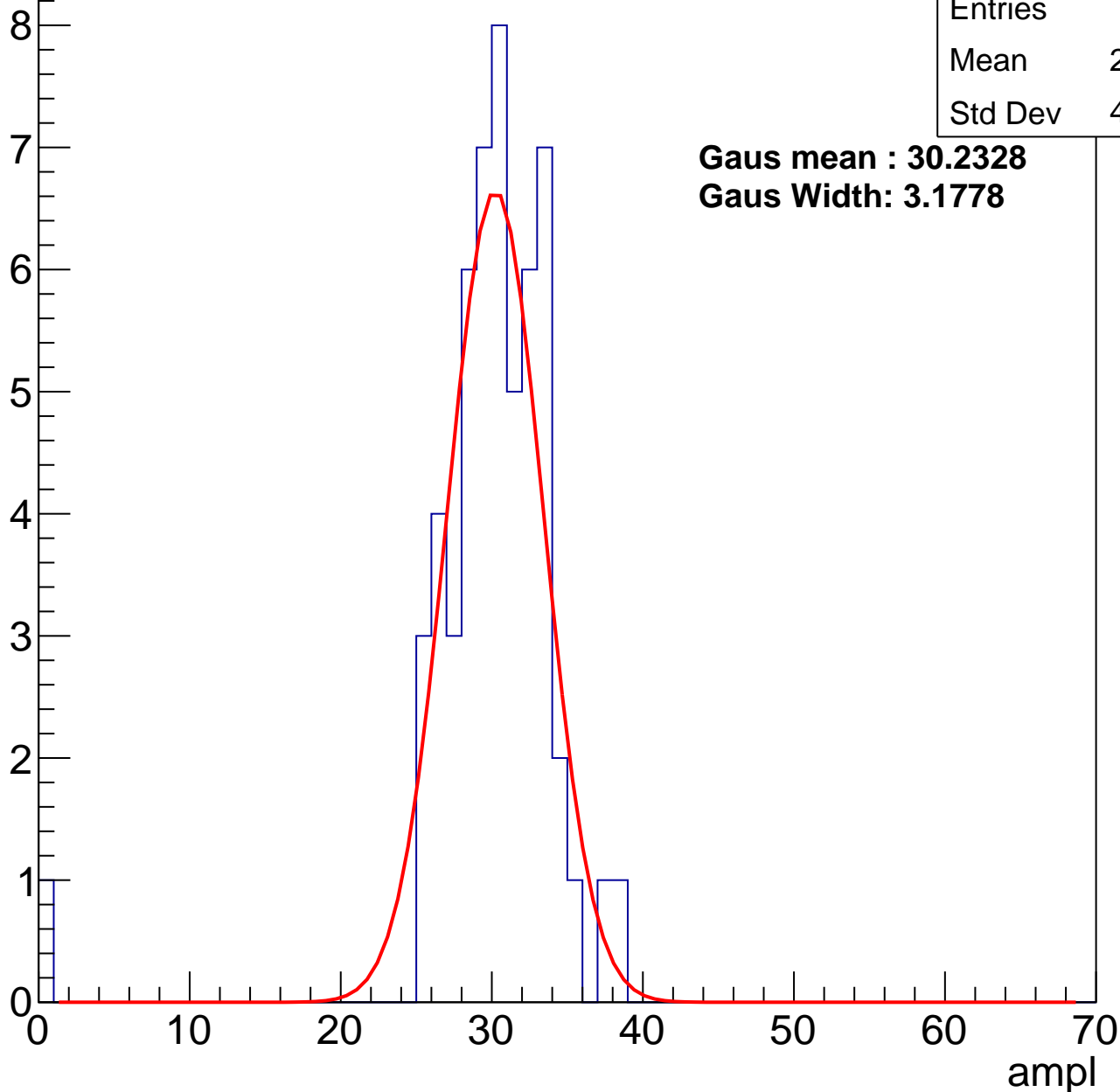
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 29.58 |
| Std Dev | 4.953 |

**Gaus mean : 30.2328**

**Gaus Width: 3.1778**



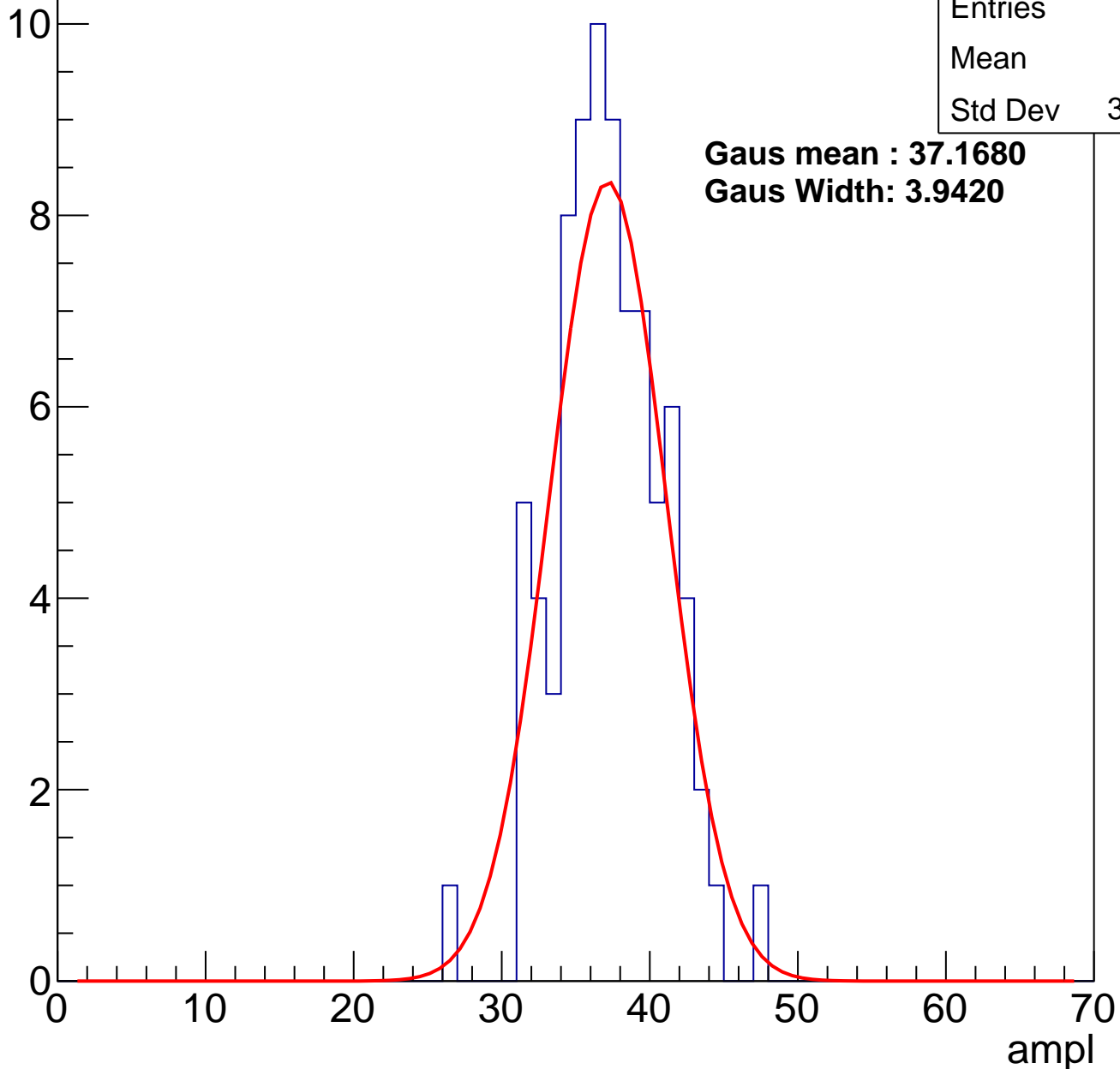
# B0L002S, U1-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 36.8  |
| Std Dev | 3.597 |

**Gaus mean : 37.1680**  
**Gaus Width: 3.9420**

Entry



# B0L002S, U1-ch88, adc2

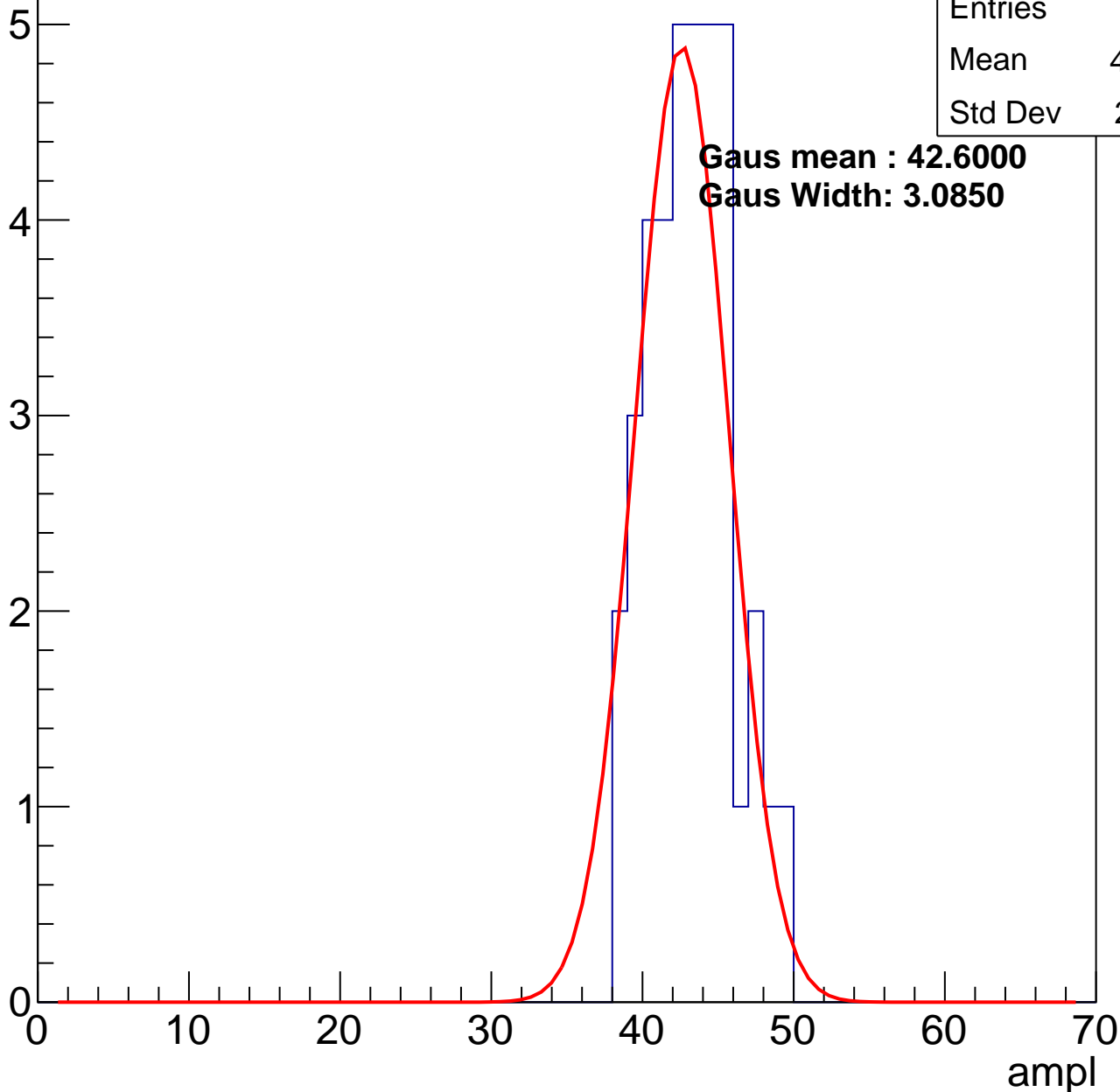
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 42.74 |
| Std Dev | 2.711 |

**Gaus mean : 42.6000**

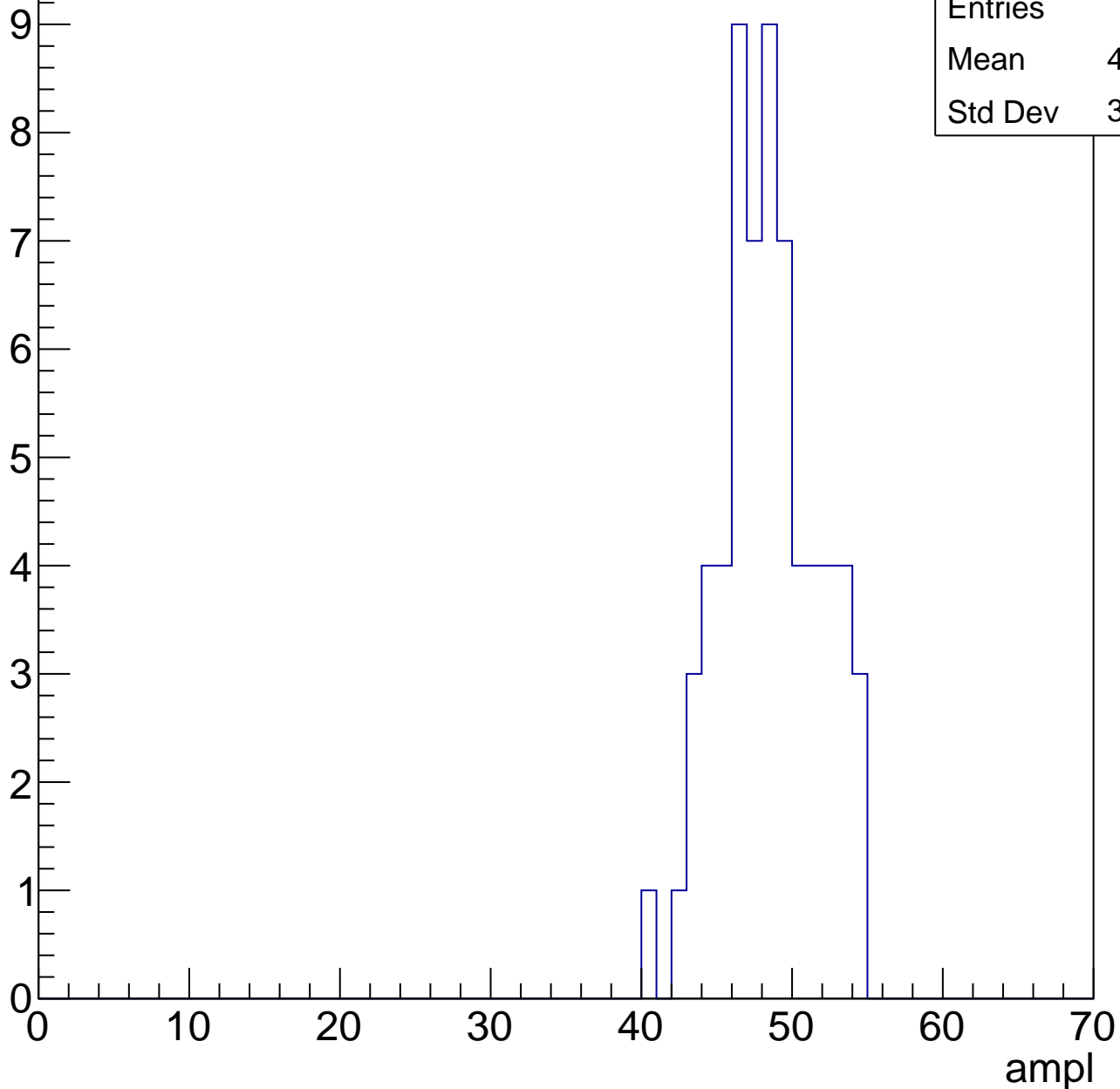
**Gaus Width: 3.0850**



# B0L002S, U1-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

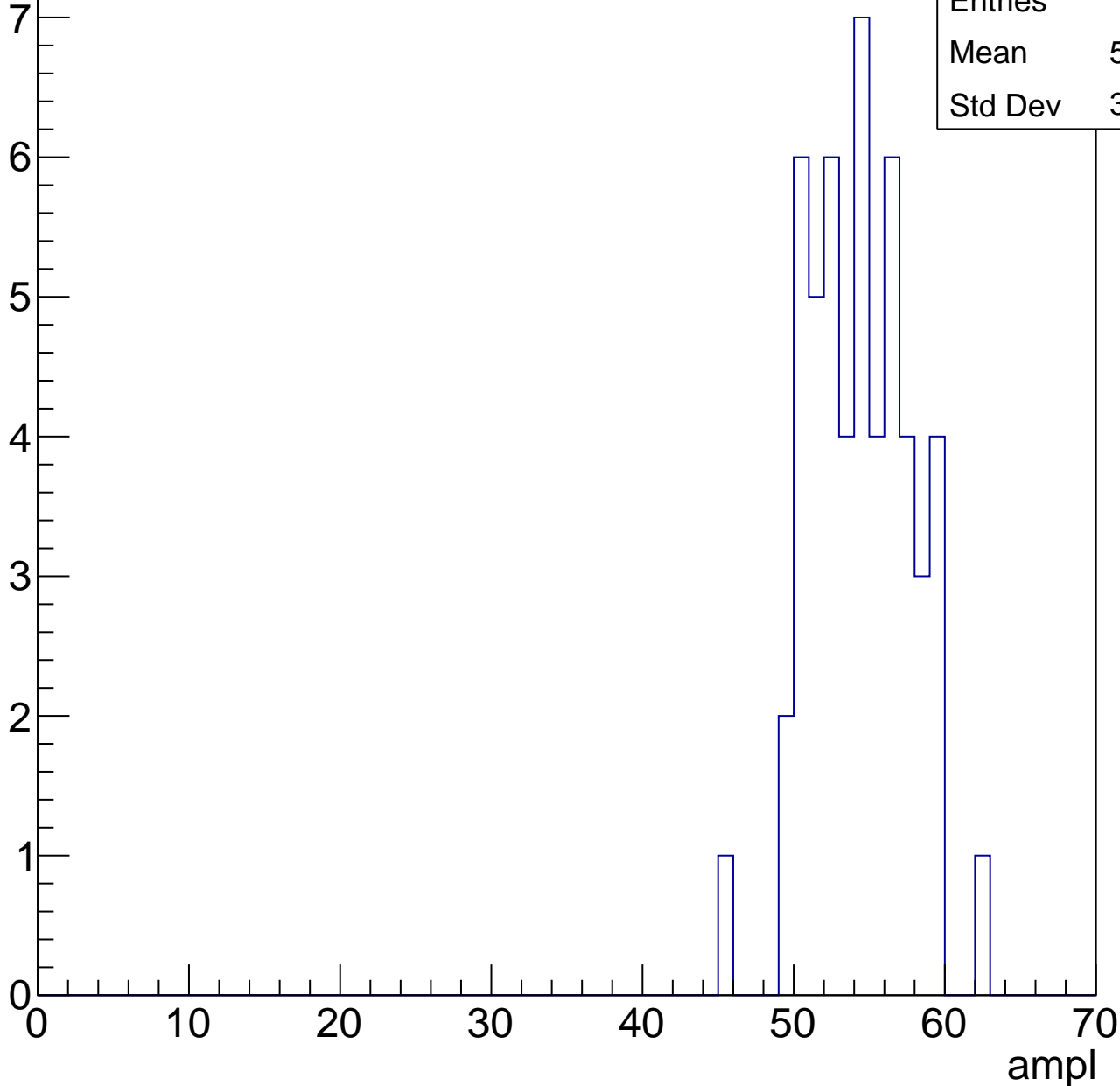


# B0L002S, U1-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

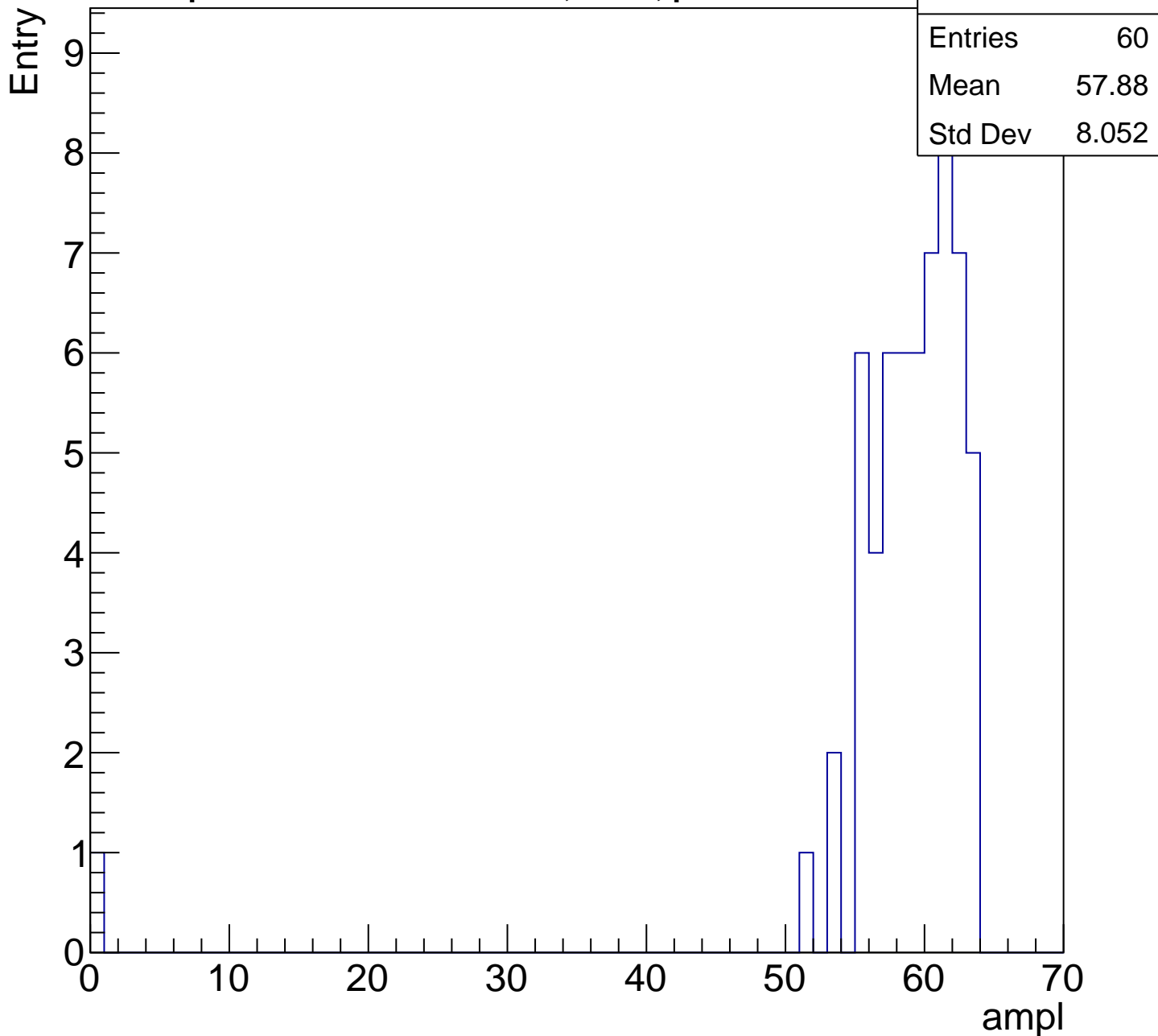
Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 53.89 |
| Std Dev | 3.295 |



# B0L002S, U1-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

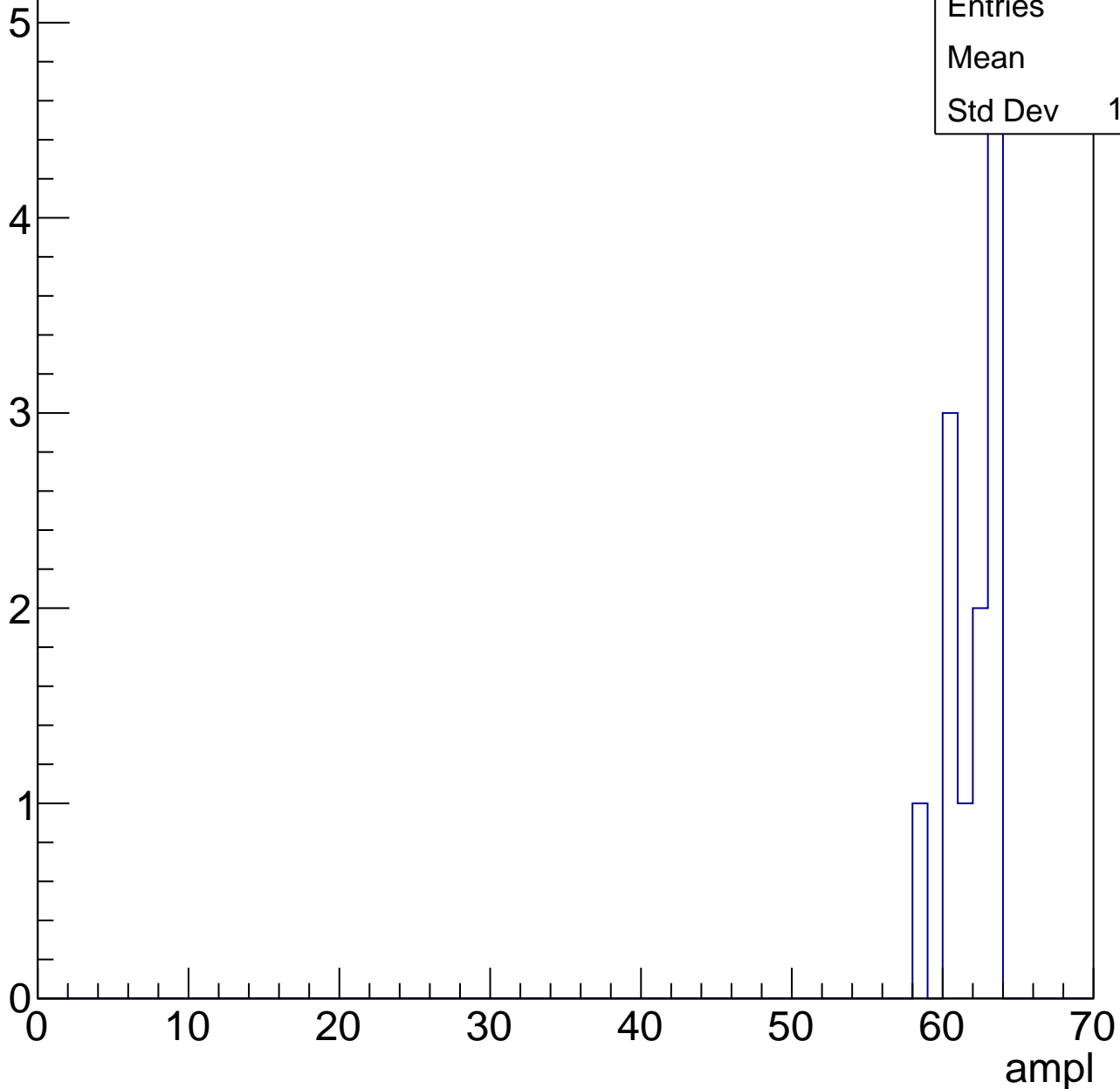


# B0L002S, U1-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 12    |
| Mean    | 61.5  |
| Std Dev | 1.607 |





# B0L002S, U1-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch89, adc0

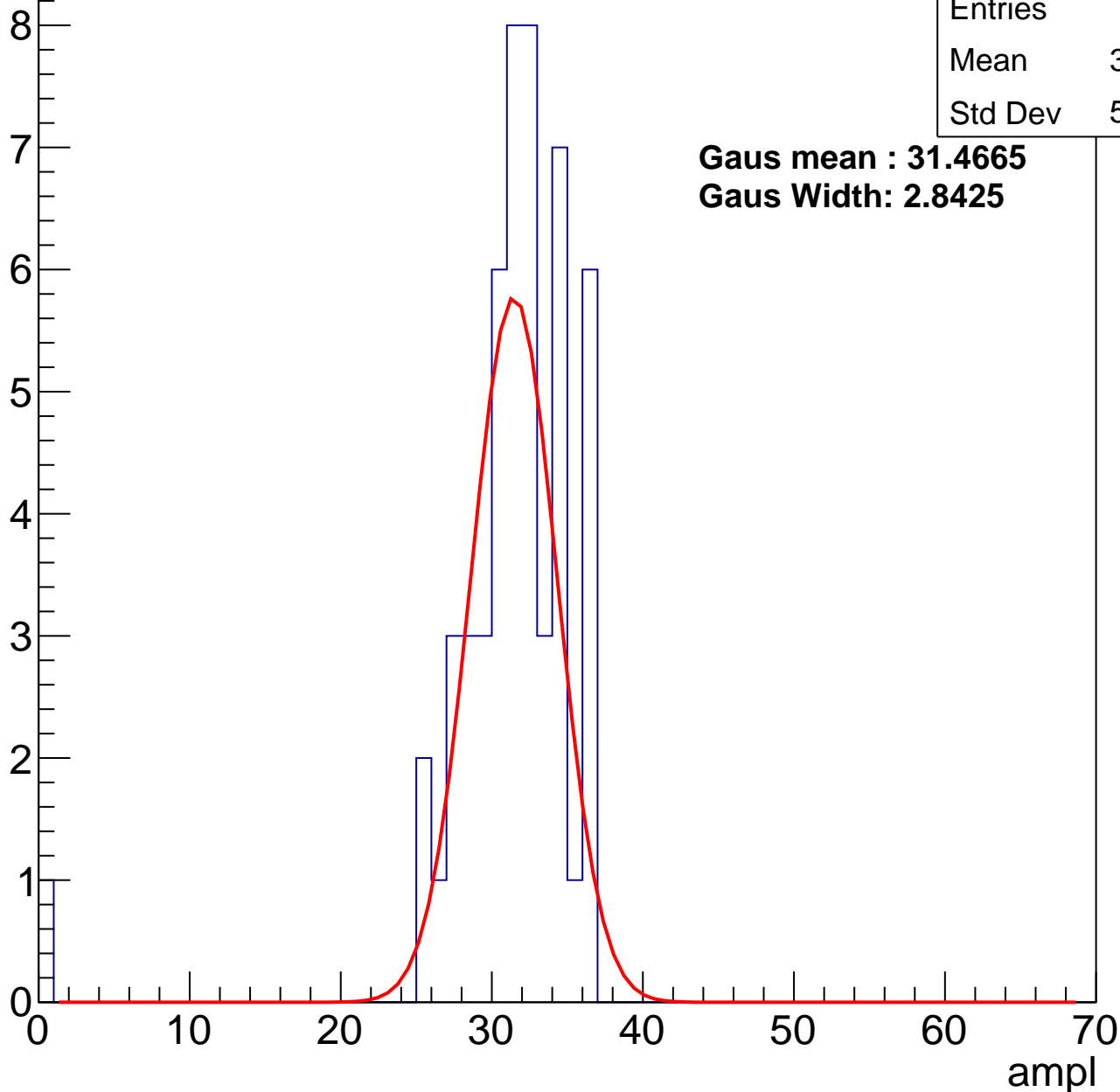
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 30.77 |
| Std Dev | 5.184 |

**Gaus mean : 31.4665**

**Gaus Width: 2.8425**



# B0L002S, U1-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 38.47 |
| Std Dev | 3.58  |

**Gaus mean : 39.2081**

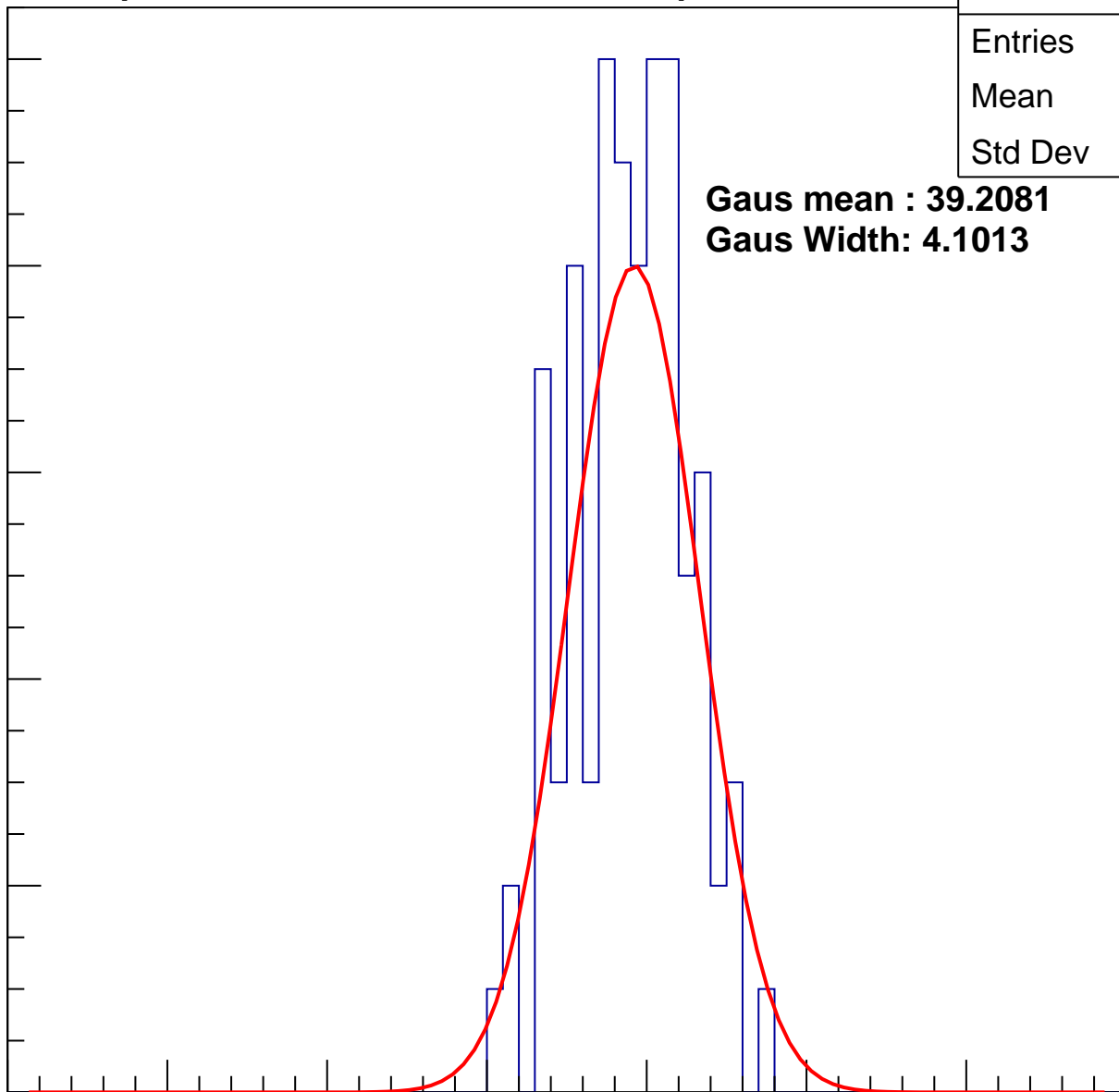
**Gaus Width: 4.1013**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch89, adc2

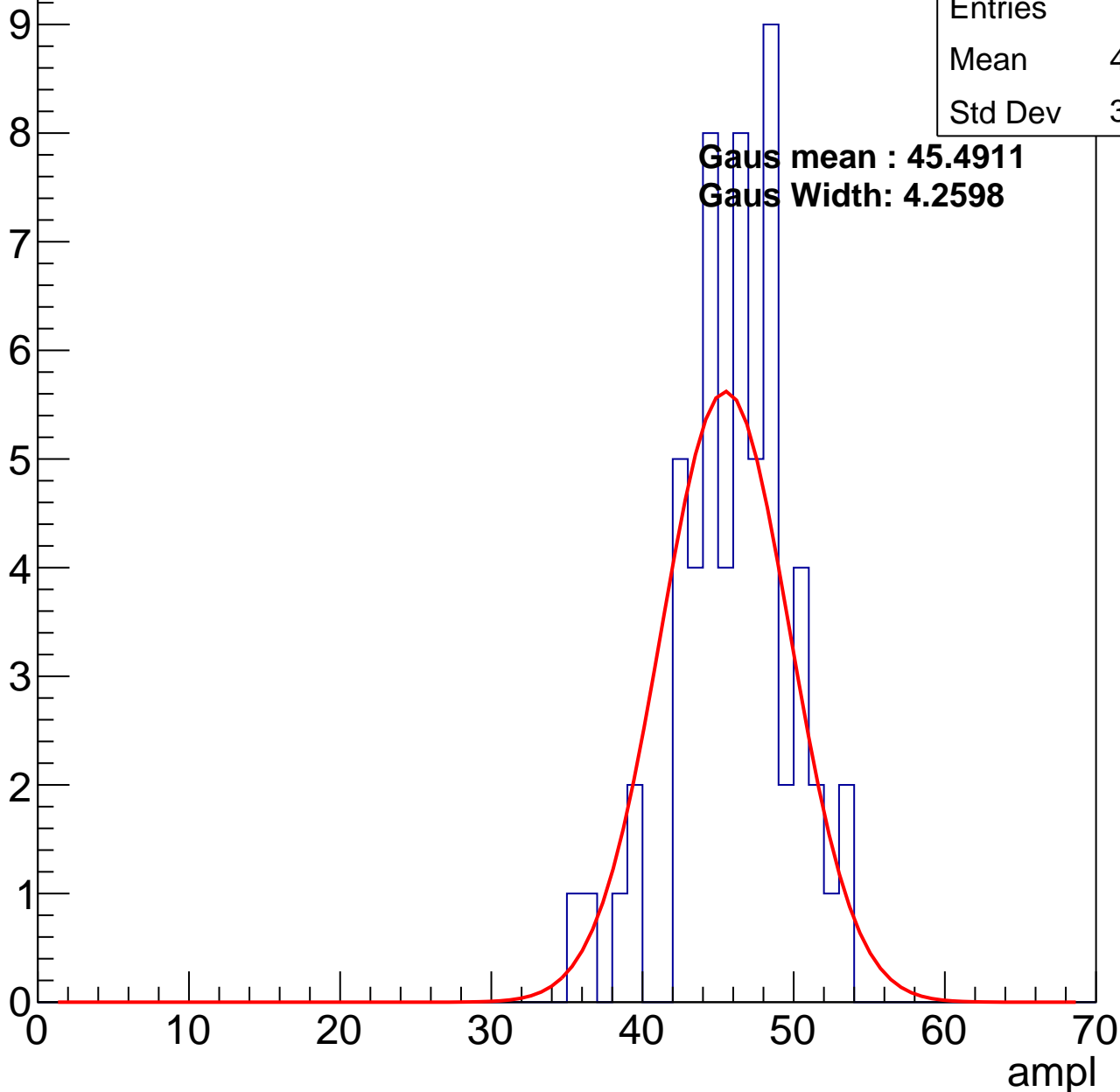
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 45.66 |
| Std Dev | 3.762 |

**Gaus mean : 45.4911**

**Gaus Width: 4.2598**

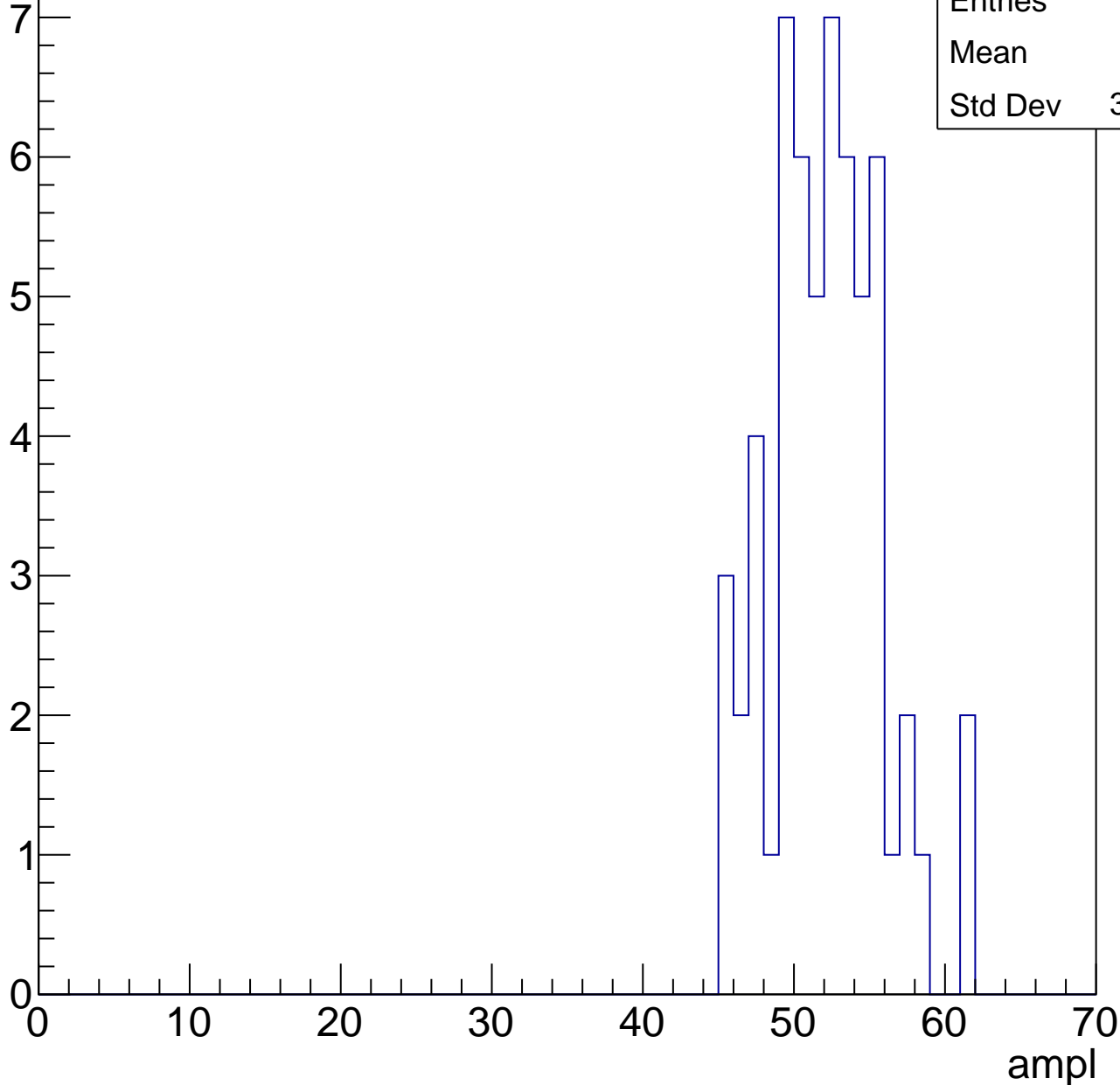


# B0L002S, U1-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

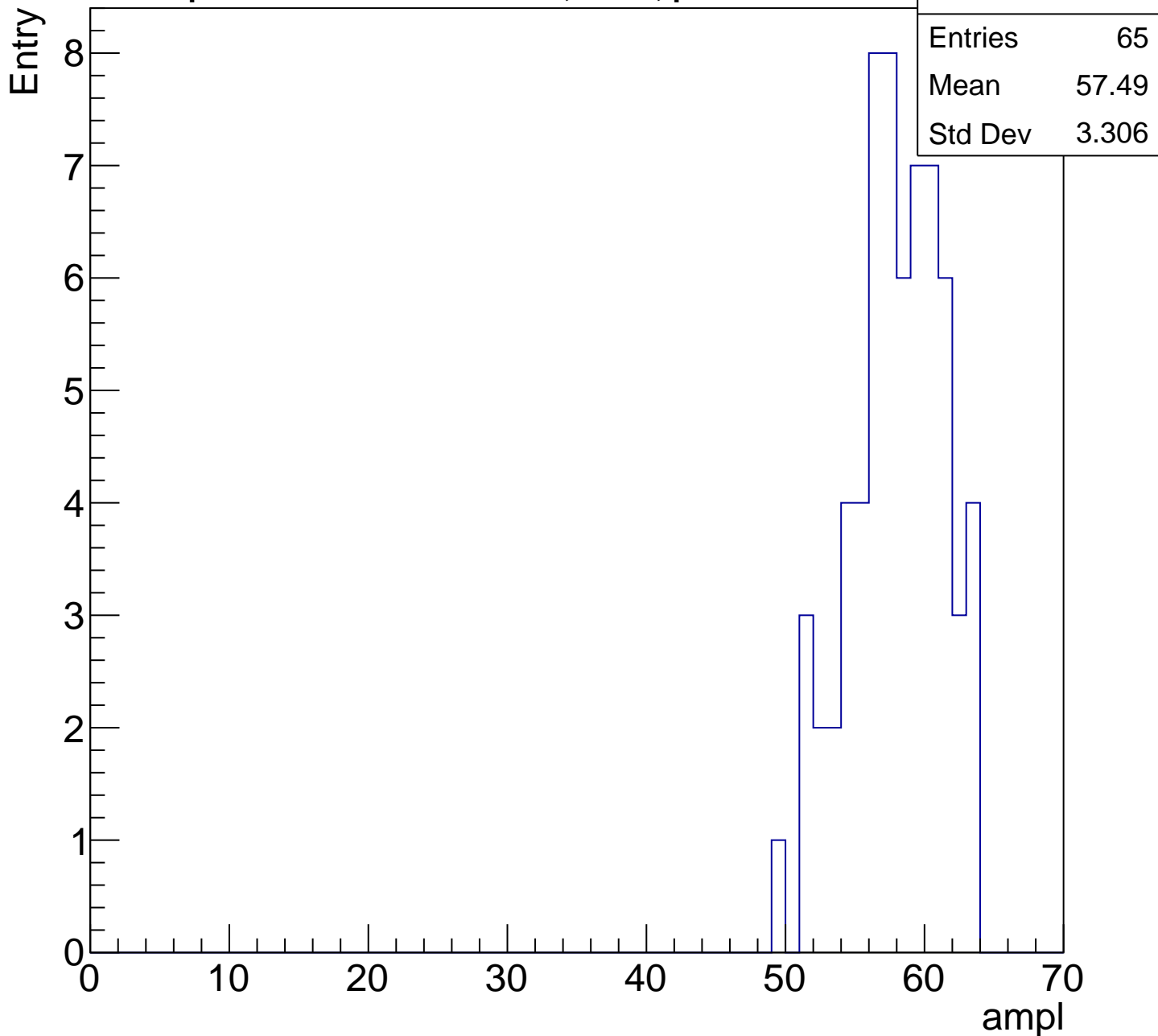
Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 51.6  |
| Std Dev | 3.624 |



# B0L002S, U1-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

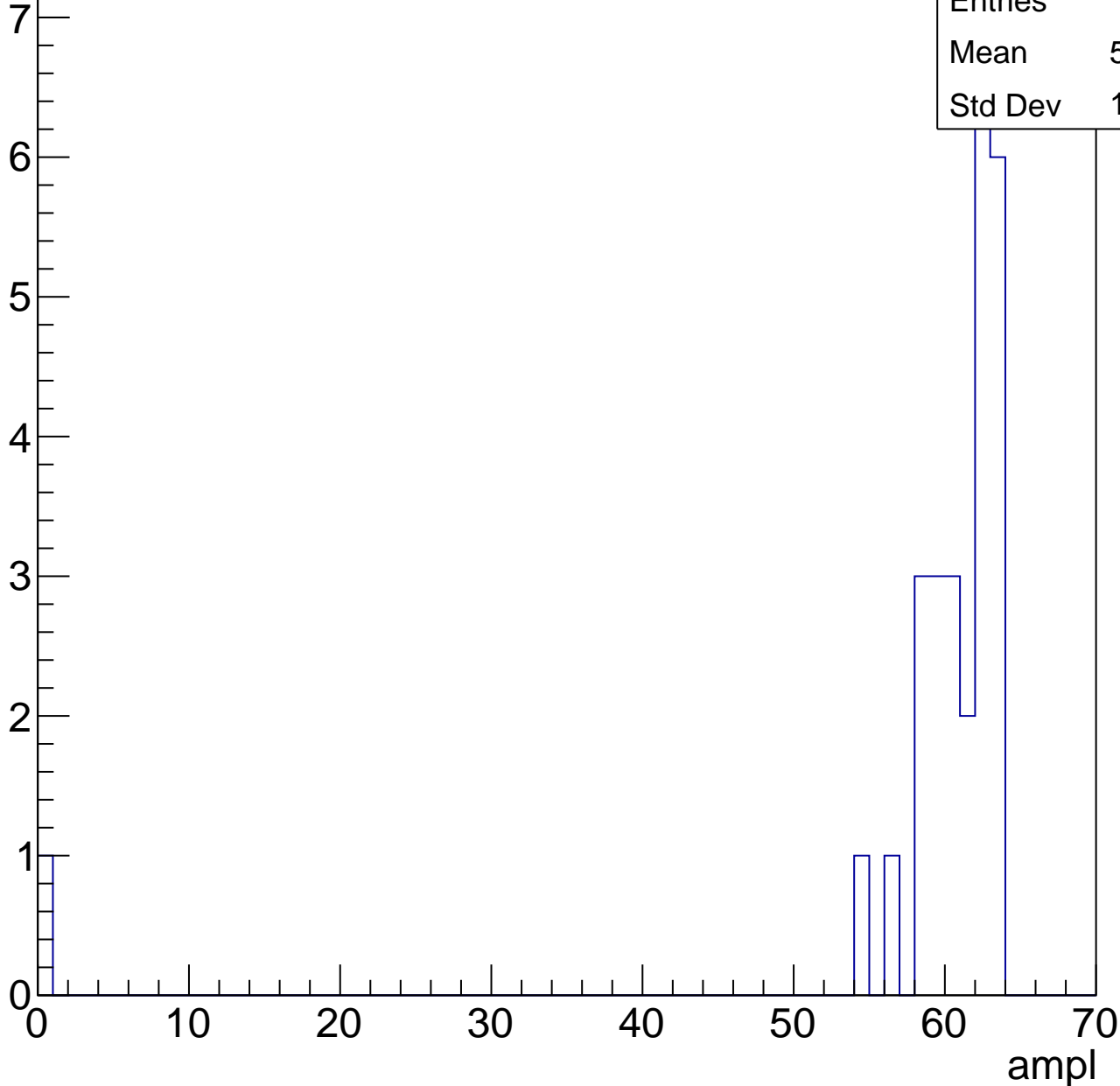


# B0L002S, U1-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 27    |
| Mean    | 58.33 |
| Std Dev | 11.67 |



# B0L002S, U1-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |



# B0L002S, U1-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch90, adc0

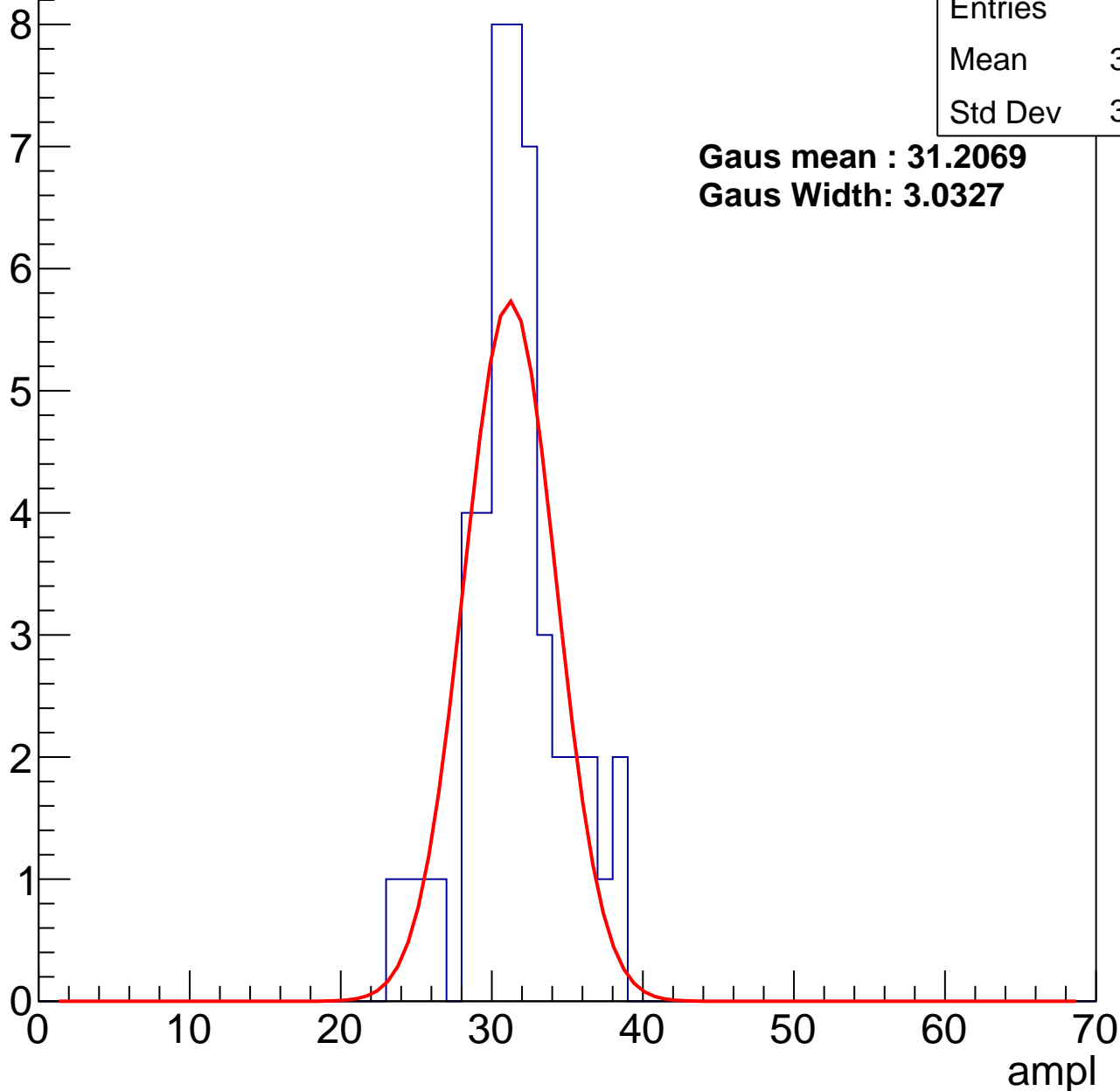
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 31.06 |
| Std Dev | 3.218 |

**Gaus mean : 31.2069**

**Gaus Width: 3.0327**



# B0L002S, U1-ch90, adc1

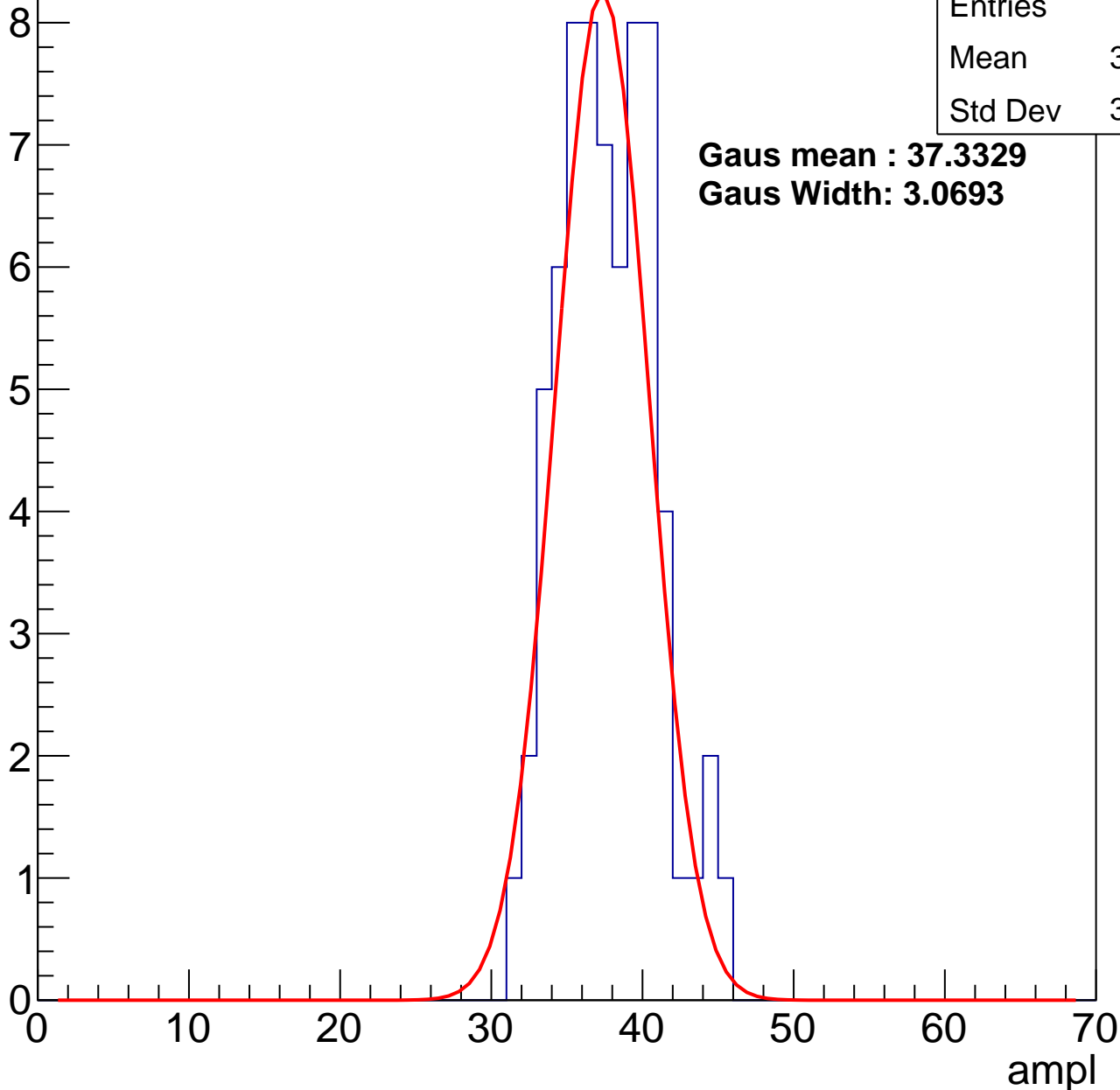
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 37.25 |
| Std Dev | 3.098 |

**Gaus mean : 37.3329**

**Gaus Width: 3.0693**



# B0L002S, U1-ch90, adc2

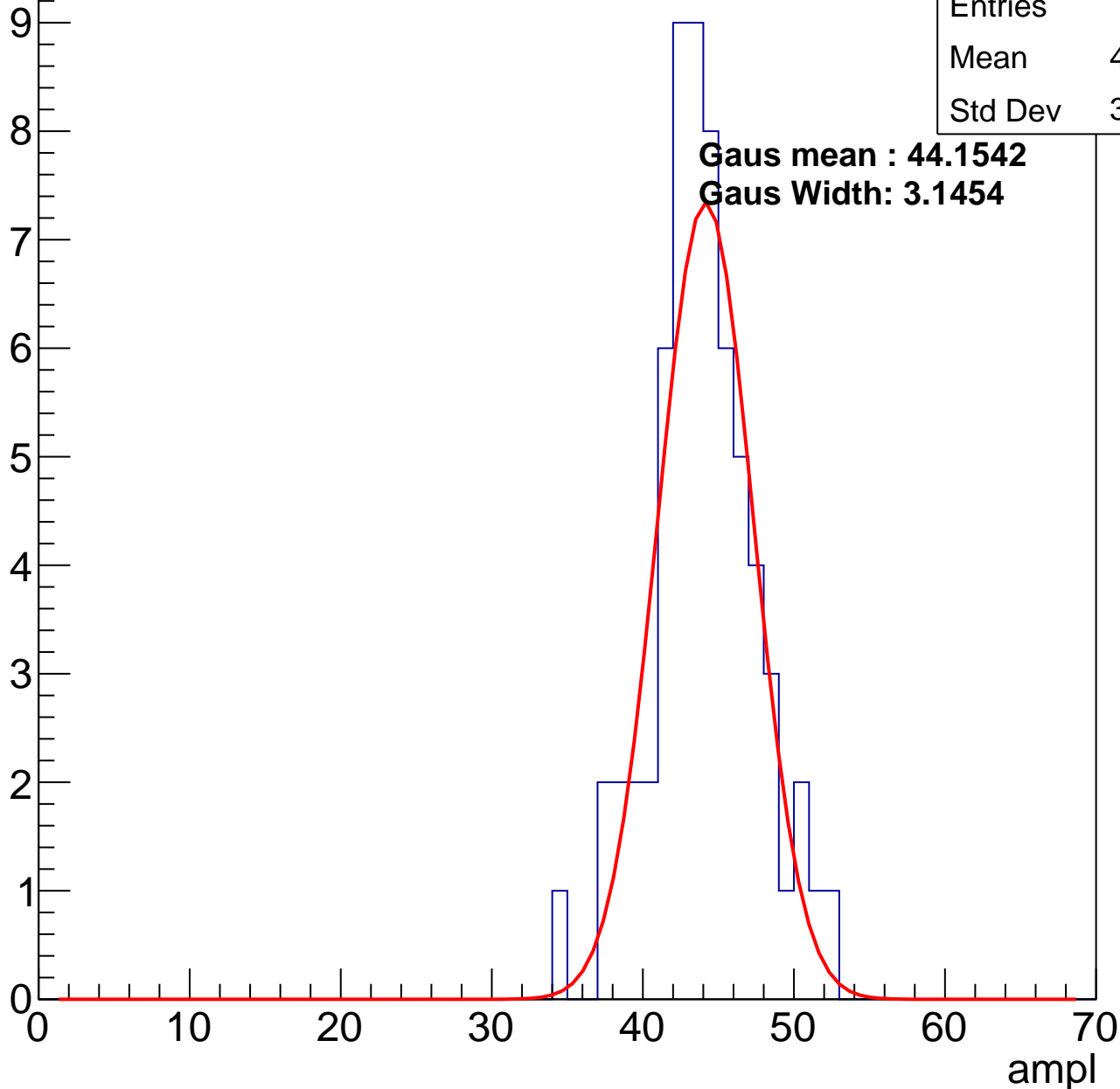
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 43.58 |
| Std Dev | 3.445 |

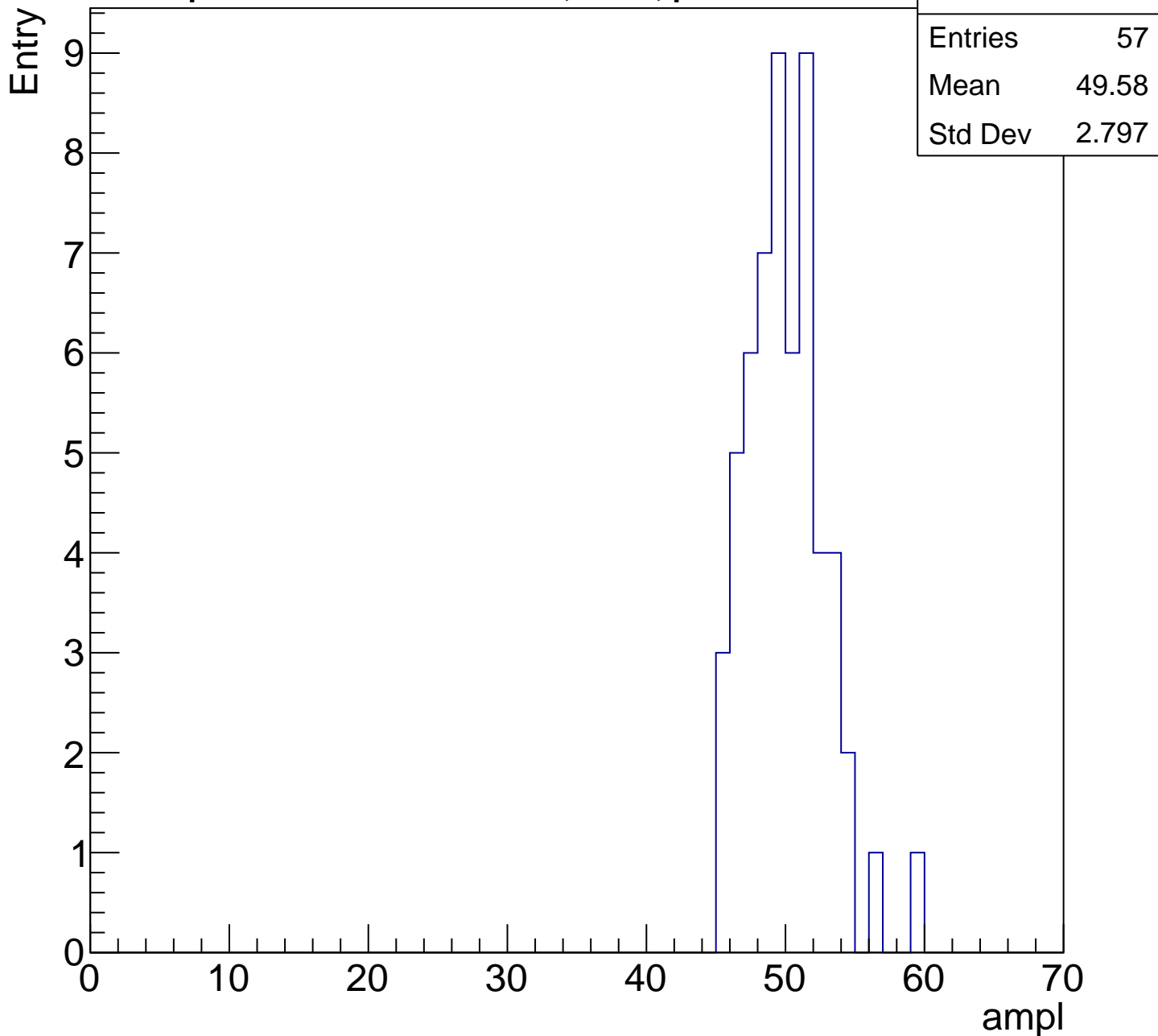
**Gaus mean : 44.1542**

**Gaus Width: 3.1454**



# B0L002S, U1-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

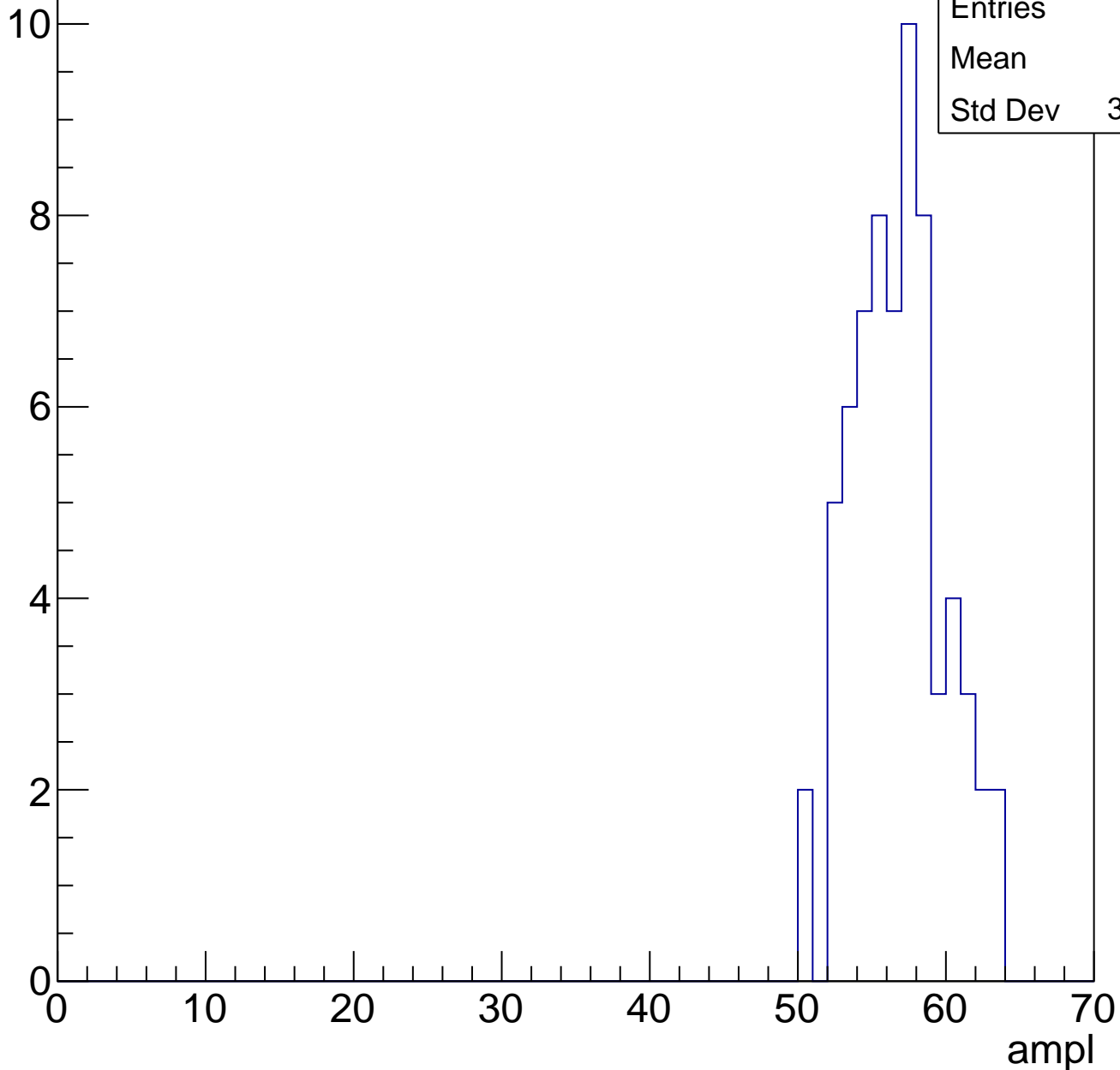


# B0L002S, U1-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 56.3  |
| Std Dev | 3.027 |

Entry

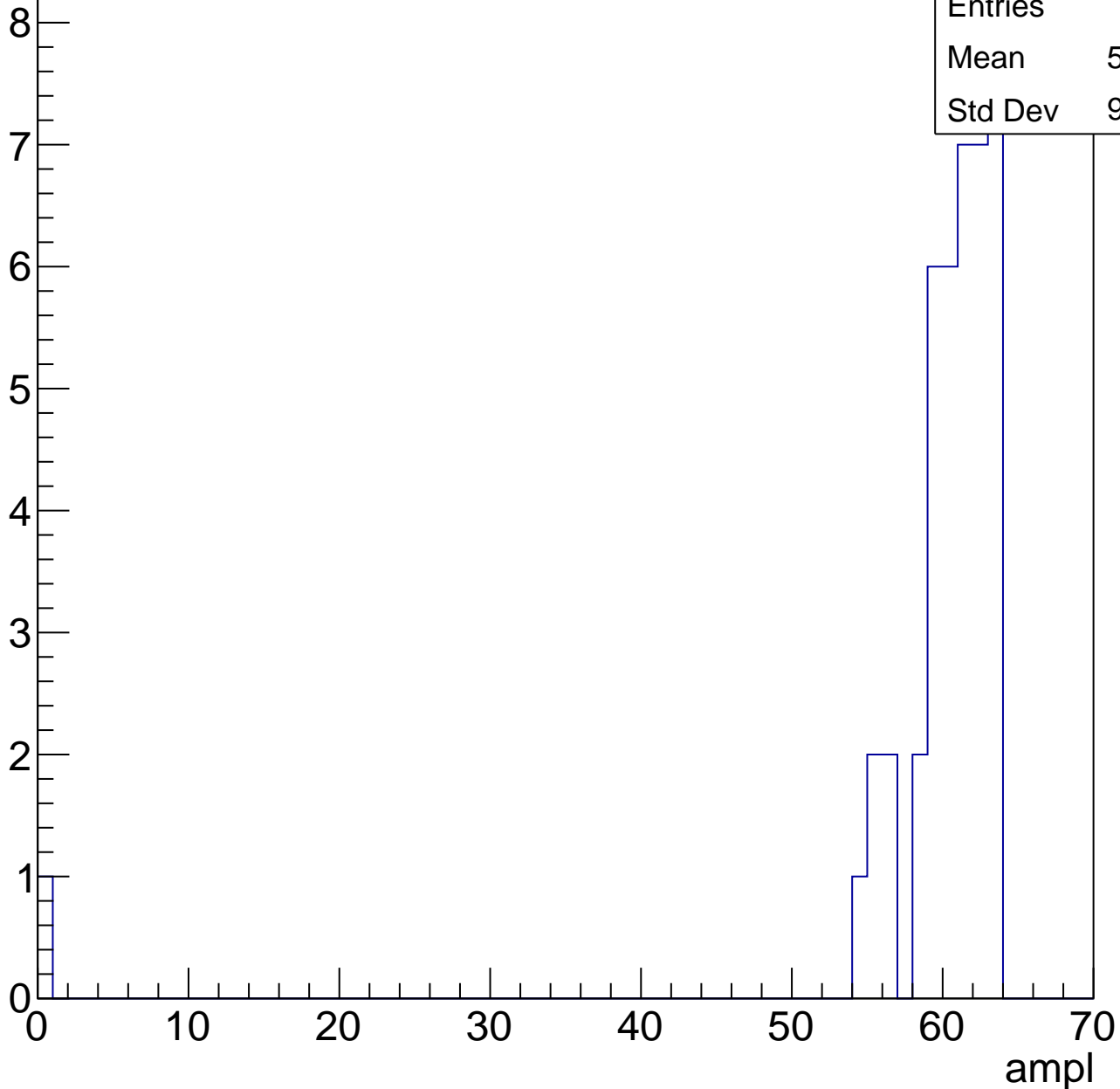


# B0L002S, U1-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 58.83 |
| Std Dev | 9.489 |



# B0L002S, U1-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch91, adc0

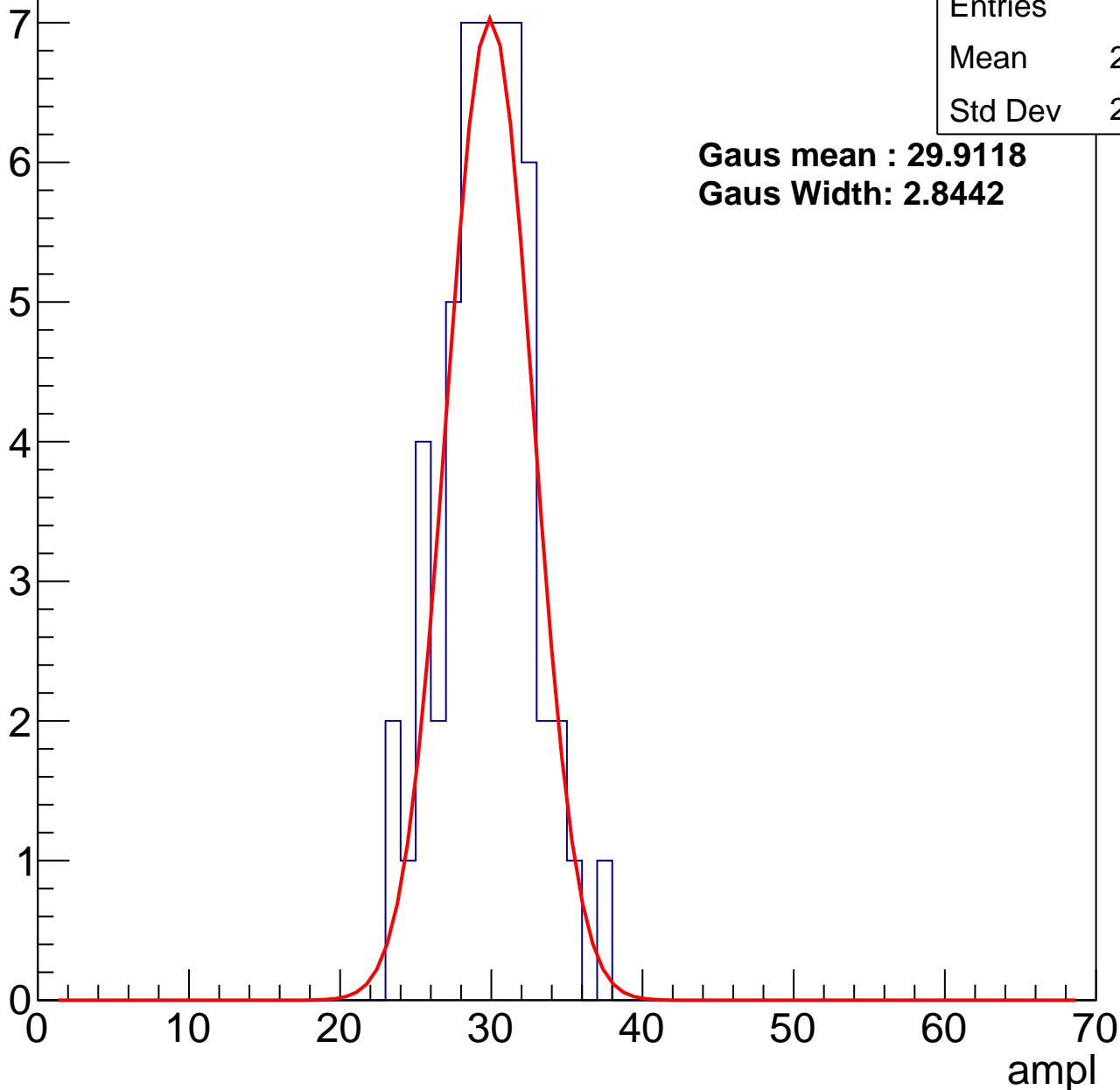
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 29.28 |
| Std Dev | 2.959 |

**Gaus mean : 29.9118**

**Gaus Width: 2.8442**



# B0L002S, U1-ch91, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

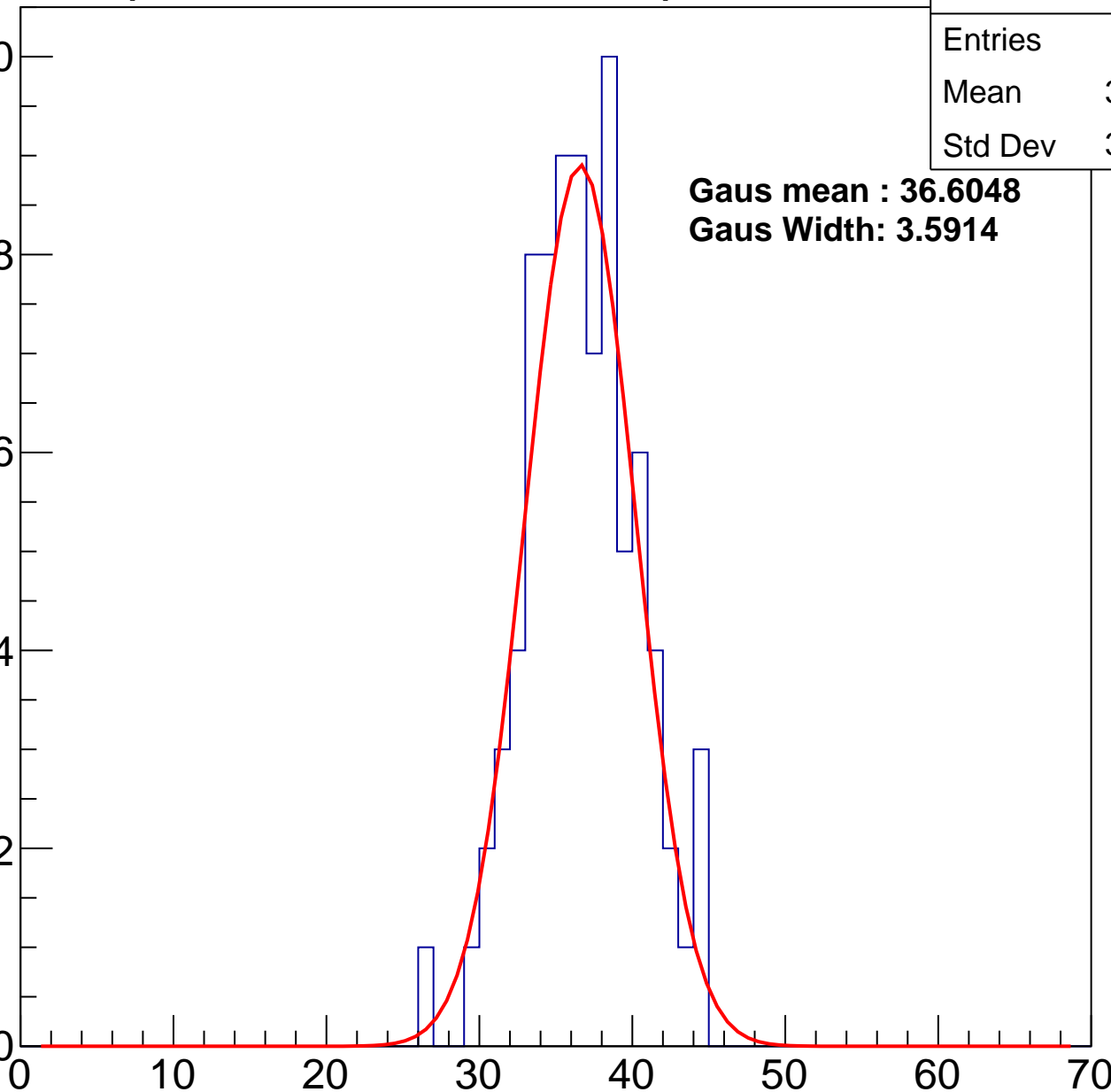
|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 36.24 |
| Std Dev | 3.589 |

**Gaus mean : 36.6048**

**Gaus Width: 3.5914**

10  
8  
6  
4  
2  
0

ampl



# B0L002S, U1-ch91, adc2

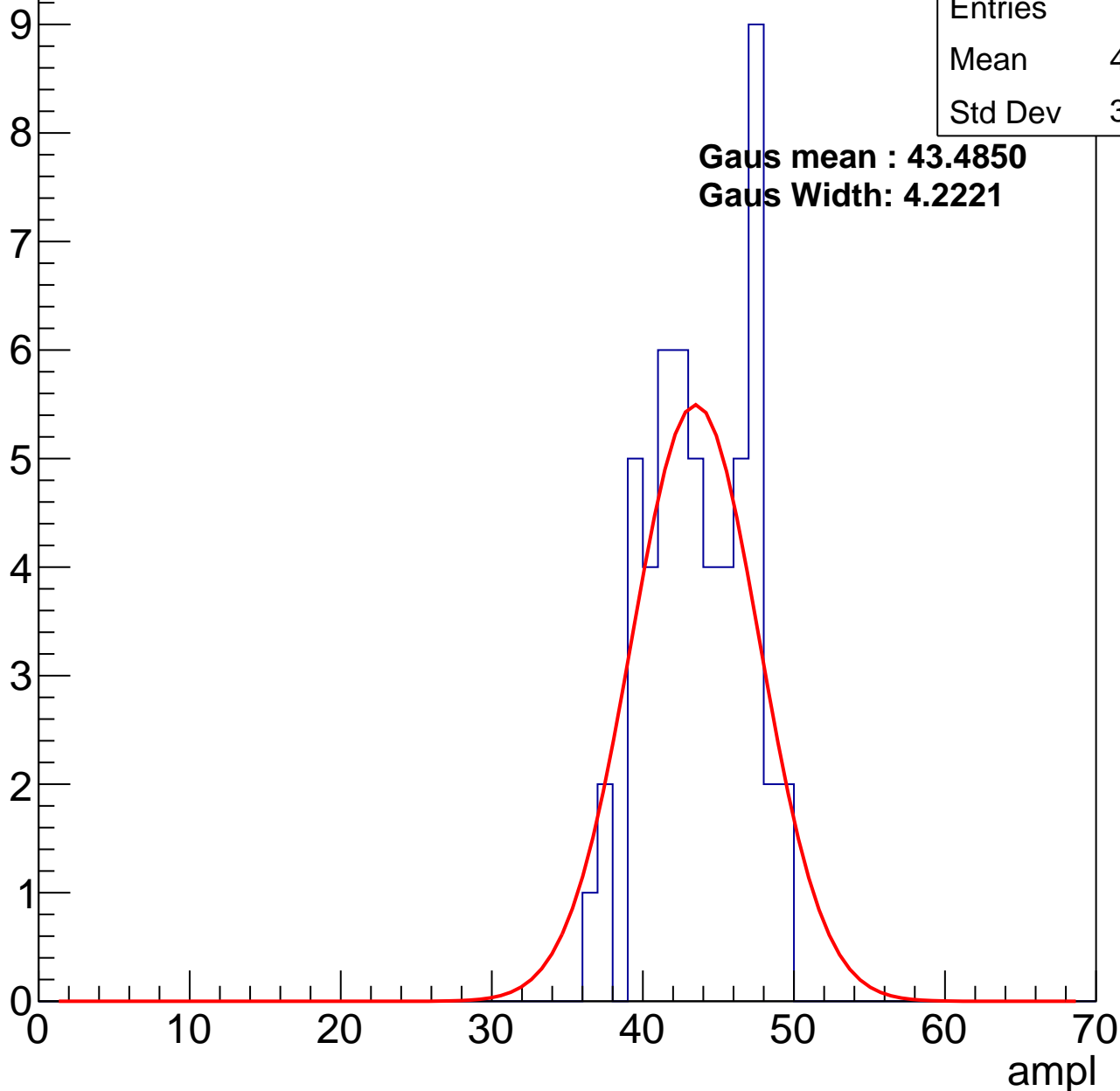
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 43.29 |
| Std Dev | 3.285 |

**Gaus mean : 43.4850**

**Gaus Width: 4.2221**

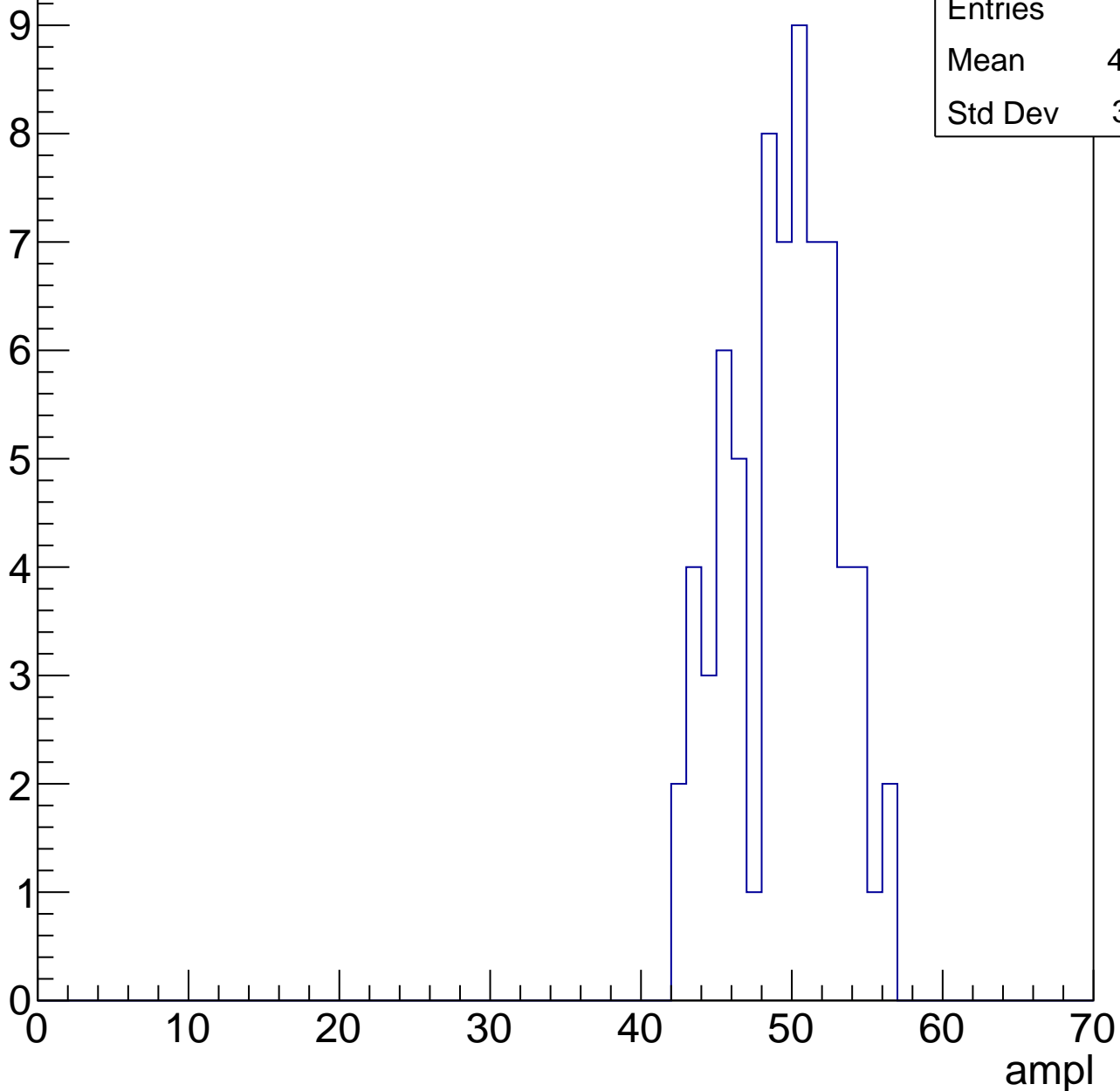


# B0L002S, U1-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 48.97 |
| Std Dev | 3.521 |



# B0L002S, U1-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

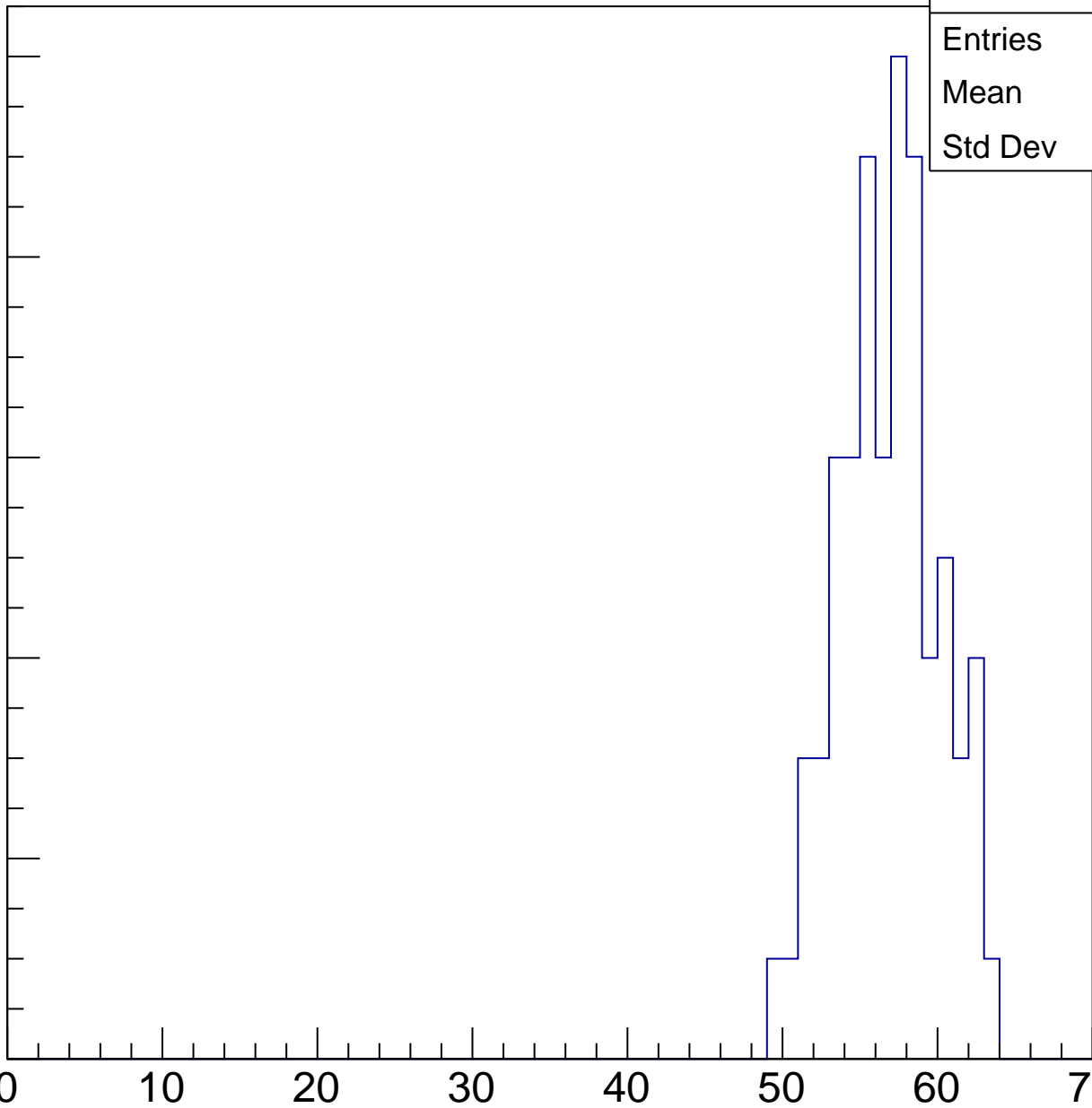
|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 56.38 |
| Std Dev | 3.173 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

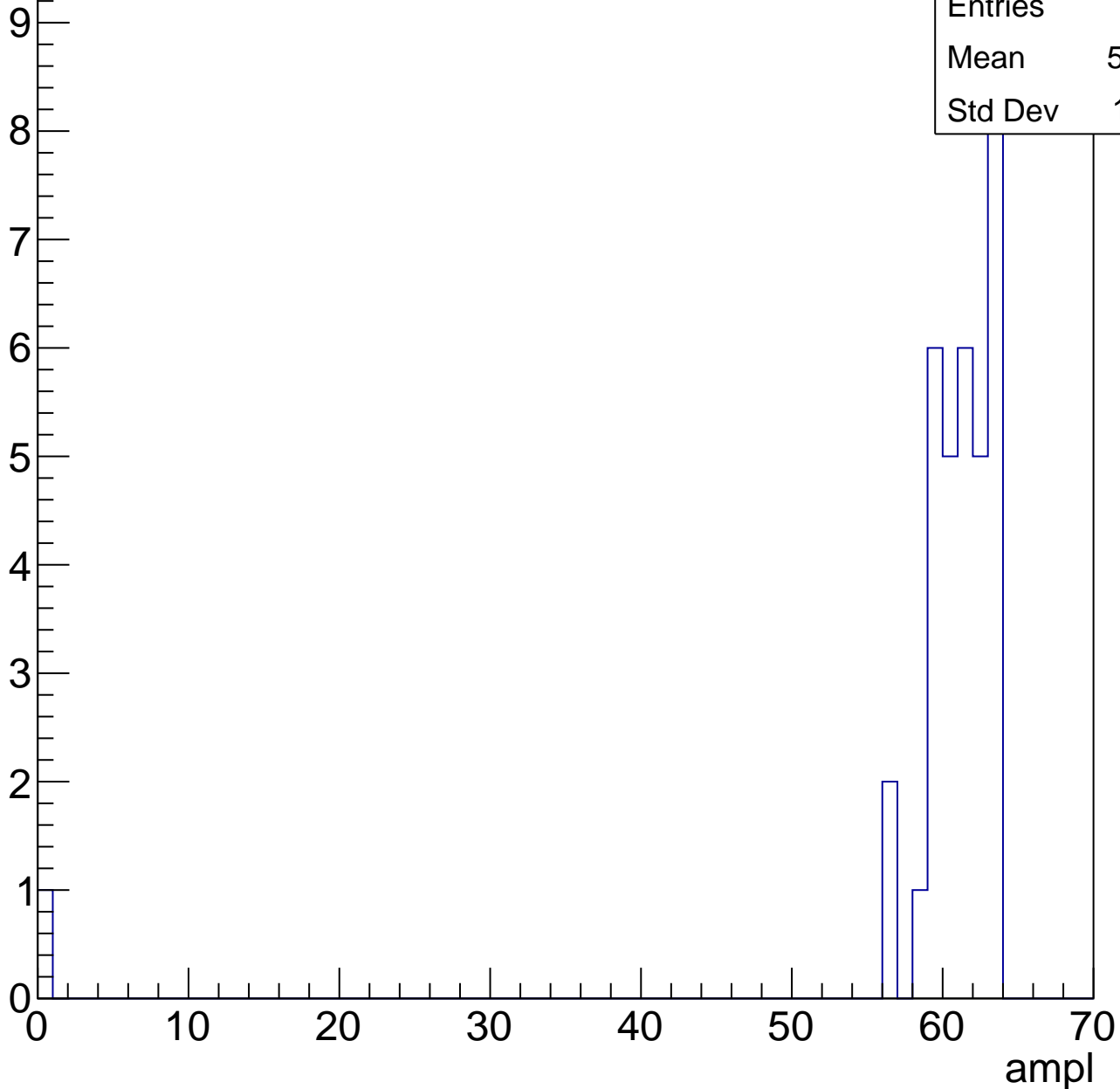


# B0L002S, U1-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 35    |
| Mean    | 59.06 |
| Std Dev | 10.31 |



# B0L002S, U1-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch92, adc0

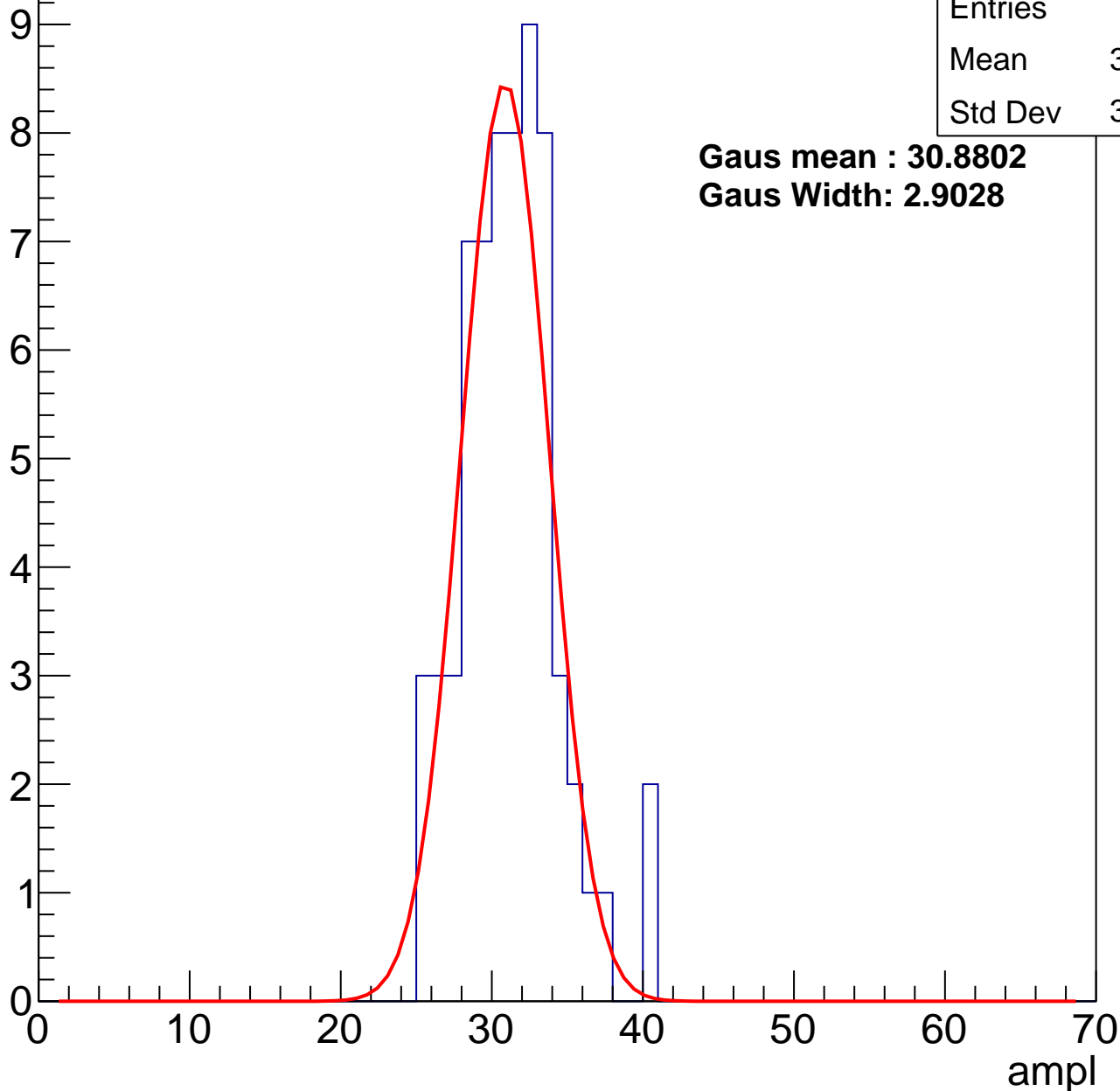
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 30.74 |
| Std Dev | 3.159 |

**Gaus mean : 30.8802**

**Gaus Width: 2.9028**



# B0L002S, U1-ch92, adc1

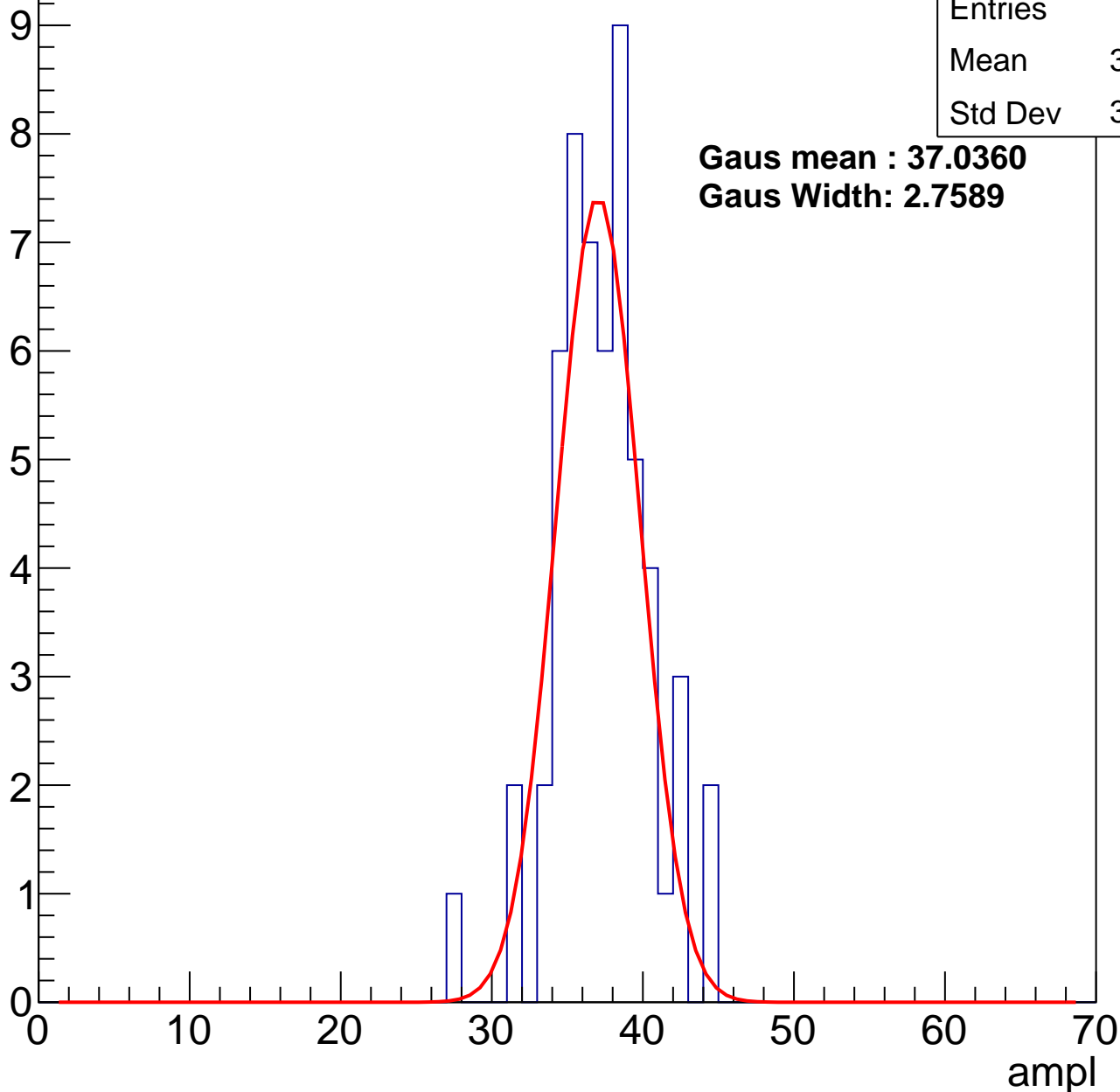
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 36.88 |
| Std Dev | 3.134 |

**Gaus mean : 37.0360**

**Gaus Width: 2.7589**



# B0L002S, U1-ch92, adc2

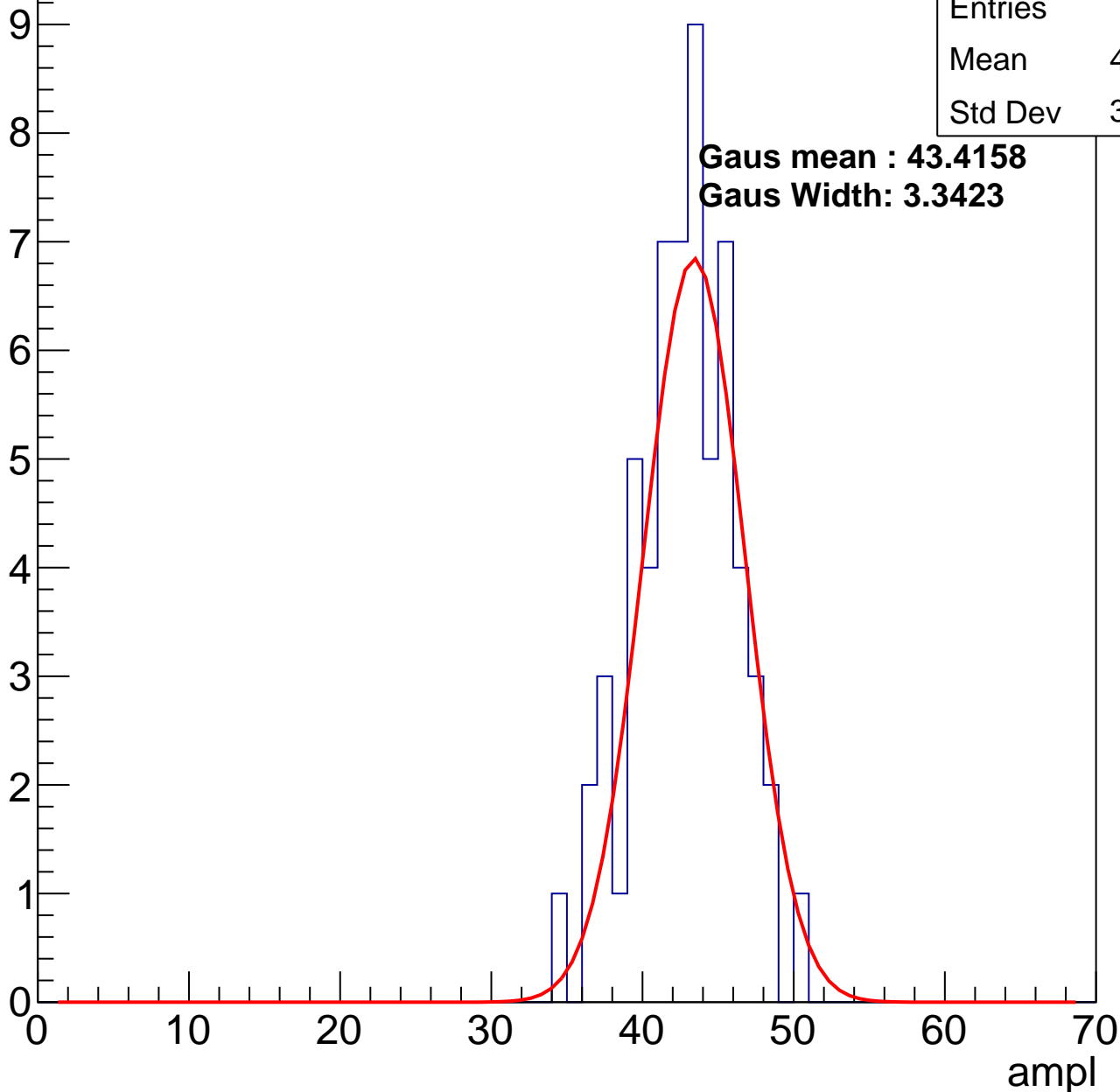
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 42.36 |
| Std Dev | 3.275 |

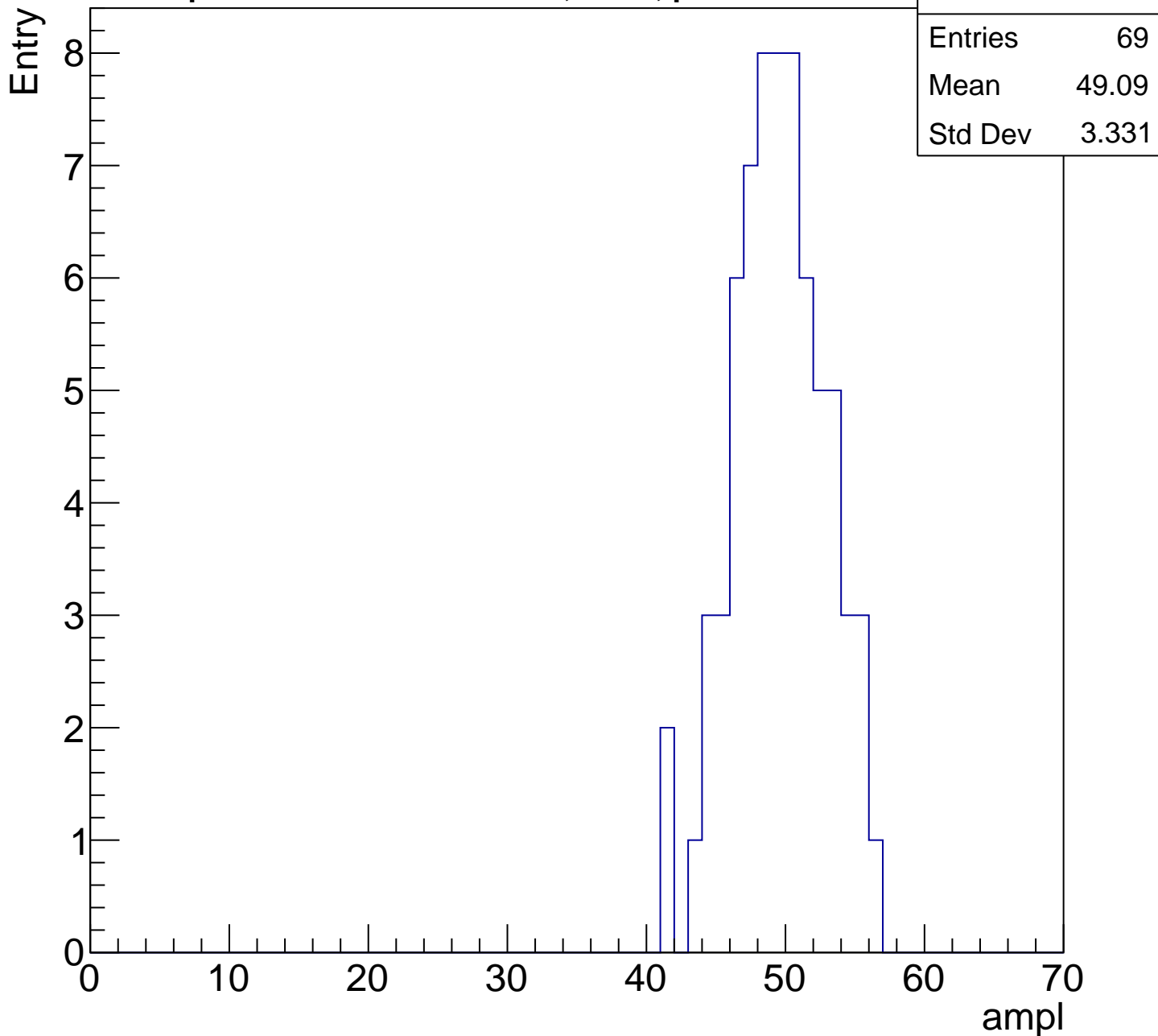
**Gaus mean : 43.4158**

**Gaus Width: 3.3423**



# B0L002S, U1-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

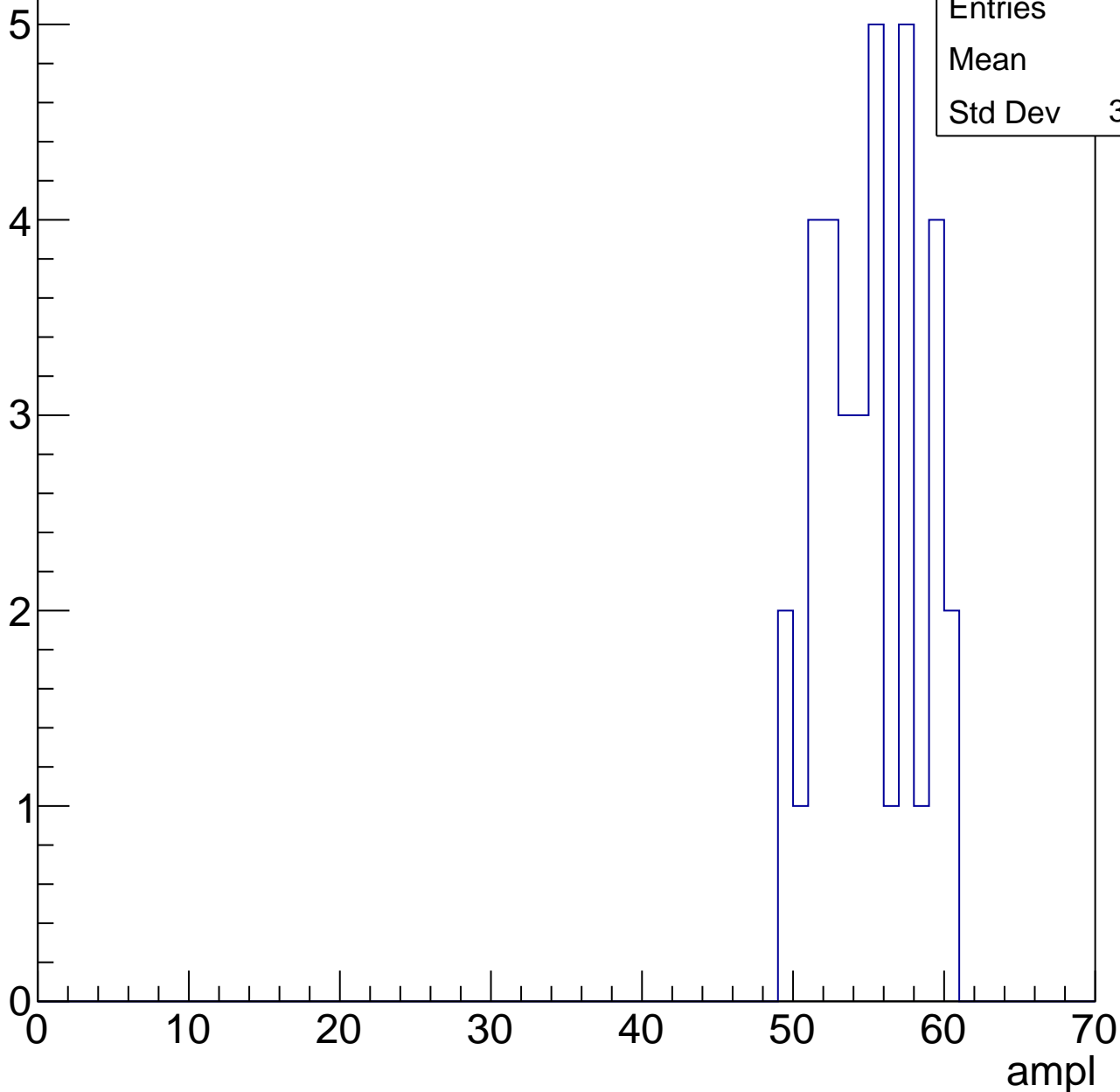


# B0L002S, U1-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

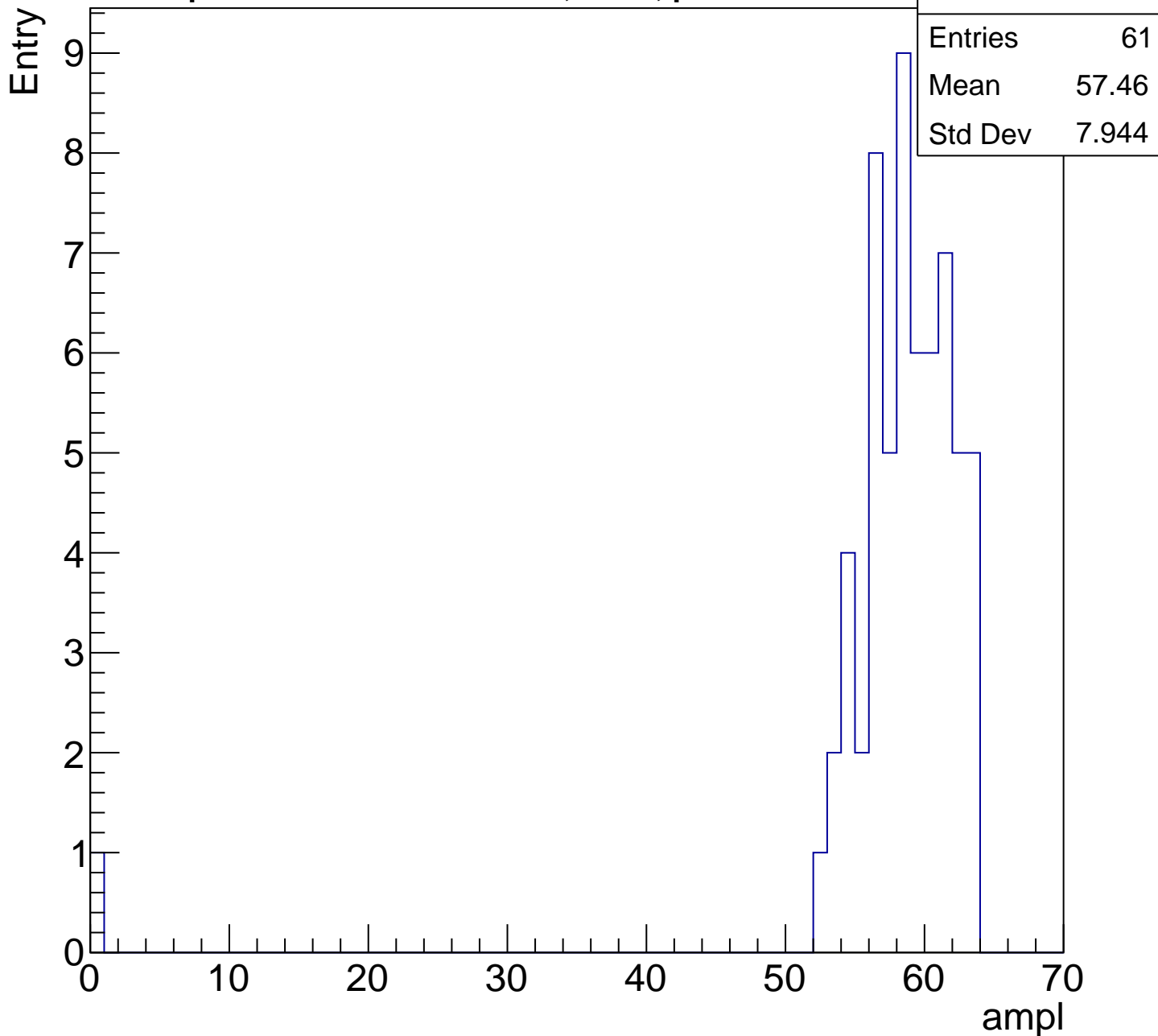
Entry

|         |       |
|---------|-------|
| Entries | 35    |
| Mean    | 54.6  |
| Std Dev | 3.164 |



# B0L002S, U1-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

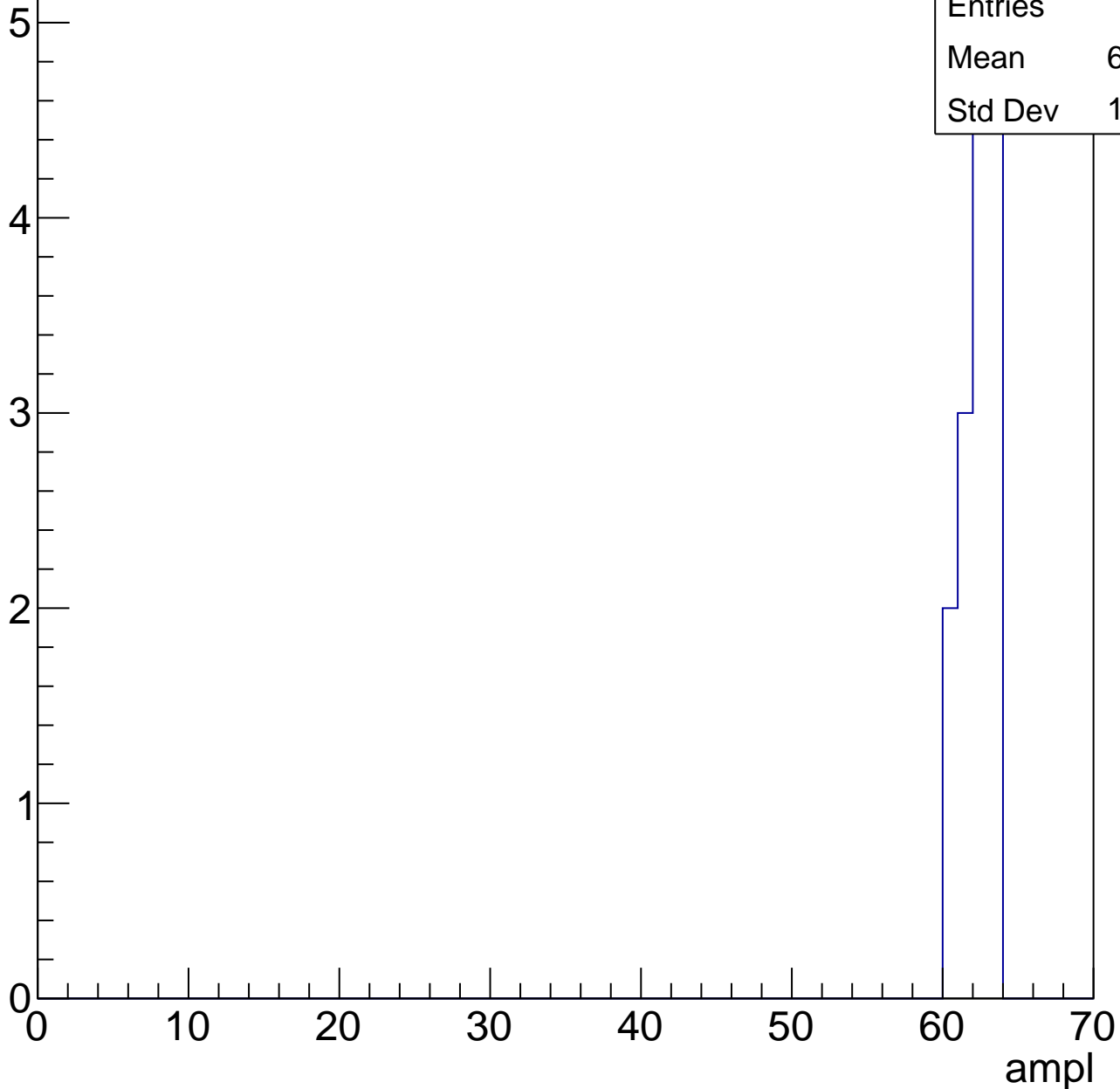


# B0L002S, U1-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 15    |
| Mean    | 61.87 |
| Std Dev | 1.024 |





# B0L002S, U1-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 24 |
| Std Dev | 0  |

# B0L002S, U1-ch93, adc0

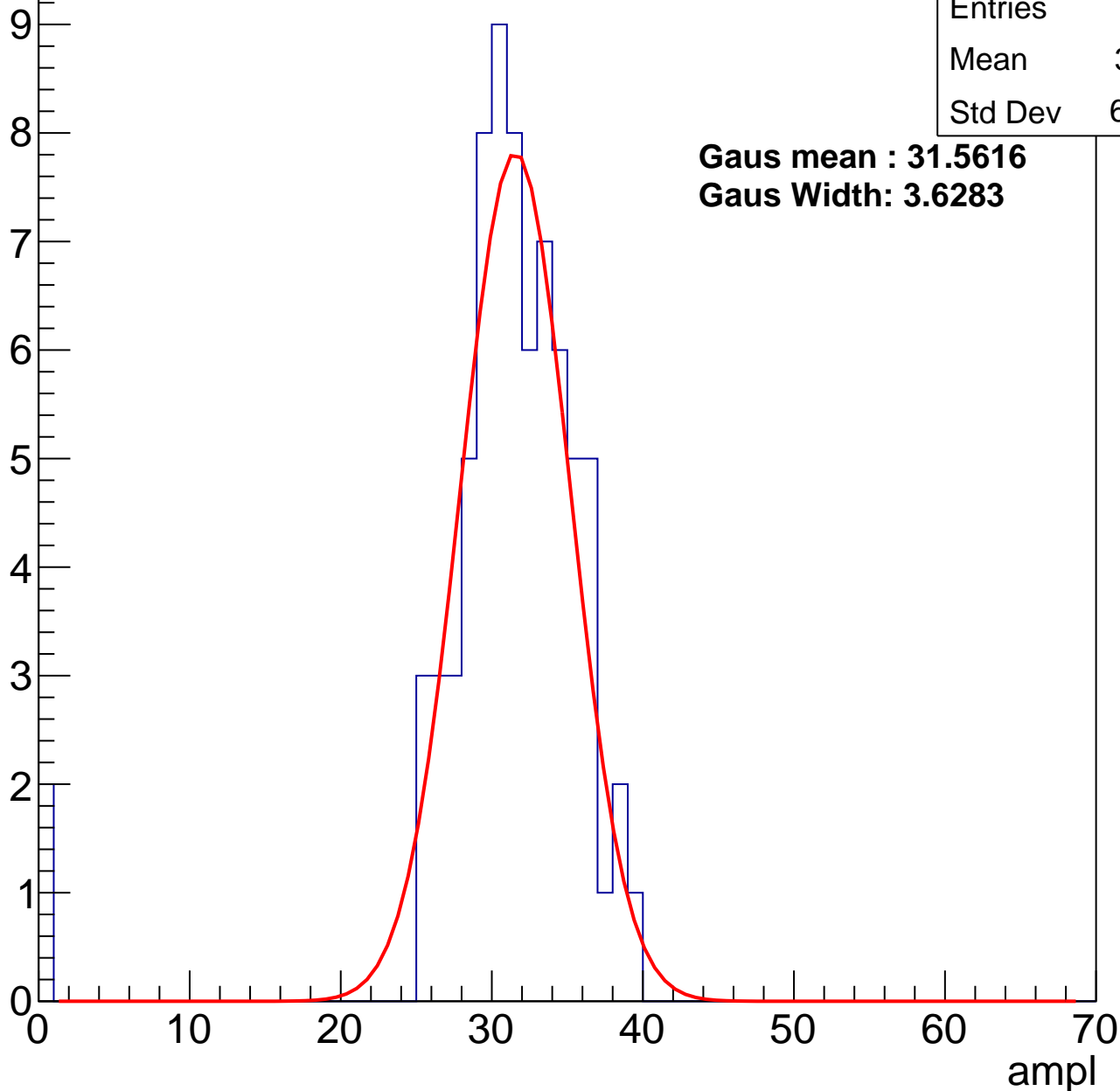
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 30.51 |
| Std Dev | 6.054 |

**Gaus mean : 31.5616**

**Gaus Width: 3.6283**



# B0L002S, U1-ch93, adc1

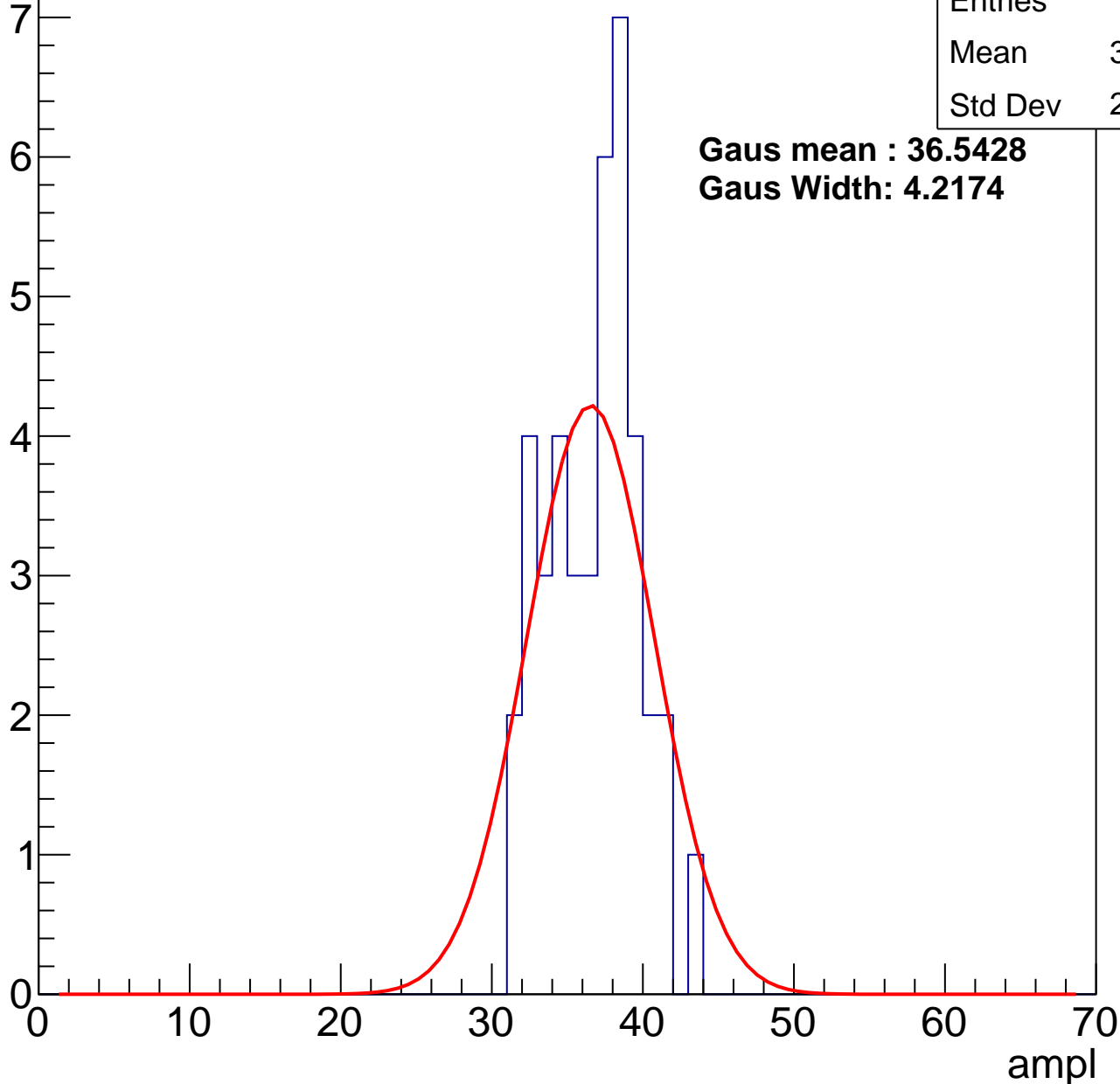
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 36.27 |
| Std Dev | 2.955 |

**Gaus mean : 36.5428**

**Gaus Width: 4.2174**



# B0L002S, U1-ch93, adc2

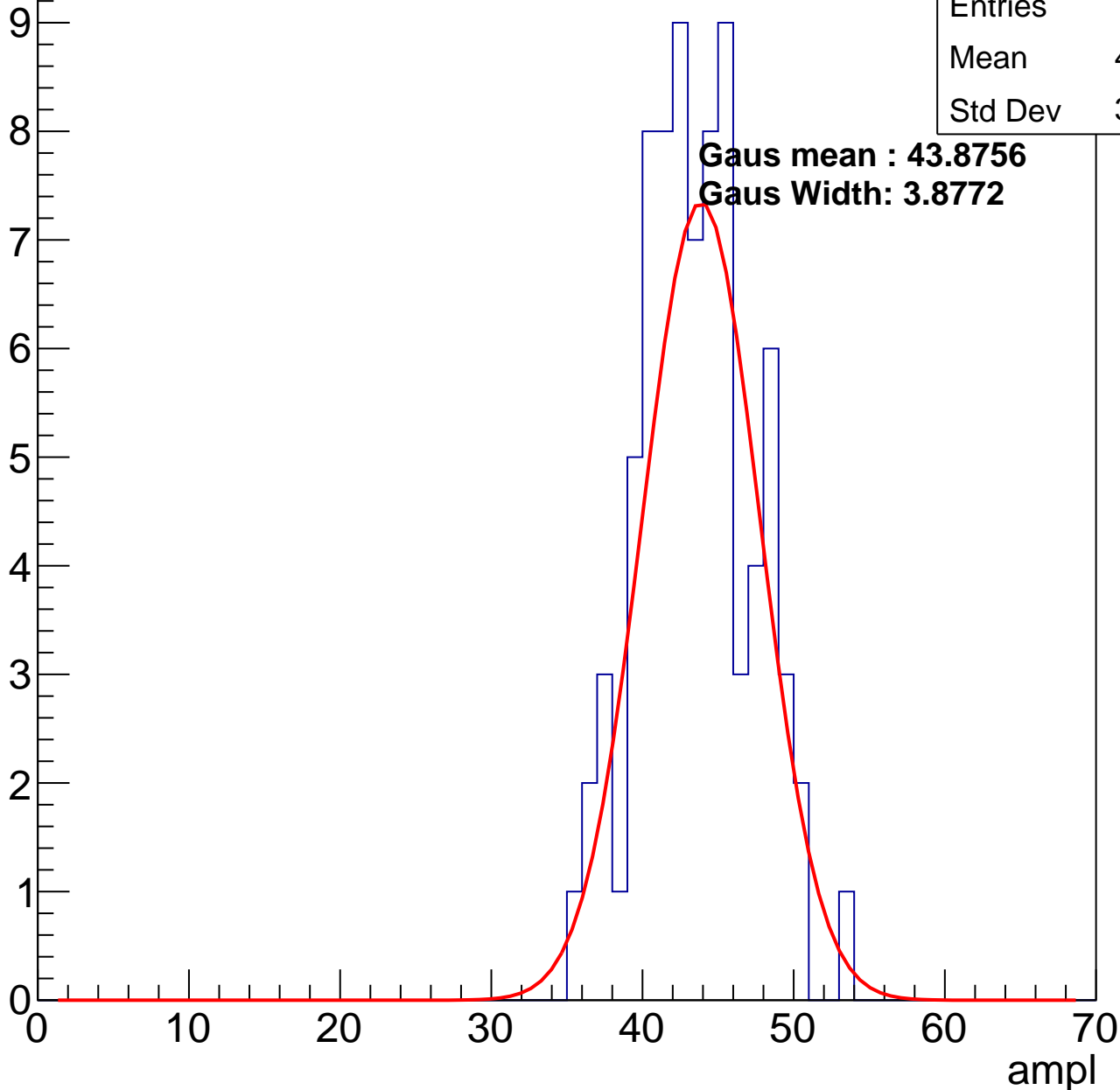
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 43.11 |
| Std Dev | 3.671 |

**Gaus mean : 43.8756**

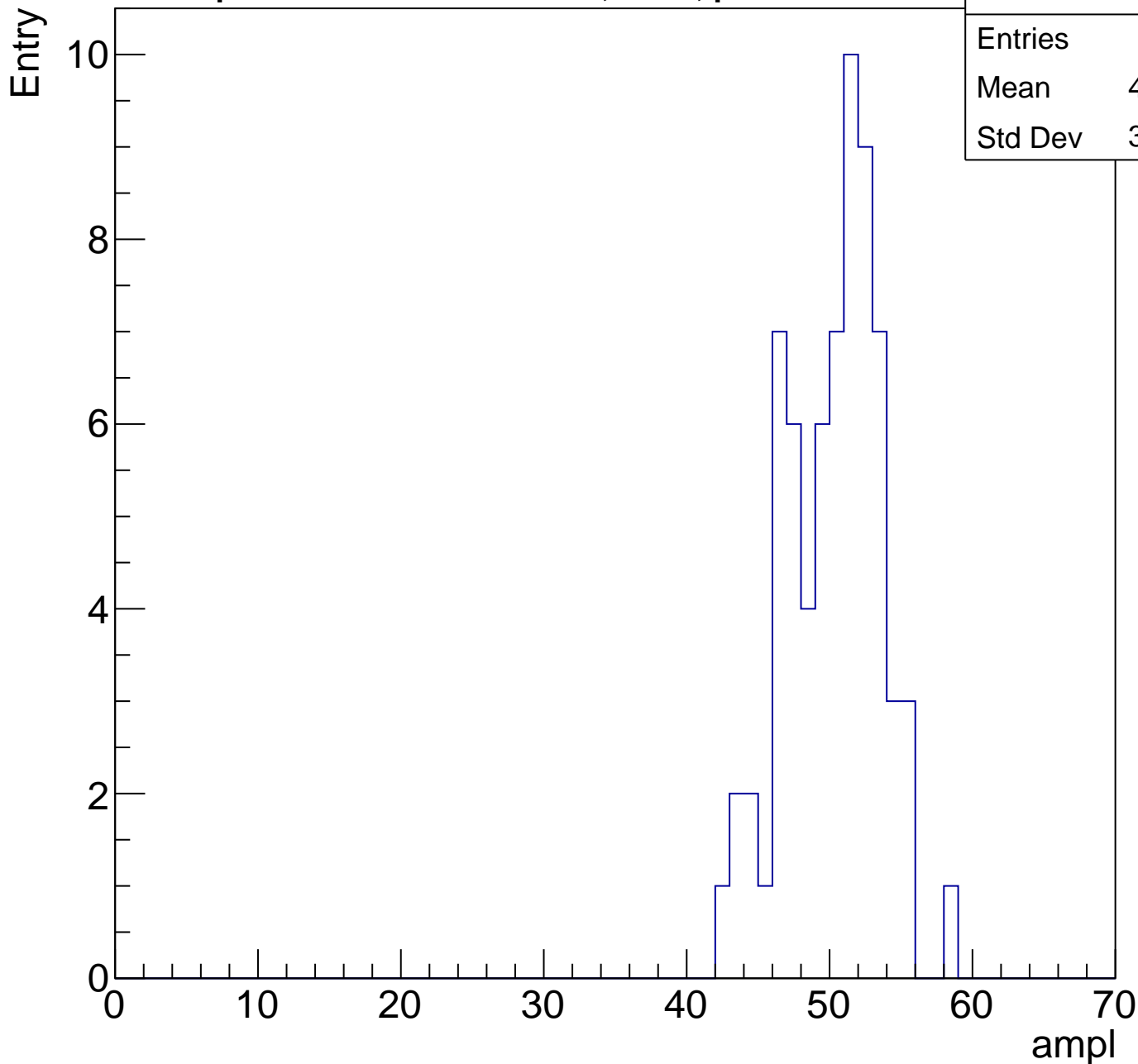
**Gaus Width: 3.8772**



# B0L002S, U1-ch93, adc3

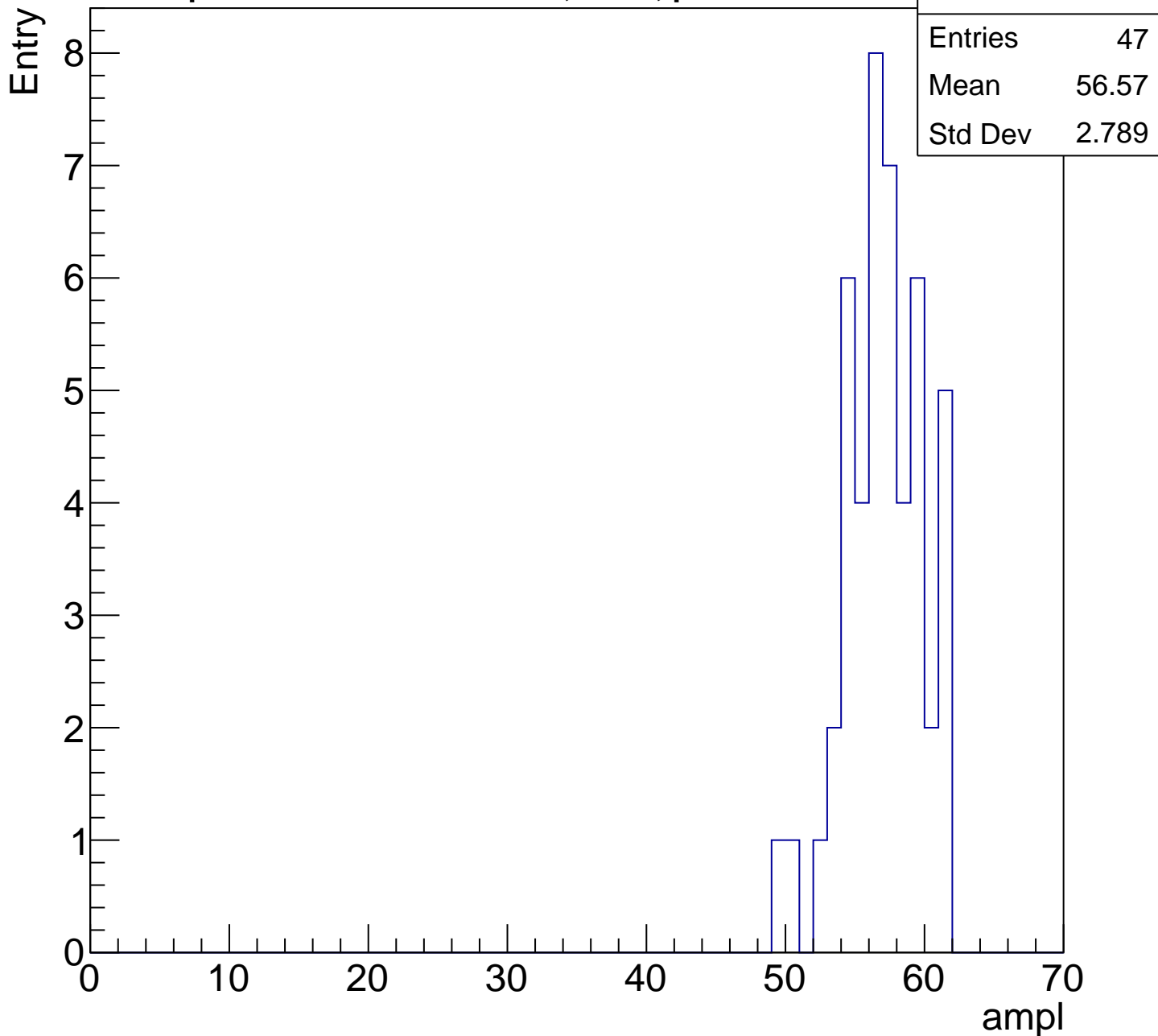
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 49.78 |
| Std Dev | 3.274 |



# B0L002S, U1-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

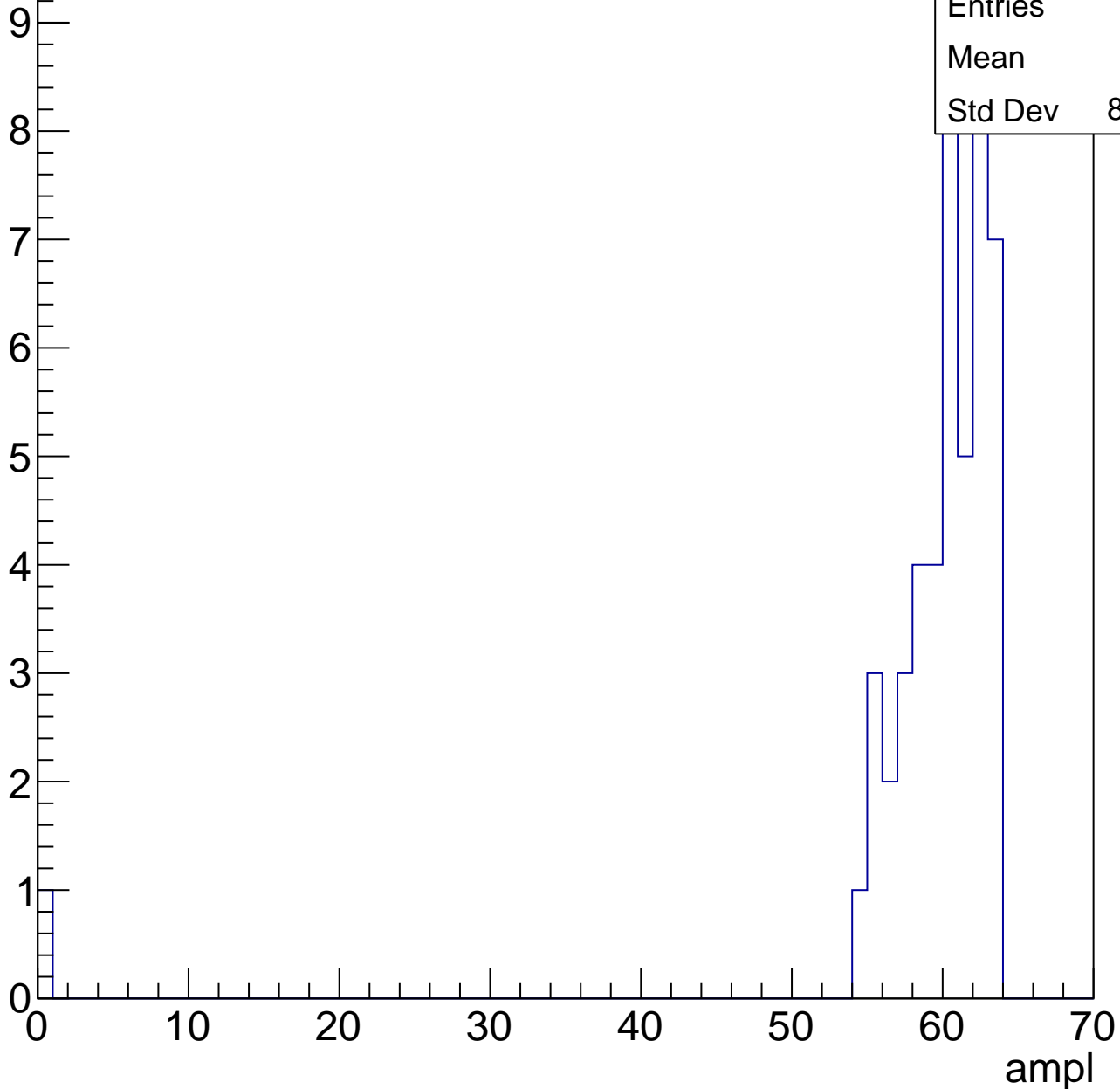


# B0L002S, U1-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

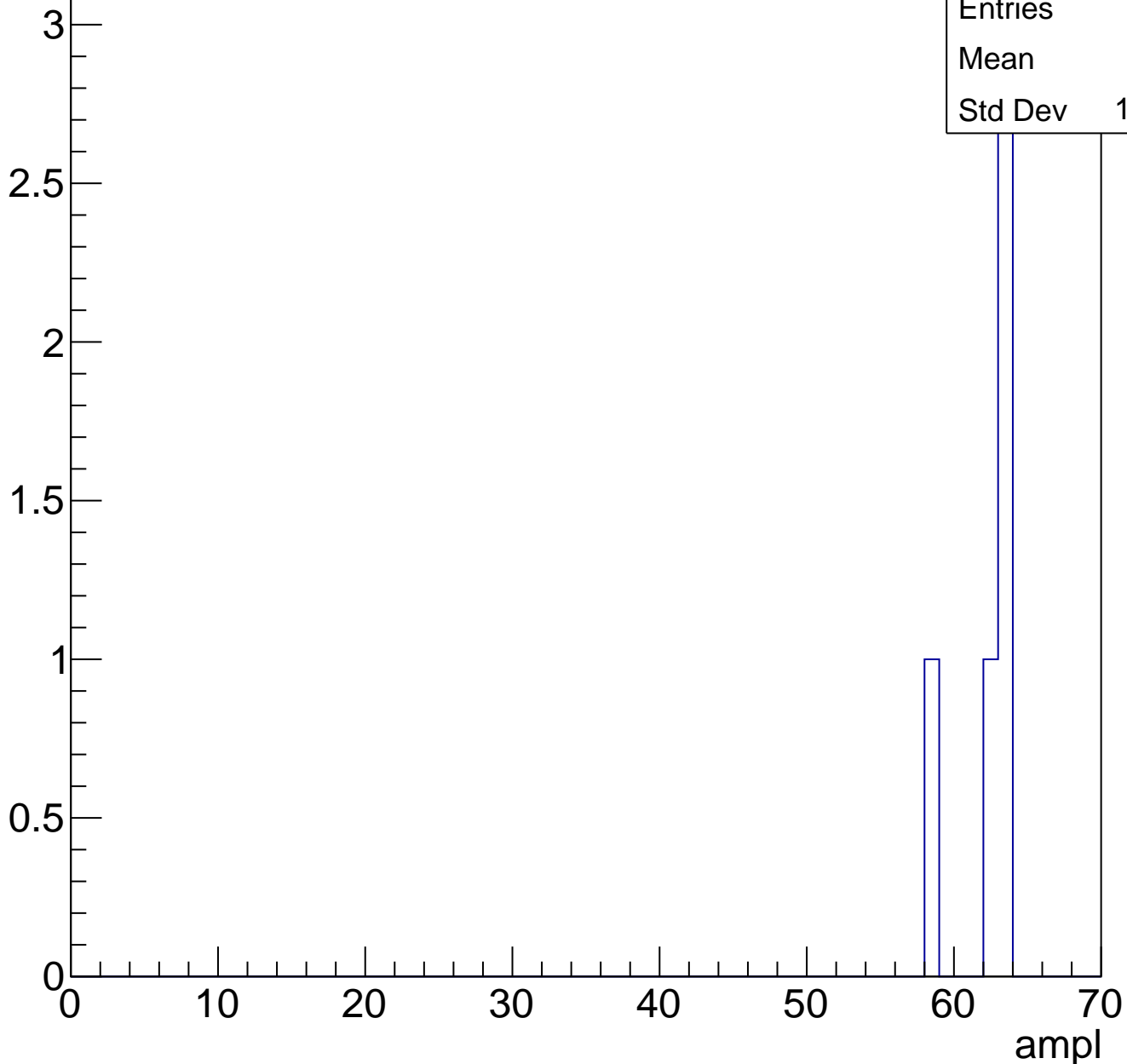
|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 58.6  |
| Std Dev | 8.993 |



# B0L002S, U1-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

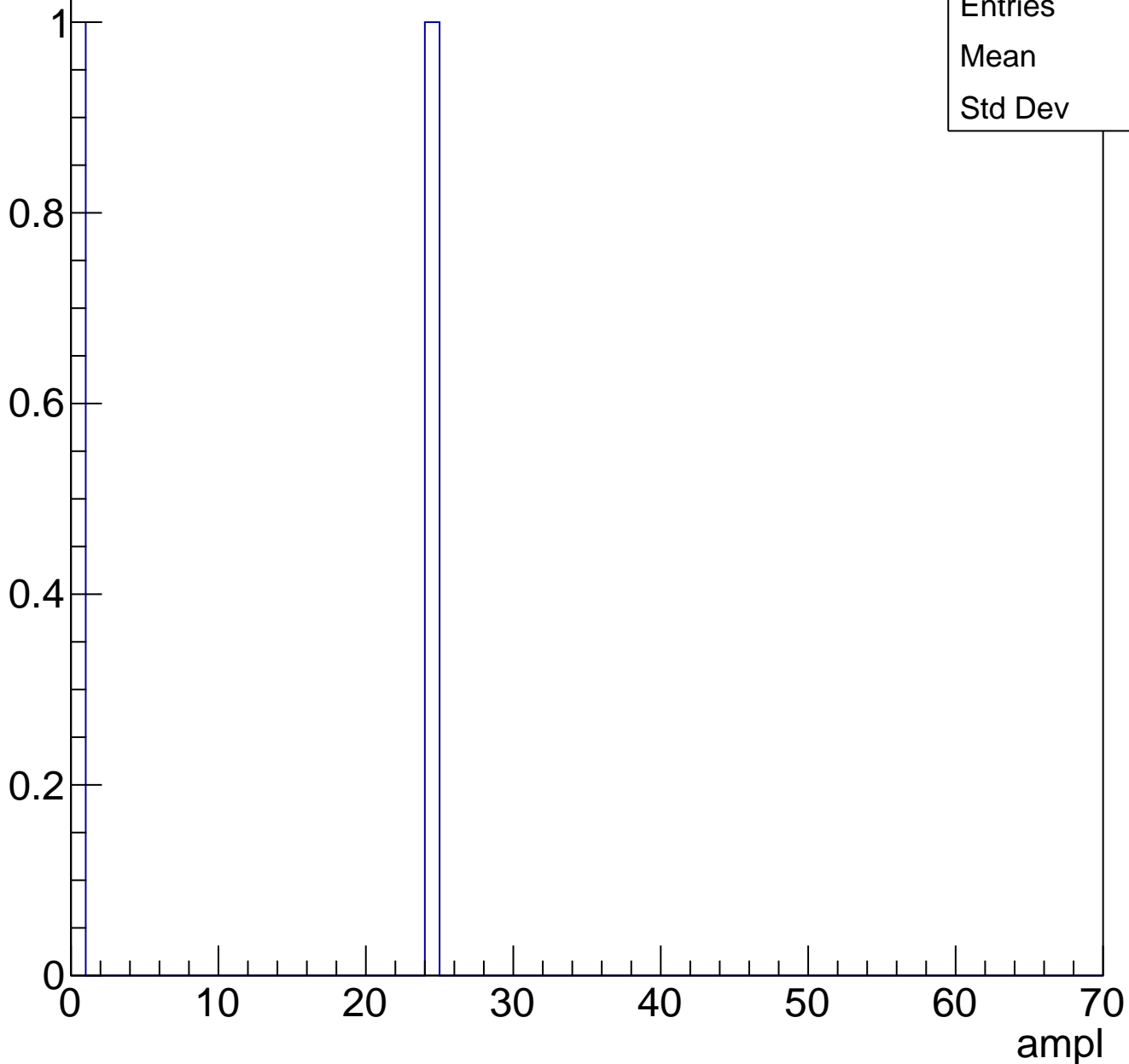




# B0L002S, U1-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch94, adc0

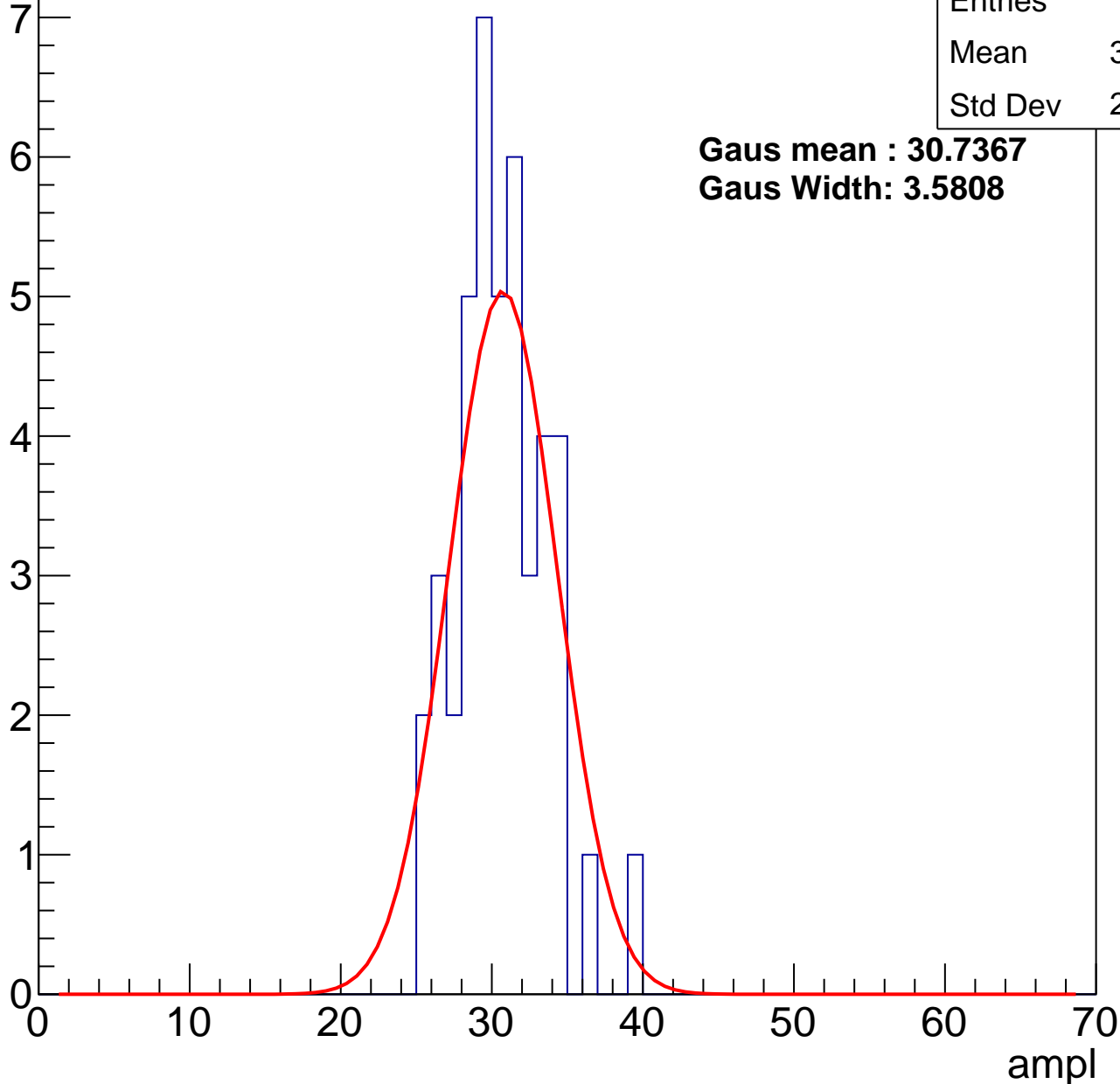
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 30.23 |
| Std Dev | 2.956 |

**Gaus mean : 30.7367**

**Gaus Width: 3.5808**



# B0L002S, U1-ch94, adc1

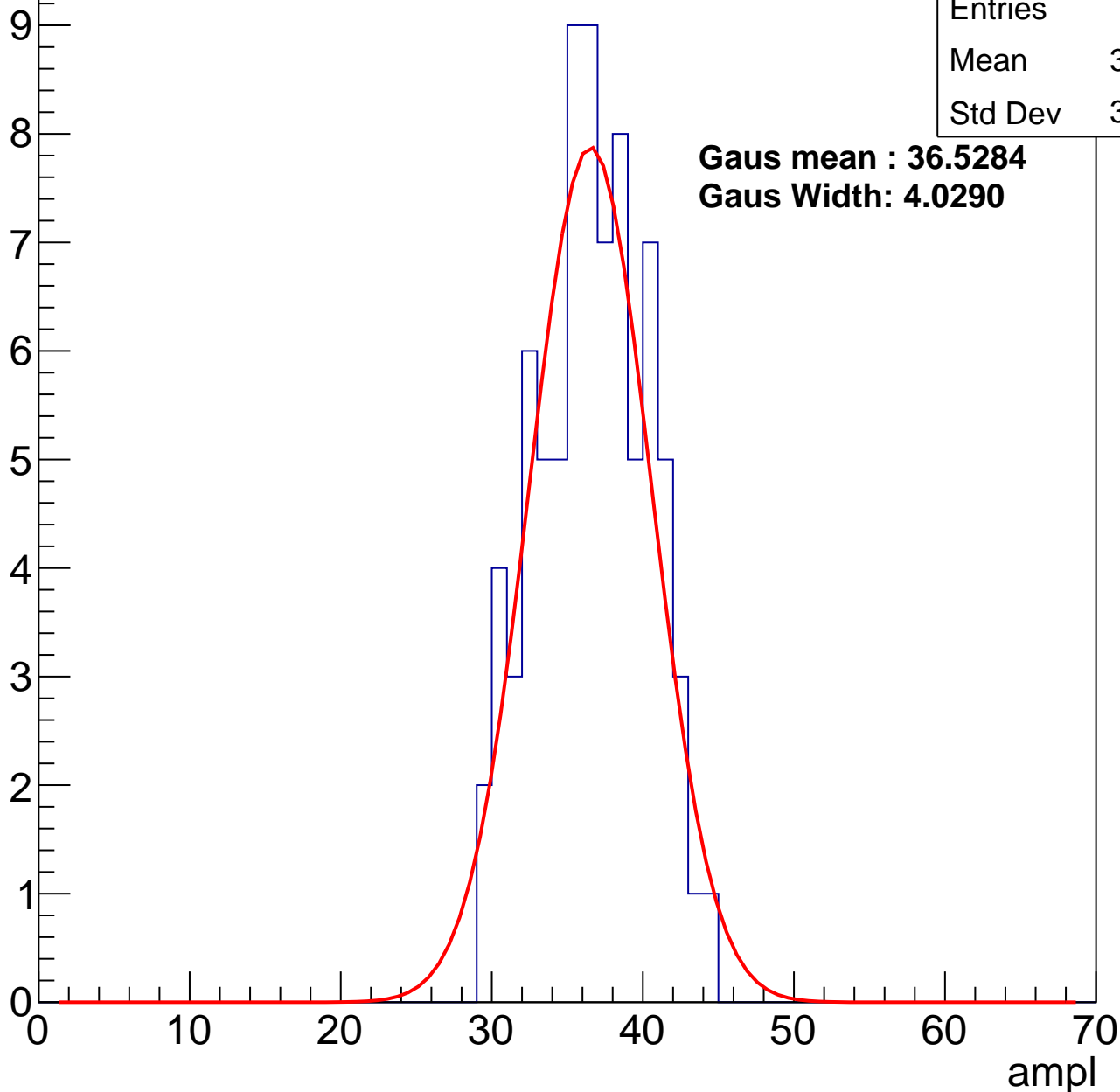
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 36.16 |
| Std Dev | 3.583 |

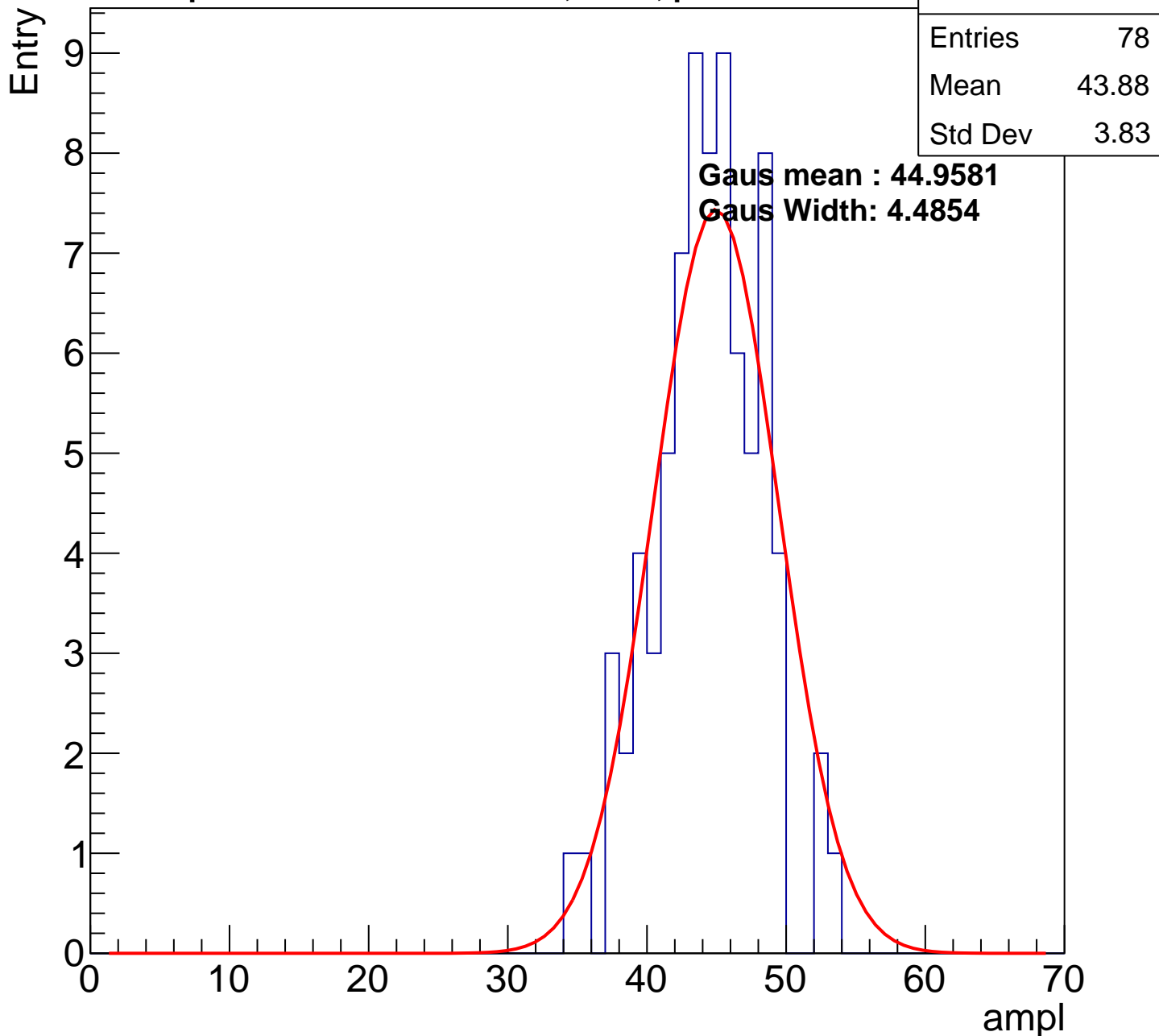
**Gaus mean : 36.5284**

**Gaus Width: 4.0290**



# B0L002S, U1-ch94, adc2

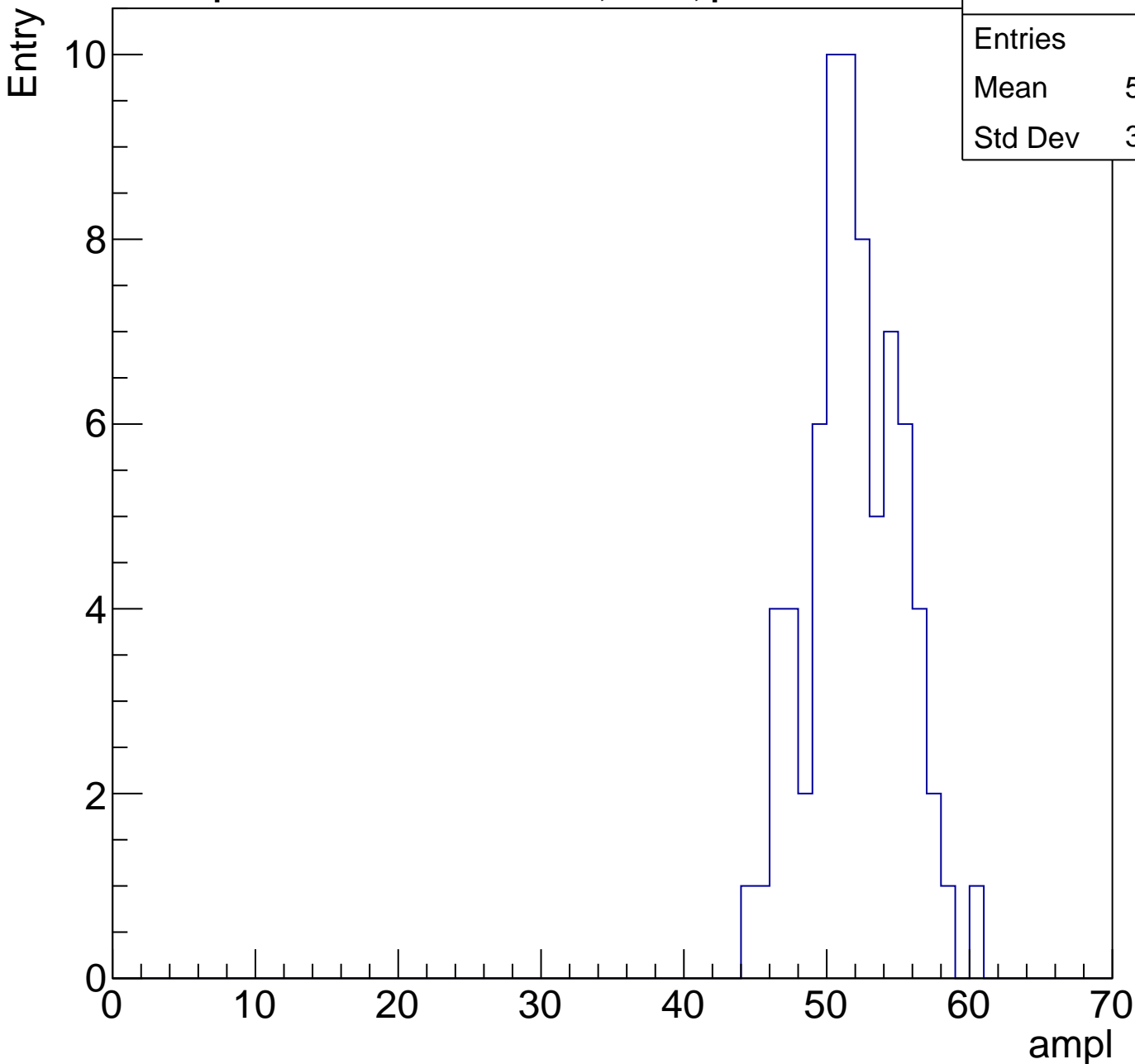
calib\_packv5\_042523\_0143.root, FC#8, port C1



# B0L002S, U1-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

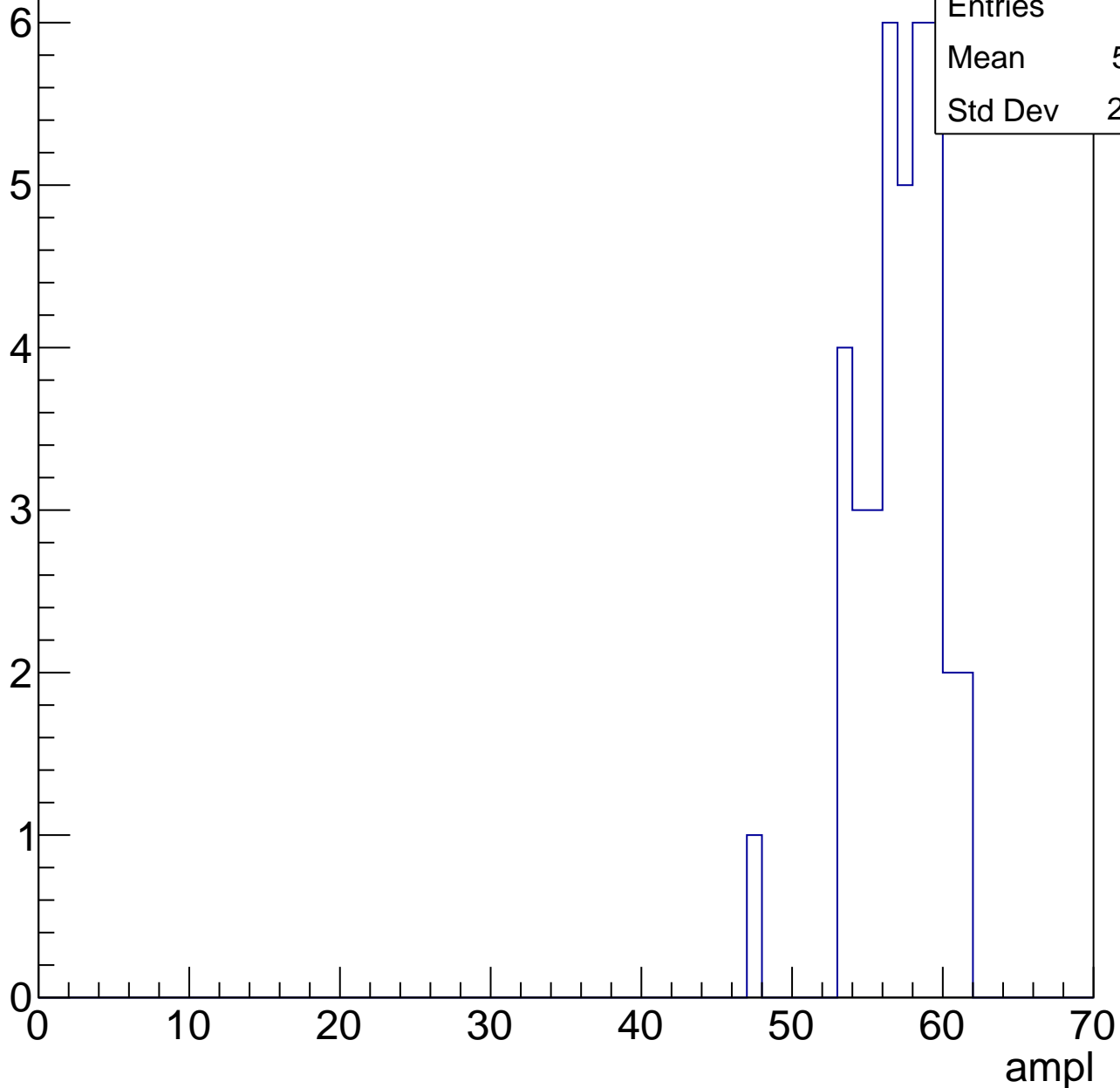
|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 51.47 |
| Std Dev | 3.279 |



# B0L002S, U1-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

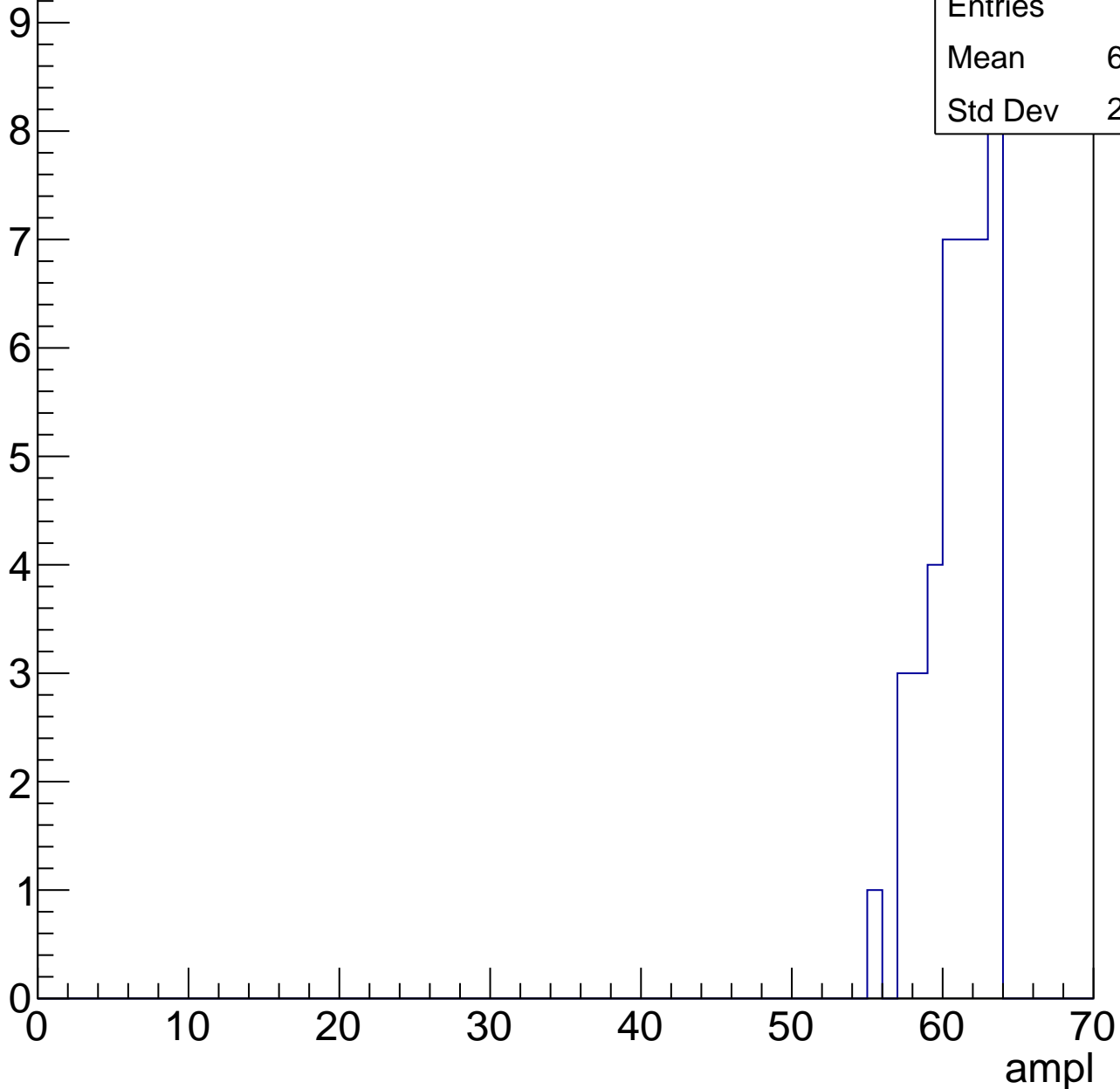


# B0L002S, U1-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

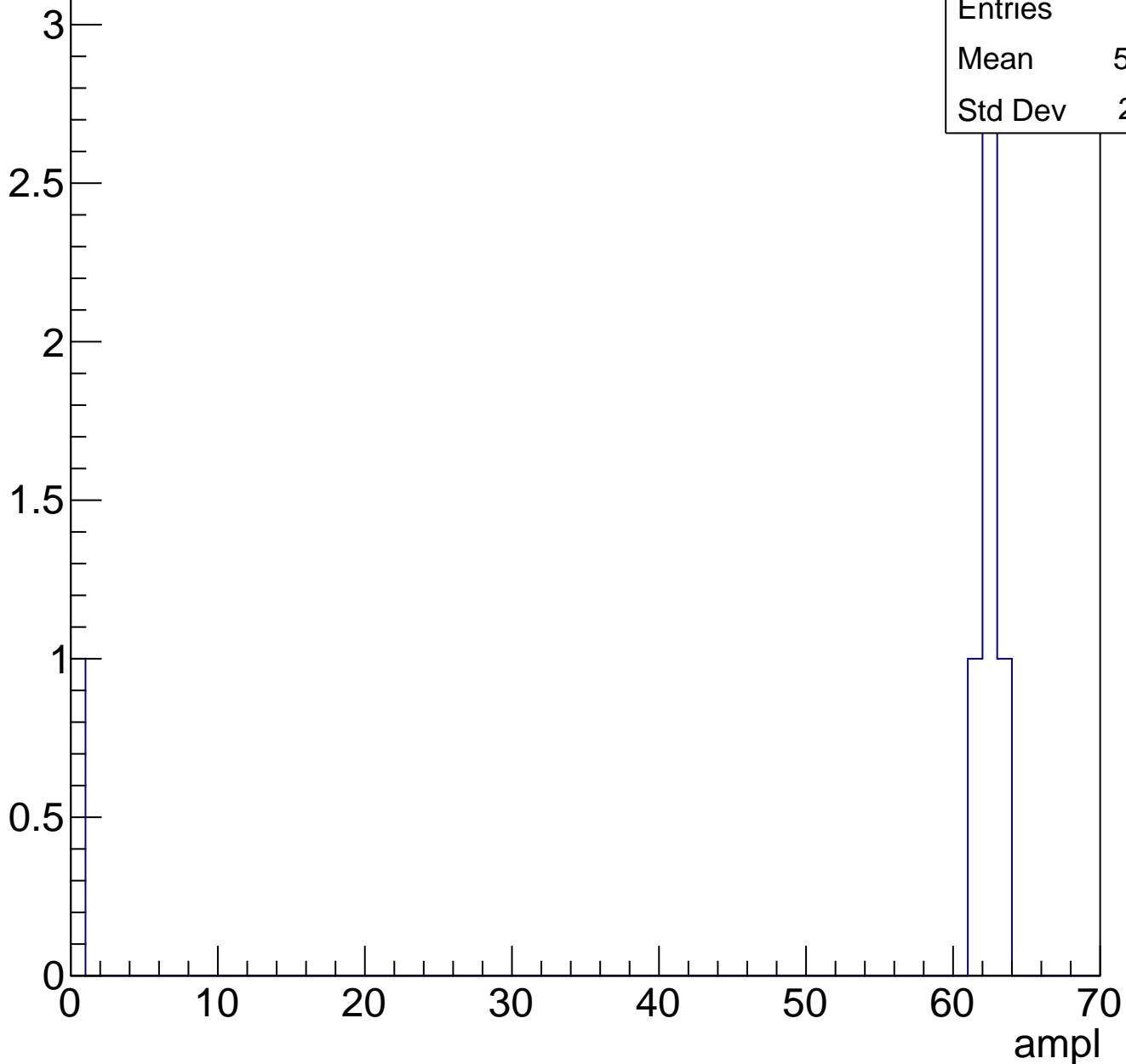
|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 60.59 |
| Std Dev | 2.036 |



# B0L002S, U1-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch95, adc0

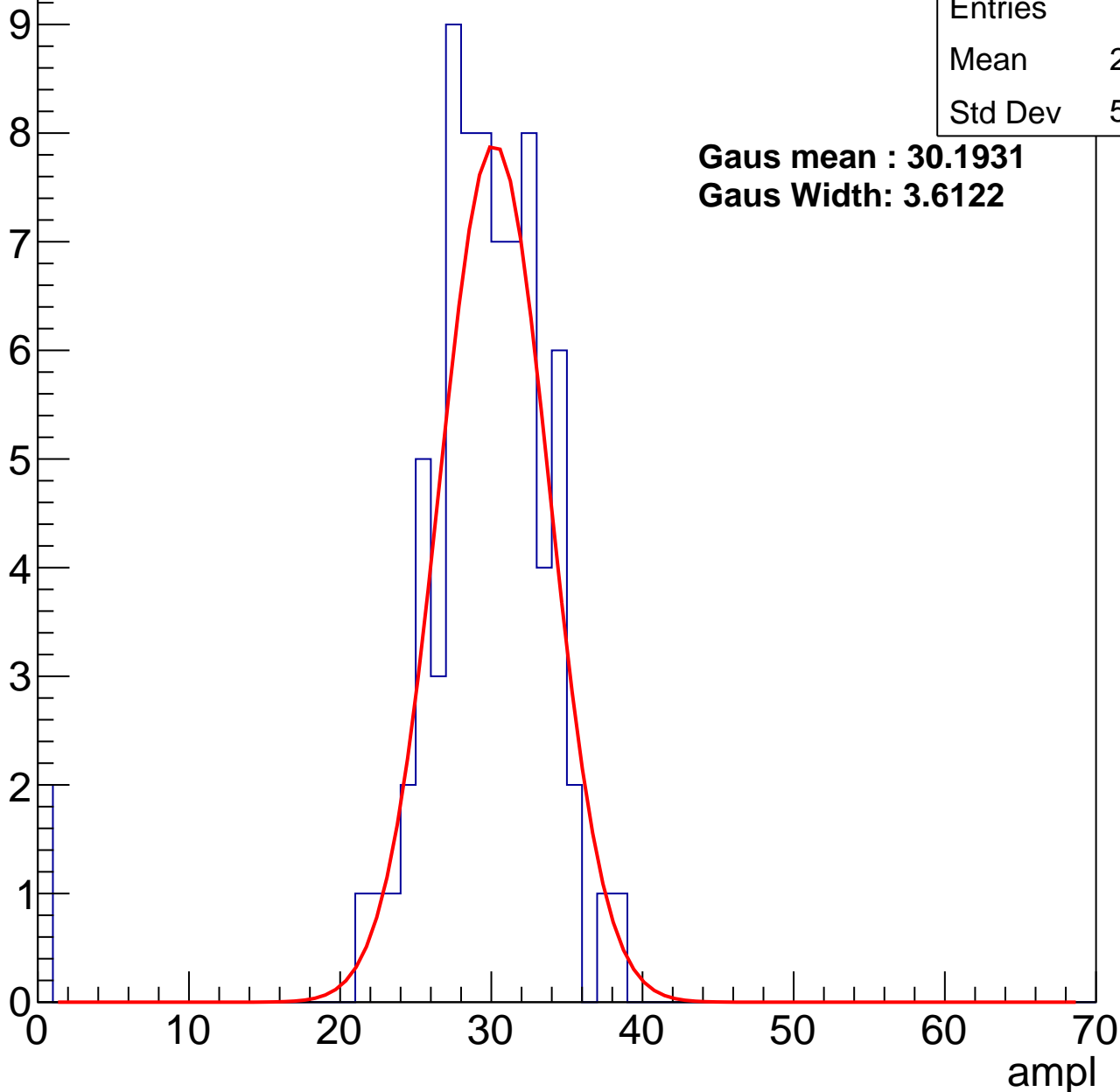
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 28.68 |
| Std Dev | 5.807 |

**Gaus mean : 30.1931**

**Gaus Width: 3.6122**



# B0L002S, U1-ch95, adc1

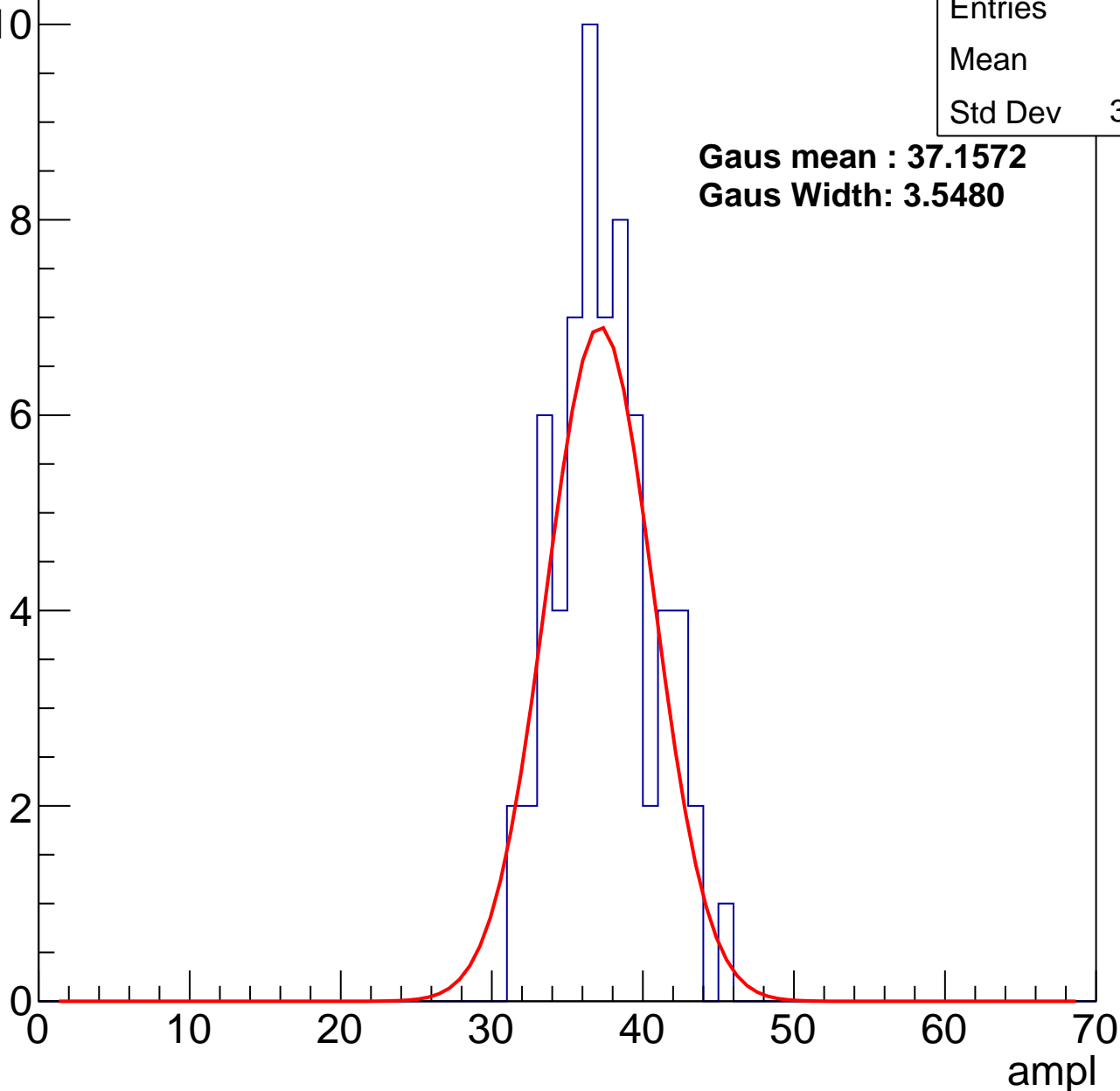
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 37    |
| Std Dev | 3.143 |

**Gaus mean : 37.1572**

**Gaus Width: 3.5480**

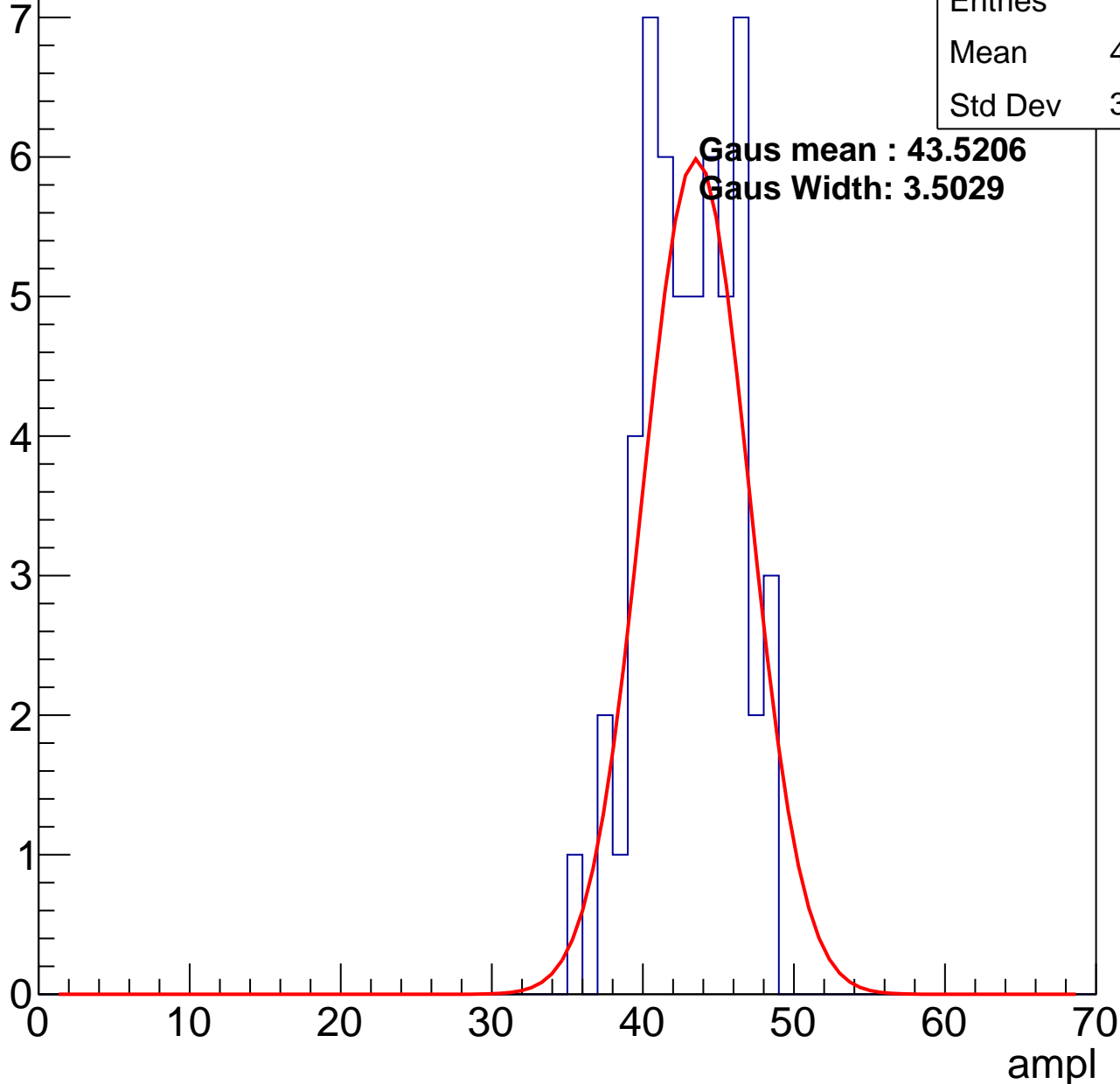


# B0L002S, U1-ch95, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

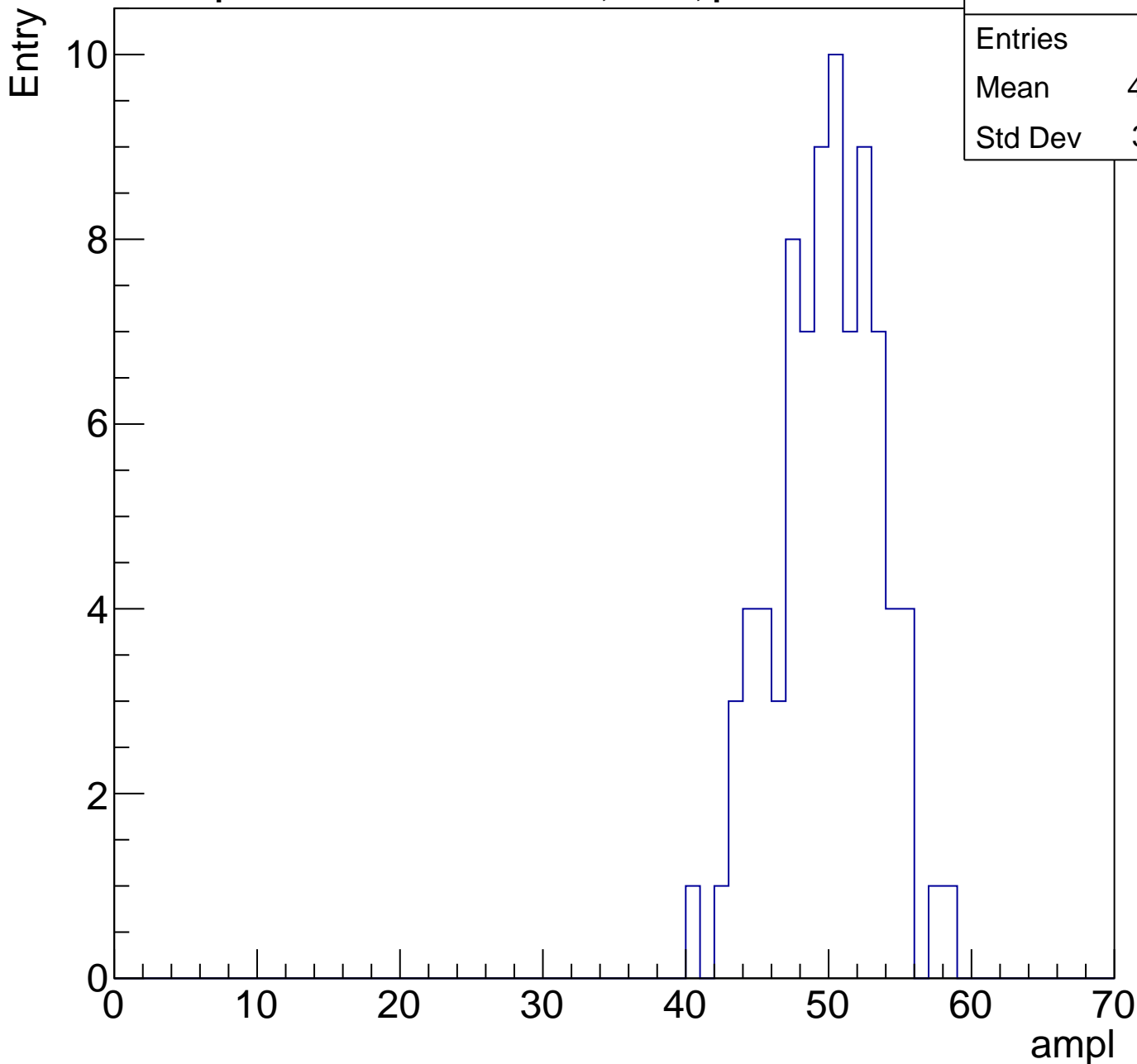
|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 42.65 |
| Std Dev | 3.062 |



# B0L002S, U1-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 49.46 |
| Std Dev | 3.591 |

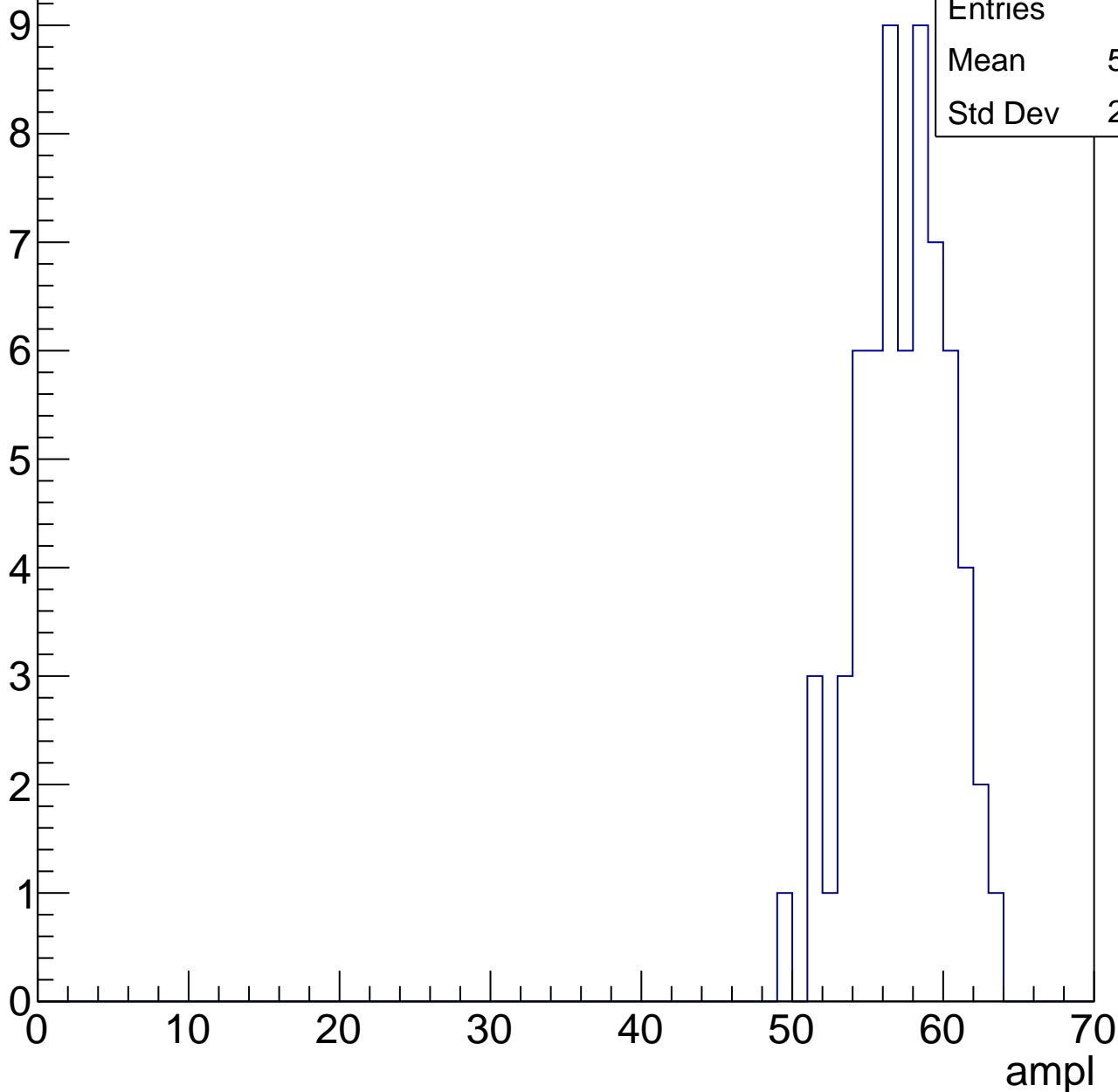


# B0L002S, U1-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 56.86 |
| Std Dev | 2.989 |

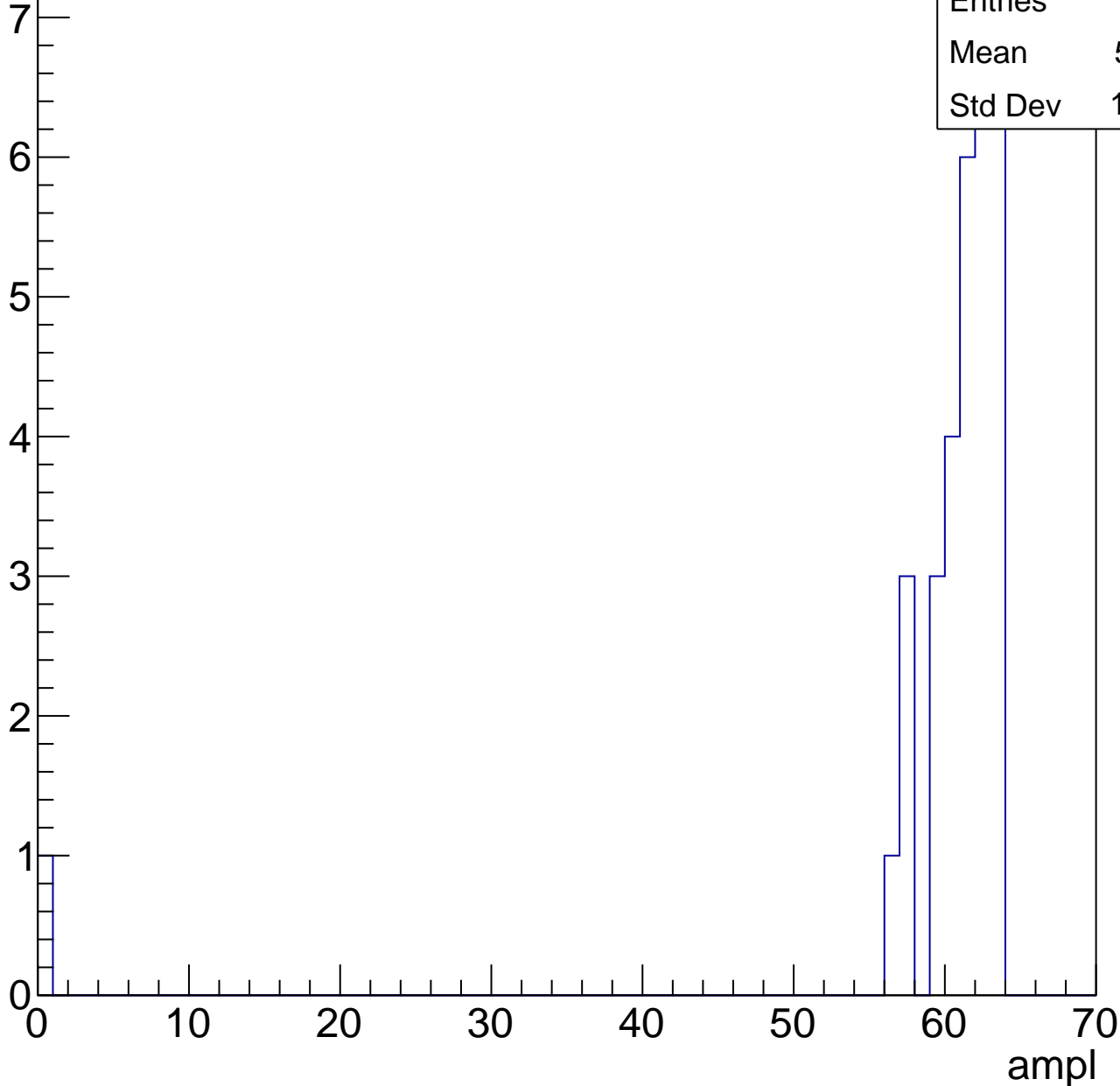


# B0L002S, U1-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 32    |
| Mean    | 58.91 |
| Std Dev | 10.76 |



# B0L002S, U1-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



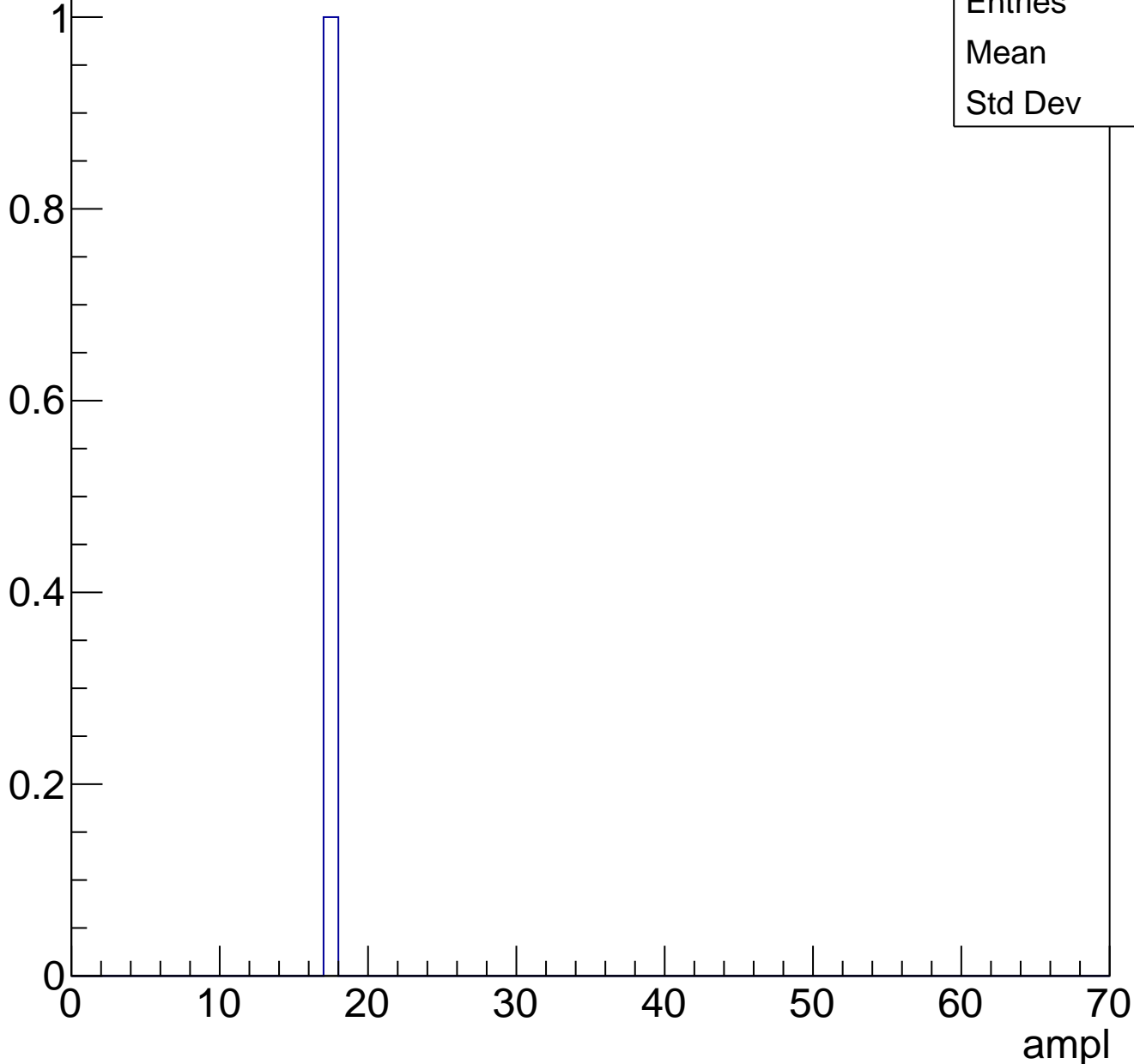
|         |        |
|---------|--------|
| Entries | 3      |
| Mean    | 62.67  |
| Std Dev | 0.4714 |



# B0L002S, U1-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 17 |
| Std Dev | 0  |

# B0L002S, U1-ch96, adc0

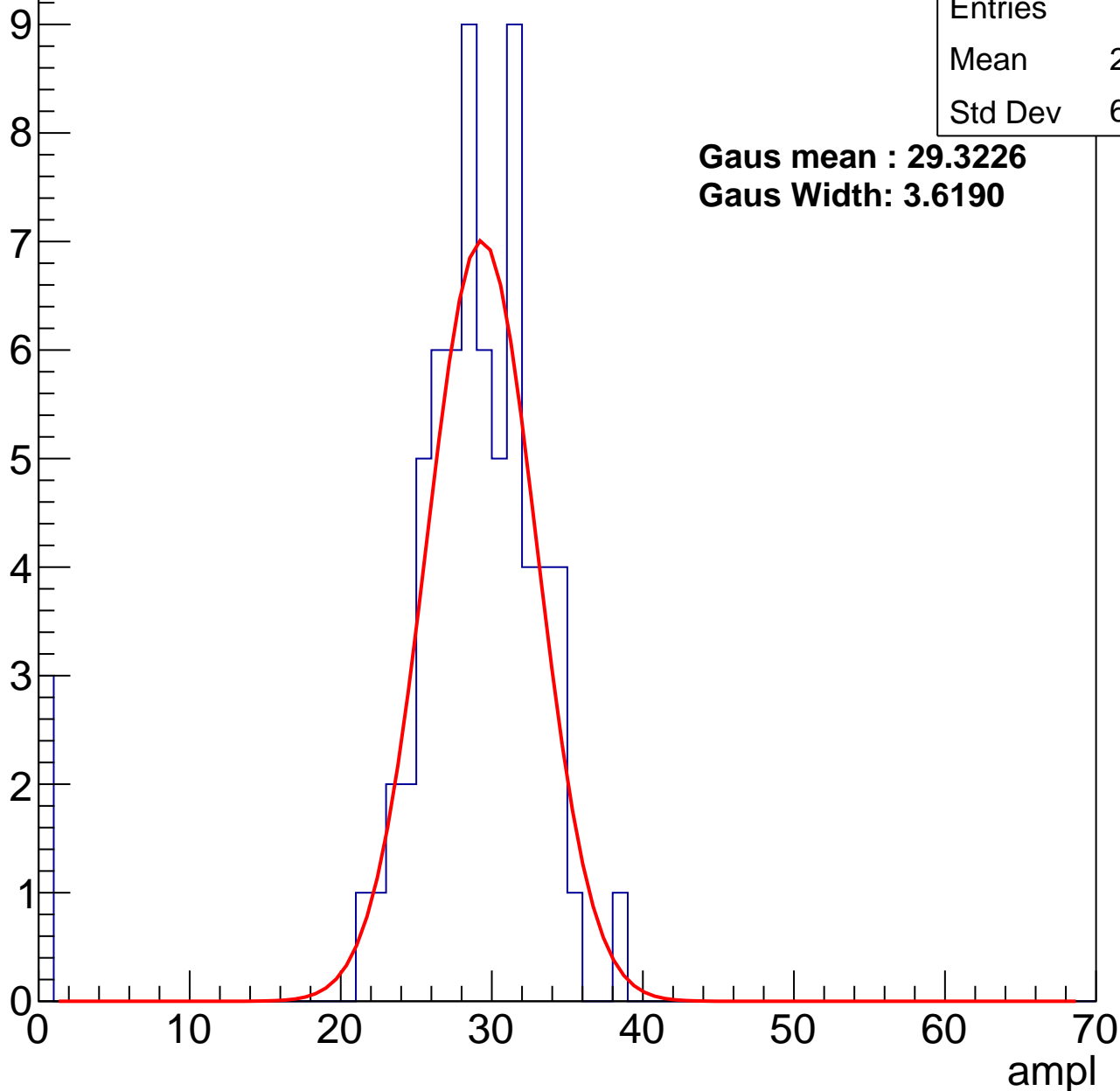
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 27.59 |
| Std Dev | 6.753 |

**Gaus mean : 29.3226**

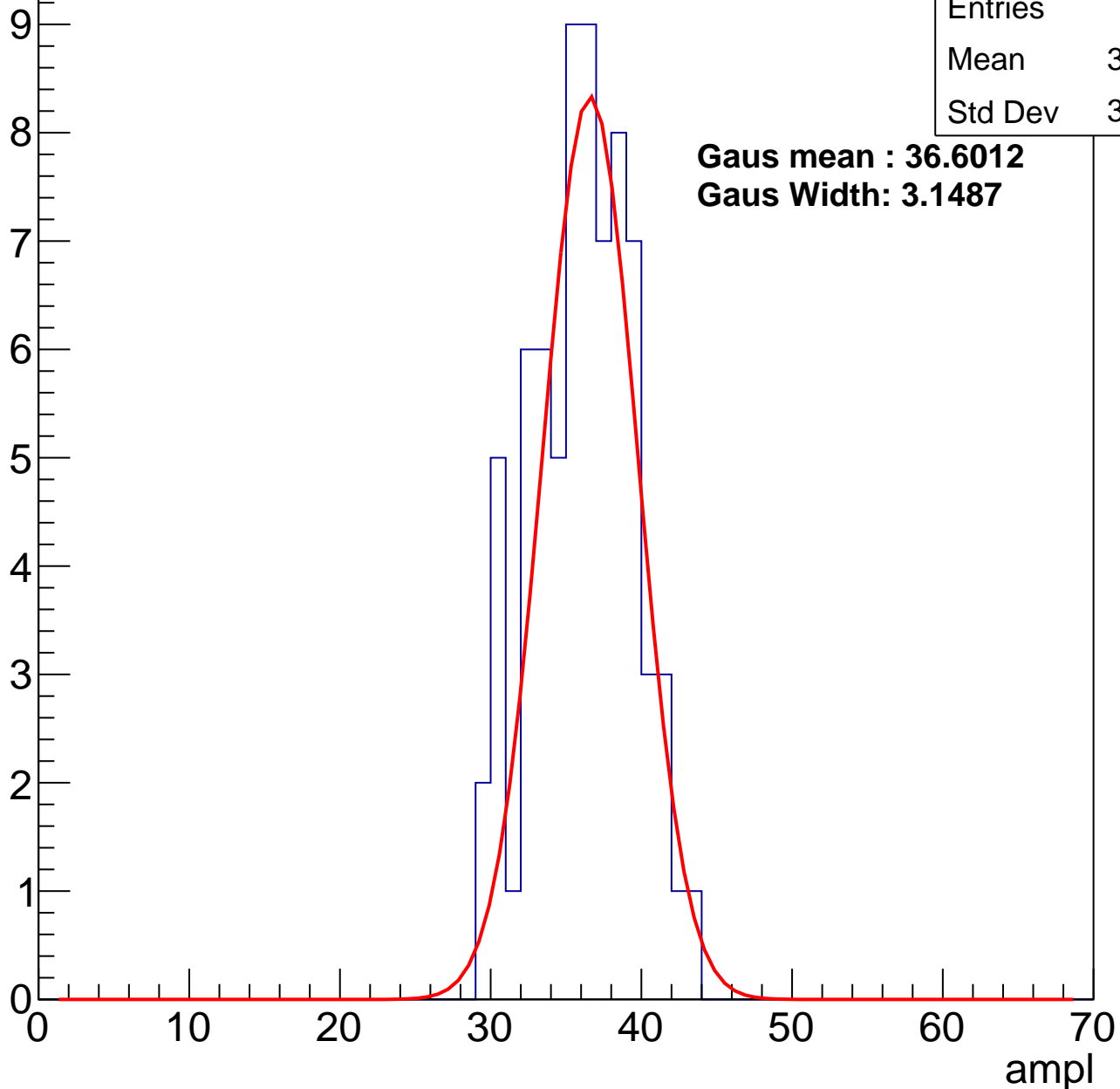
**Gaus Width: 3.6190**



# B0L002S, U1-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch96, adc2

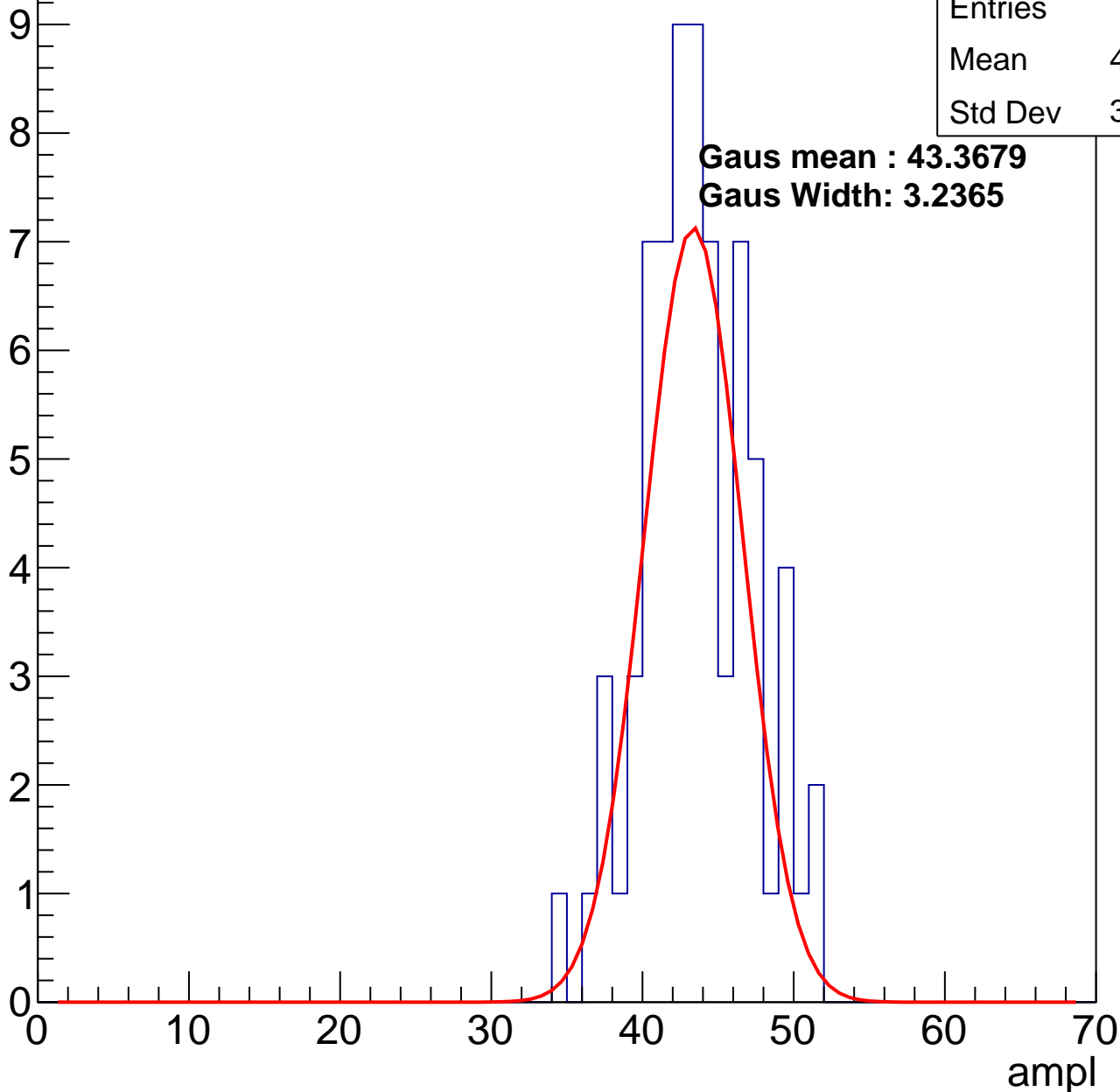
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 43.15 |
| Std Dev | 3.594 |

**Gaus mean : 43.3679**

**Gaus Width: 3.2365**

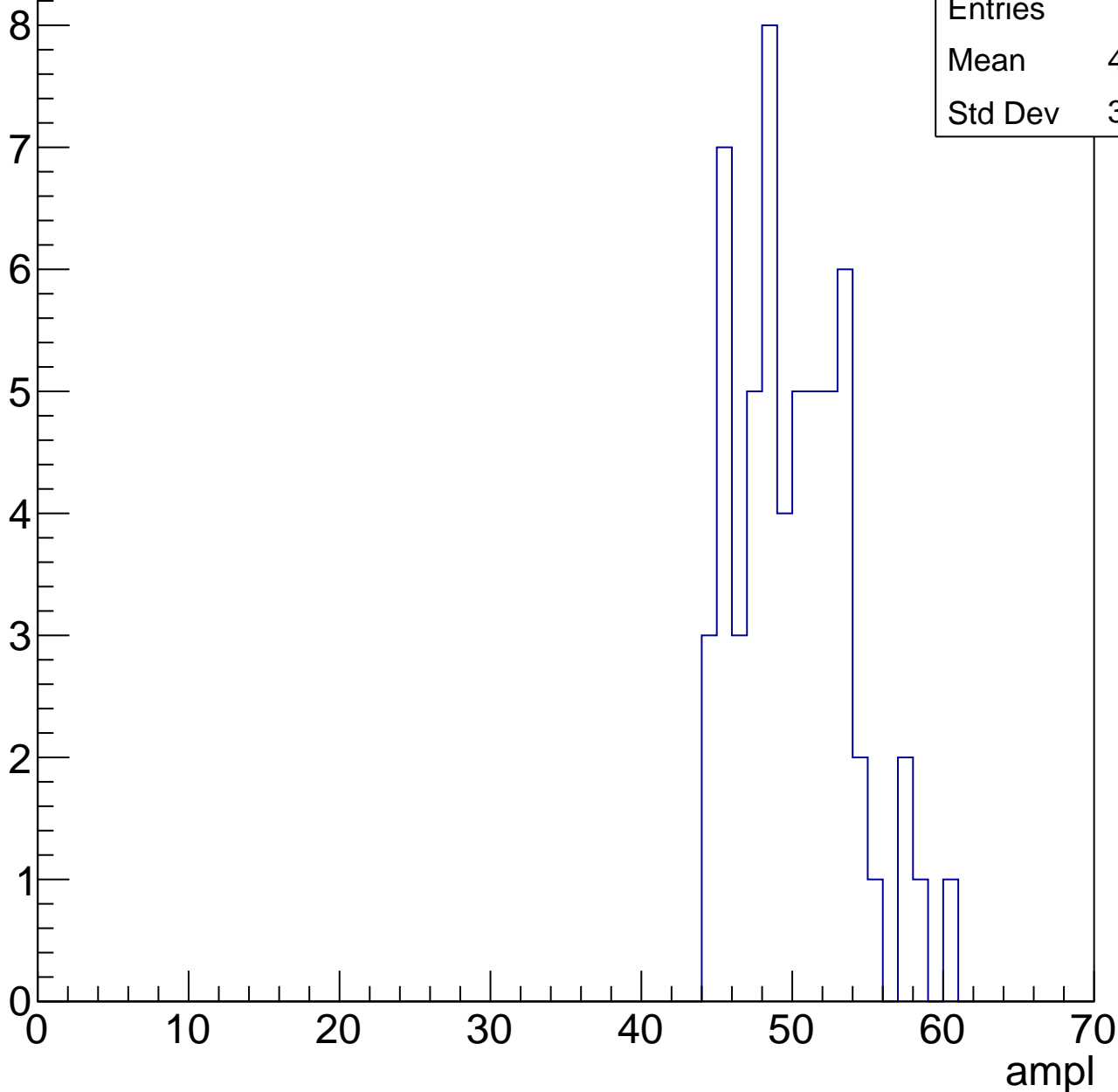


# B0L002S, U1-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 49.62 |
| Std Dev | 3.718 |

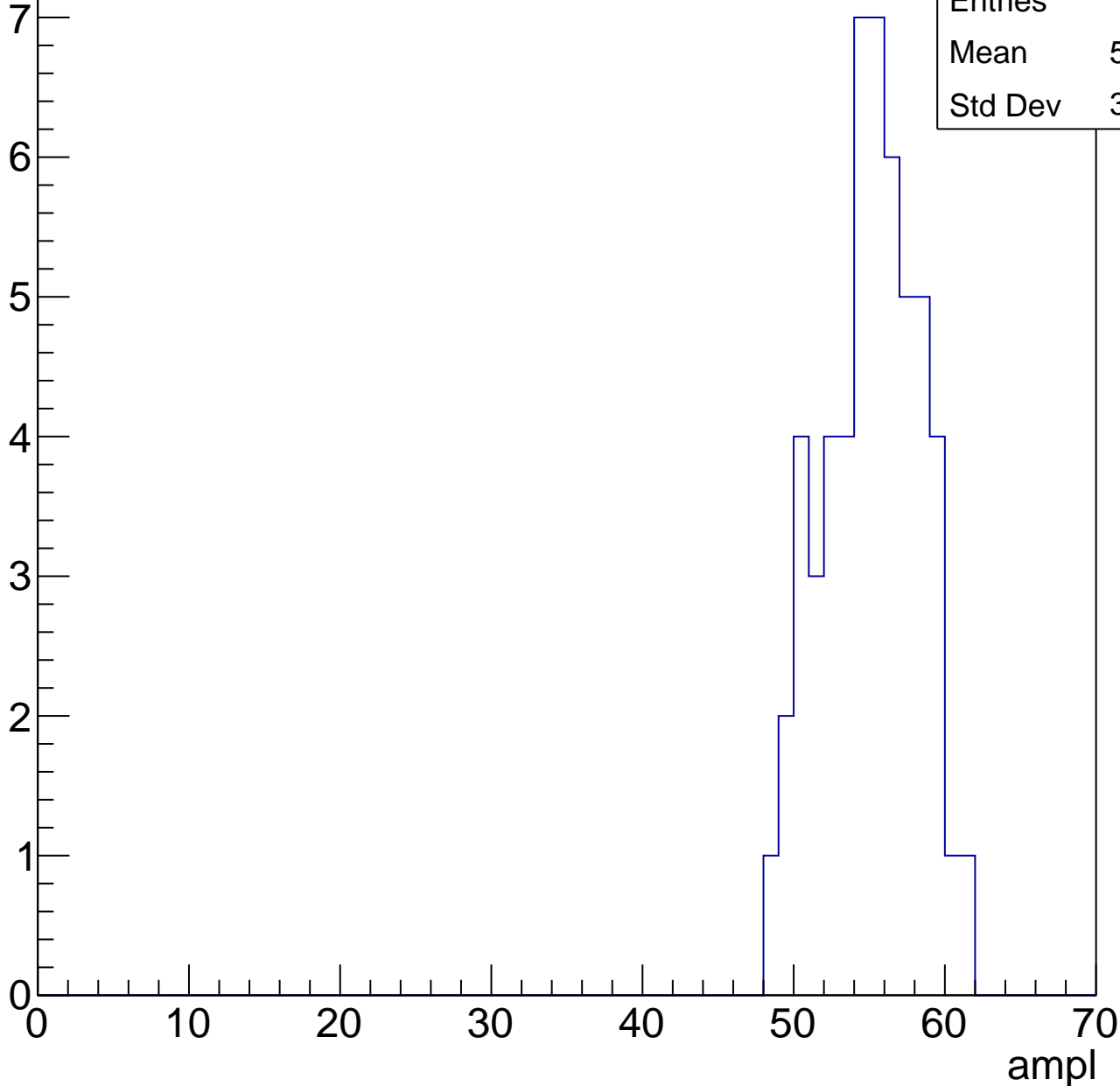


# B0L002S, U1-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 54.63 |
| Std Dev | 3.093 |



# B0L002S, U1-ch96, adc5

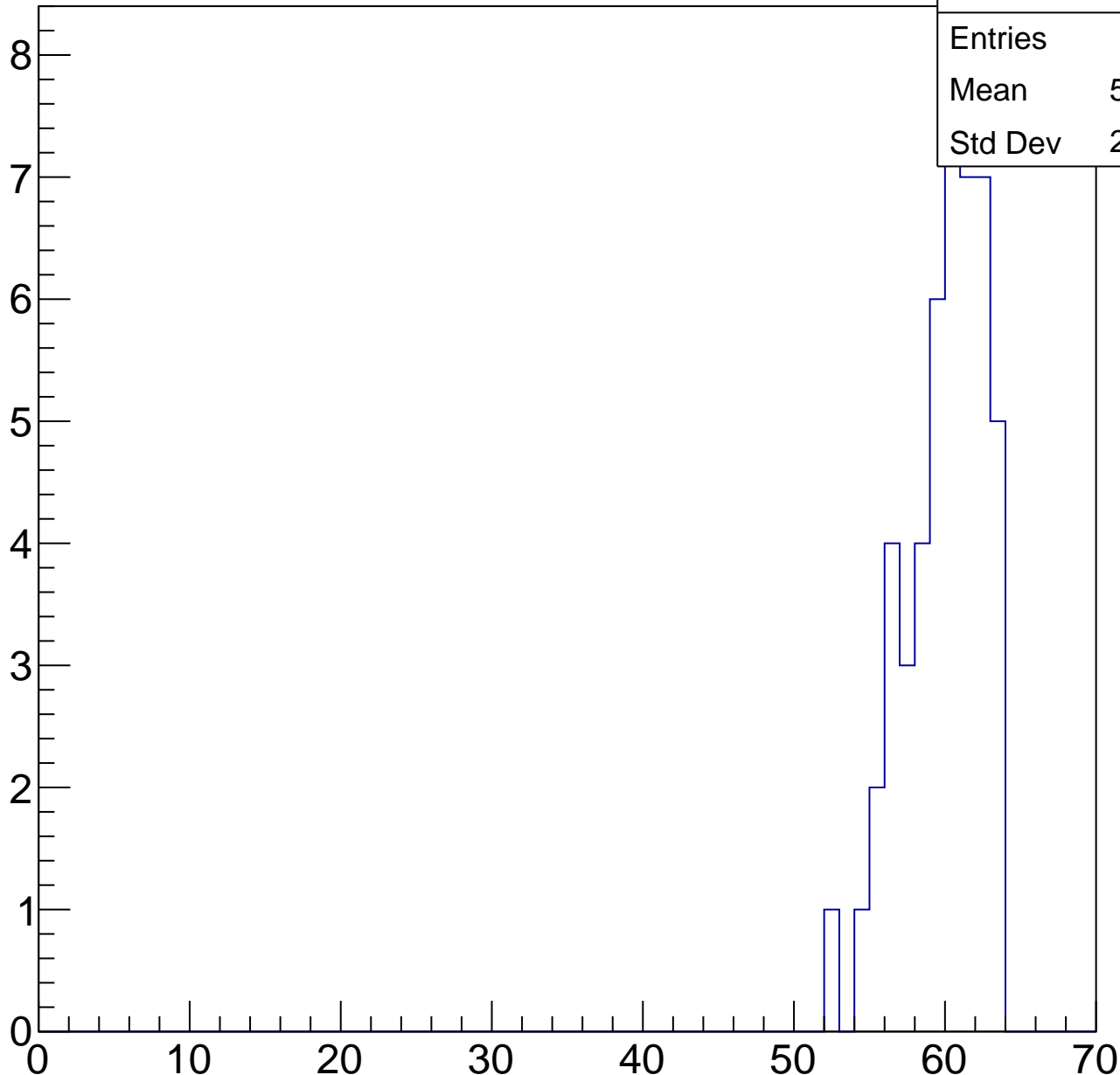
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 59.44 |
| Std Dev | 2.613 |

ampl

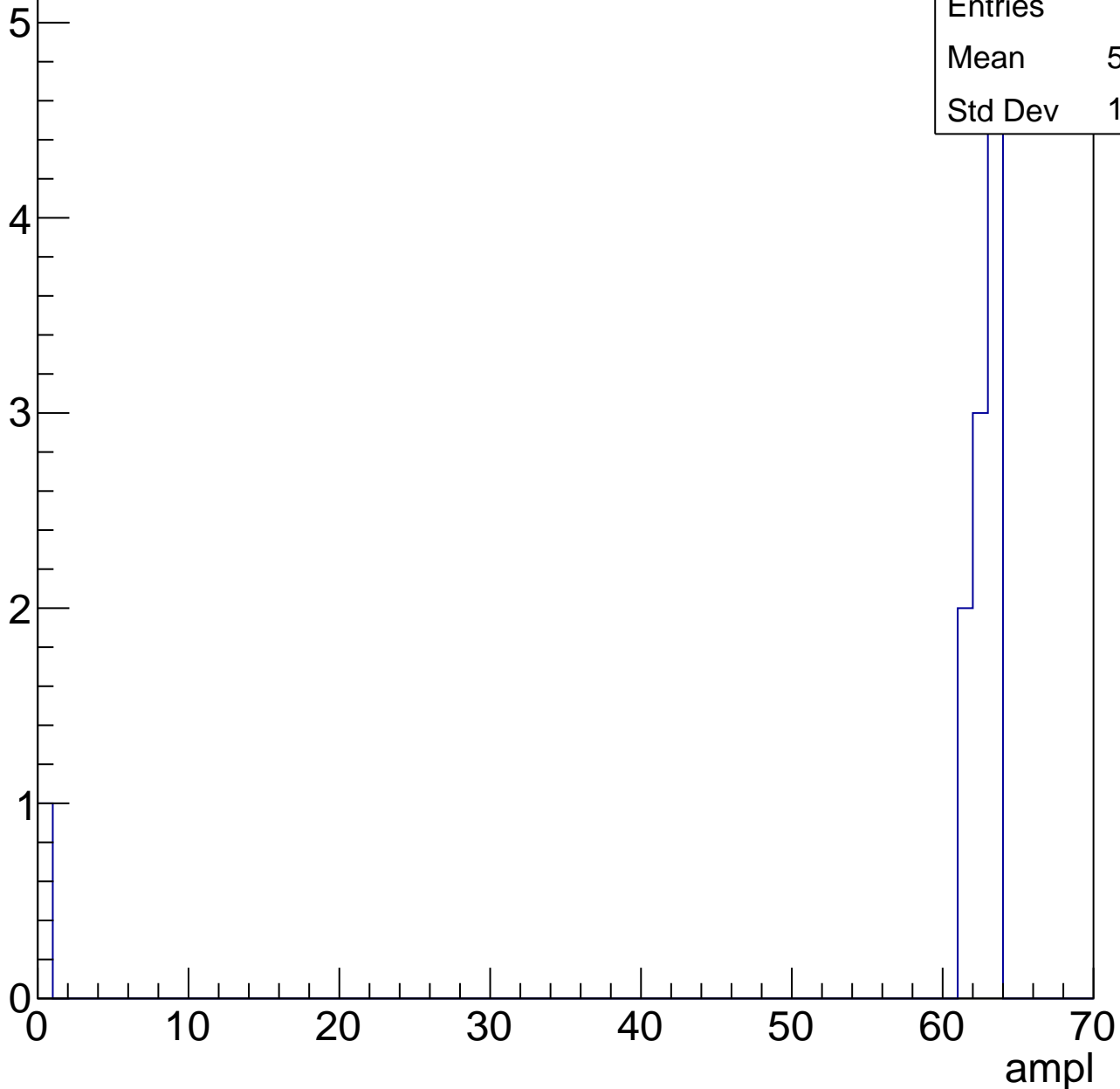


# B0L002S, U1-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 56.64 |
| Std Dev | 17.93 |





# B0L002S, U1-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch97, adc0

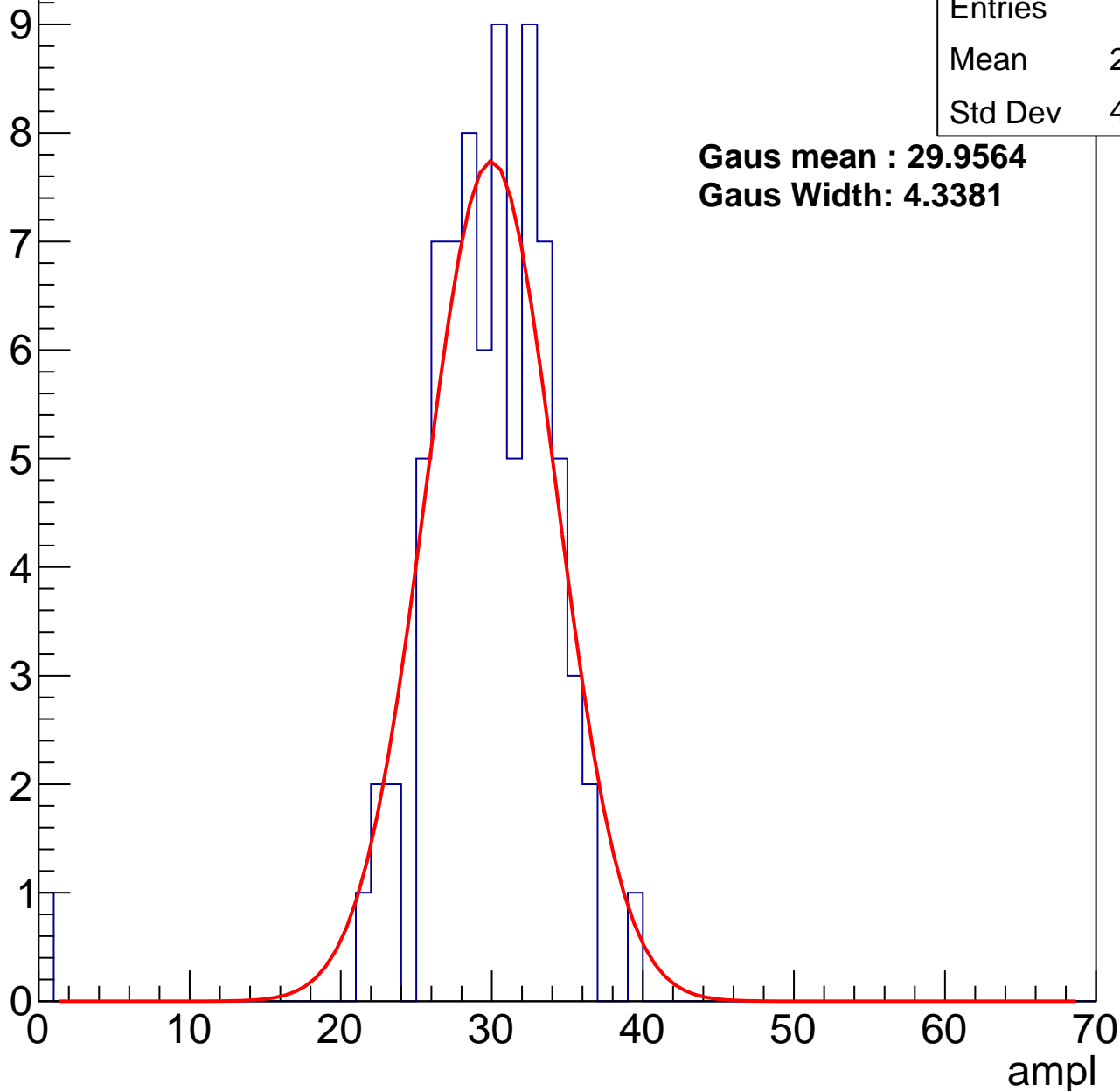
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 29.19 |
| Std Dev | 4.879 |

**Gaus mean : 29.9564**

**Gaus Width: 4.3381**



# B0L002S, U1-ch97, adc1

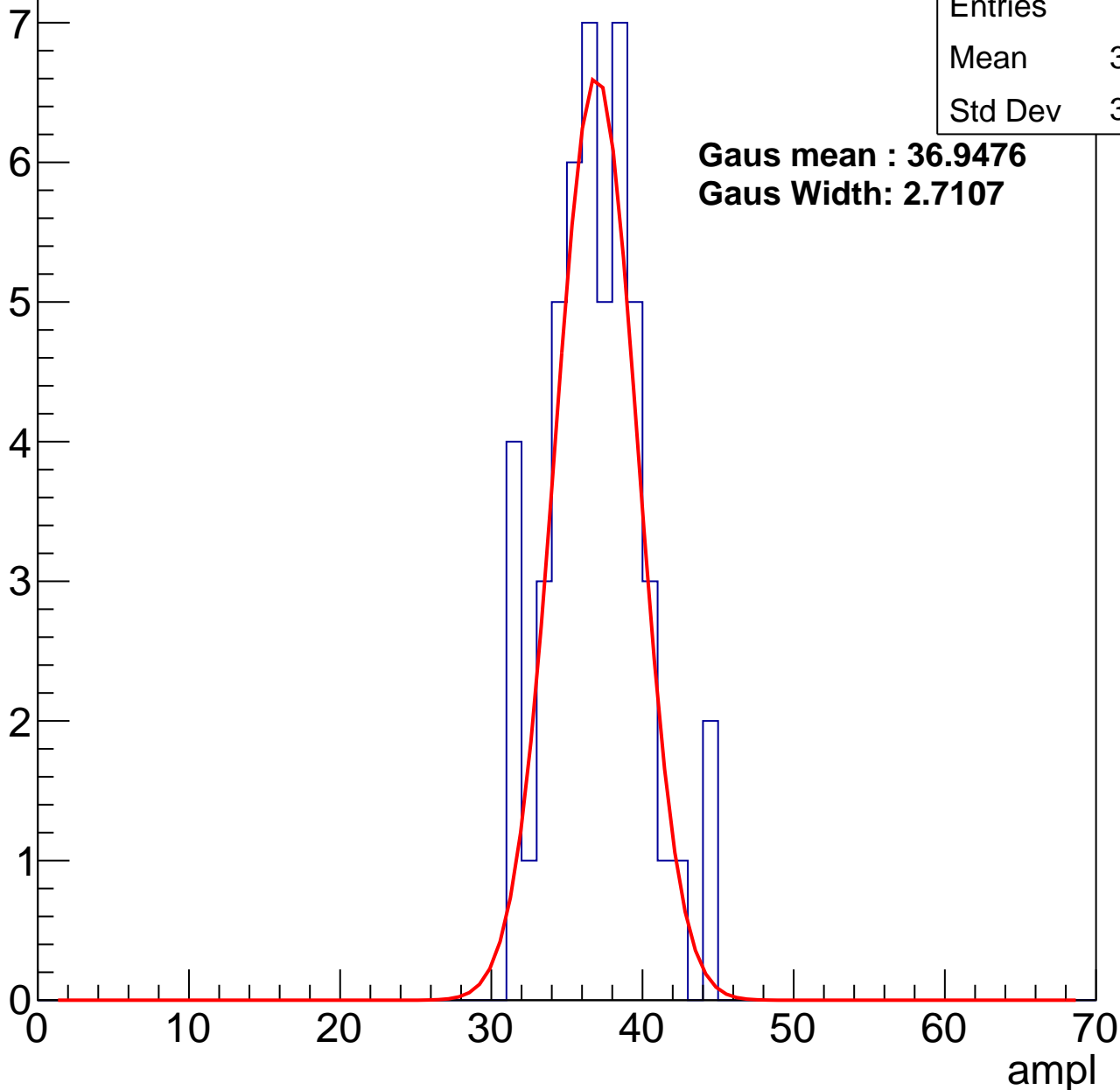
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 36.48 |
| Std Dev | 3.074 |

**Gaus mean : 36.9476**

**Gaus Width: 2.7107**



# B0L002S, U1-ch97, adc2

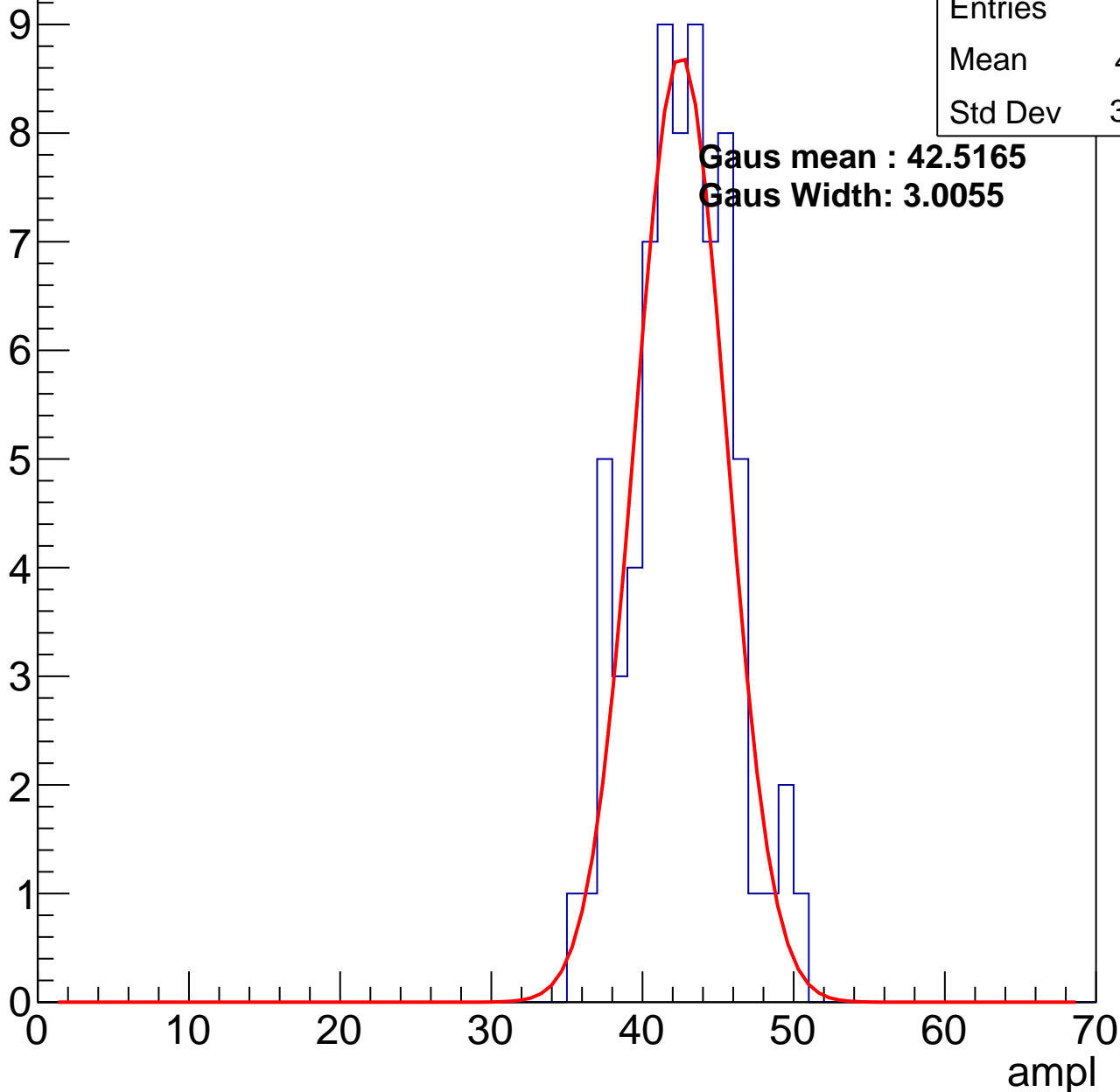
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 42.21 |
| Std Dev | 3.206 |

**Gaus mean : 42.5165**

**Gaus Width: 3.0055**

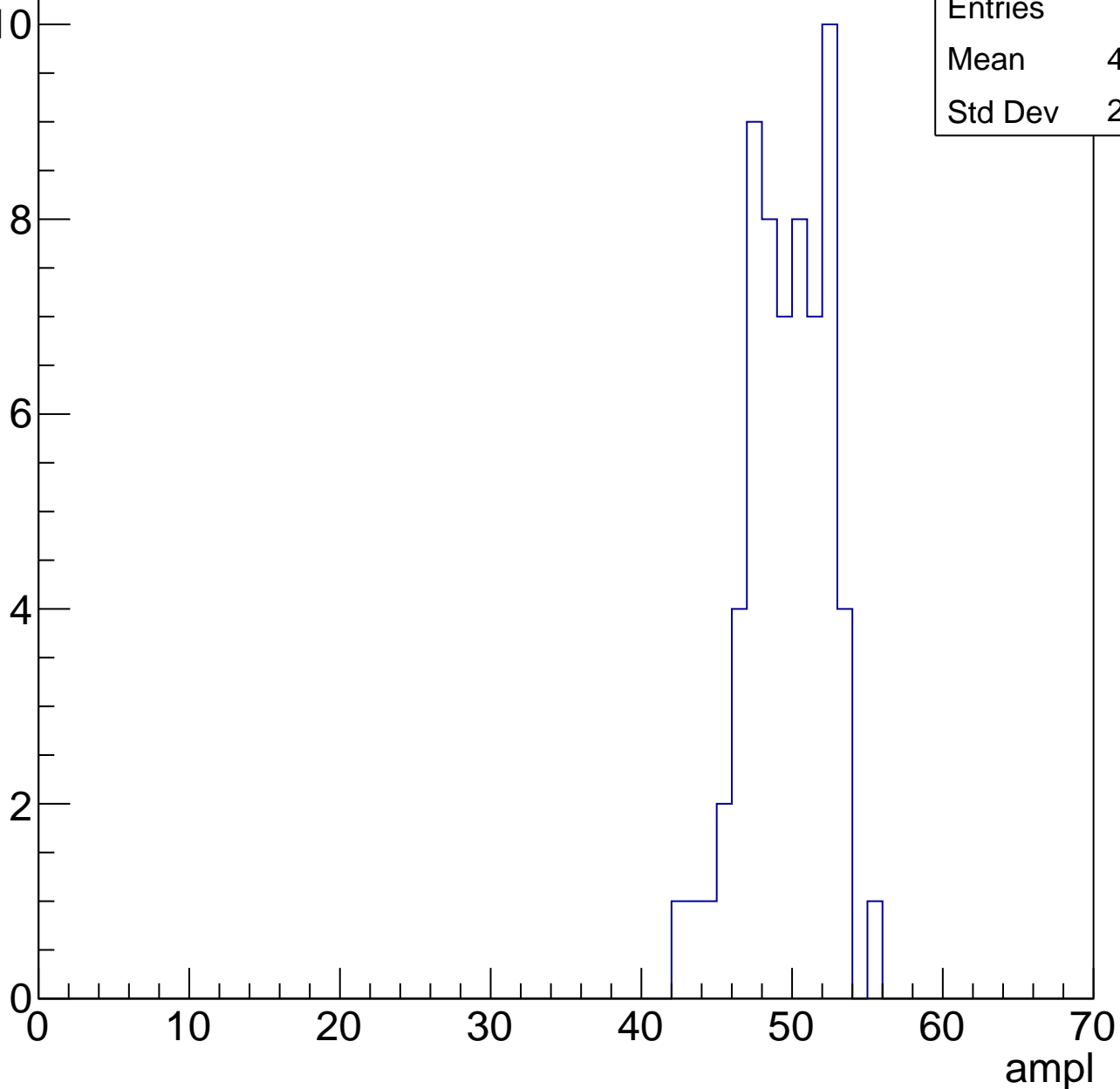


# B0L002S, U1-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 49.16 |
| Std Dev | 2.662 |

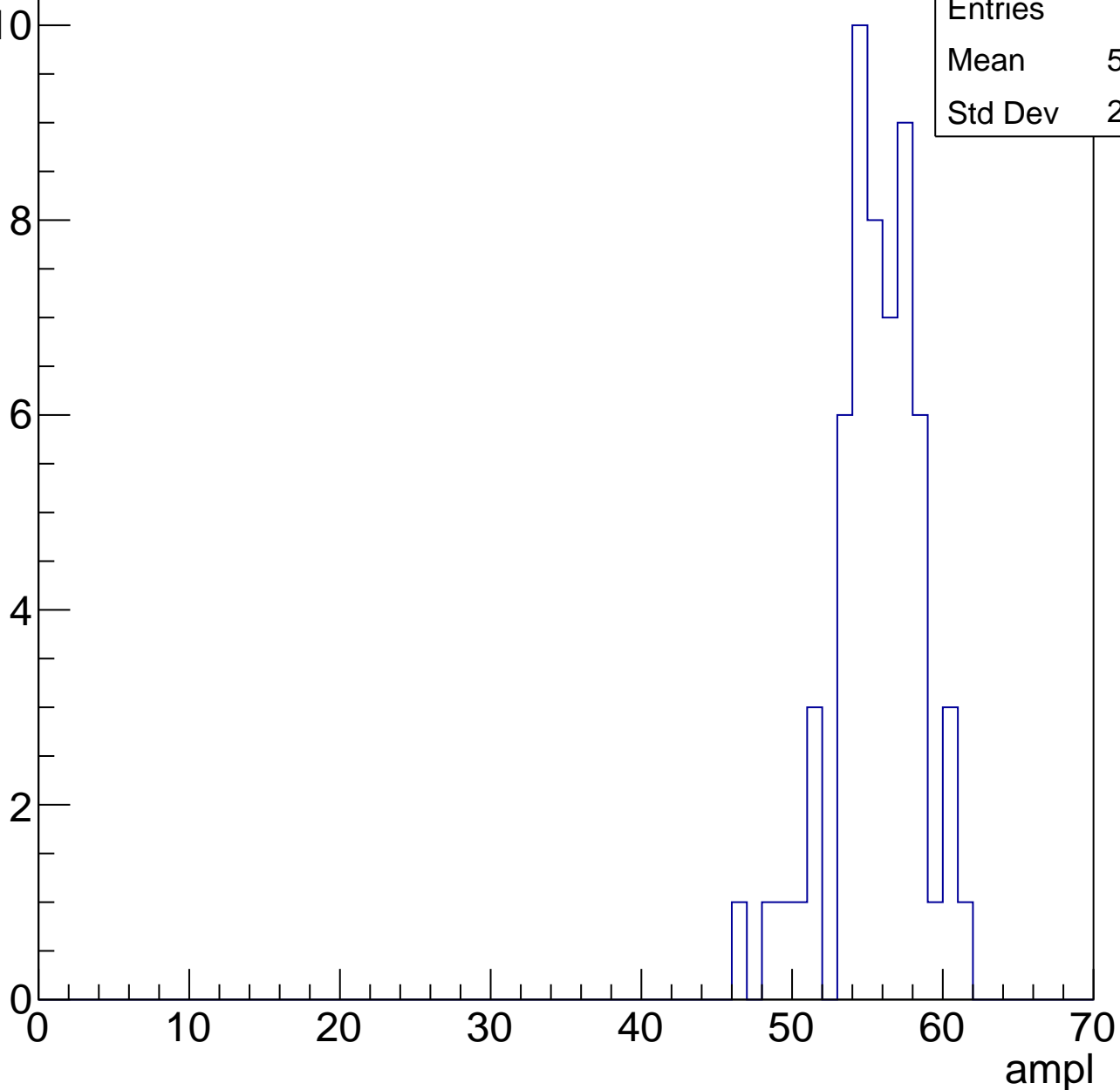


# B0L002S, U1-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 55.12 |
| Std Dev | 2.925 |

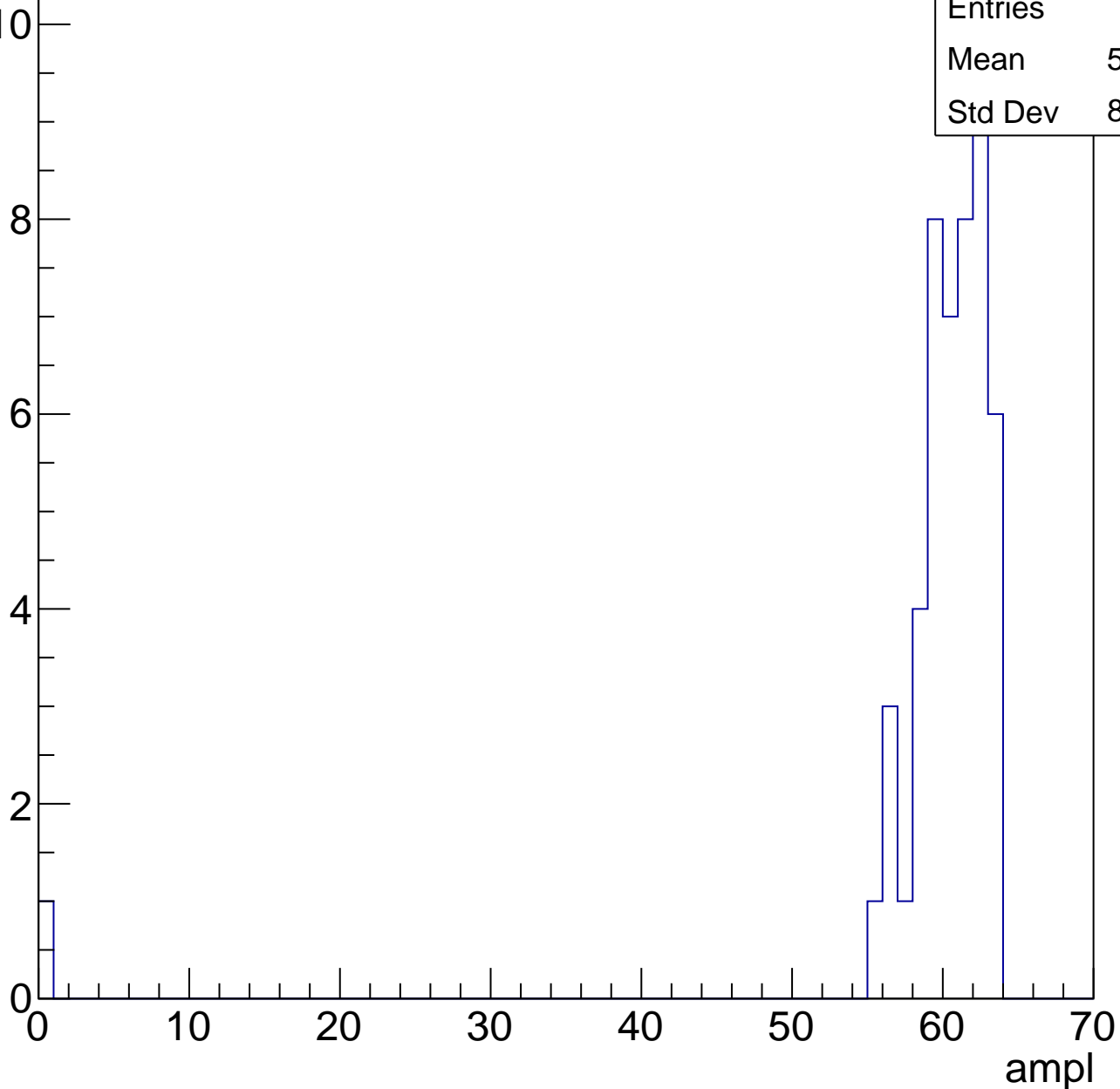


# B0L002S, U1-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

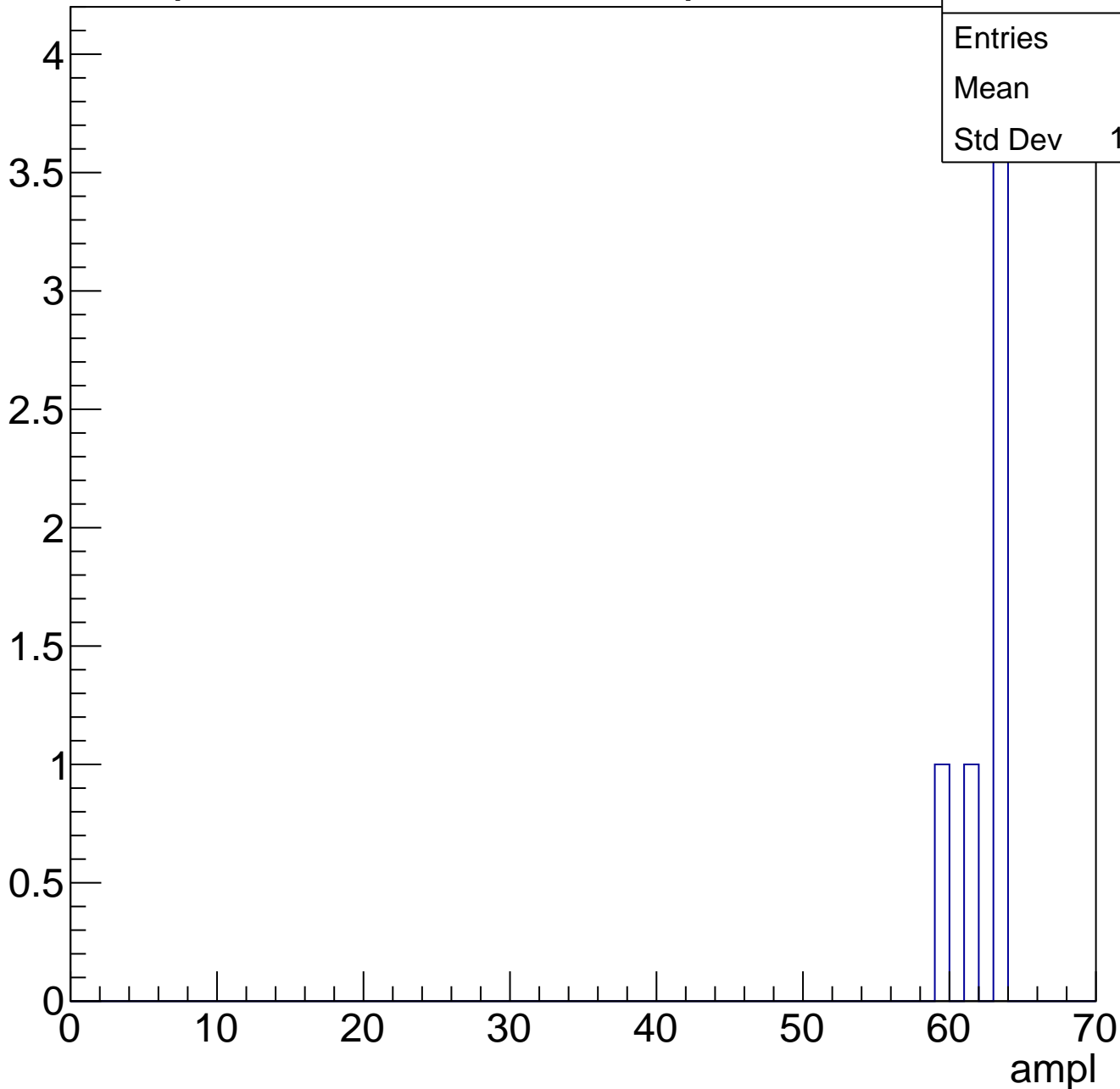
|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 58.98 |
| Std Dev | 8.756 |



# B0L002S, U1-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch98, adc0

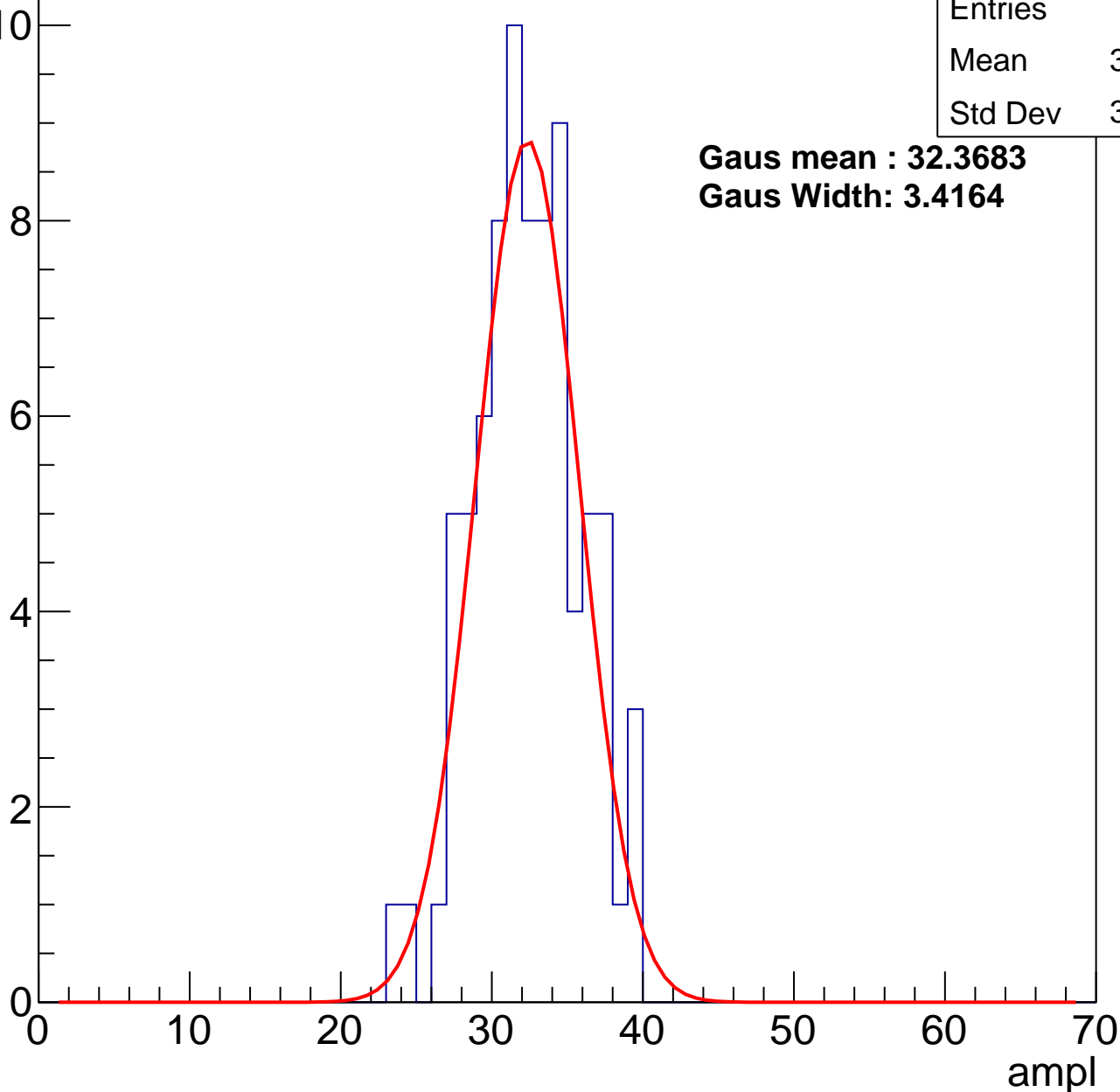
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 31.98 |
| Std Dev | 3.446 |

**Gaus mean : 32.3683**

**Gaus Width: 3.4164**



# B0L002S, U1-ch98, adc1

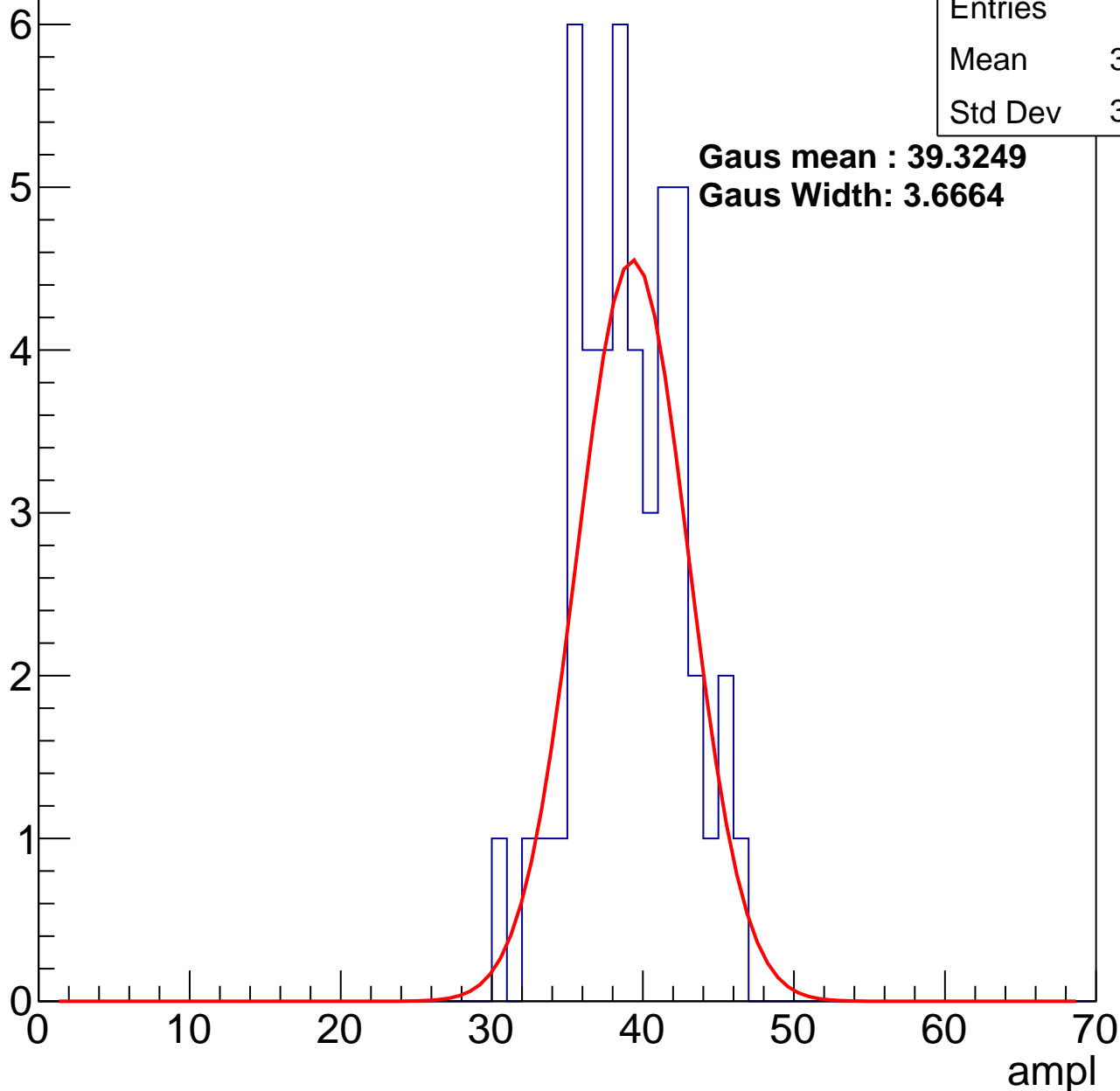
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 38.64 |
| Std Dev | 3.528 |

**Gaus mean : 39.3249**

**Gaus Width: 3.6664**



# B0L002S, U1-ch98, adc2

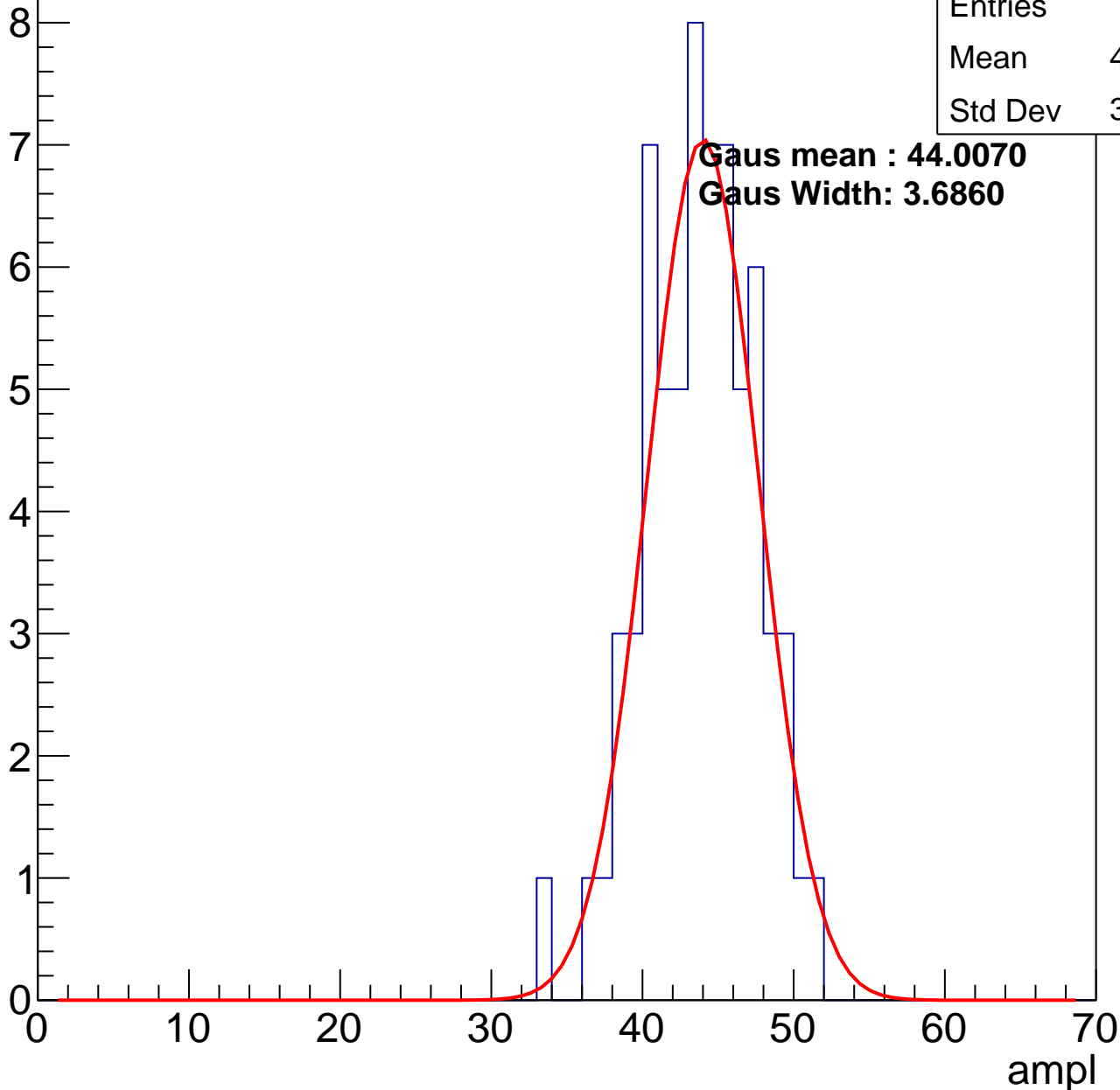
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 43.33 |
| Std Dev | 3.584 |

**Gaus mean : 44.0070**

**Gaus Width: 3.6860**

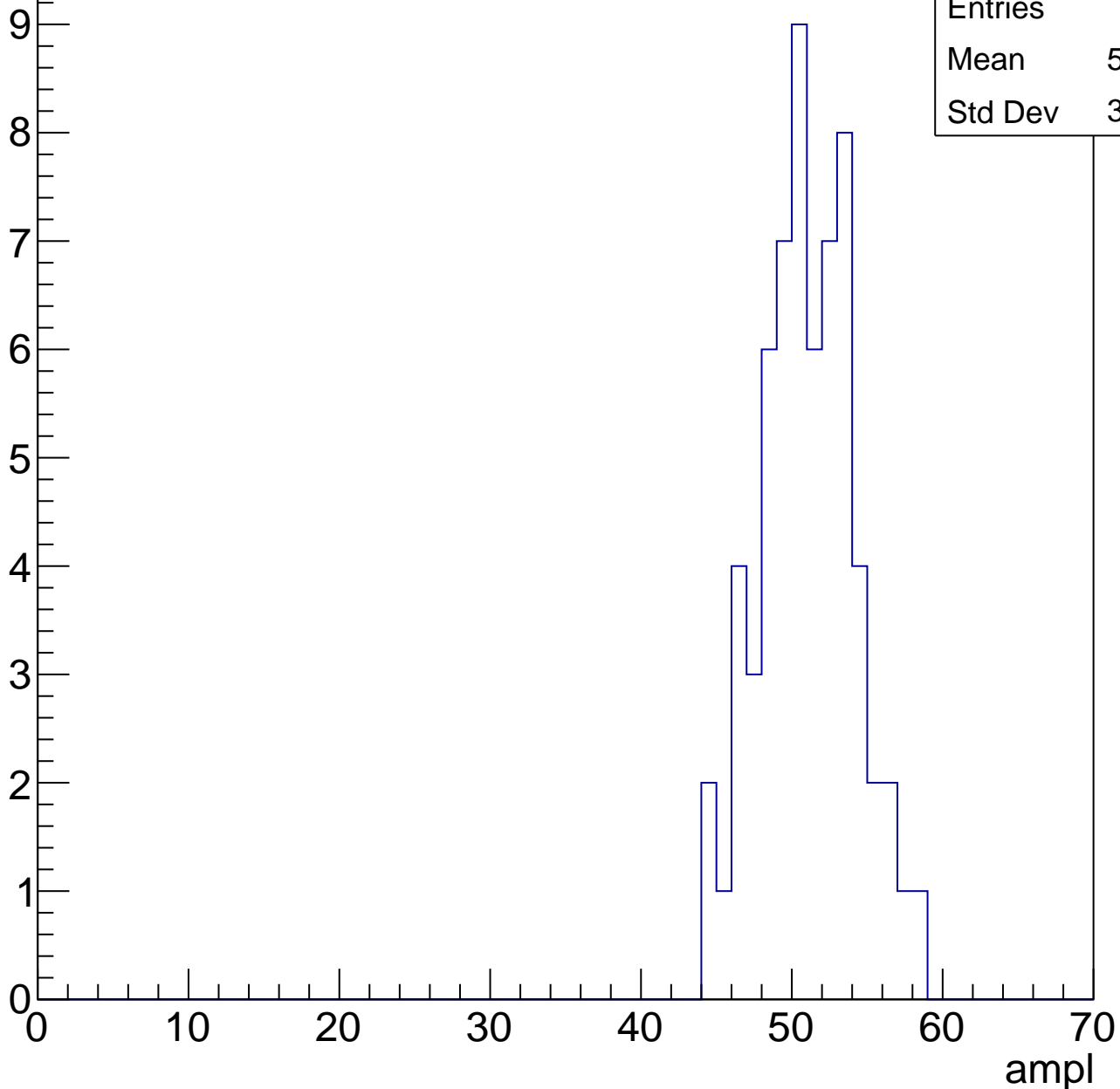


# B0L002S, U1-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 50.57 |
| Std Dev | 3.095 |

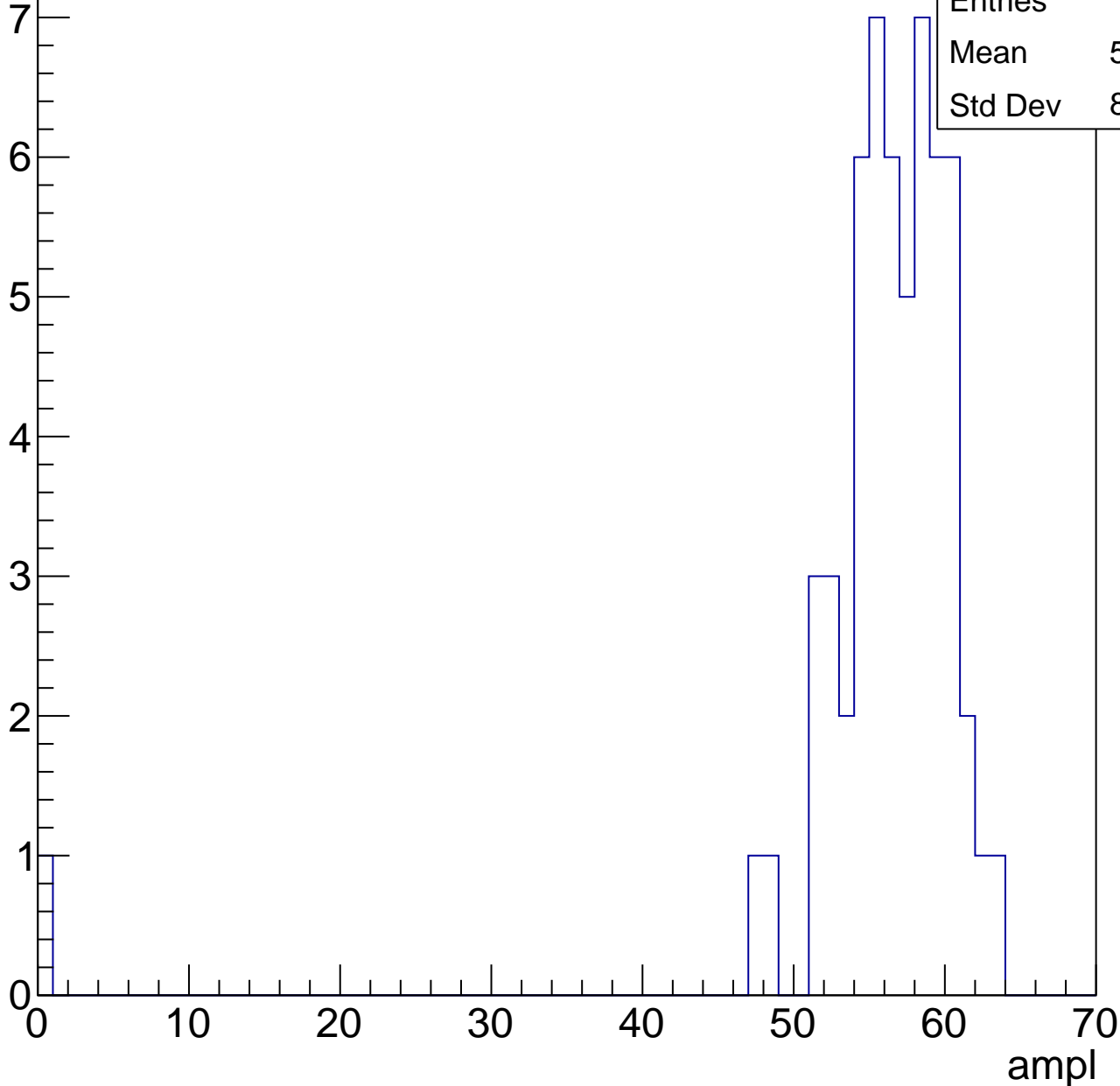


# B0L002S, U1-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 55.29 |
| Std Dev | 8.032 |

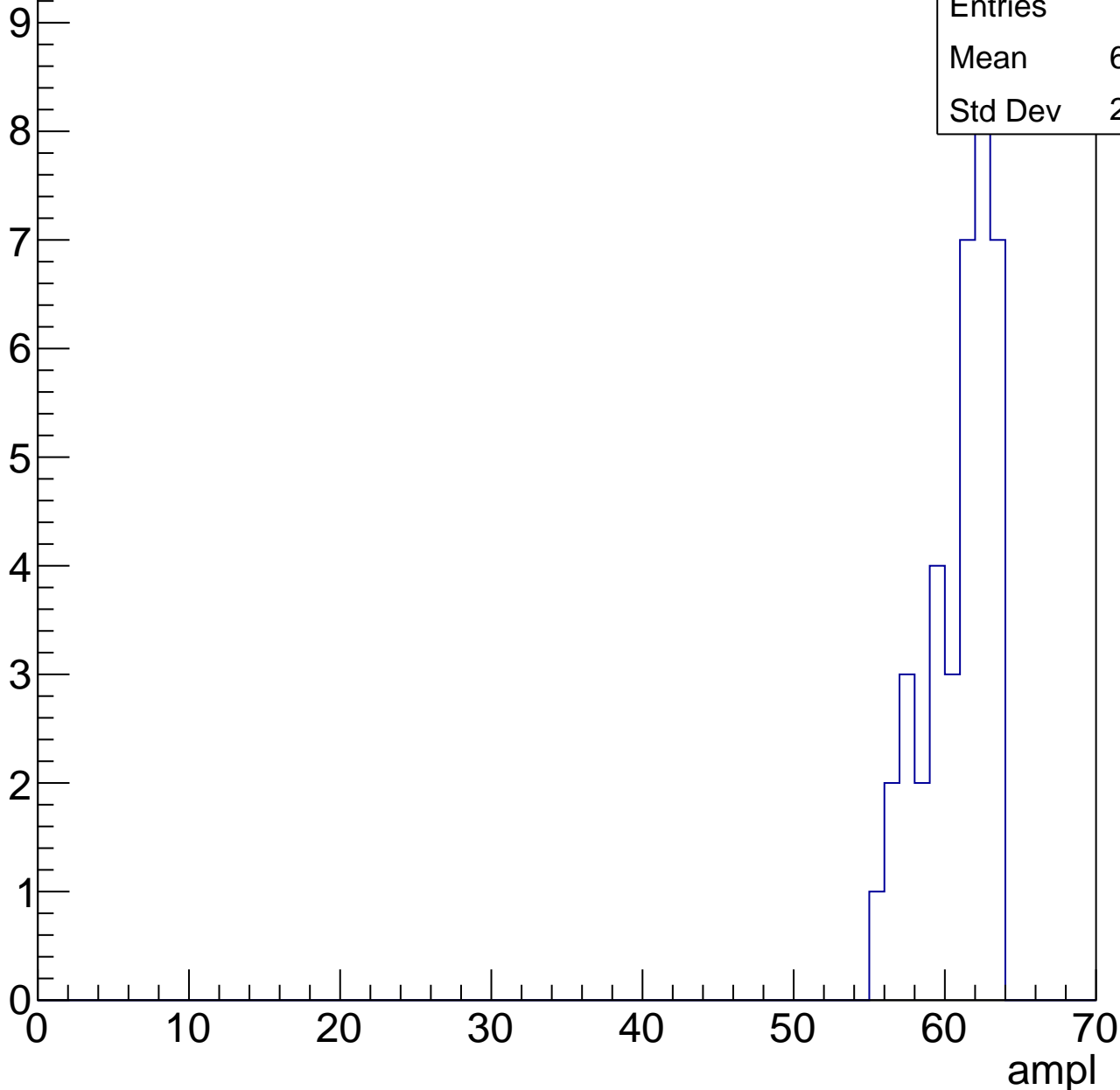


# B0L002S, U1-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

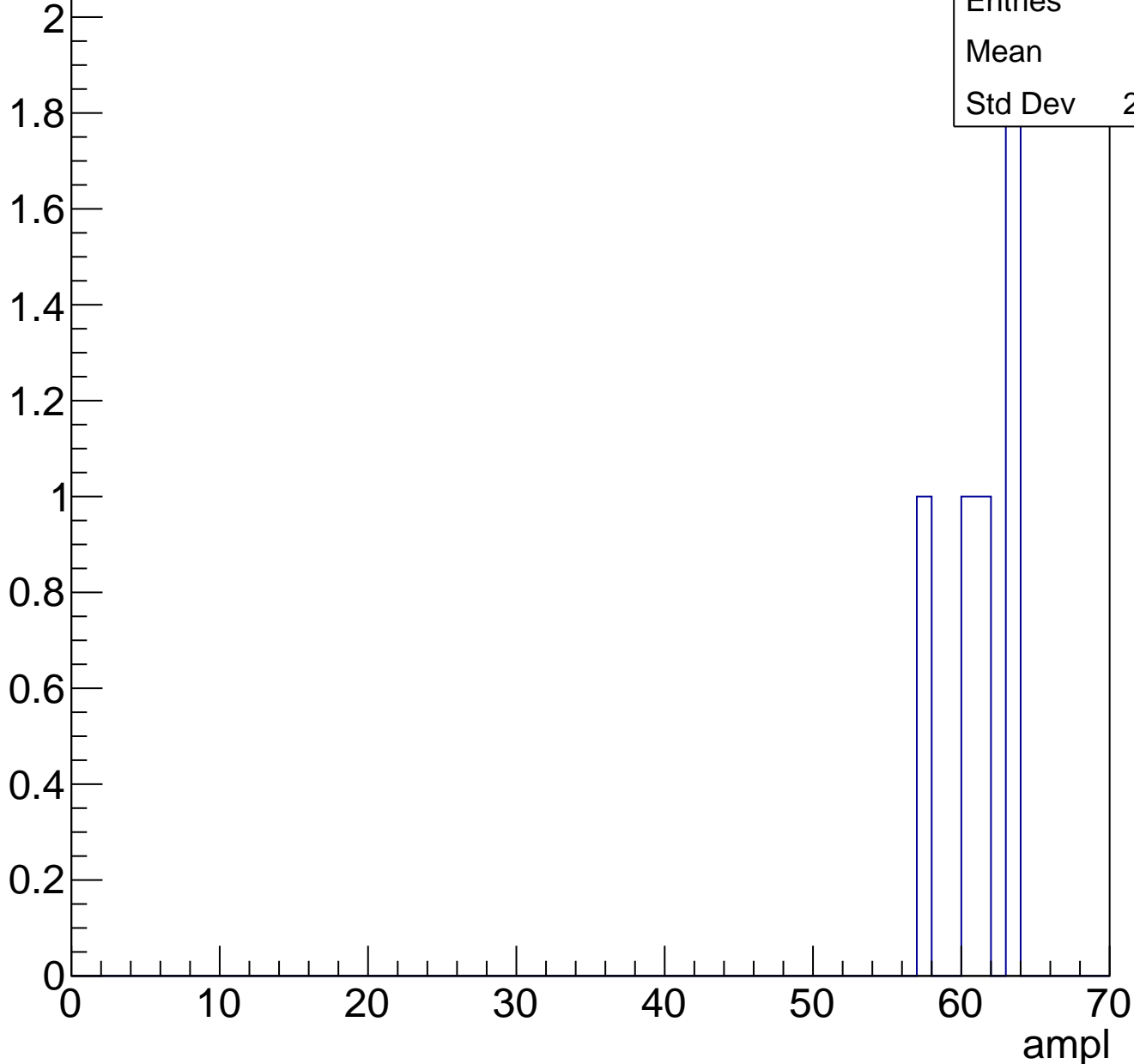
|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 60.42 |
| Std Dev | 2.267 |



# B0L002S, U1-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch99, adc0

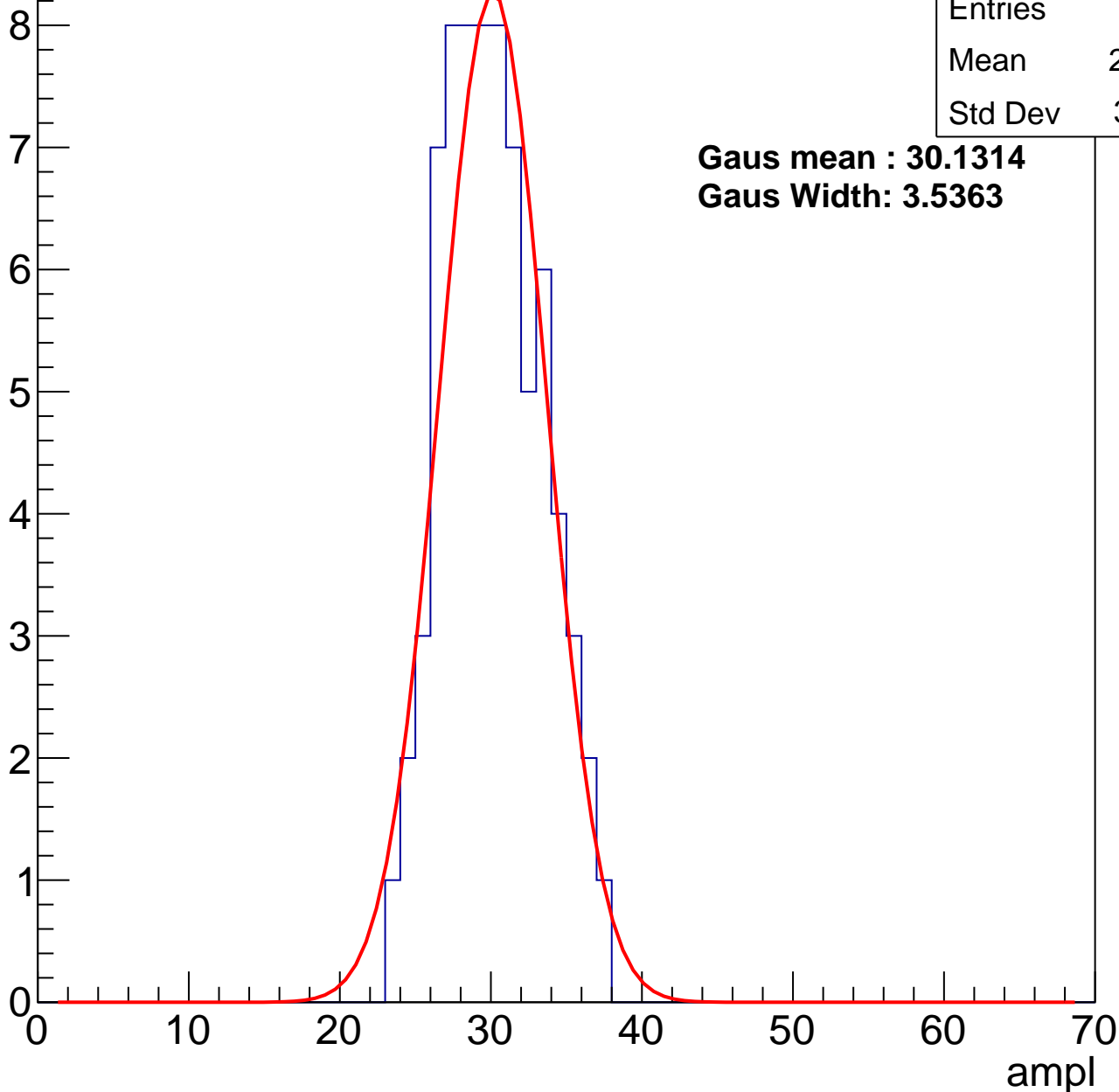
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 29.66 |
| Std Dev | 3.211 |

**Gaus mean : 30.1314**

**Gaus Width: 3.5363**



# B0L002S, U1-ch99, adc1

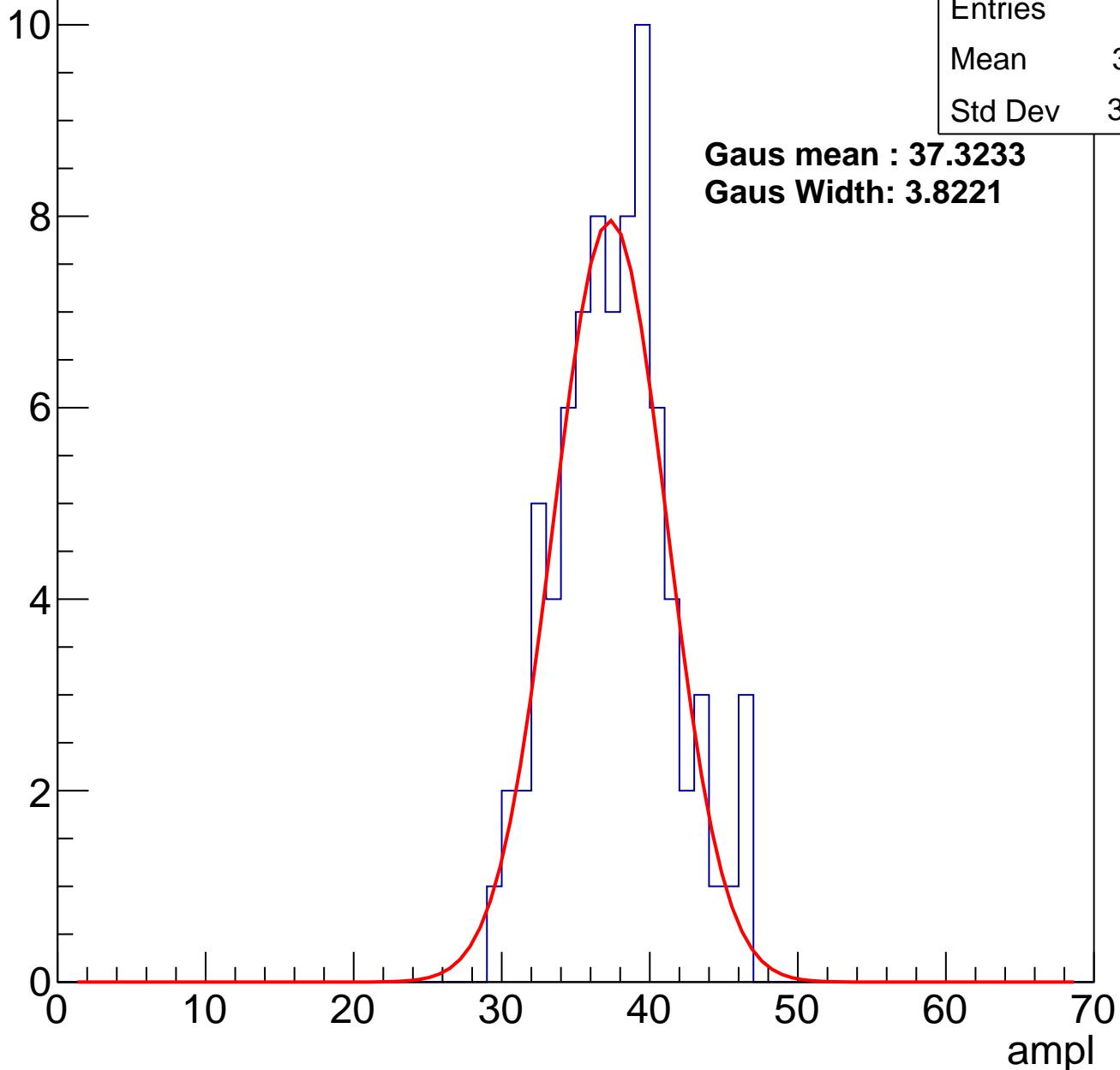
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 37.21 |
| Std Dev | 3.856 |

**Gaus mean : 37.3233**

**Gaus Width: 3.8221**

Entry



# B0L002S, U1-ch99, adc2

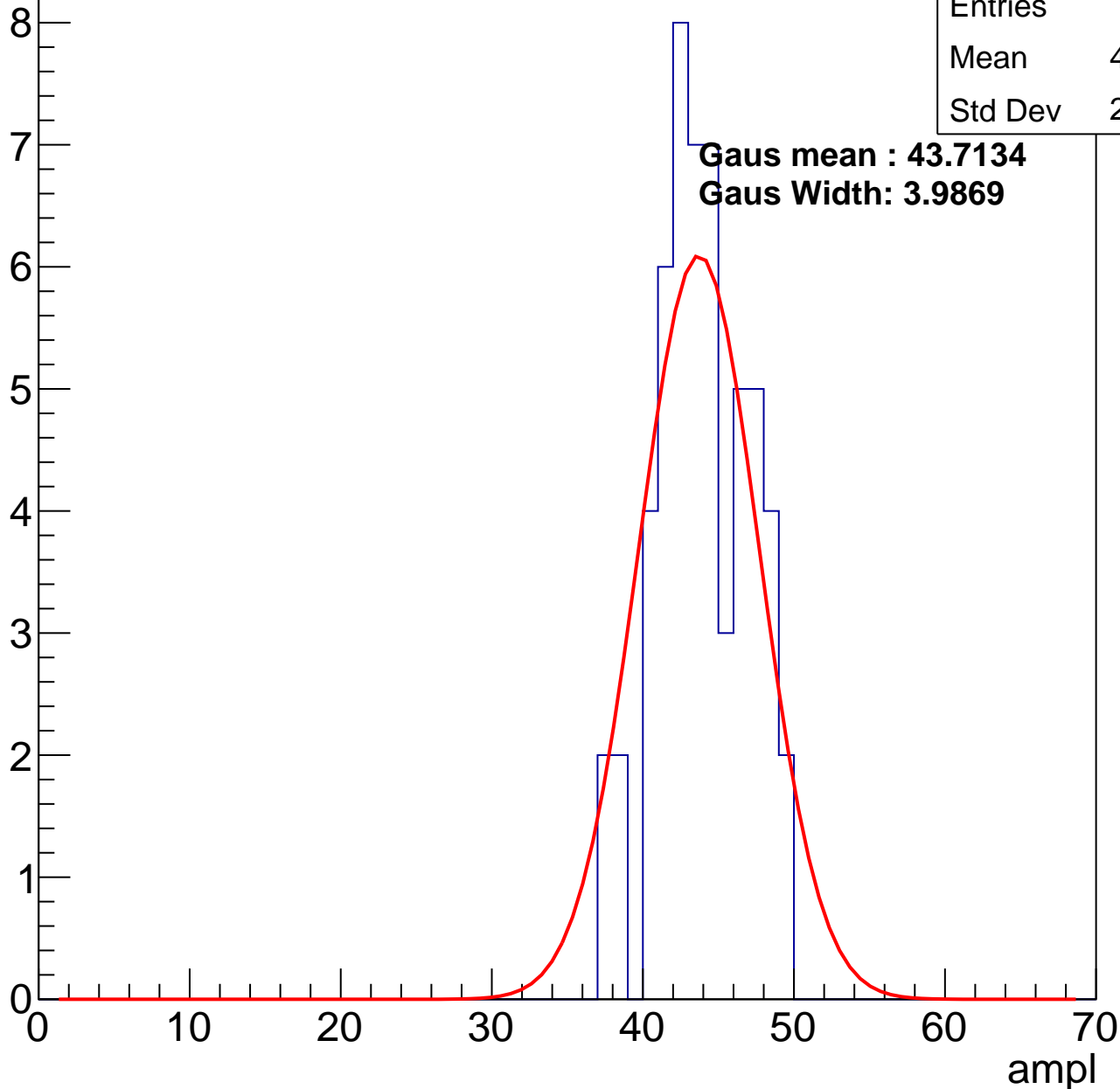
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 55    |
| Mean    | 43.47 |
| Std Dev | 2.996 |

**Gaus mean : 43.7134**

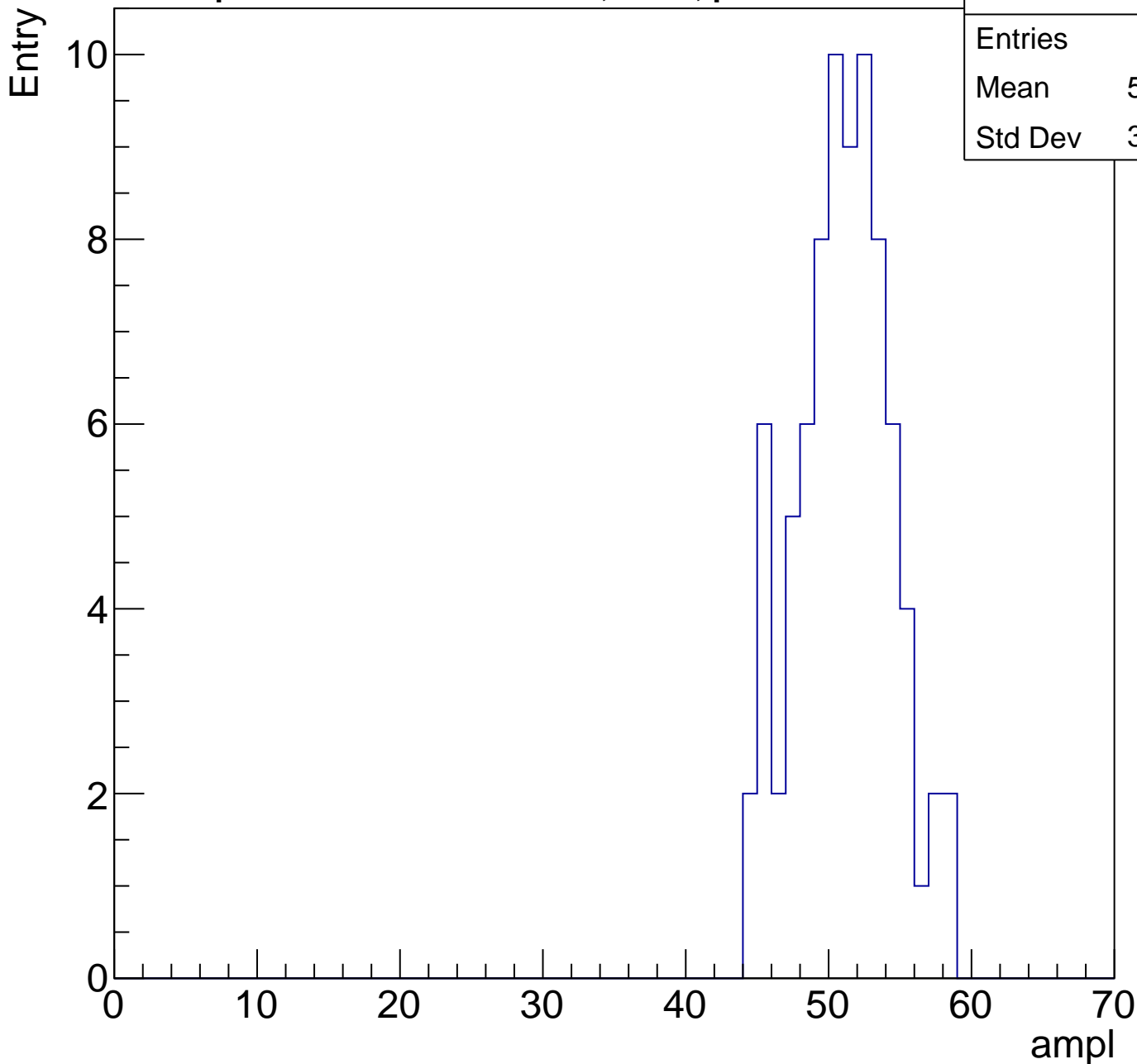
**Gaus Width: 3.9869**



# B0L002S, U1-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 50.59 |
| Std Dev | 3.299 |

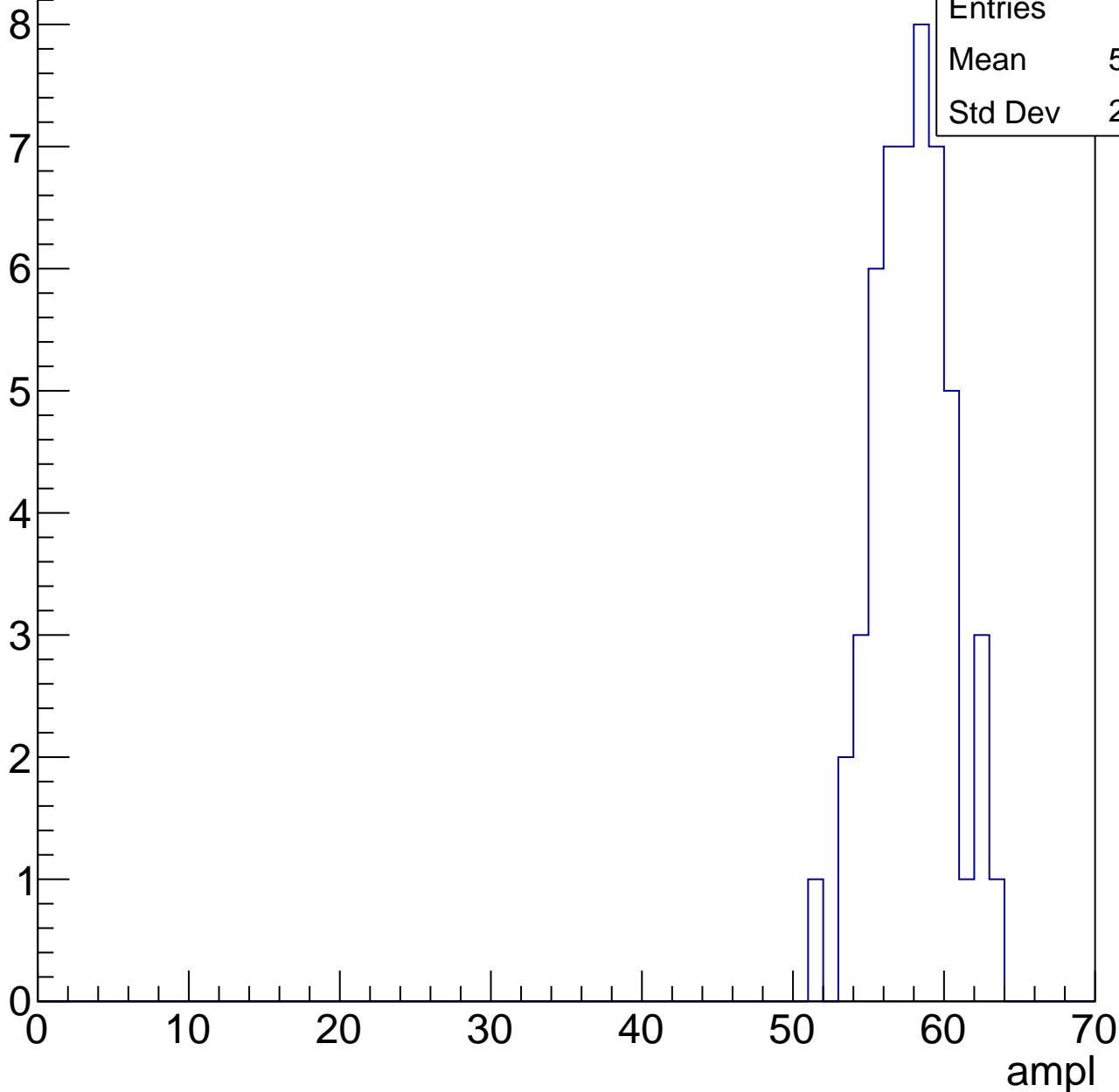


# B0L002S, U1-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

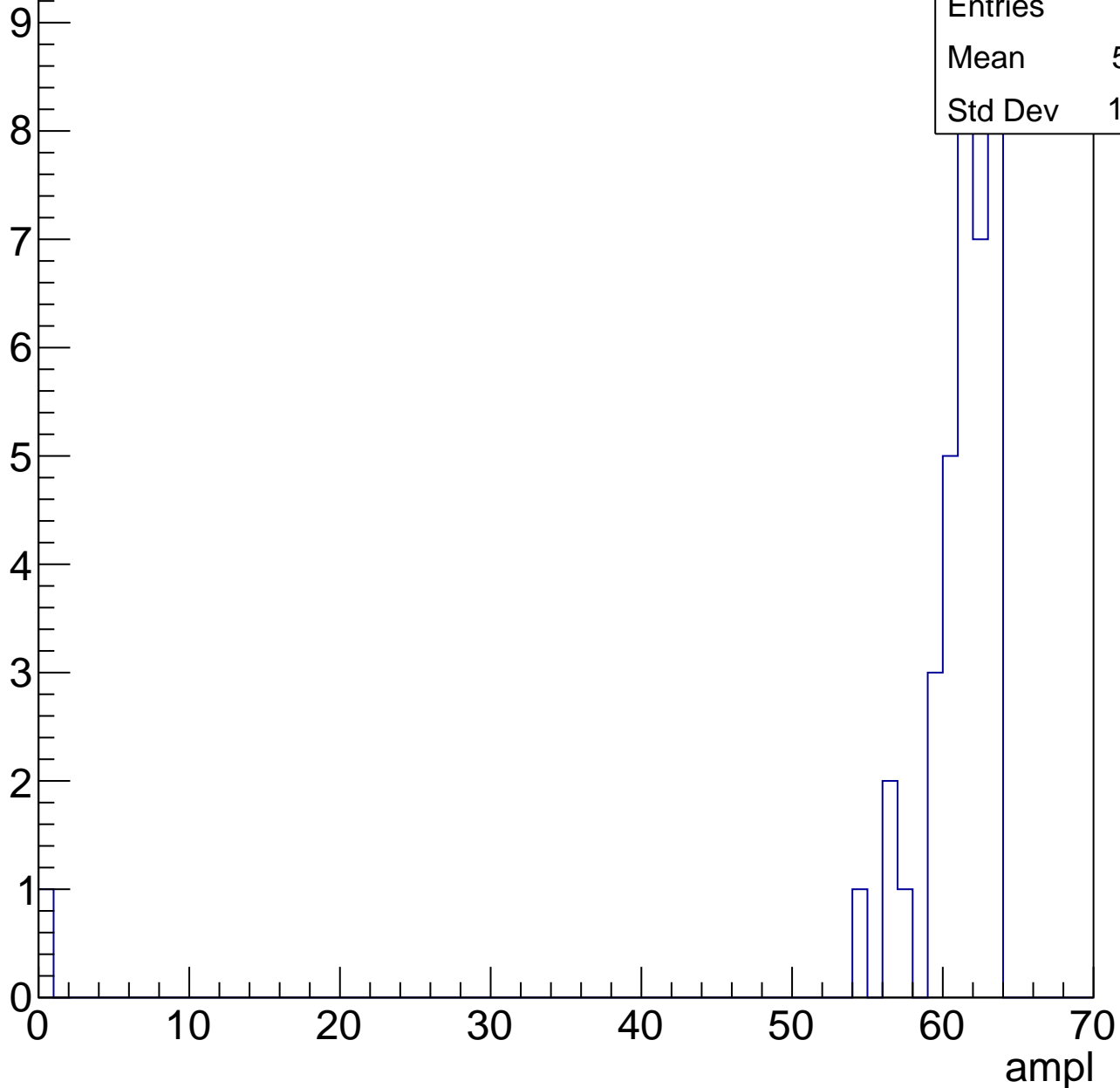
|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 57.39 |
| Std Dev | 2.529 |



# B0L002S, U1-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 29.59 |
| Std Dev | 3.561 |

**Gaus mean : 30.1441**

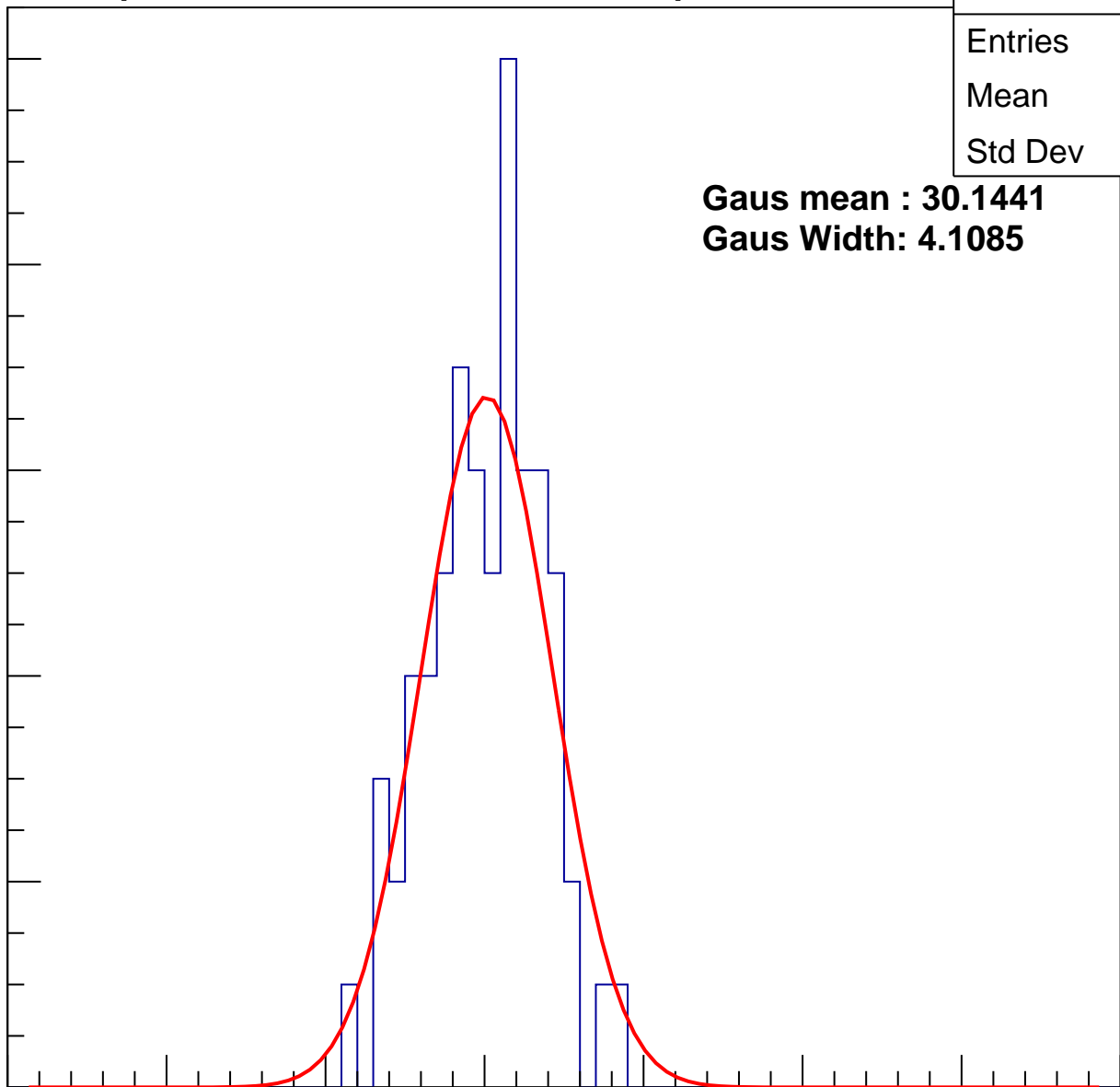
**Gaus Width: 4.1085**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch100, adc1

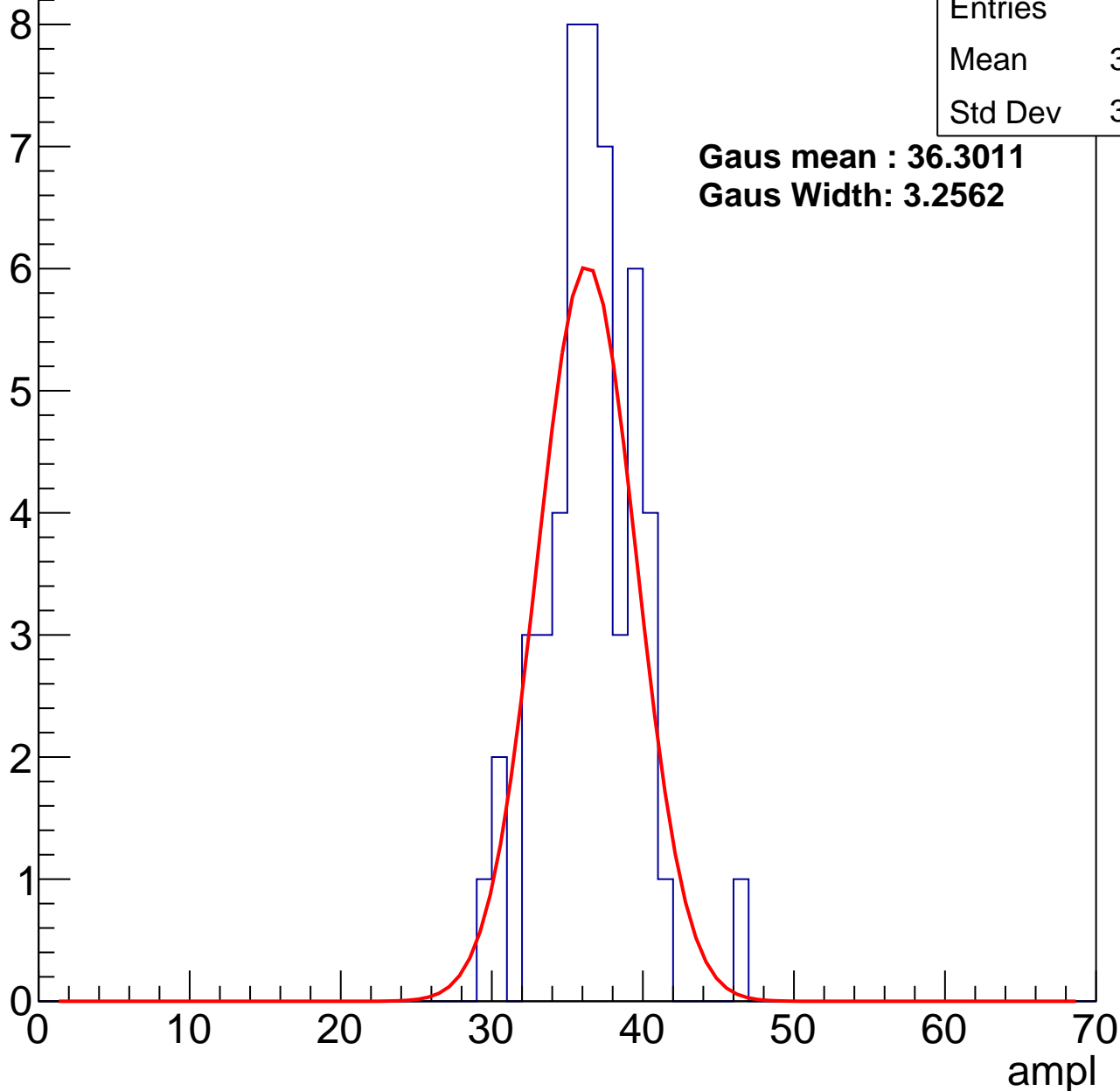
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 36.12 |
| Std Dev | 3.072 |

**Gaus mean : 36.3011**

**Gaus Width: 3.2562**



# B0L002S, U1-ch100, adc2

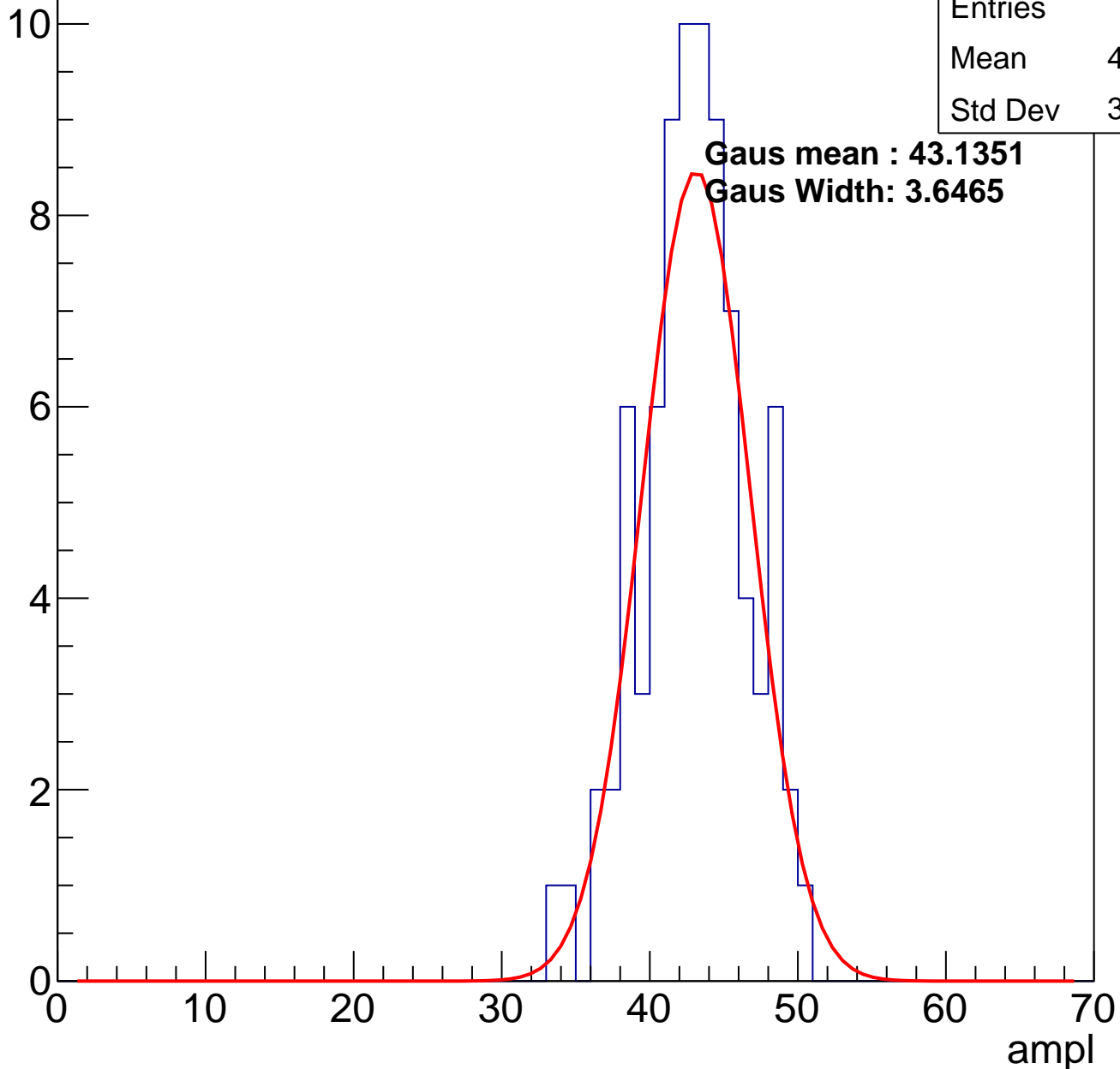
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 42.55 |
| Std Dev | 3.534 |

**Gaus mean : 43.1351**

**Gaus Width: 3.6465**

Entry

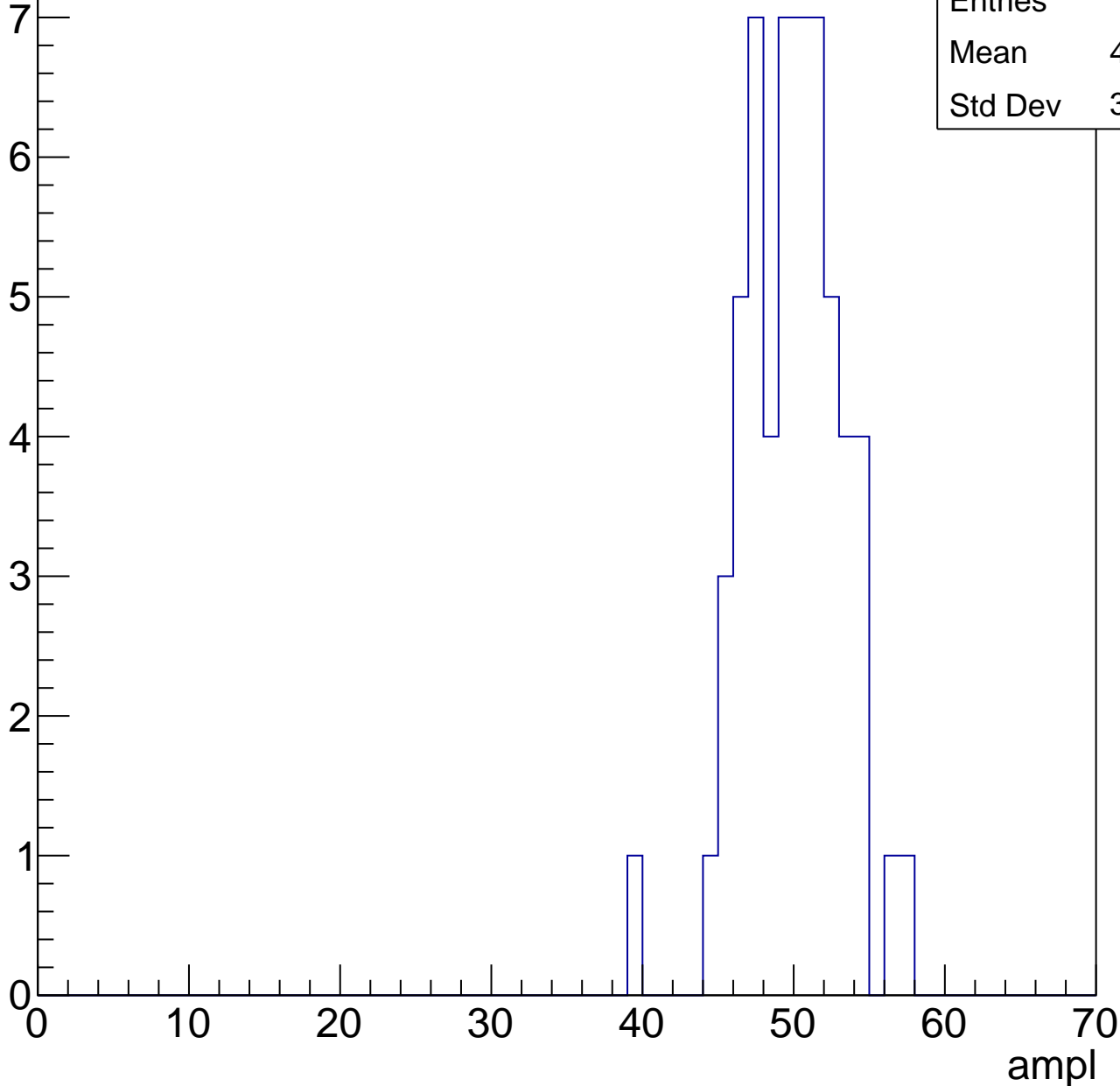


# B0L002S, U1-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 49.47 |
| Std Dev | 3.229 |

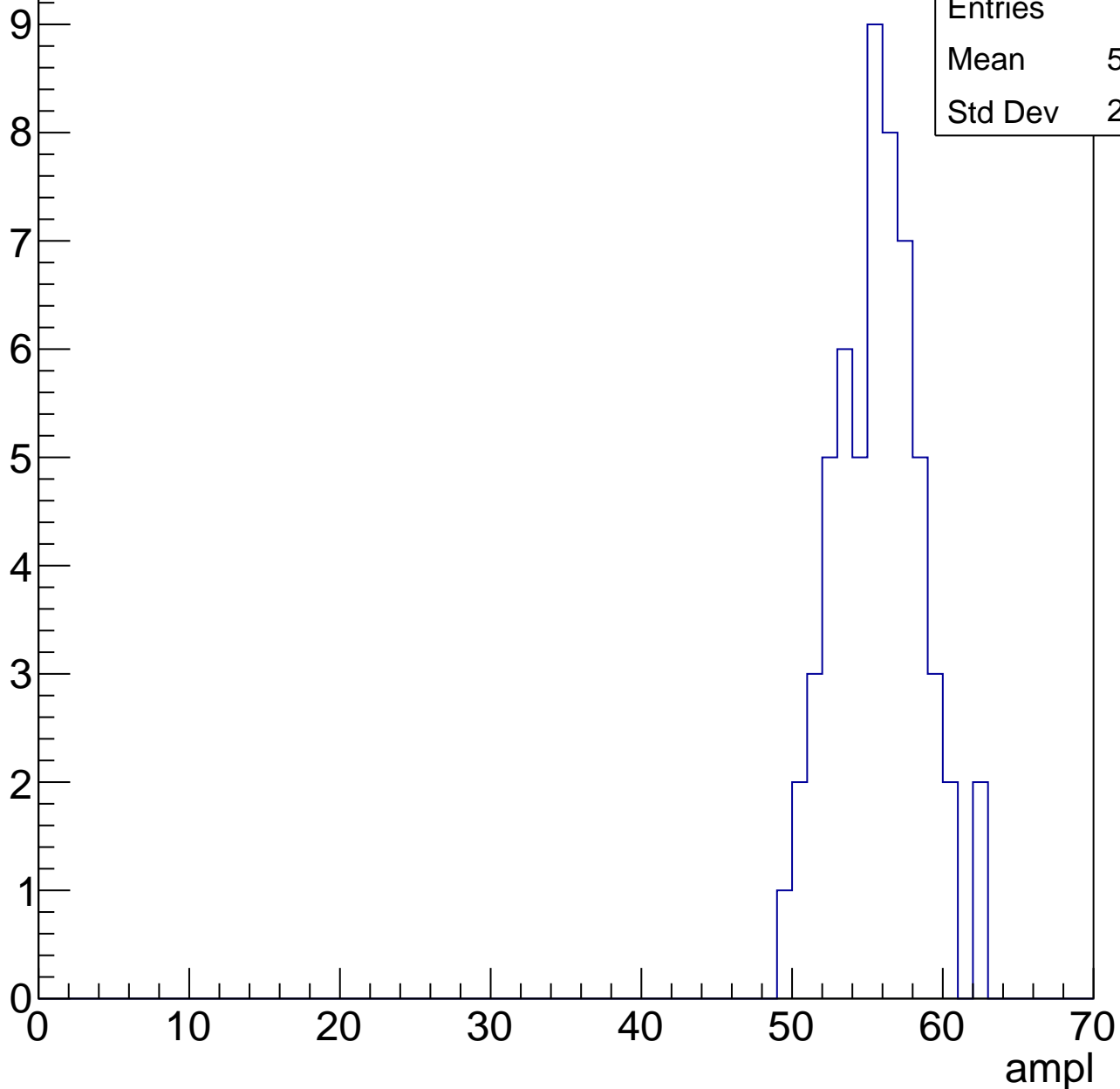


# B0L002S, U1-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 55.22 |
| Std Dev | 2.883 |

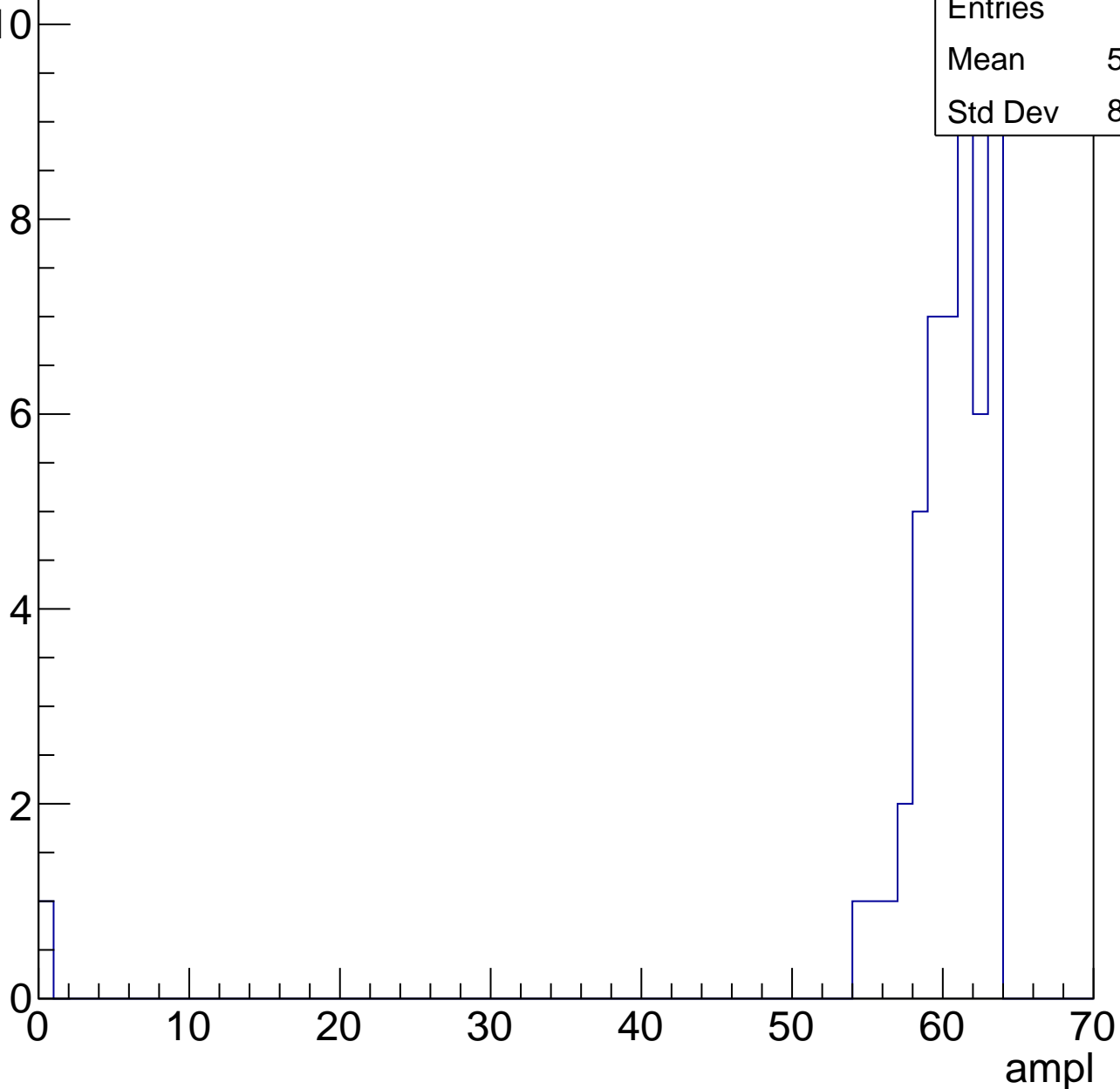


# B0L002S, U1-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 59.06 |
| Std Dev | 8.719 |



# B0L002S, U1-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

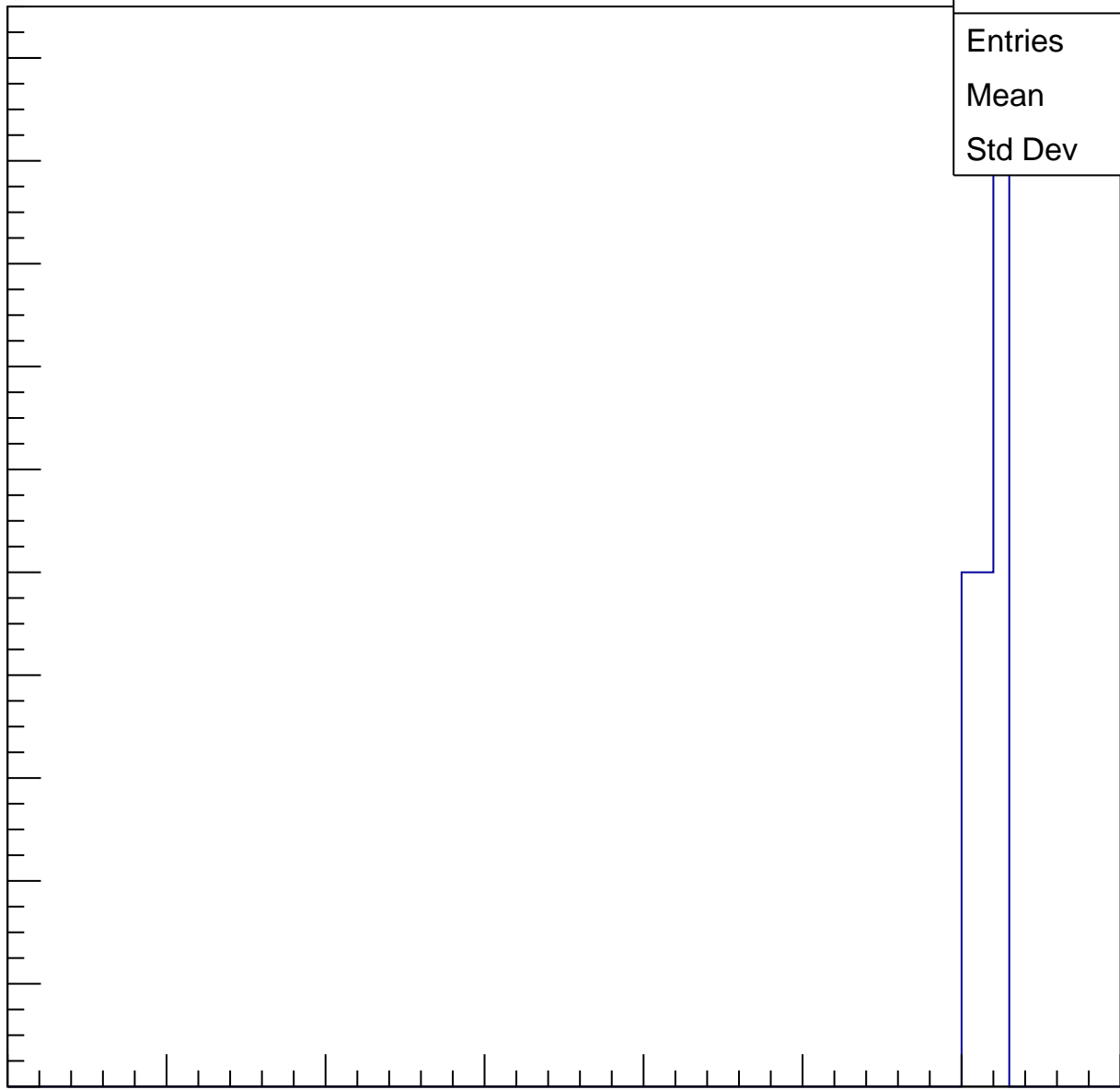
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 4      |
| Mean    | 61.25  |
| Std Dev | 0.8292 |

ampl

0 10 20 30 40 50 60 70





# B0L002S, U1-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 30.04 |
| Std Dev | 4.877 |

**Gaus mean : 31.2911**

**Gaus Width: 3.1969**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

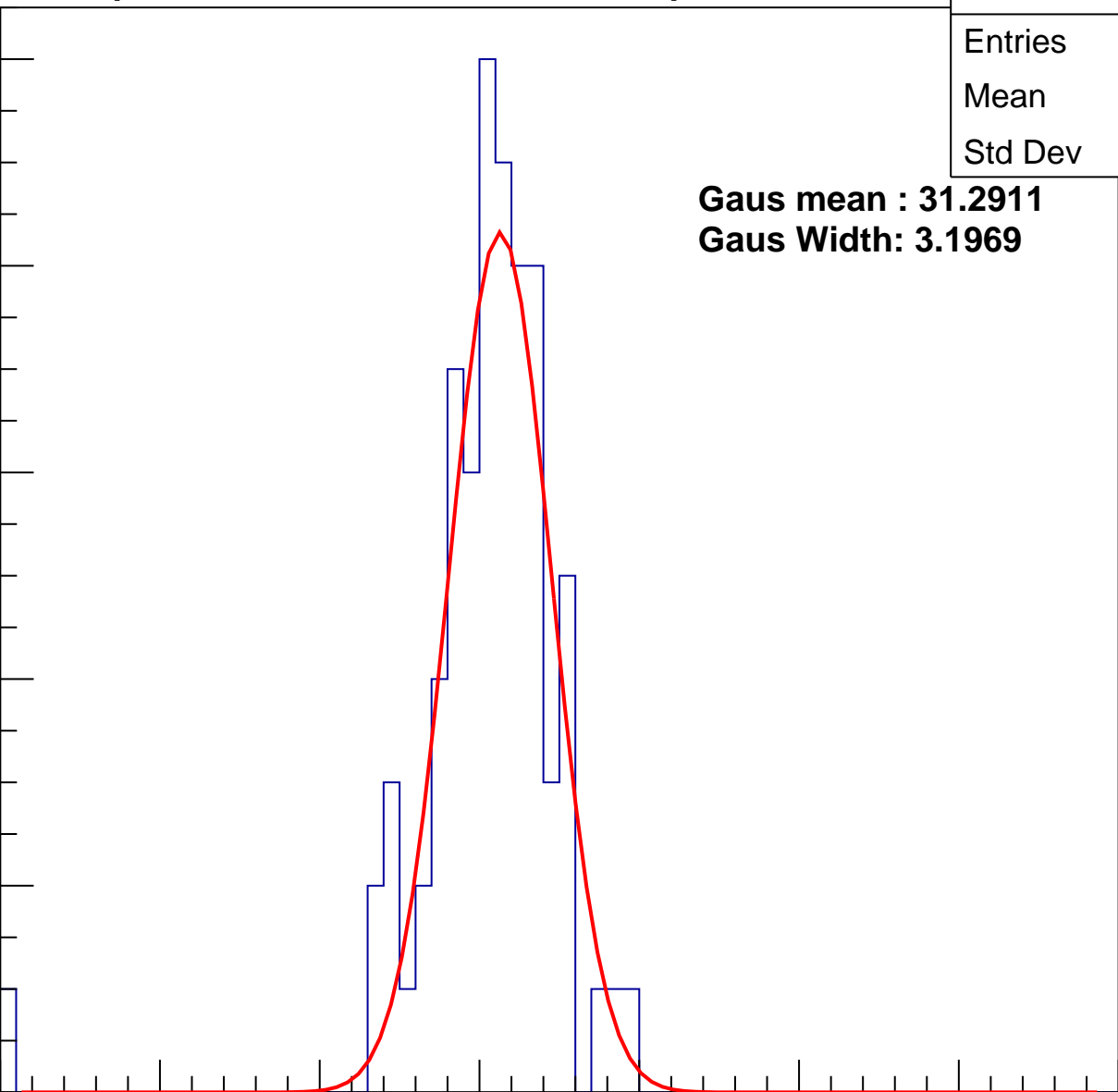
30

40

50

60

70



# B0L002S, U1-ch101, adc1

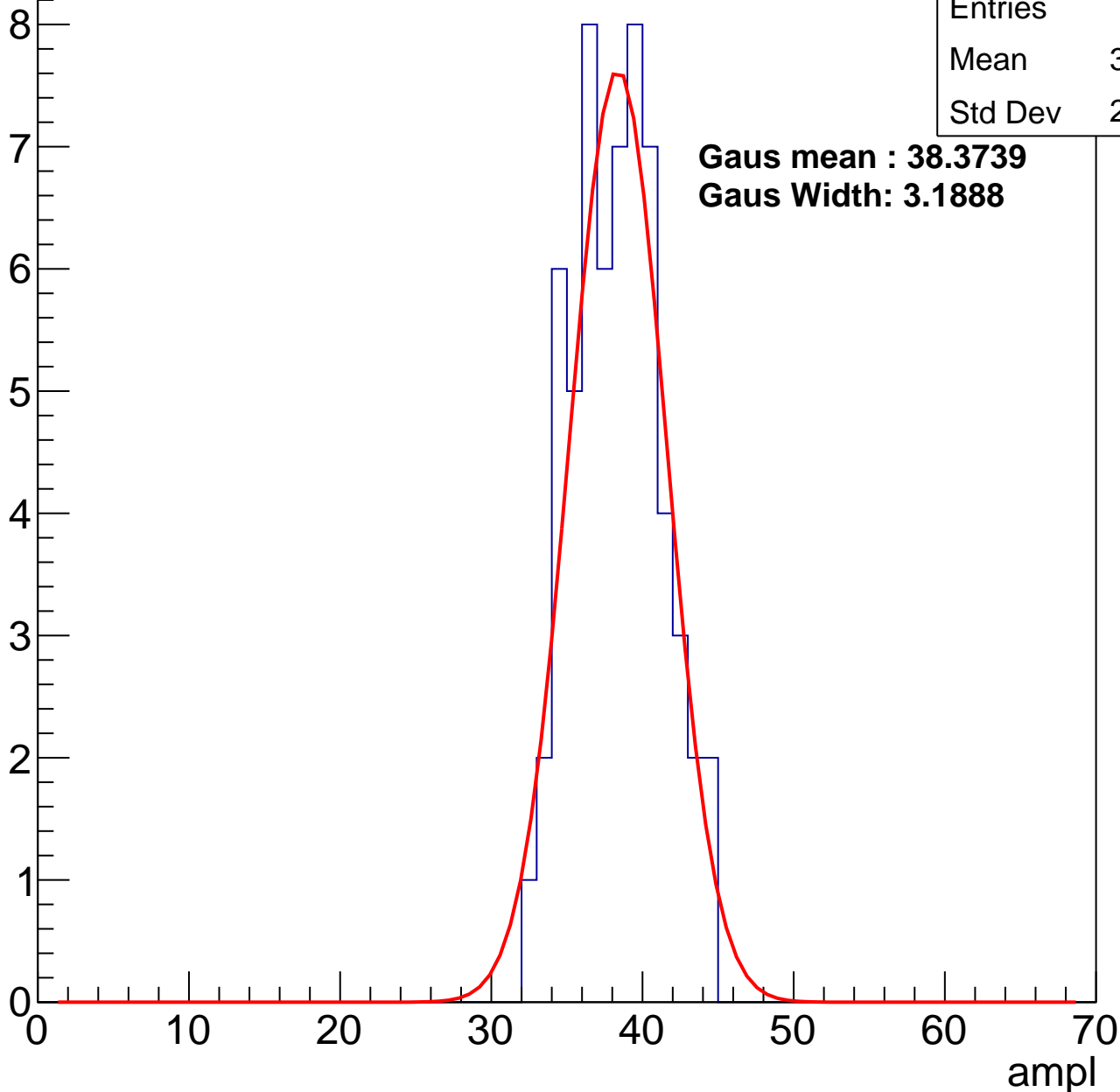
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 37.85 |
| Std Dev | 2.879 |

**Gaus mean : 38.3739**

**Gaus Width: 3.1888**

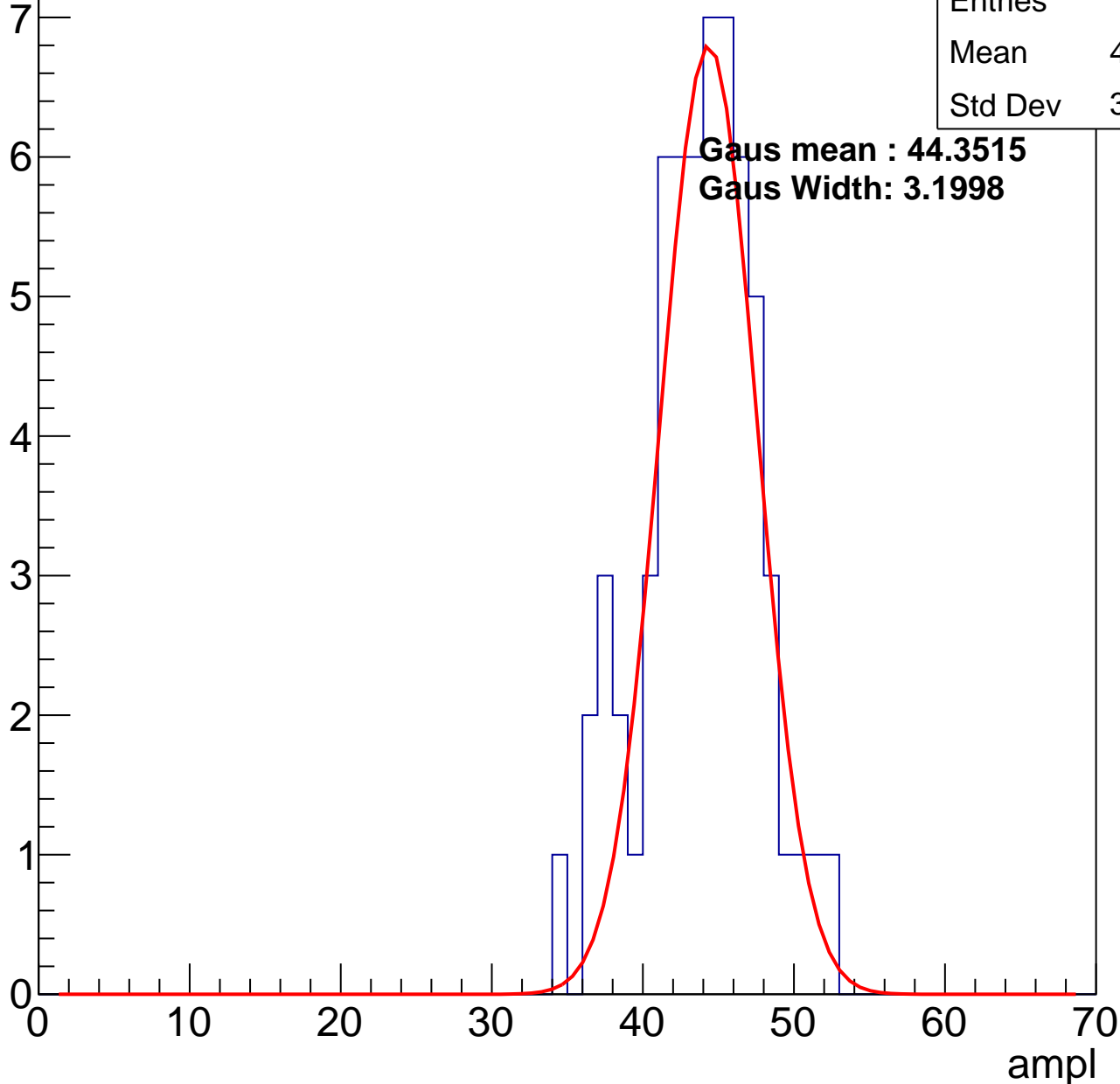


# B0L002S, U1-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 43.35 |
| Std Dev | 3.759 |

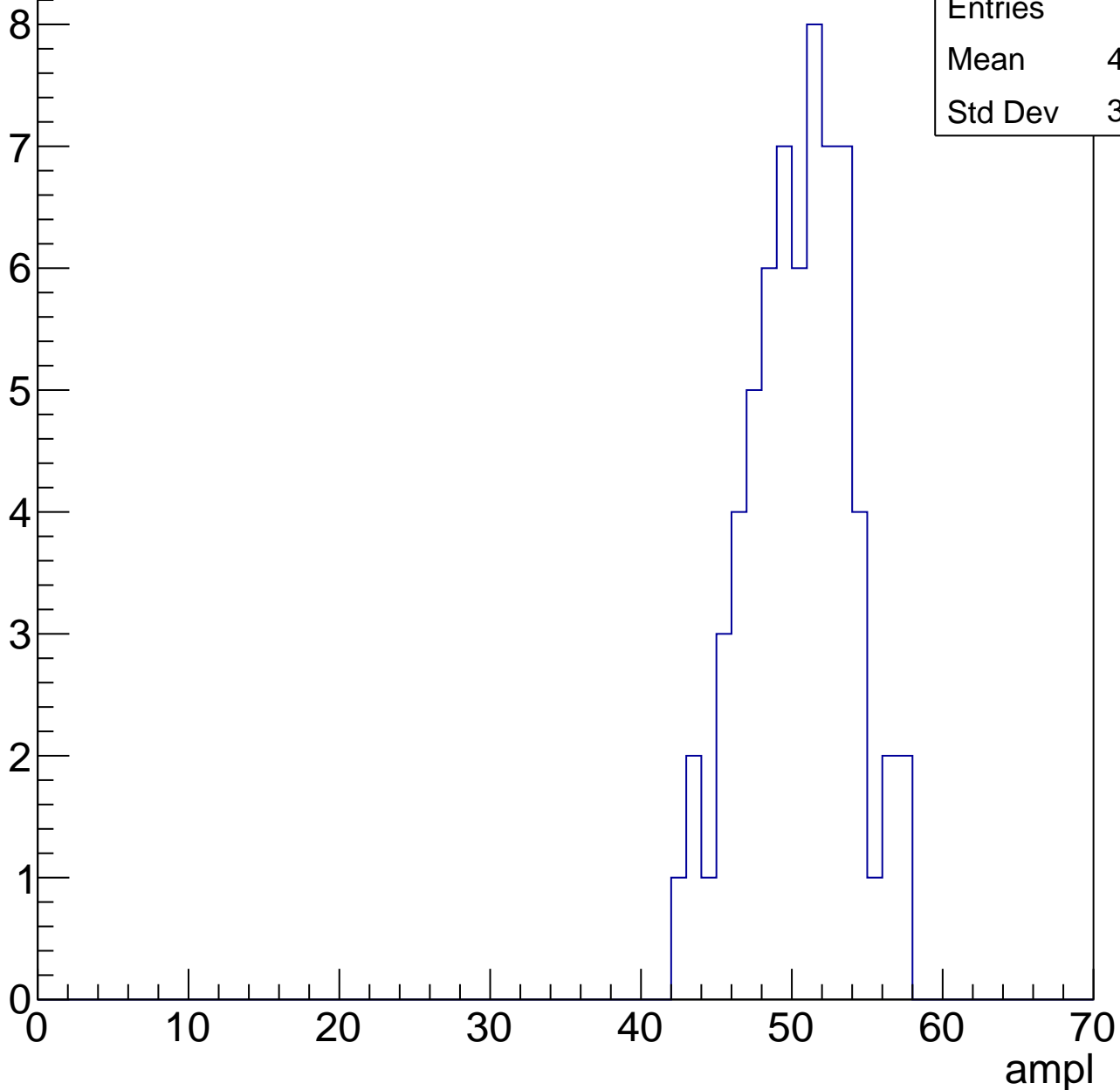


# B0L002S, U1-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 49.95 |
| Std Dev | 3.418 |

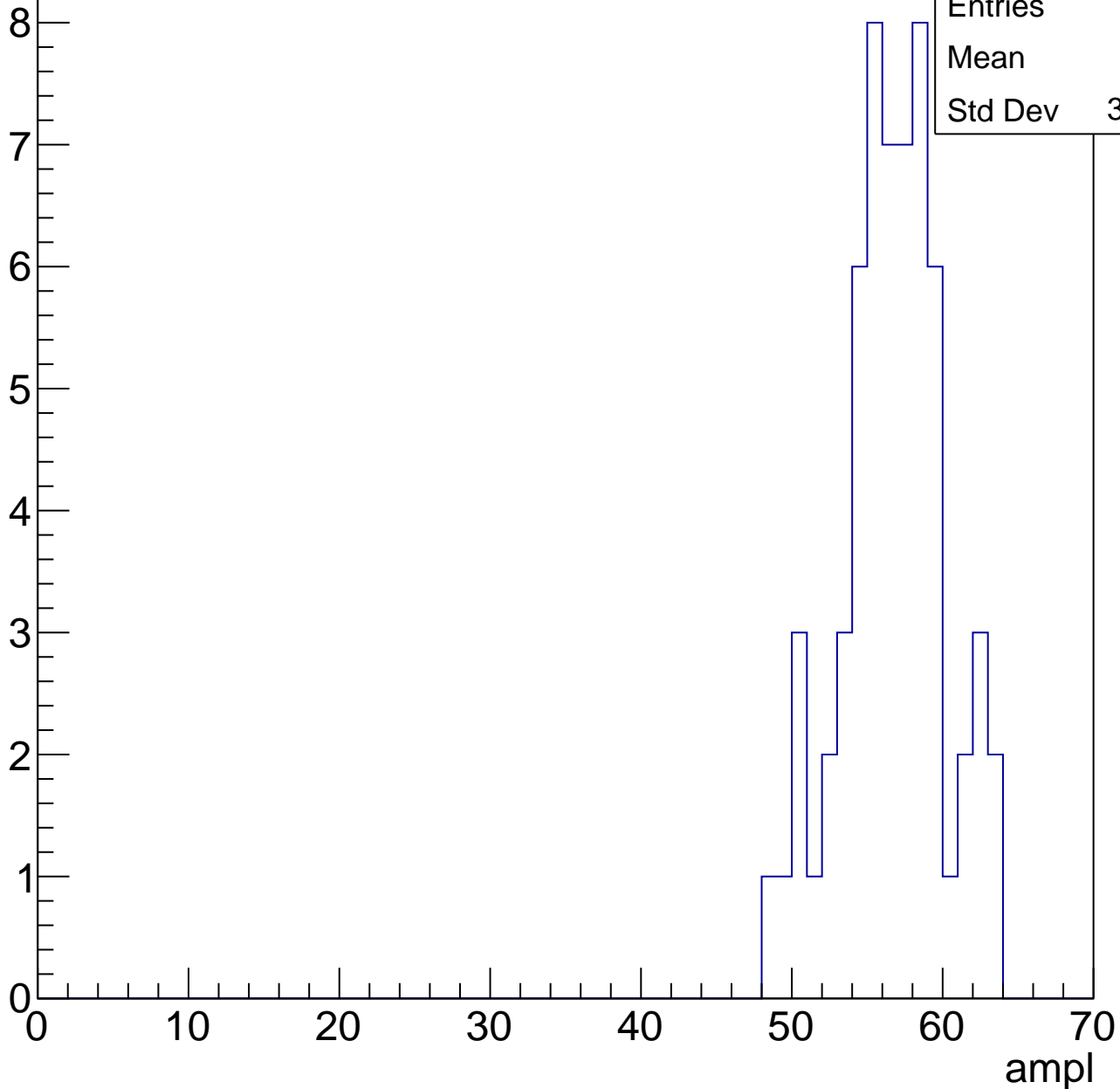


# B0L002S, U1-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

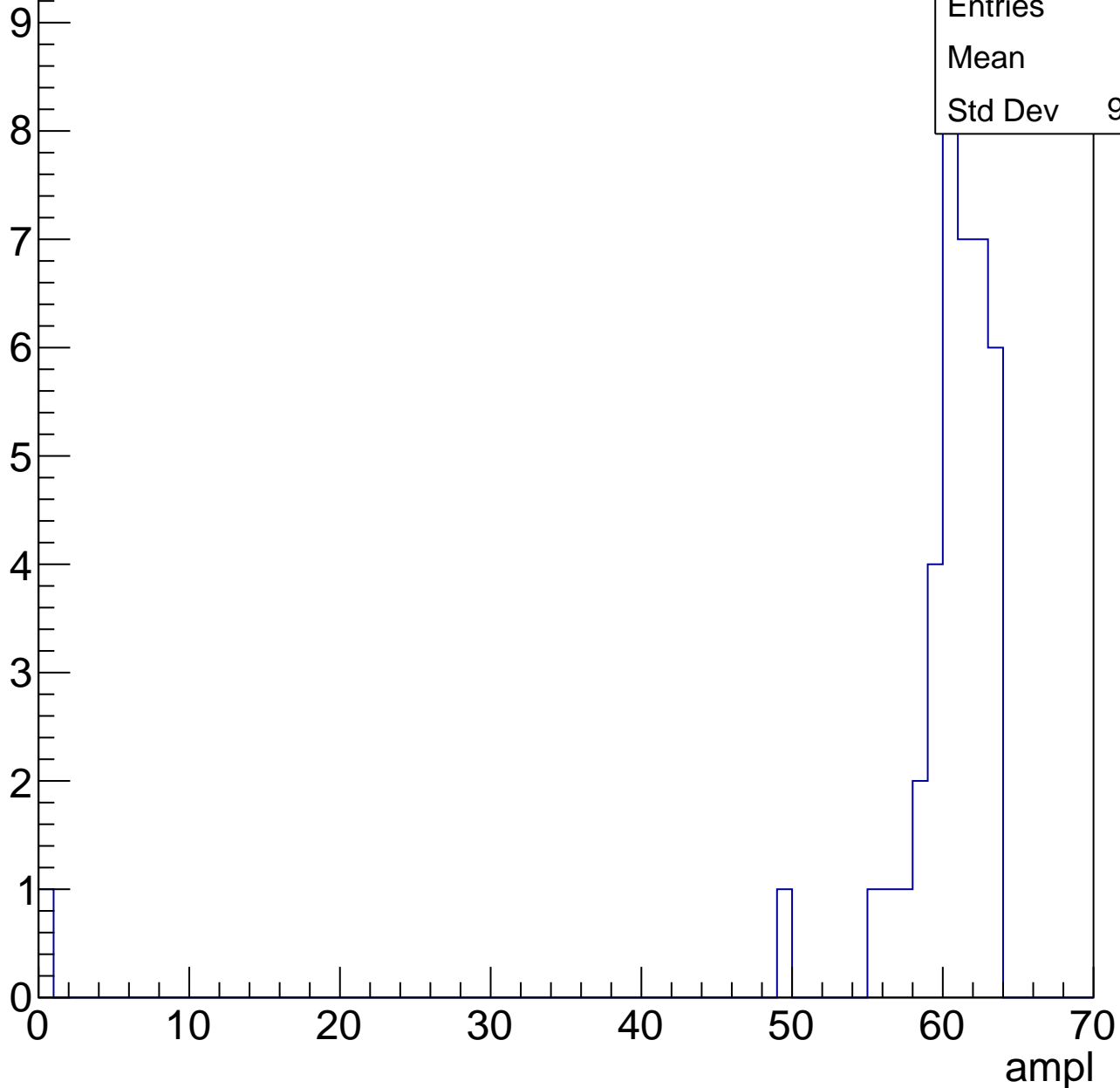
|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 56.2  |
| Std Dev | 3.387 |



# B0L002S, U1-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

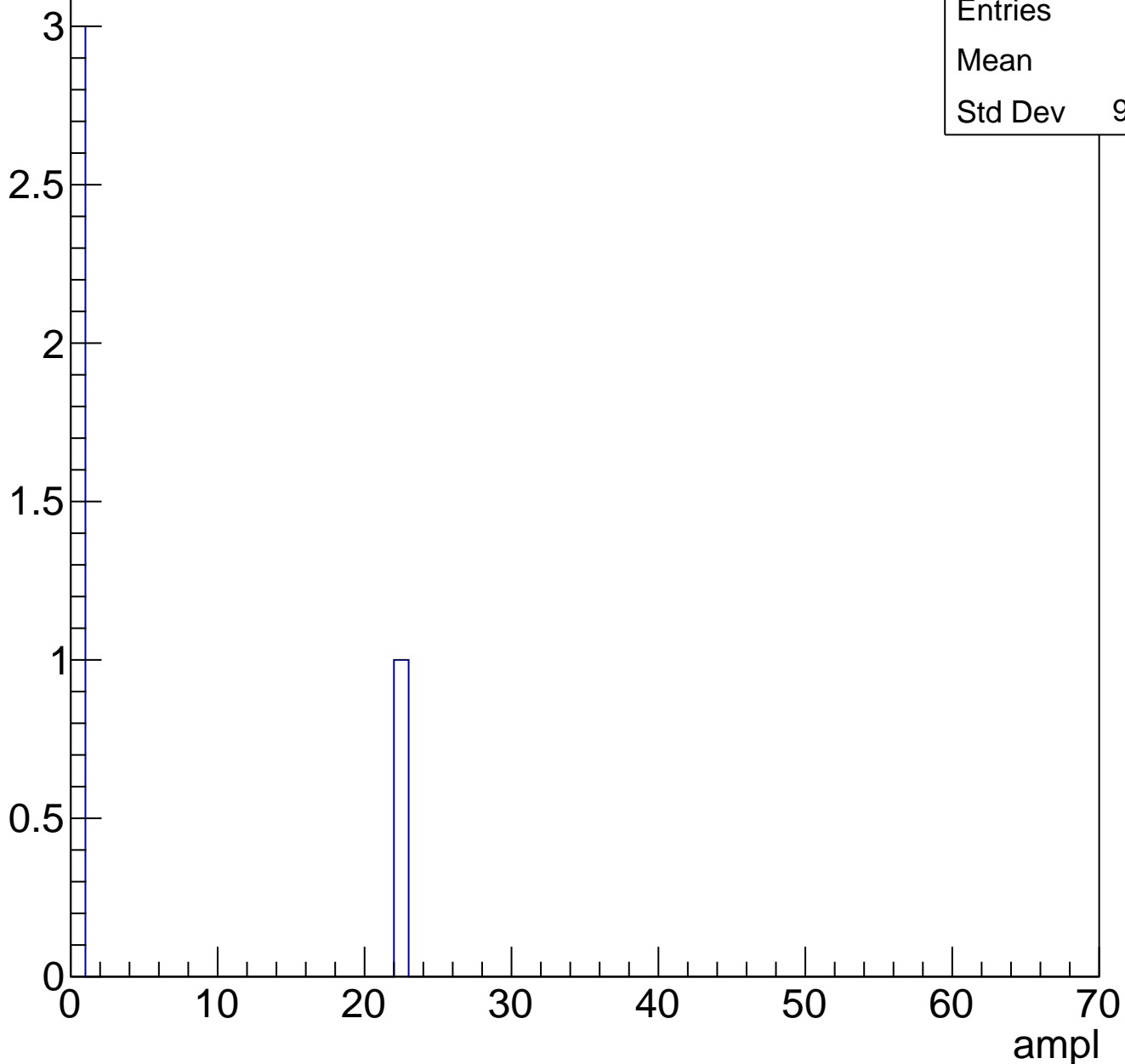




# B0L002S, U1-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 5.5   |
| Std Dev | 9.526 |

# B0L002S, U1-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 76    |
| Mean    | 30.55 |
| Std Dev | 6.04  |

**Gaus mean : 31.7361**

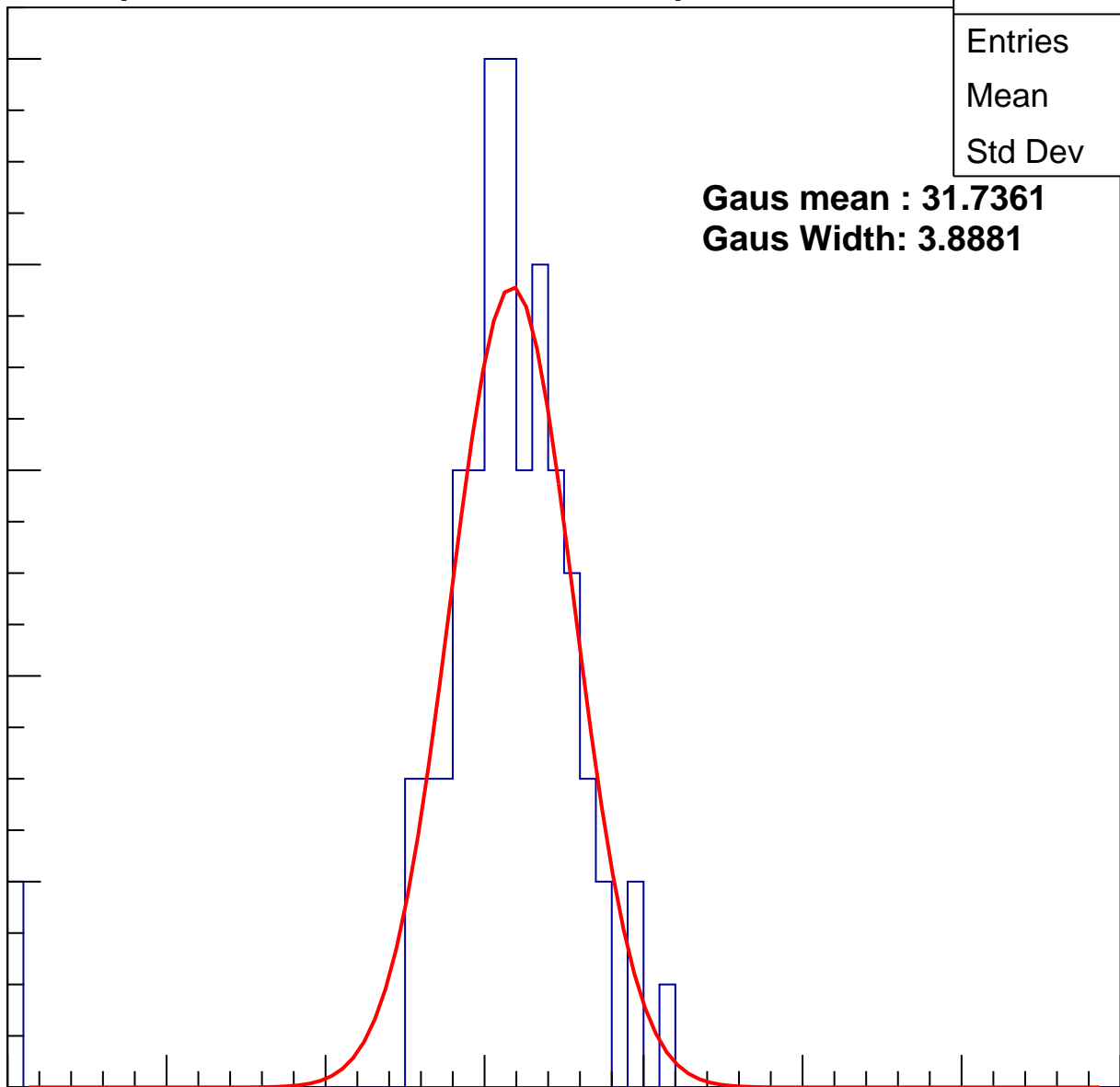
**Gaus Width: 3.8881**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



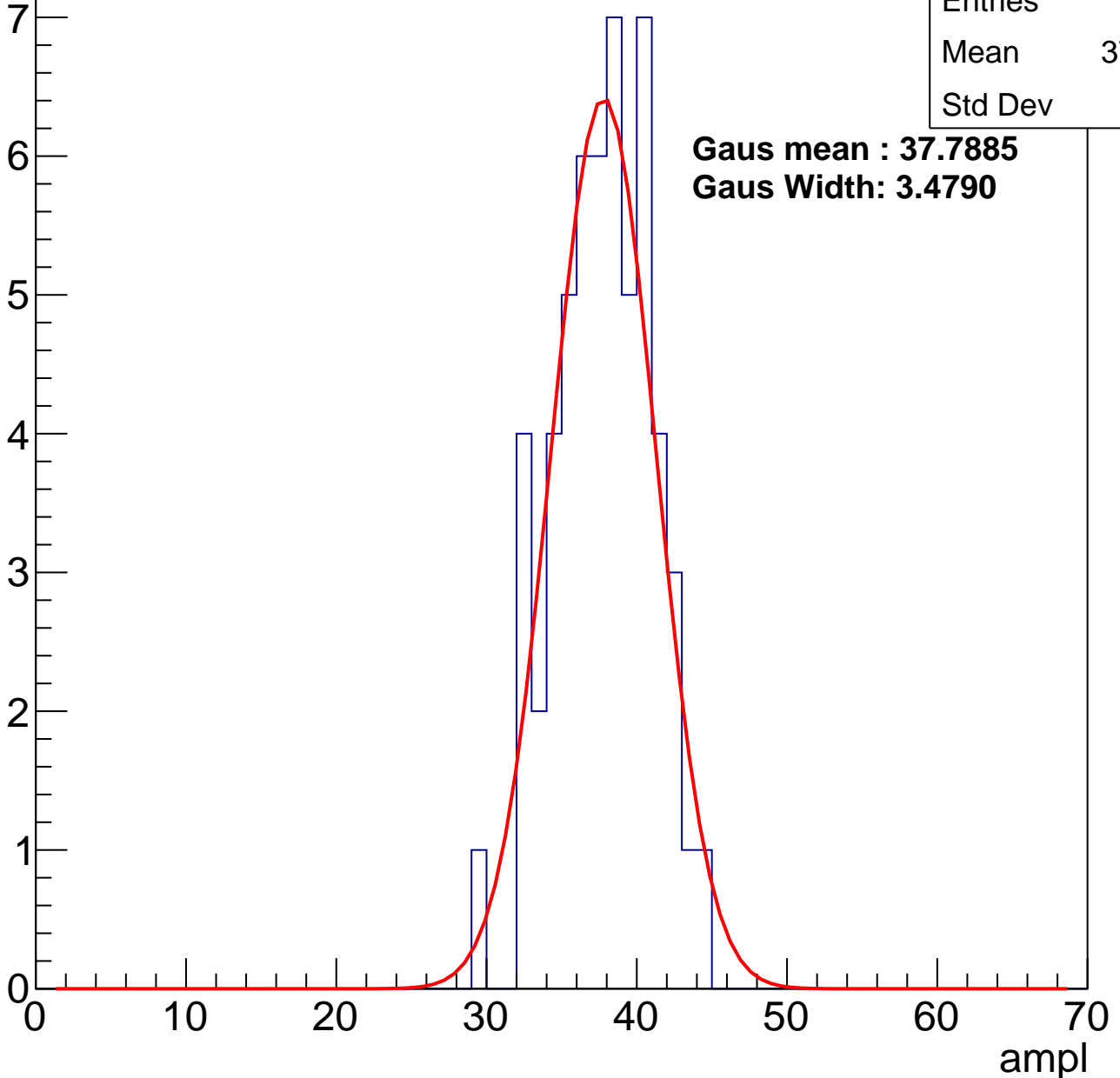
# B0L002S, U1-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 37.32 |
| Std Dev | 3.18  |

**Gaus mean : 37.7885**  
**Gaus Width: 3.4790**



# B0L002S, U1-ch102, adc2

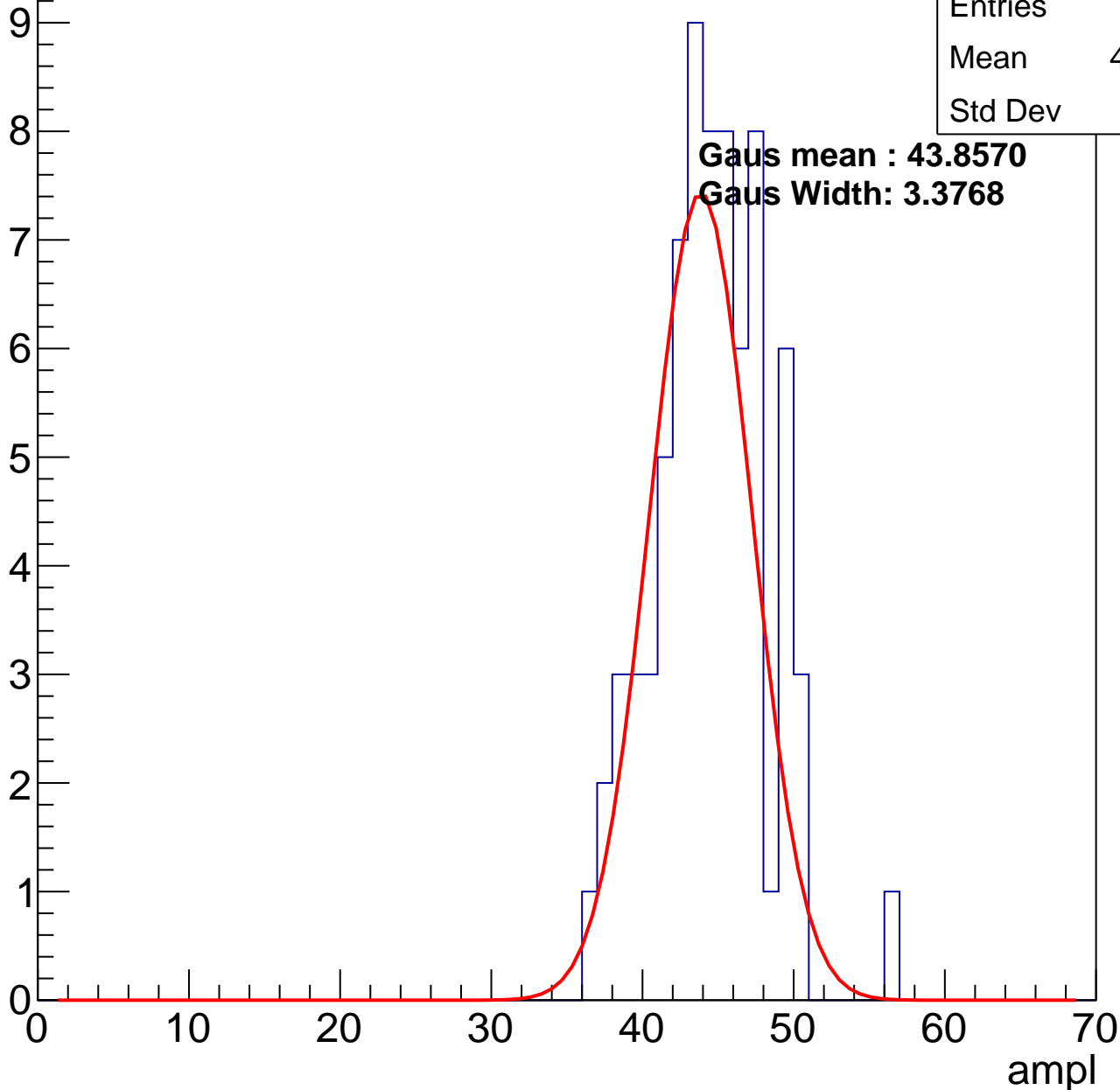
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 44.04 |
| Std Dev | 3.67  |

**Gaus mean : 43.8570**

**Gaus Width: 3.3768**

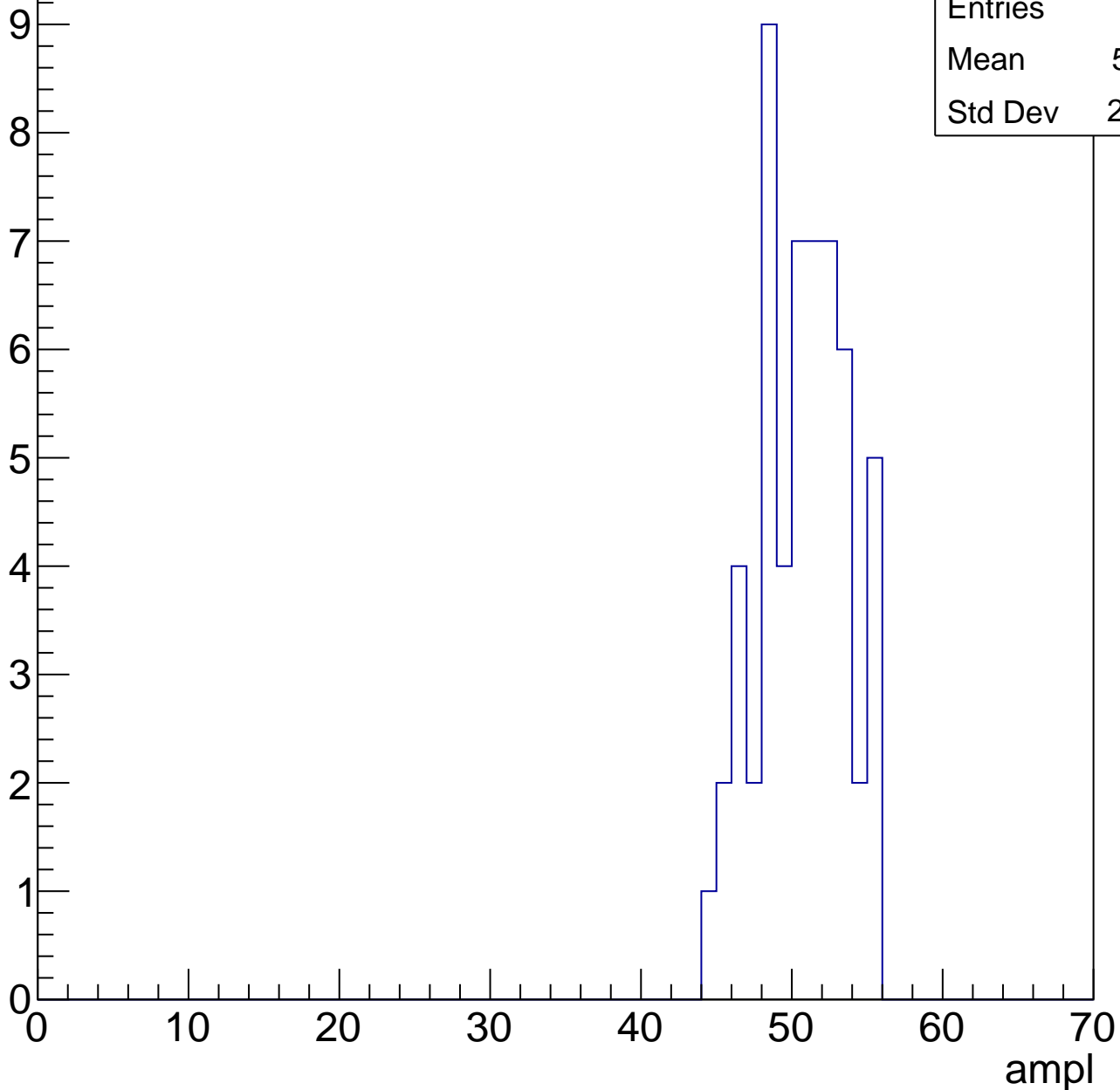


# B0L002S, U1-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 50.21 |
| Std Dev | 2.839 |

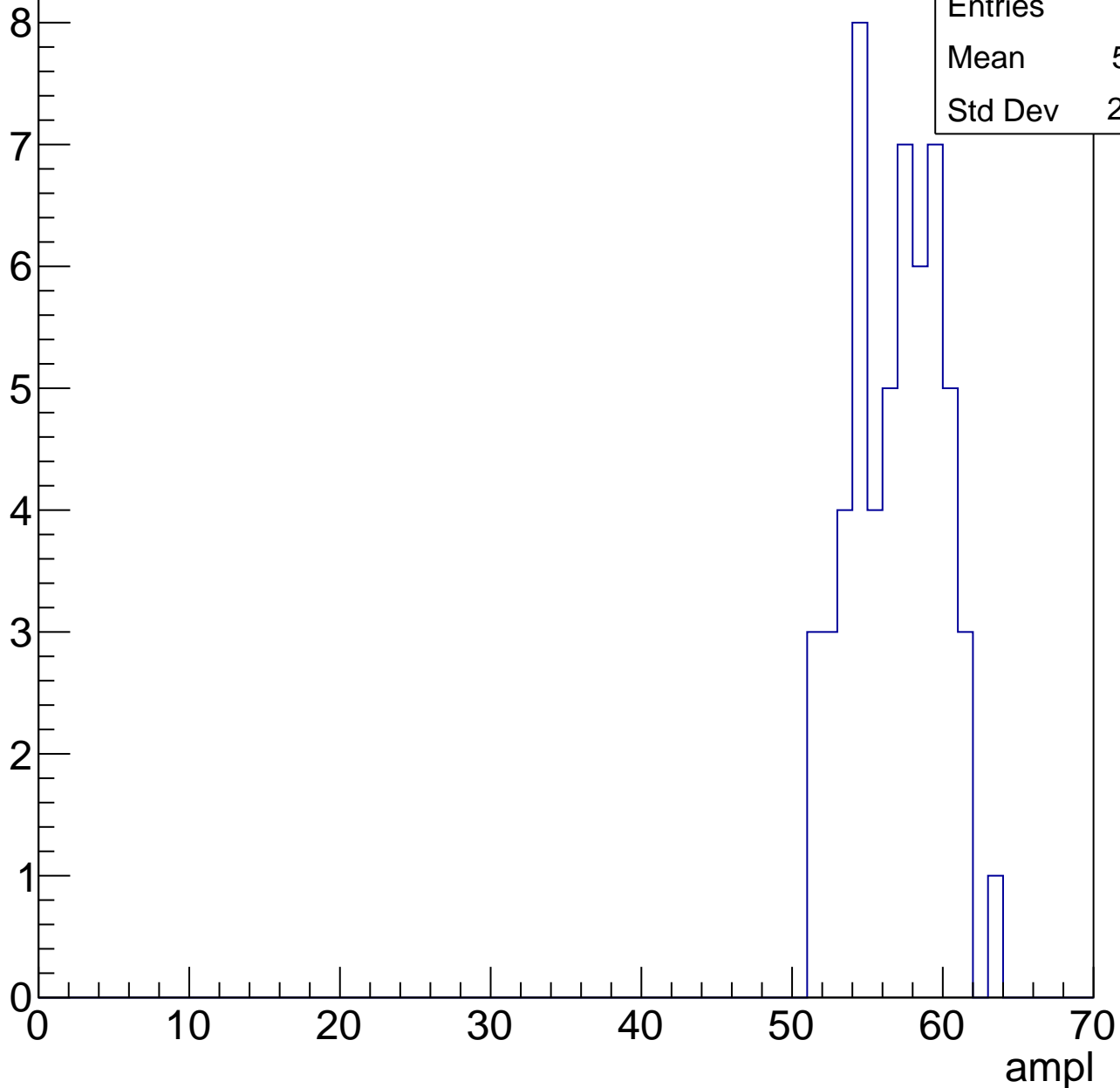


# B0L002S, U1-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 56.41 |
| Std Dev | 2.939 |

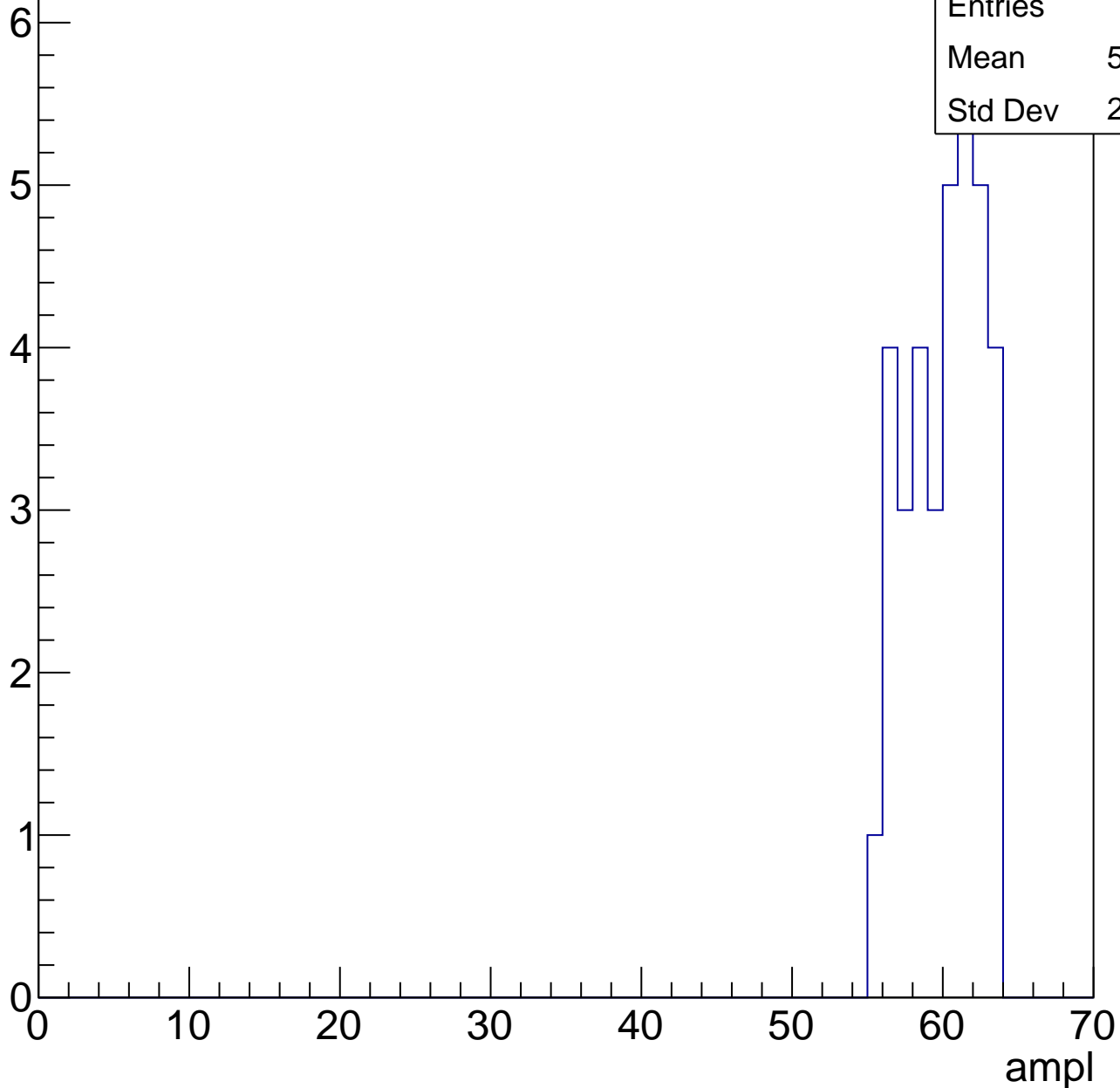


# B0L002S, U1-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 35    |
| Mean    | 59.63 |
| Std Dev | 2.343 |

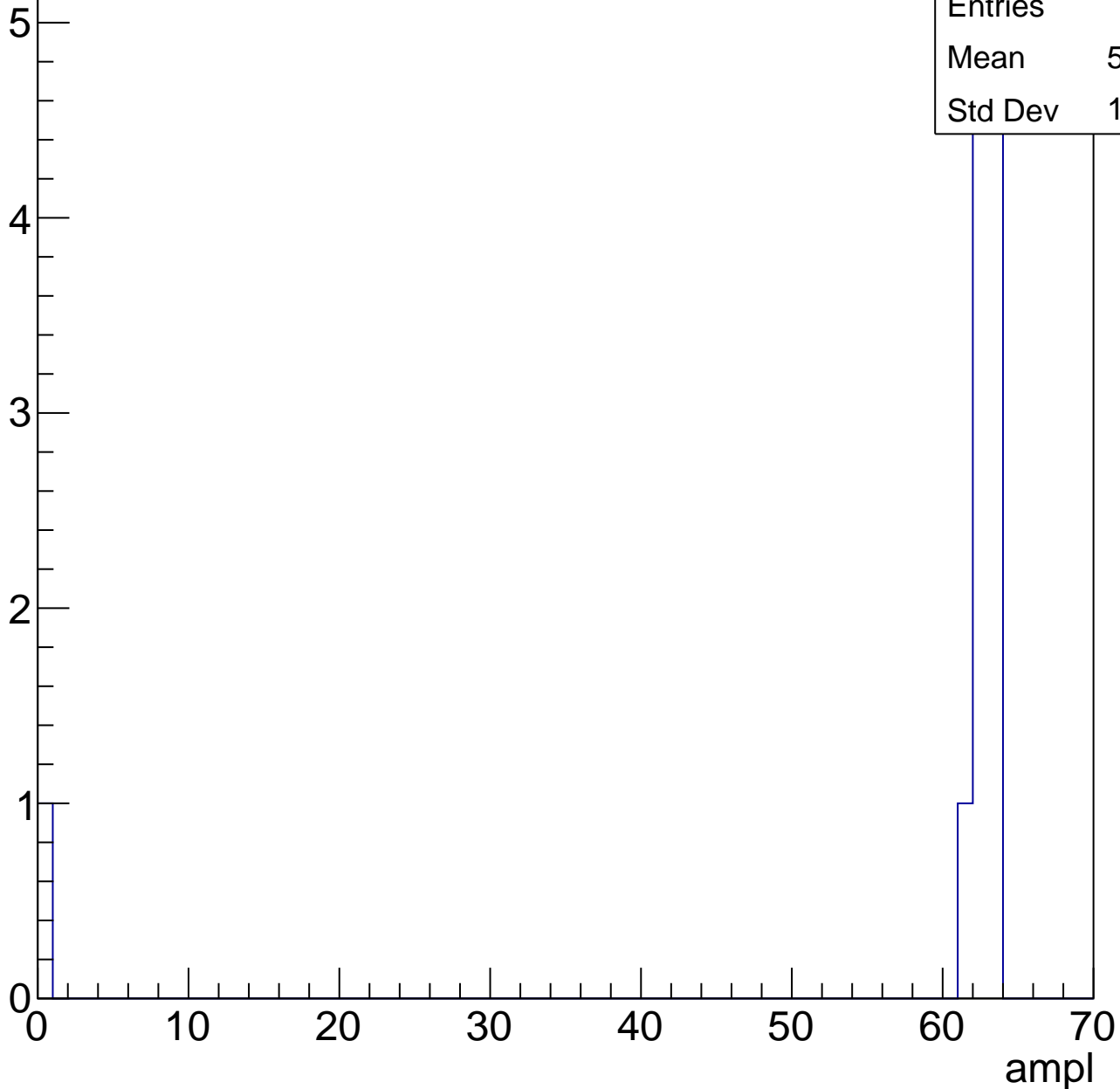


# B0L002S, U1-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 12    |
| Mean    | 57.17 |
| Std Dev | 17.25 |





# B0L002S, U1-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch103, adc0

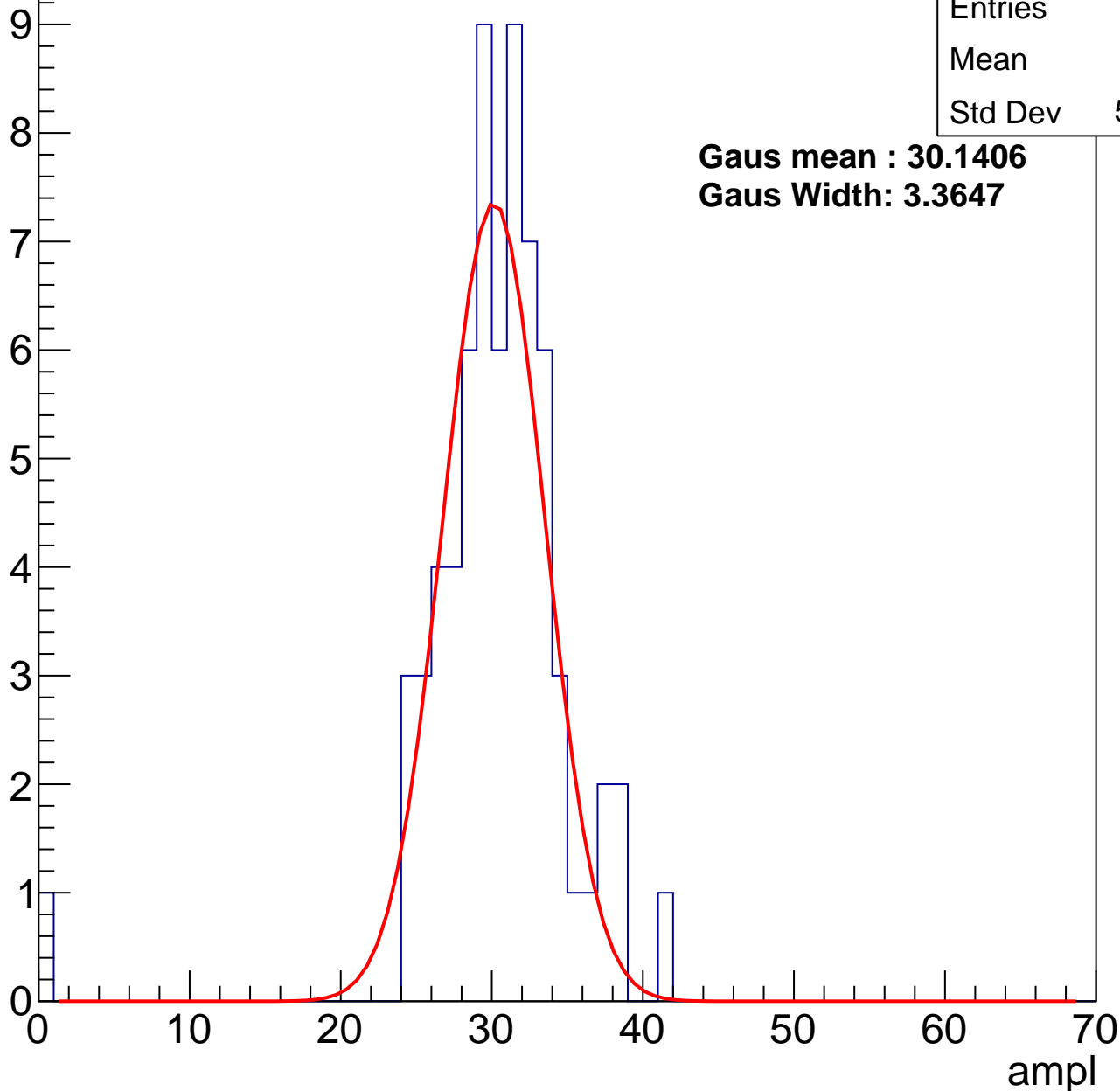
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 29.9  |
| Std Dev | 5.091 |

**Gaus mean : 30.1406**

**Gaus Width: 3.3647**



# B0L002S, U1-ch103, adc1

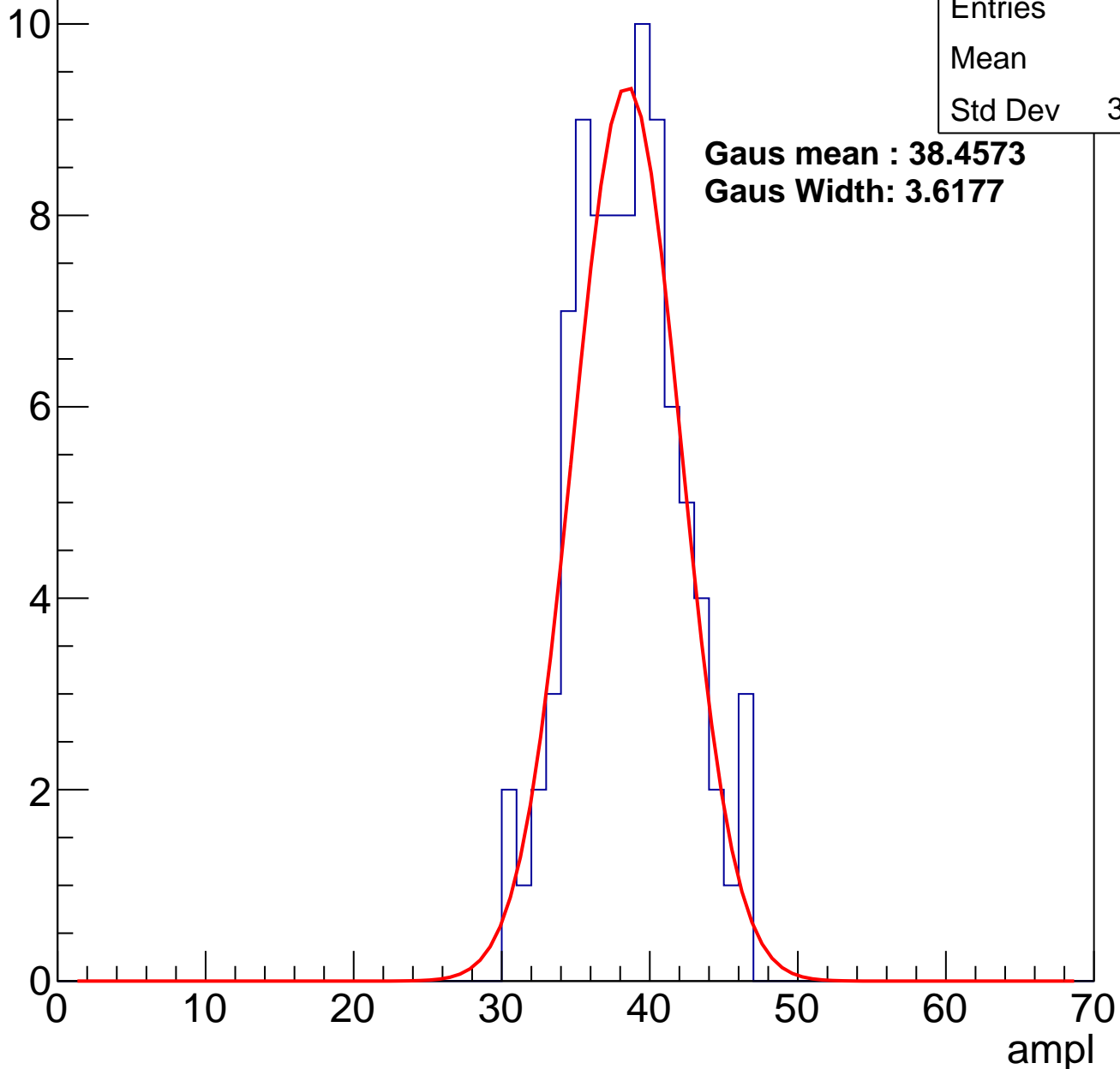
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 88    |
| Mean    | 38    |
| Std Dev | 3.615 |

**Gaus mean : 38.4573**

**Gaus Width: 3.6177**

Entry



# B0L002S, U1-ch103, adc2

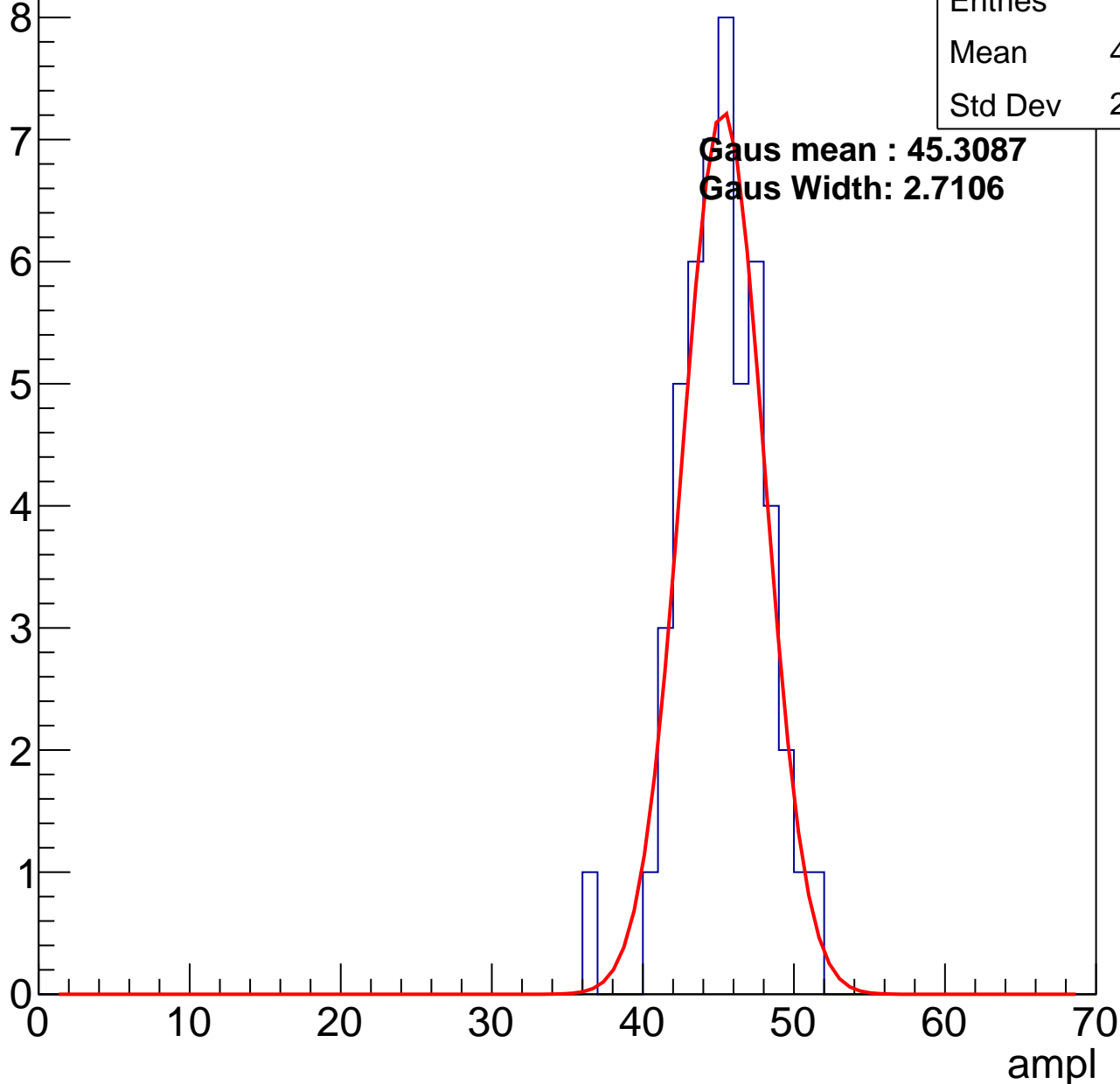
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 44.76 |
| Std Dev | 2.775 |

**Gaus mean : 45.3087**

**Gaus Width: 2.7106**

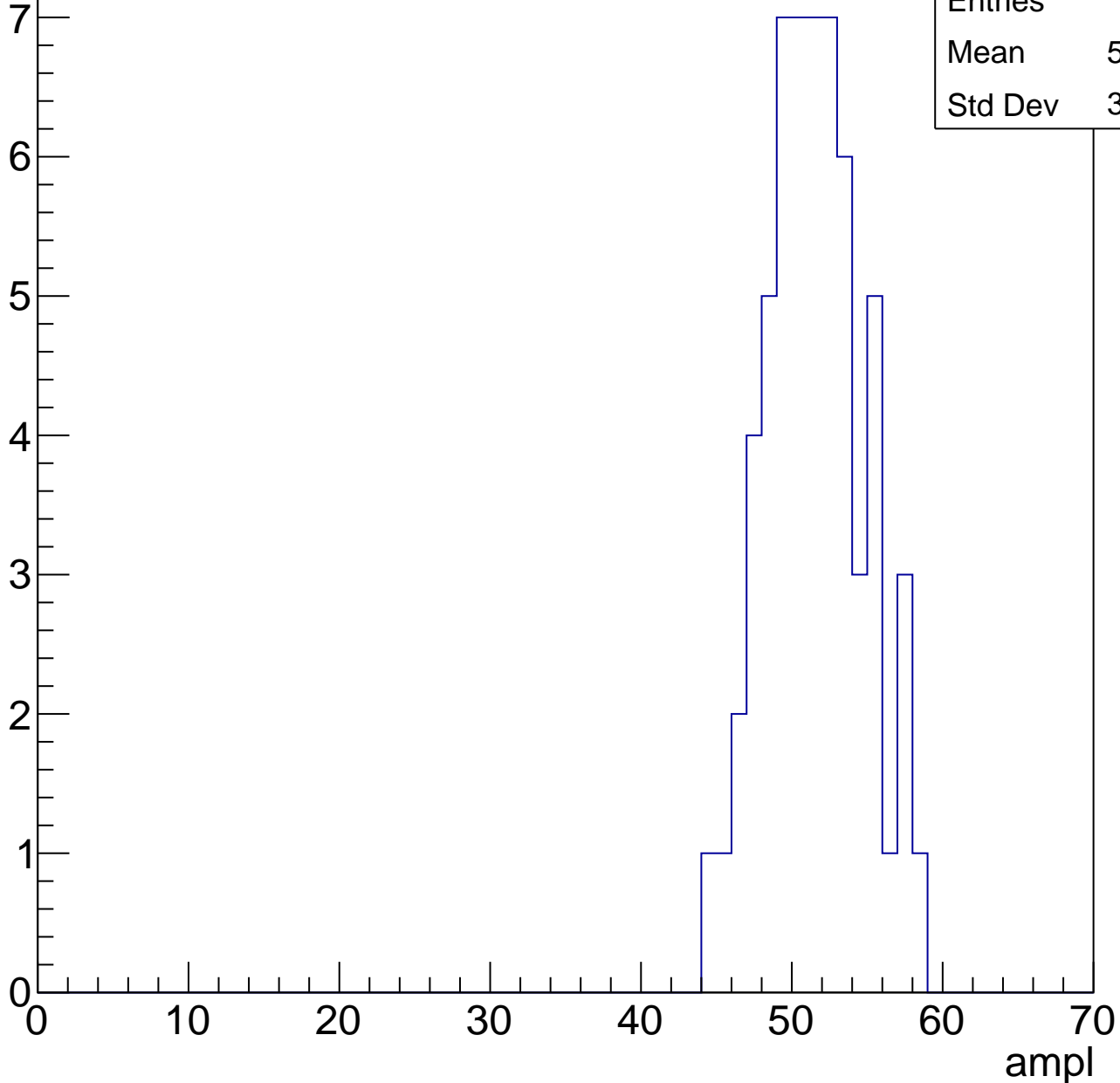


# B0L002S, U1-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 51.05 |
| Std Dev | 3.159 |

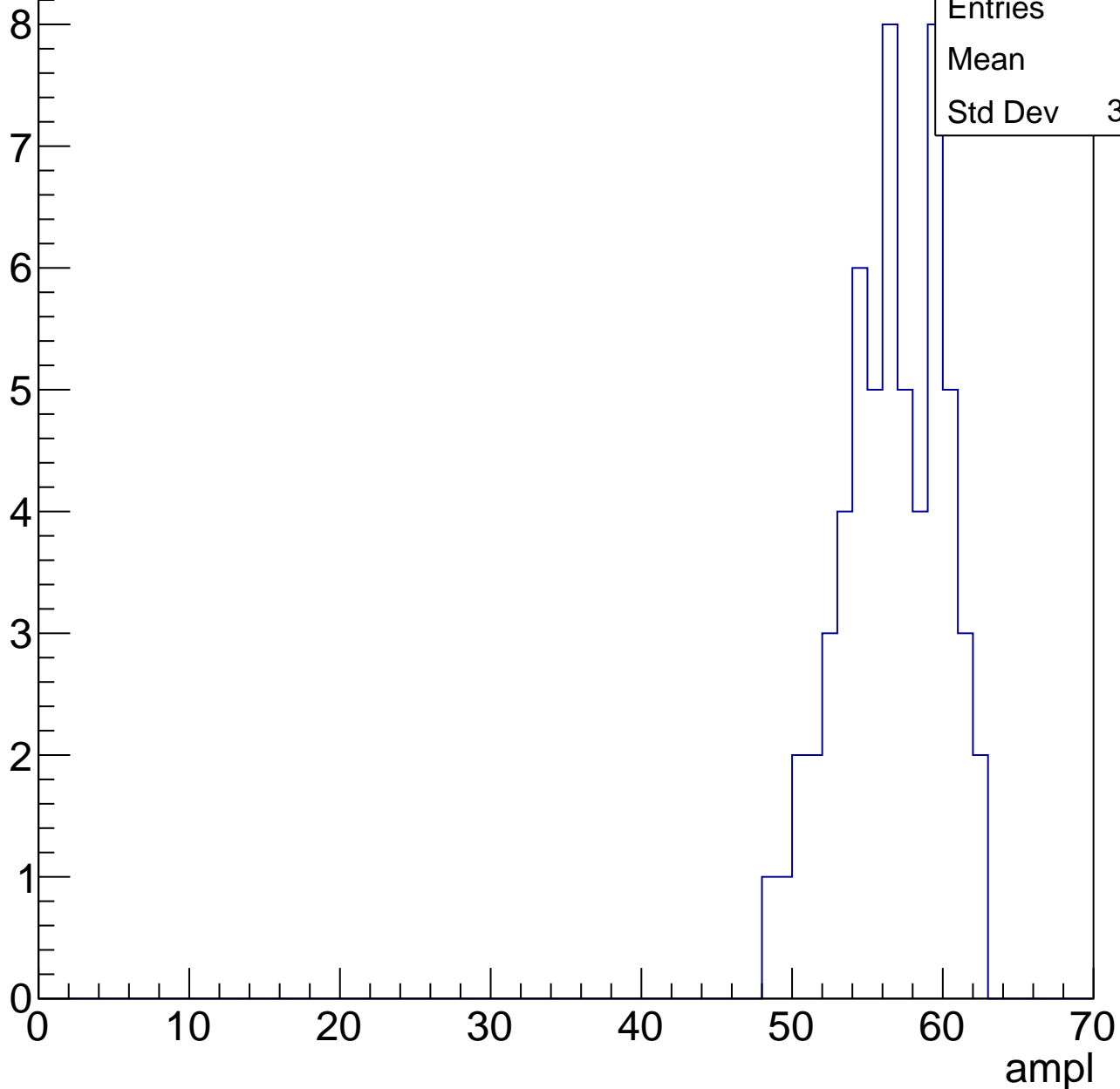


# B0L002S, U1-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 56.1  |
| Std Dev | 3.363 |

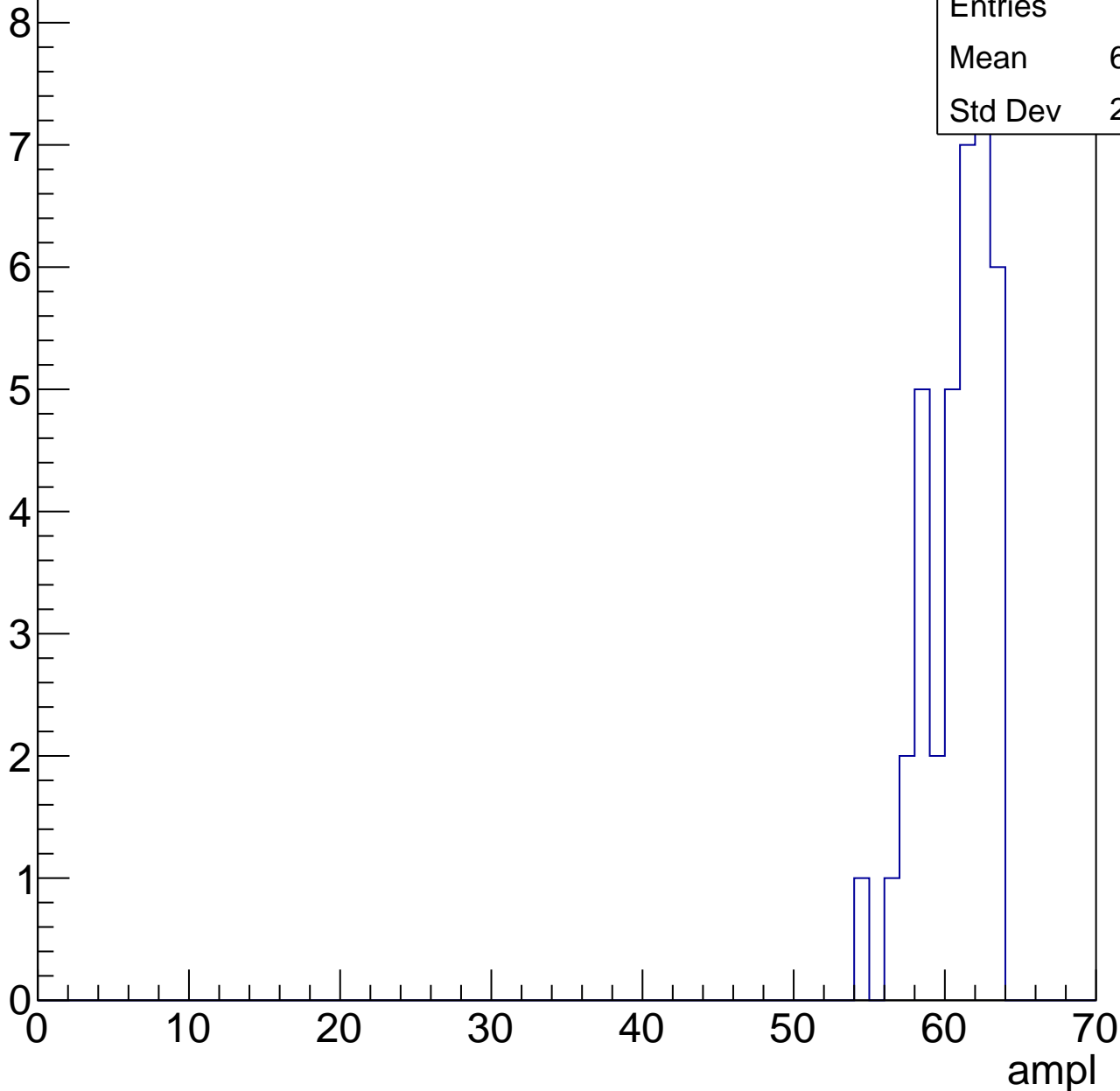


# B0L002S, U1-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

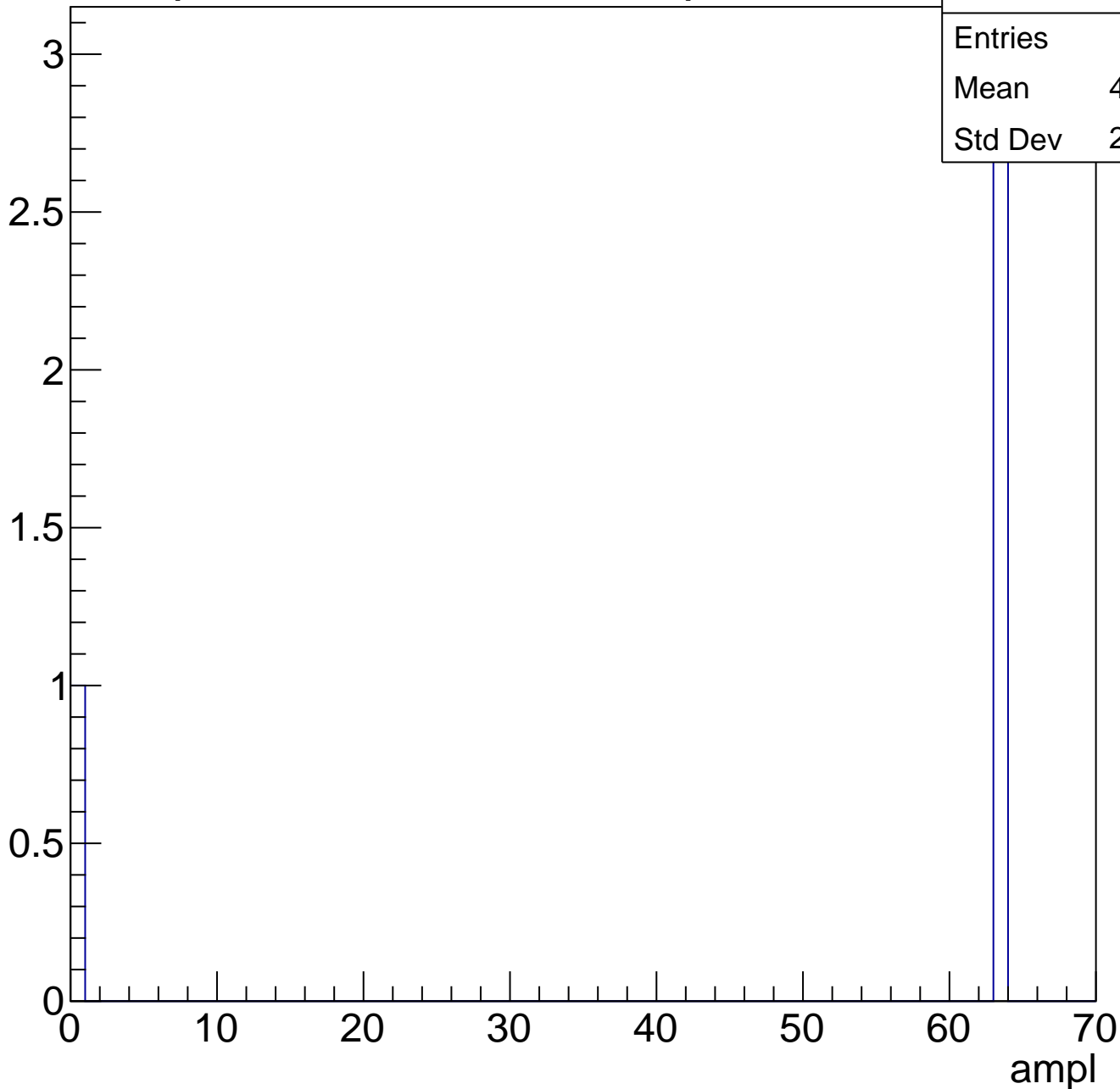
|         |       |
|---------|-------|
| Entries | 37    |
| Mean    | 60.35 |
| Std Dev | 2.208 |



# B0L002S, U1-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

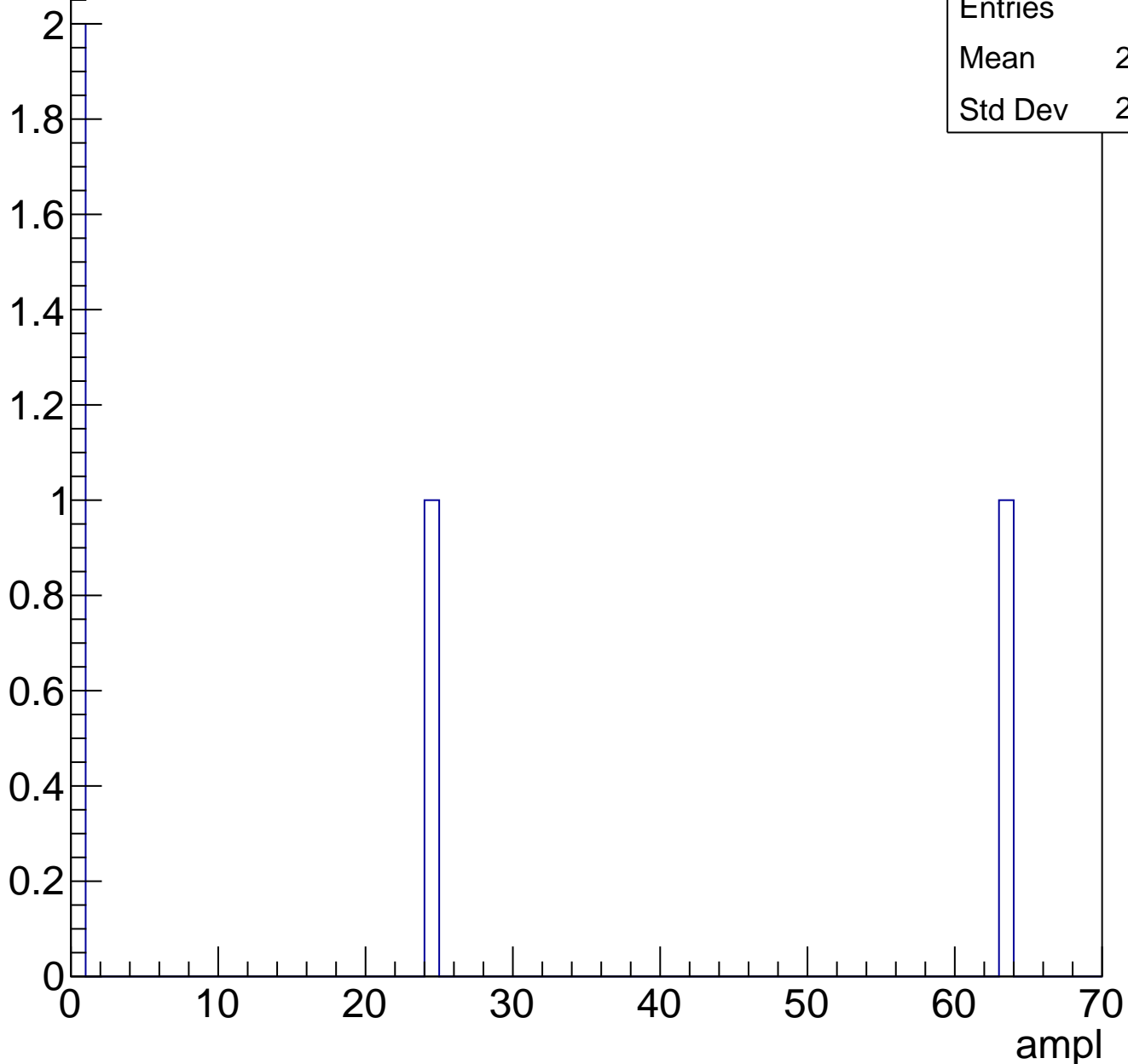




# B0L002S, U1-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |       |
|---------|-------|
| Entries | 4     |
| Mean    | 21.75 |
| Std Dev | 25.75 |

# B0L002S, U1-ch104, adc0

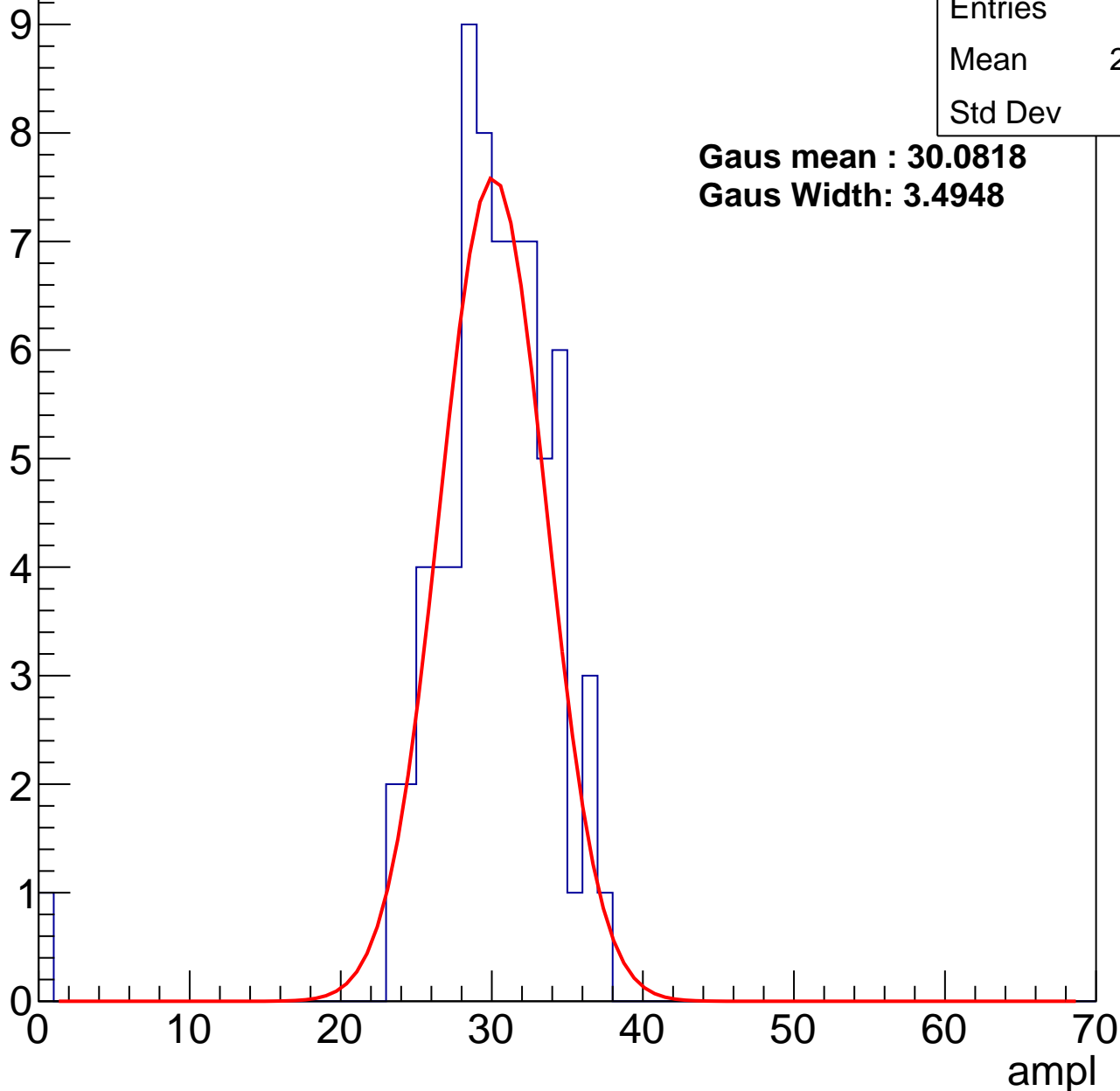
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 29.44 |
| Std Dev | 4.82  |

**Gaus mean : 30.0818**

**Gaus Width: 3.4948**



# B0L002S, U1-ch104, adc1

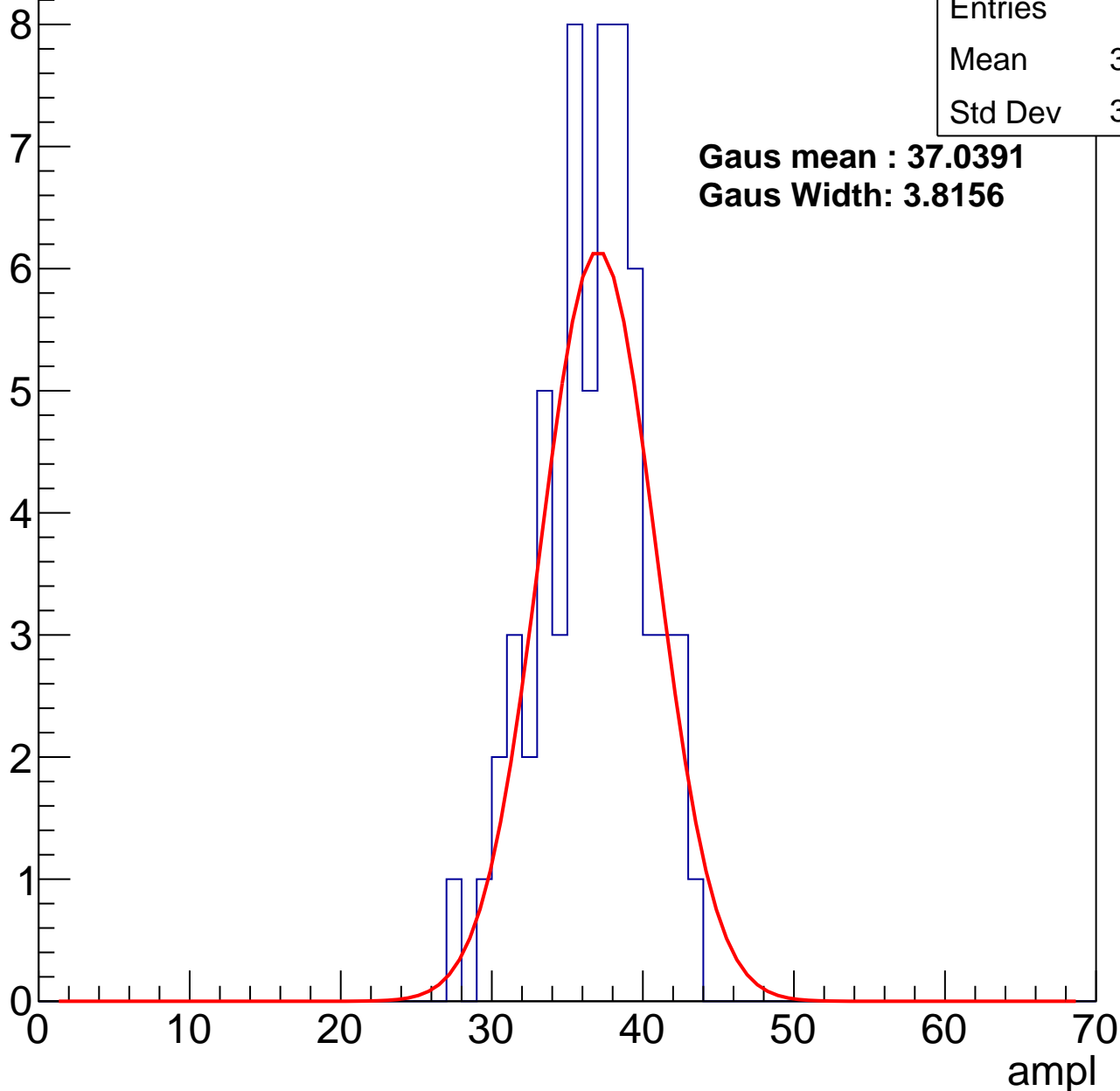
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 36.23 |
| Std Dev | 3.466 |

**Gaus mean : 37.0391**

**Gaus Width: 3.8156**



# B0L002S, U1-ch104, adc2

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 42.28 |
| Std Dev | 3.545 |

**Gaus mean : 41.9754**

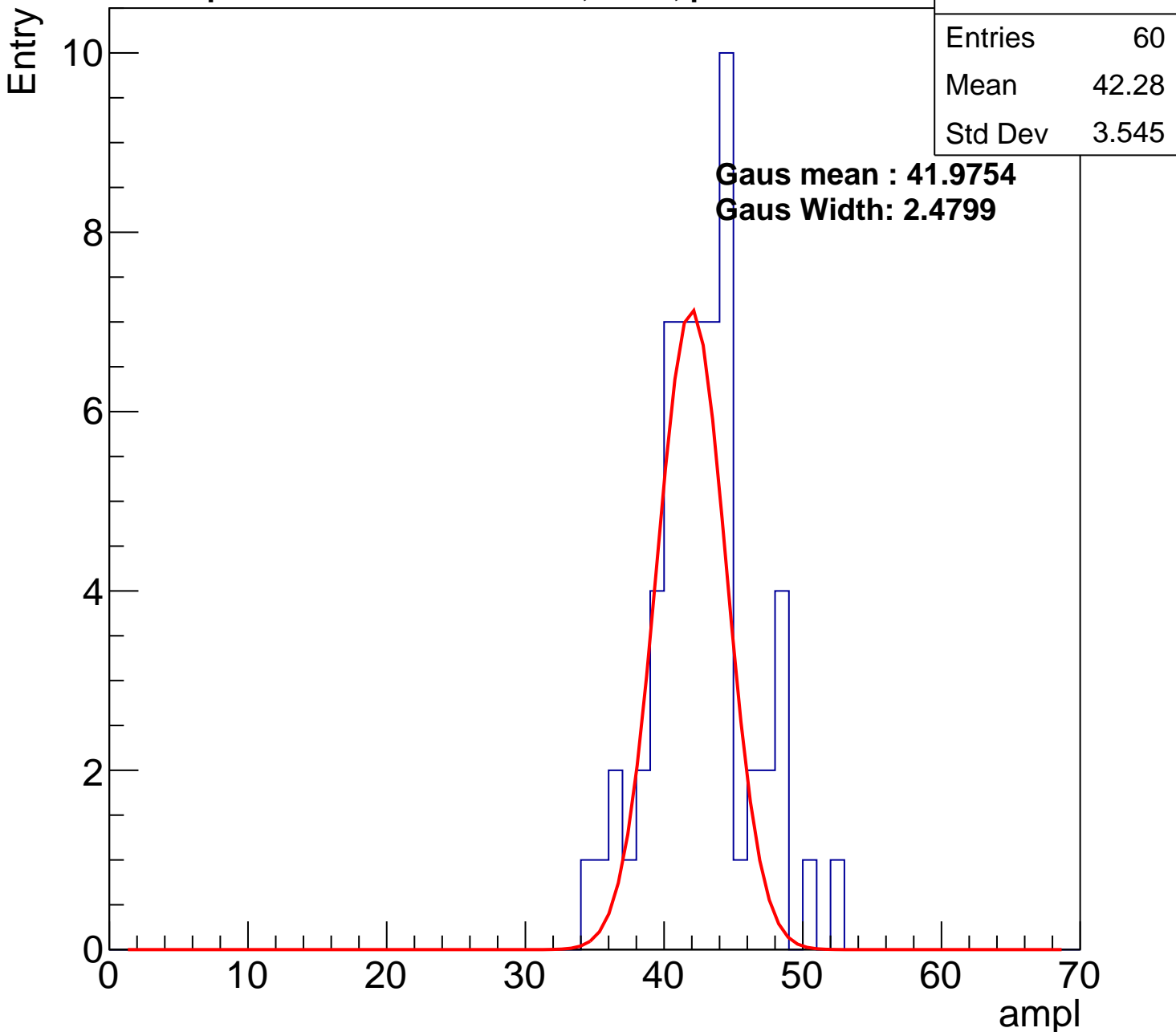
**Gaus Width: 2.4799**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

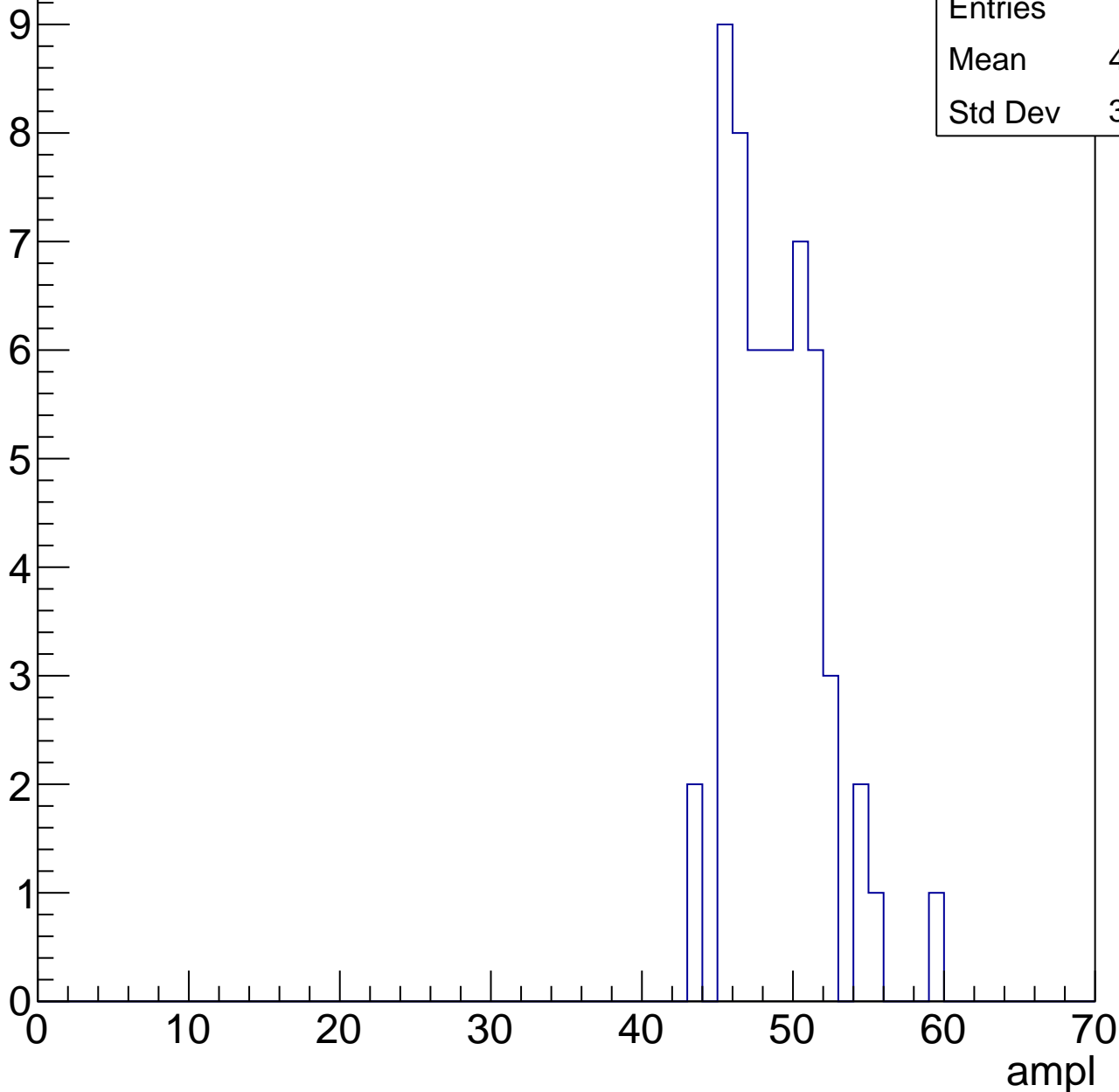


# B0L002S, U1-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 48.37 |
| Std Dev | 3.076 |

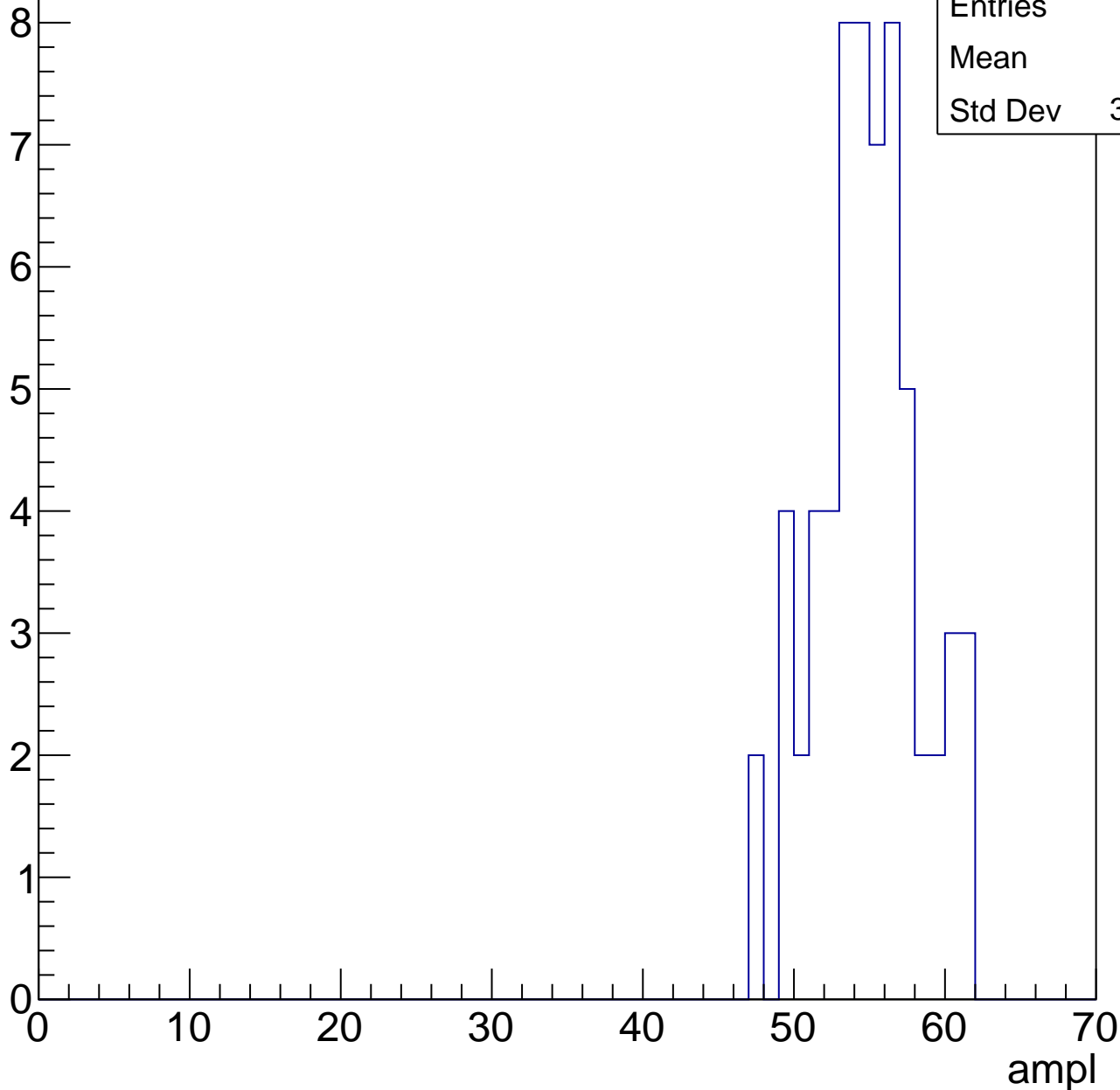


# B0L002S, U1-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 54.4  |
| Std Dev | 3.362 |

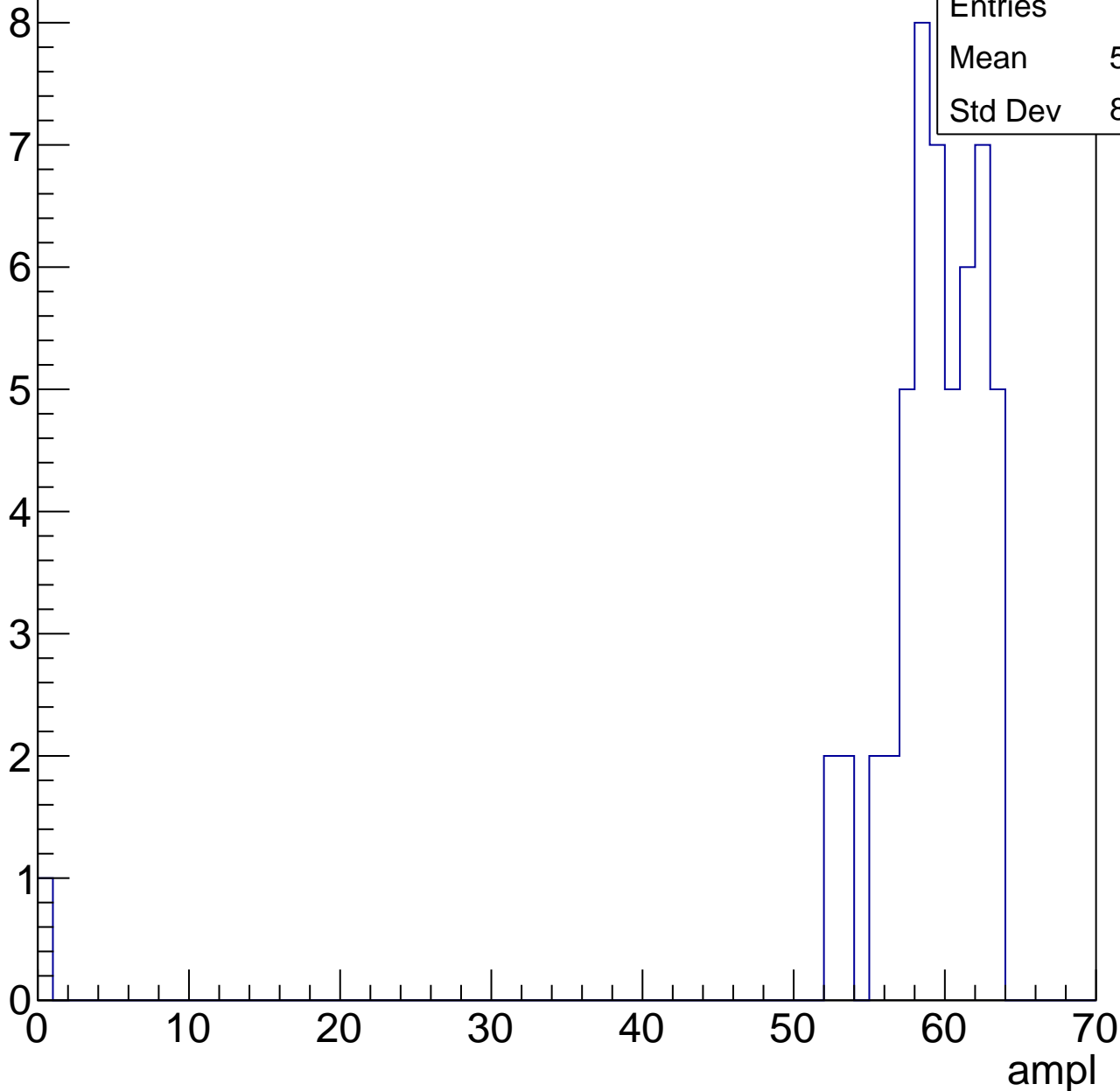


# B0L002S, U1-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 57.87 |
| Std Dev | 8.587 |

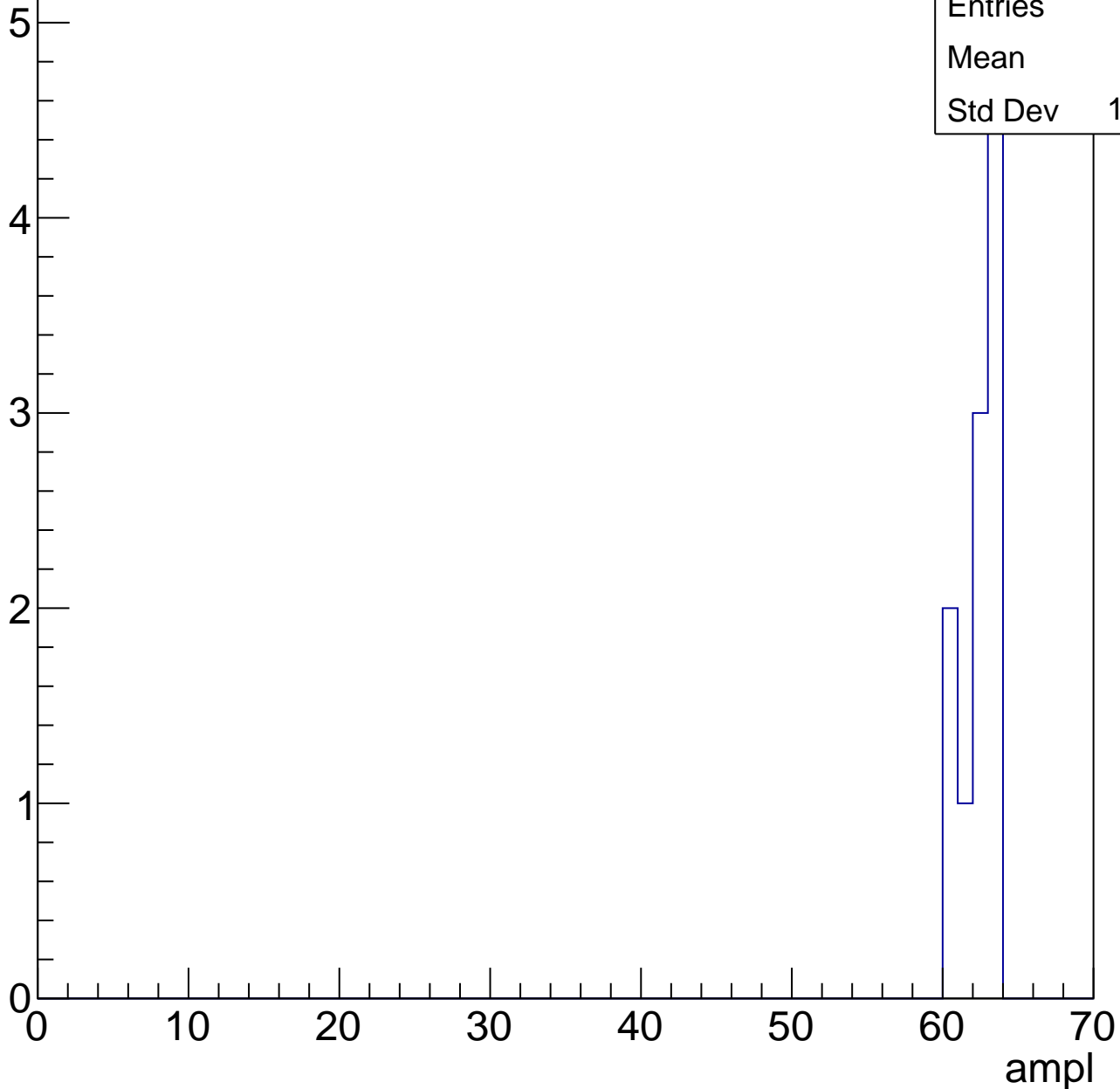


# B0L002S, U1-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 62    |
| Std Dev | 1.128 |

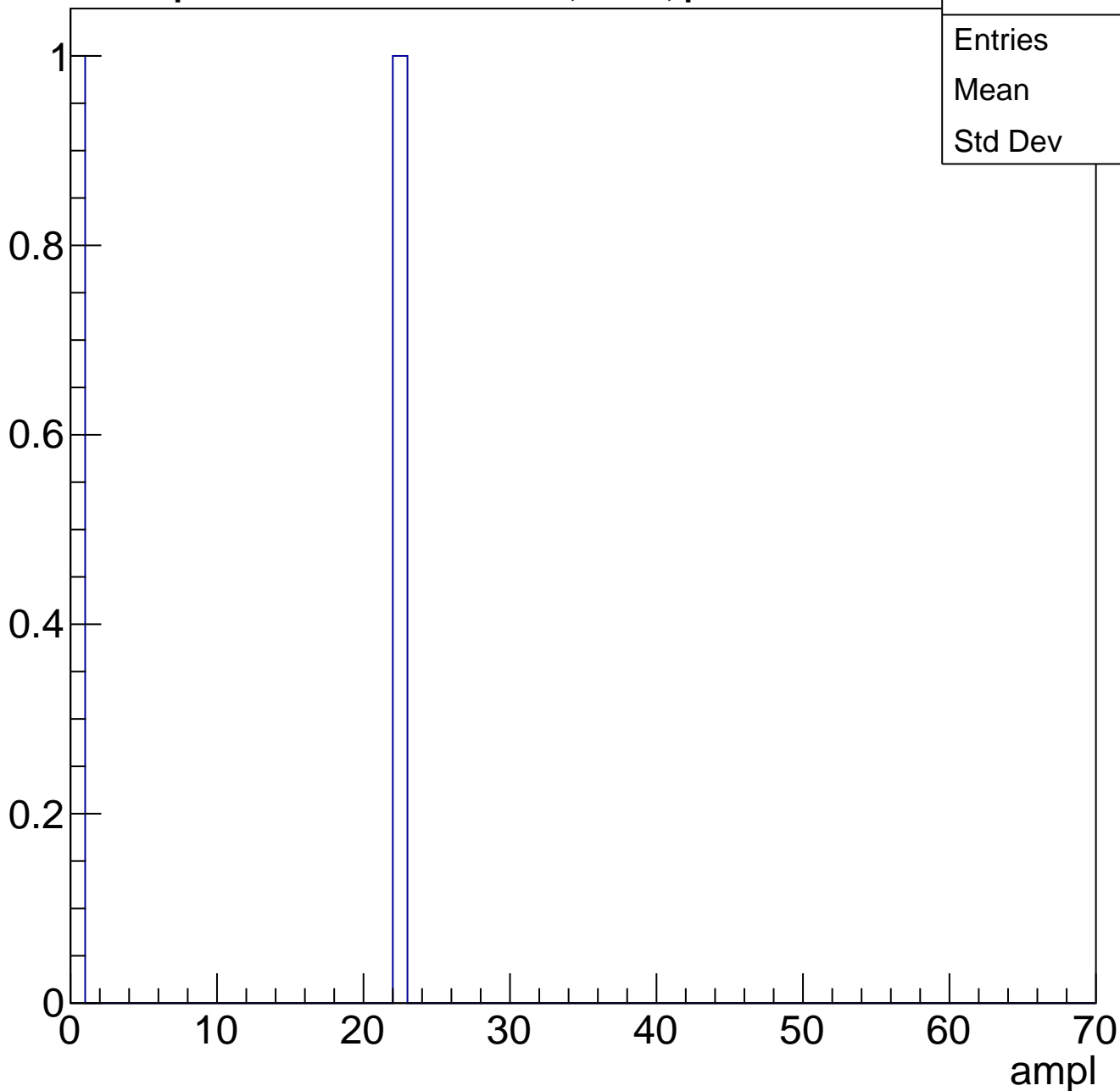




# B0L002S, U1-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 11 |
| Std Dev | 11 |

# B0L002S, U1-ch105, adc0

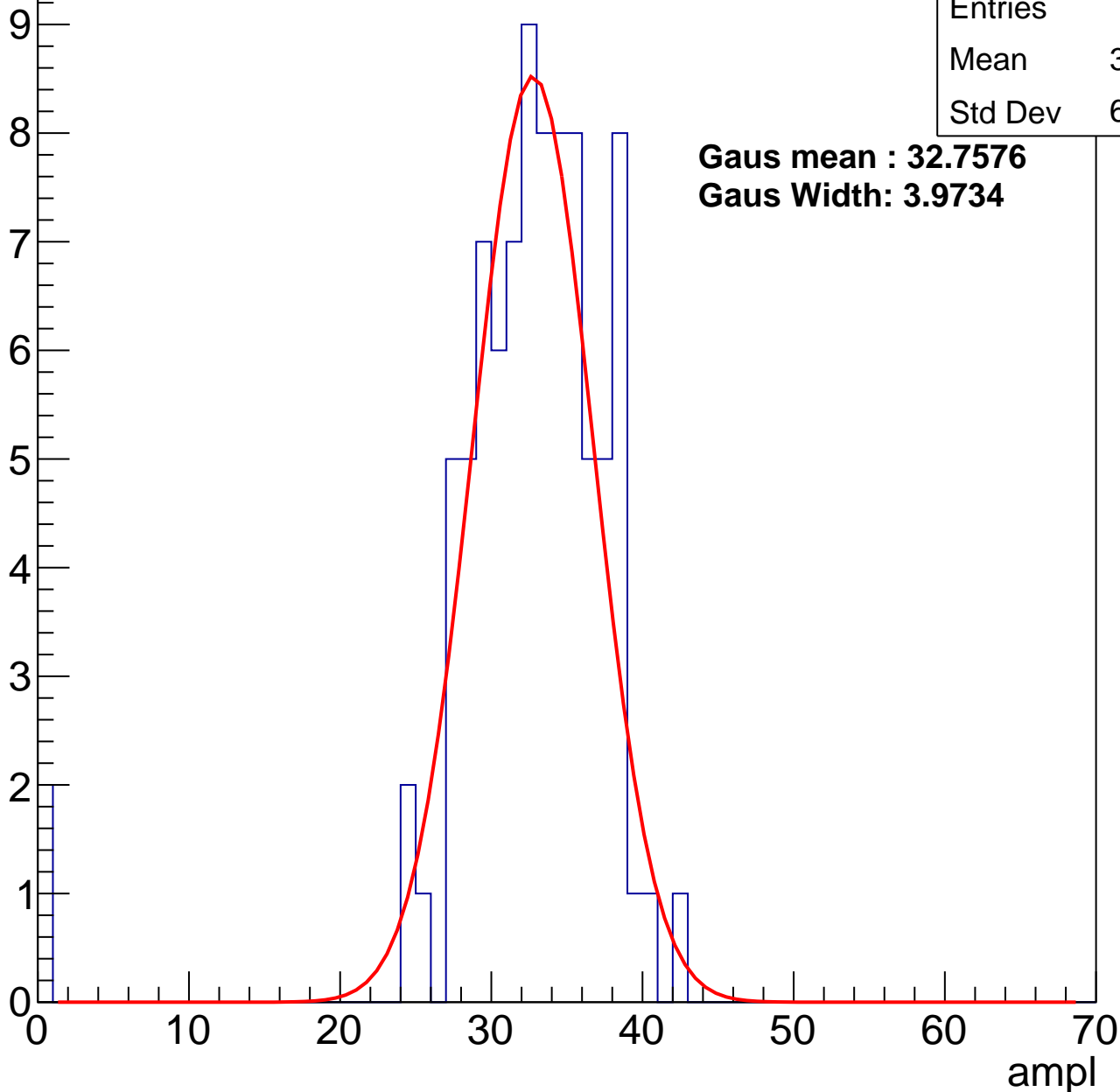
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 89    |
| Mean    | 31.93 |
| Std Dev | 6.127 |

**Gaus mean : 32.7576**

**Gaus Width: 3.9734**



# B0L002S, U1-ch105, adc1

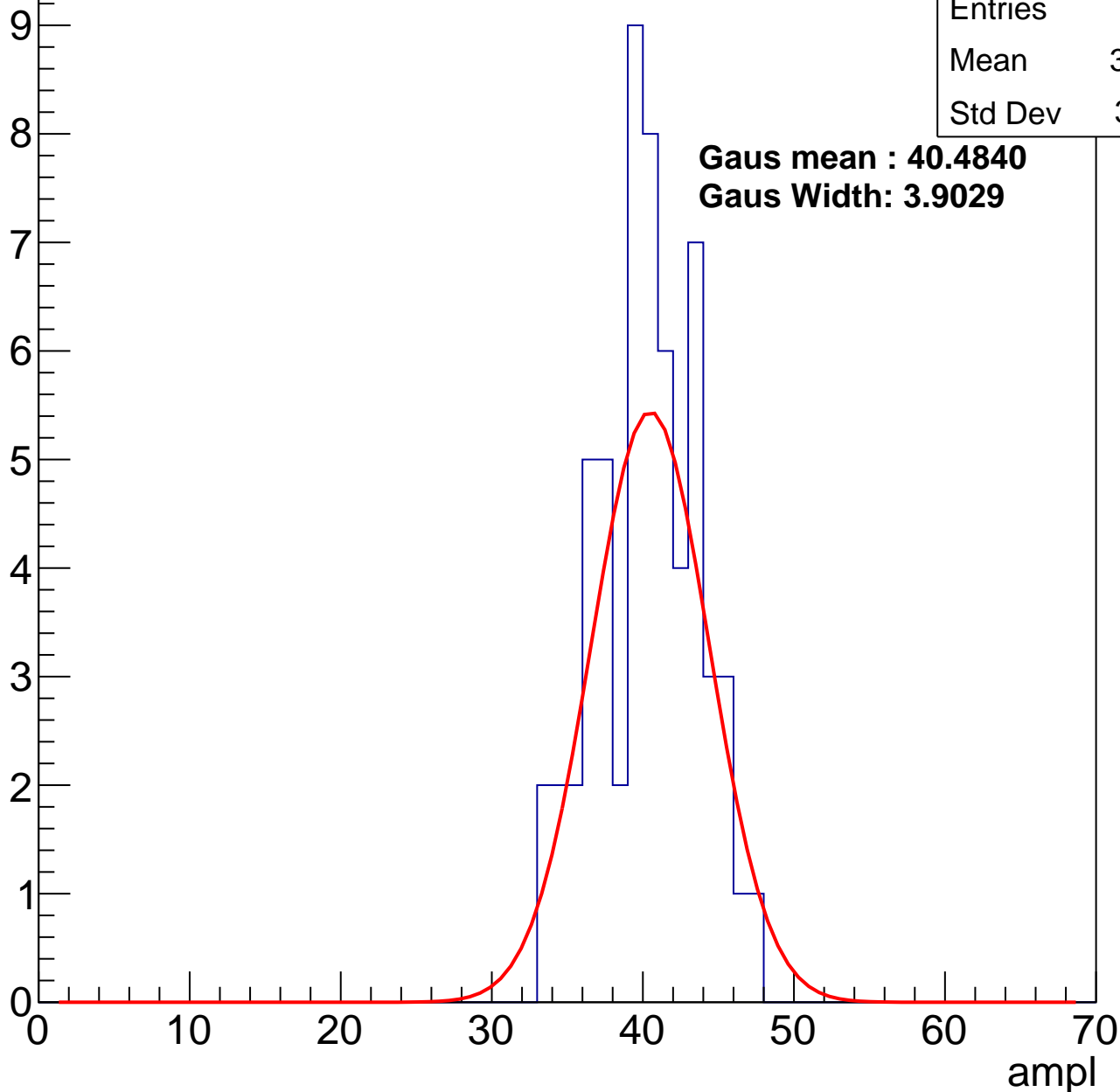
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 39.85 |
| Std Dev | 3.301 |

**Gaus mean : 40.4840**

**Gaus Width: 3.9029**



# B0L002S, U1-ch105, adc2

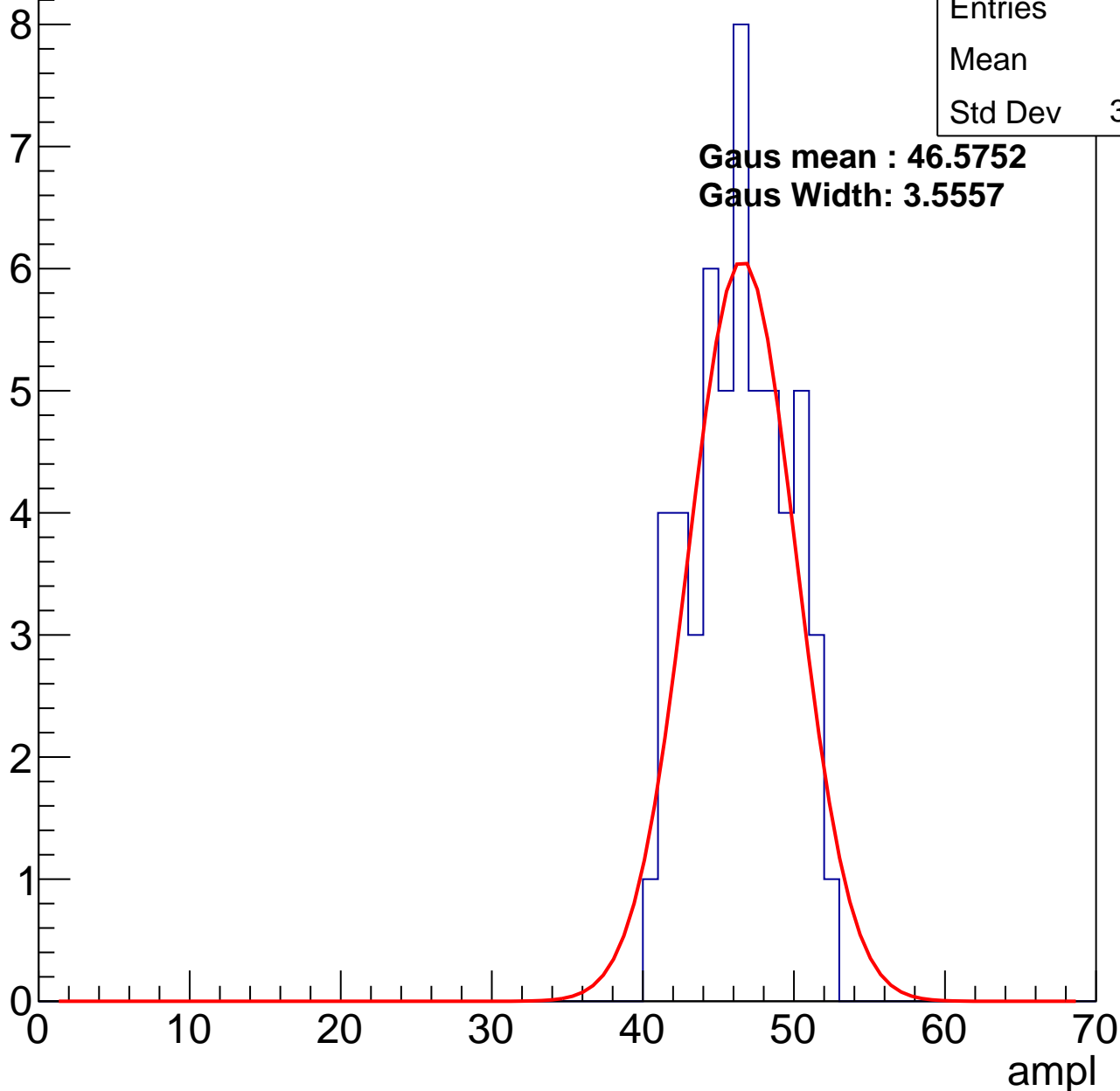
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 46    |
| Std Dev | 3.067 |

**Gaus mean : 46.5752**

**Gaus Width: 3.5557**

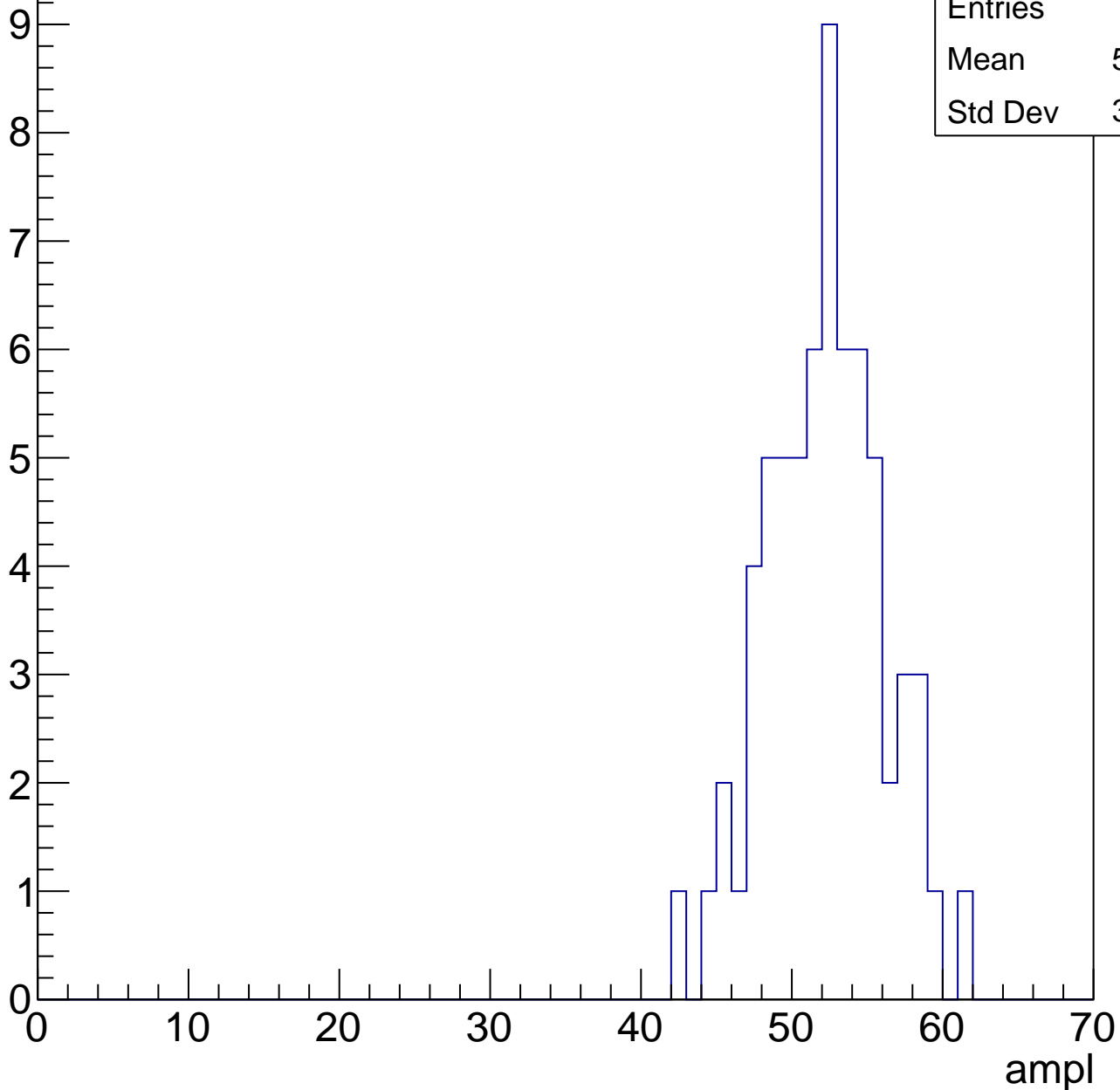


# B0L002S, U1-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 51.71 |
| Std Dev | 3.821 |

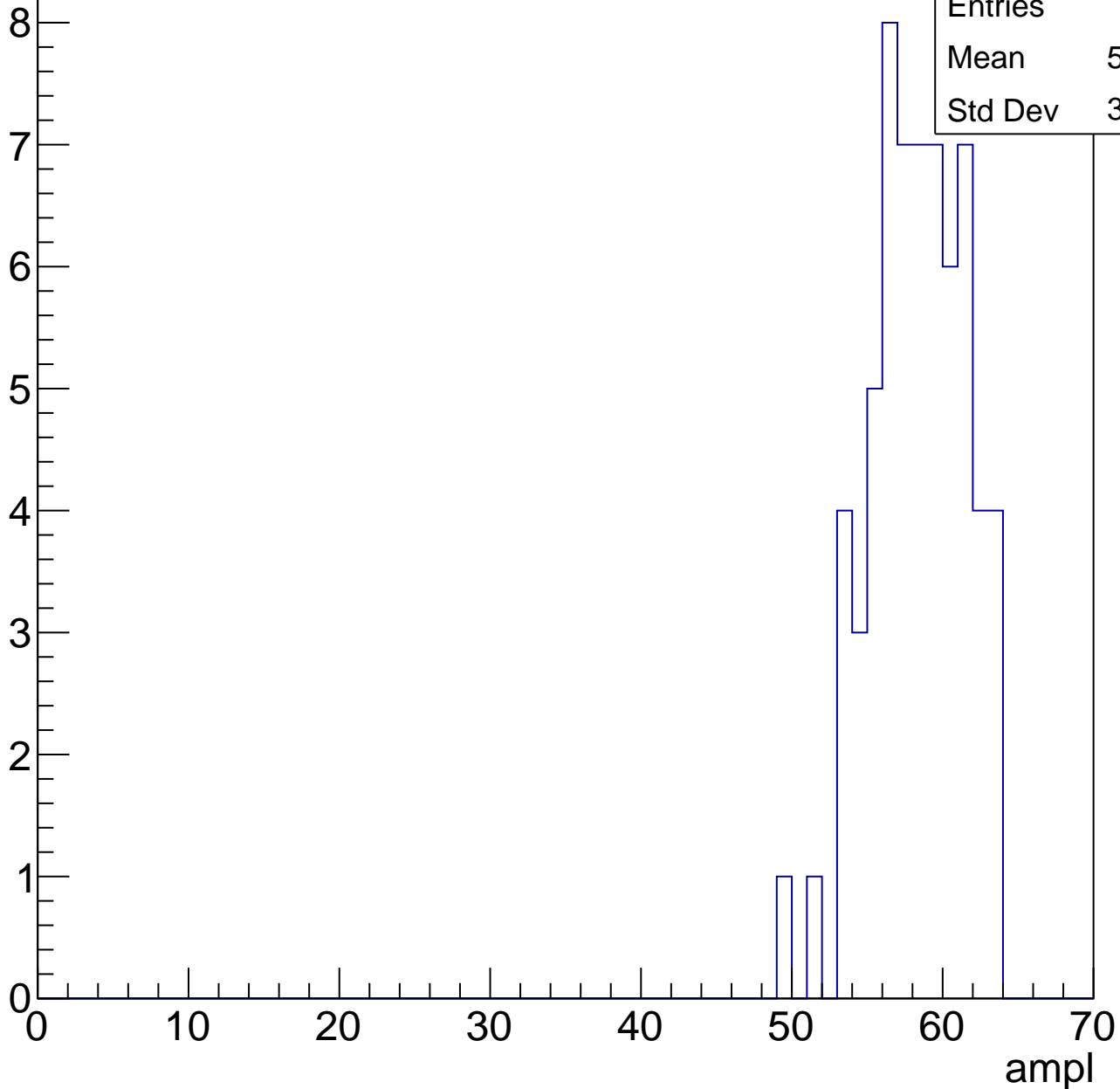


# B0L002S, U1-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 57.84 |
| Std Dev | 3.109 |

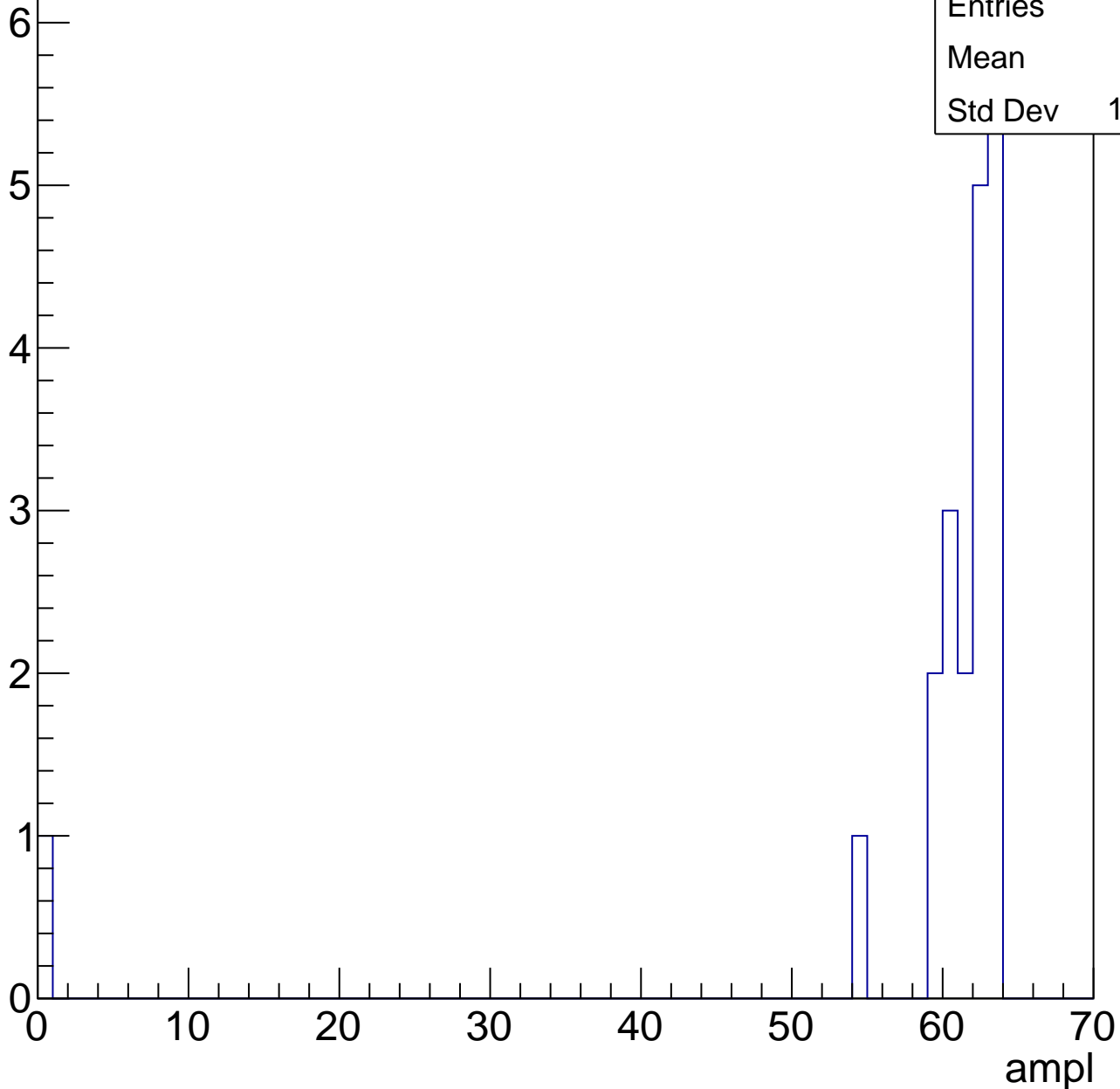


# B0L002S, U1-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 20    |
| Mean    | 58.1  |
| Std Dev | 13.49 |



# B0L002S, U1-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch106, adc0

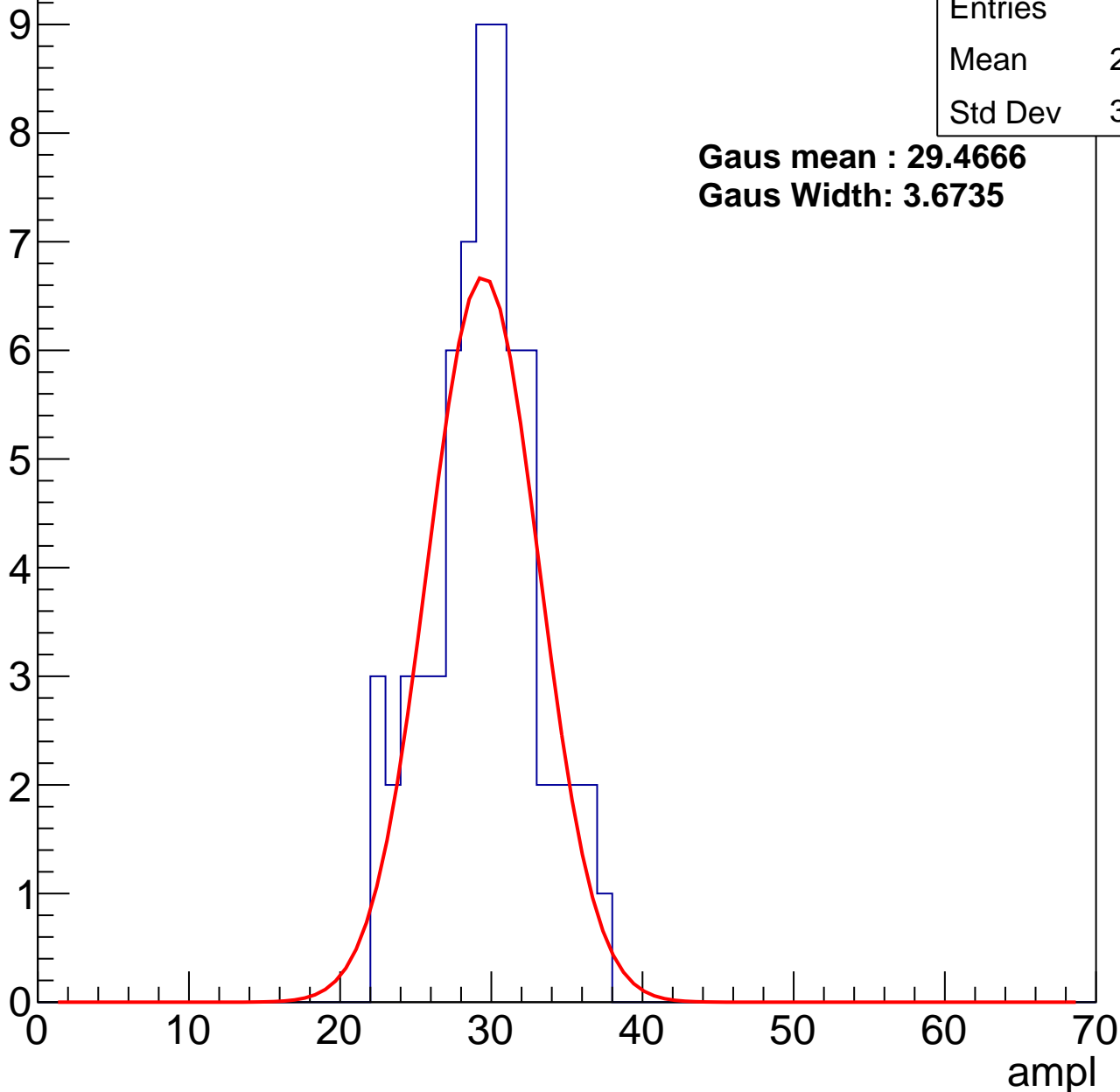
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 29.05 |
| Std Dev | 3.488 |

**Gaus mean : 29.4666**

**Gaus Width: 3.6735**



# B0L002S, U1-ch106, adc1

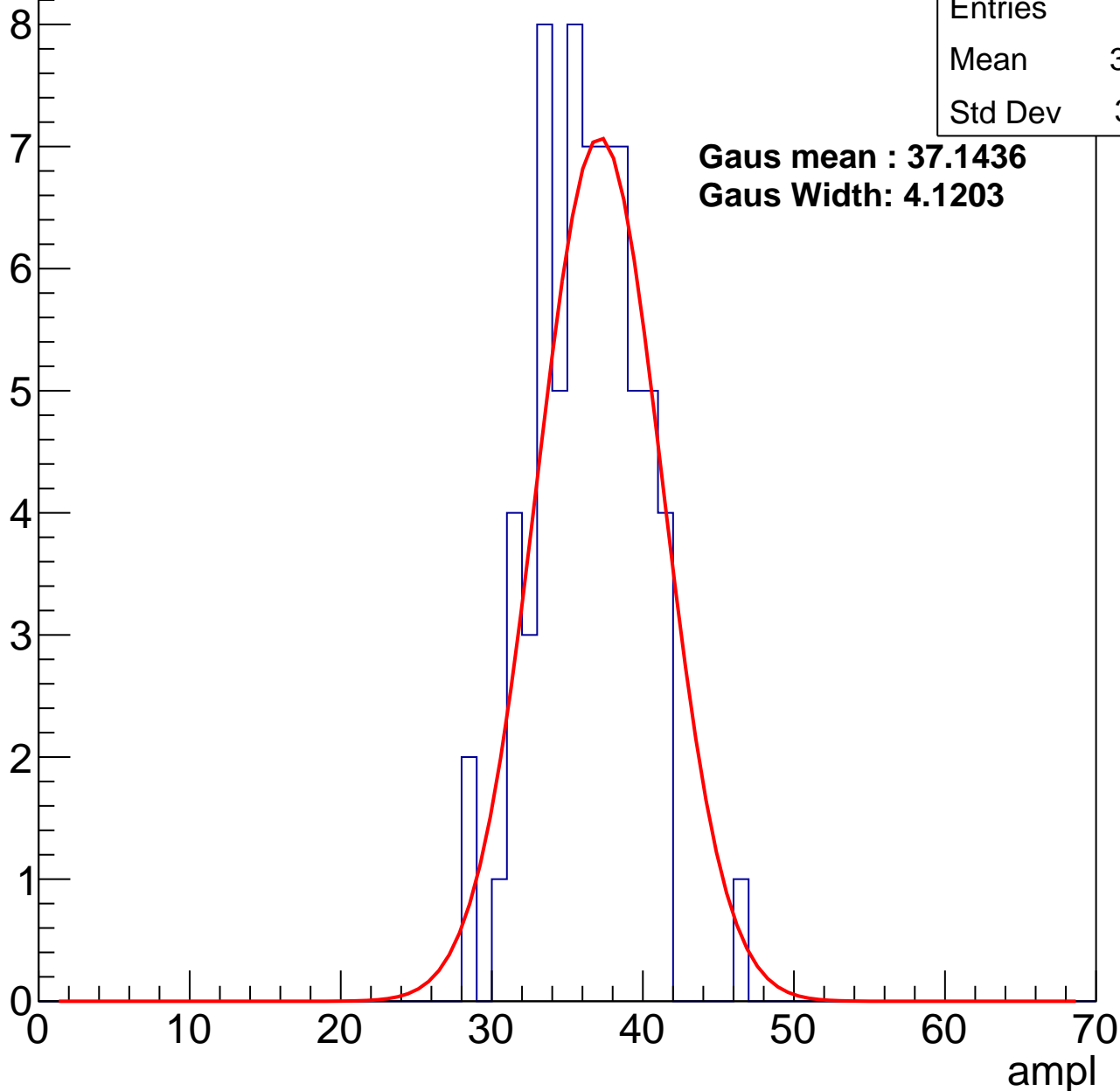
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 35.85 |
| Std Dev | 3.391 |

**Gaus mean : 37.1436**

**Gaus Width: 4.1203**



# B0L002S, U1-ch106, adc2

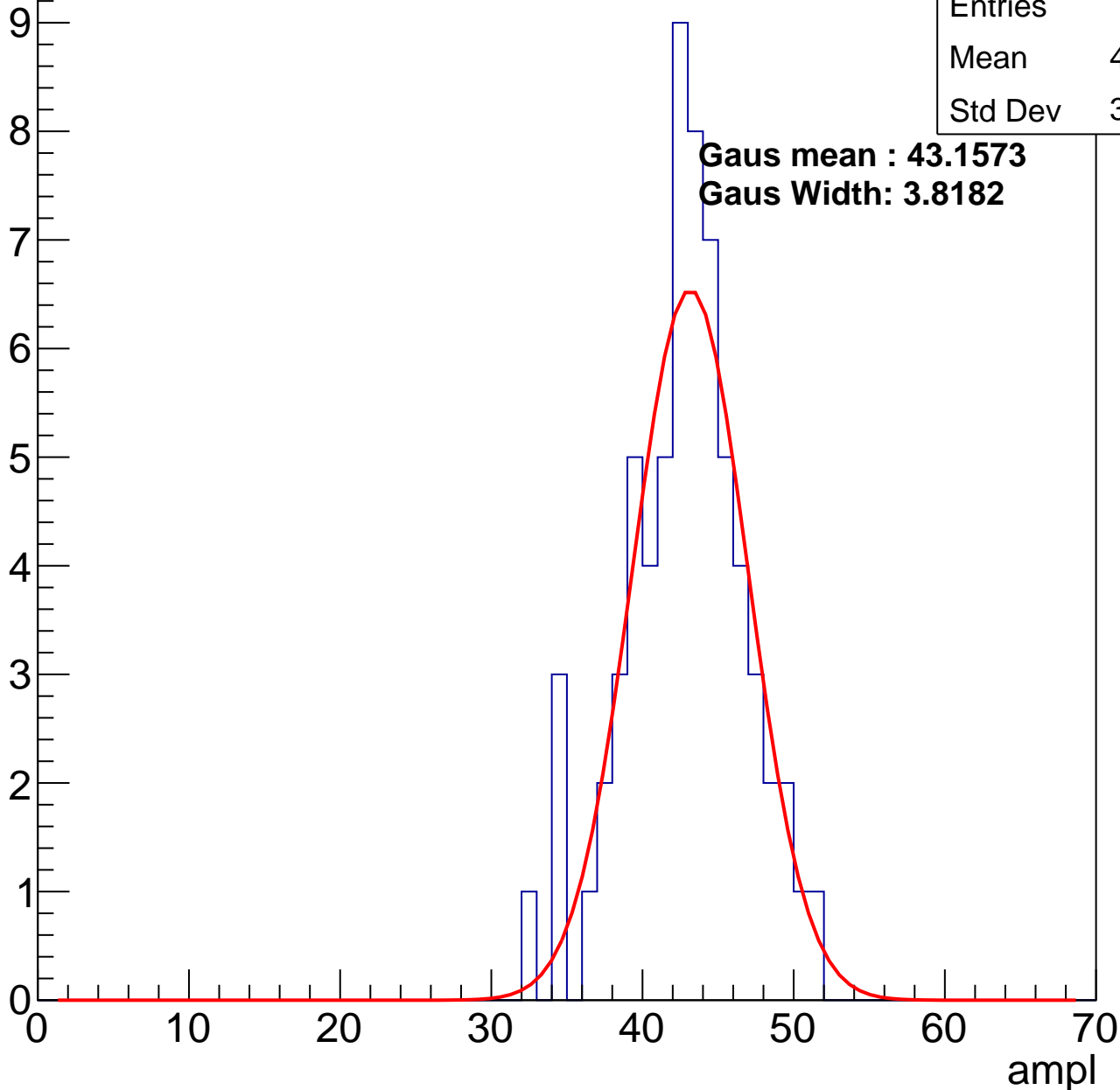
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 42.32 |
| Std Dev | 3.932 |

**Gaus mean : 43.1573**

**Gaus Width: 3.8182**

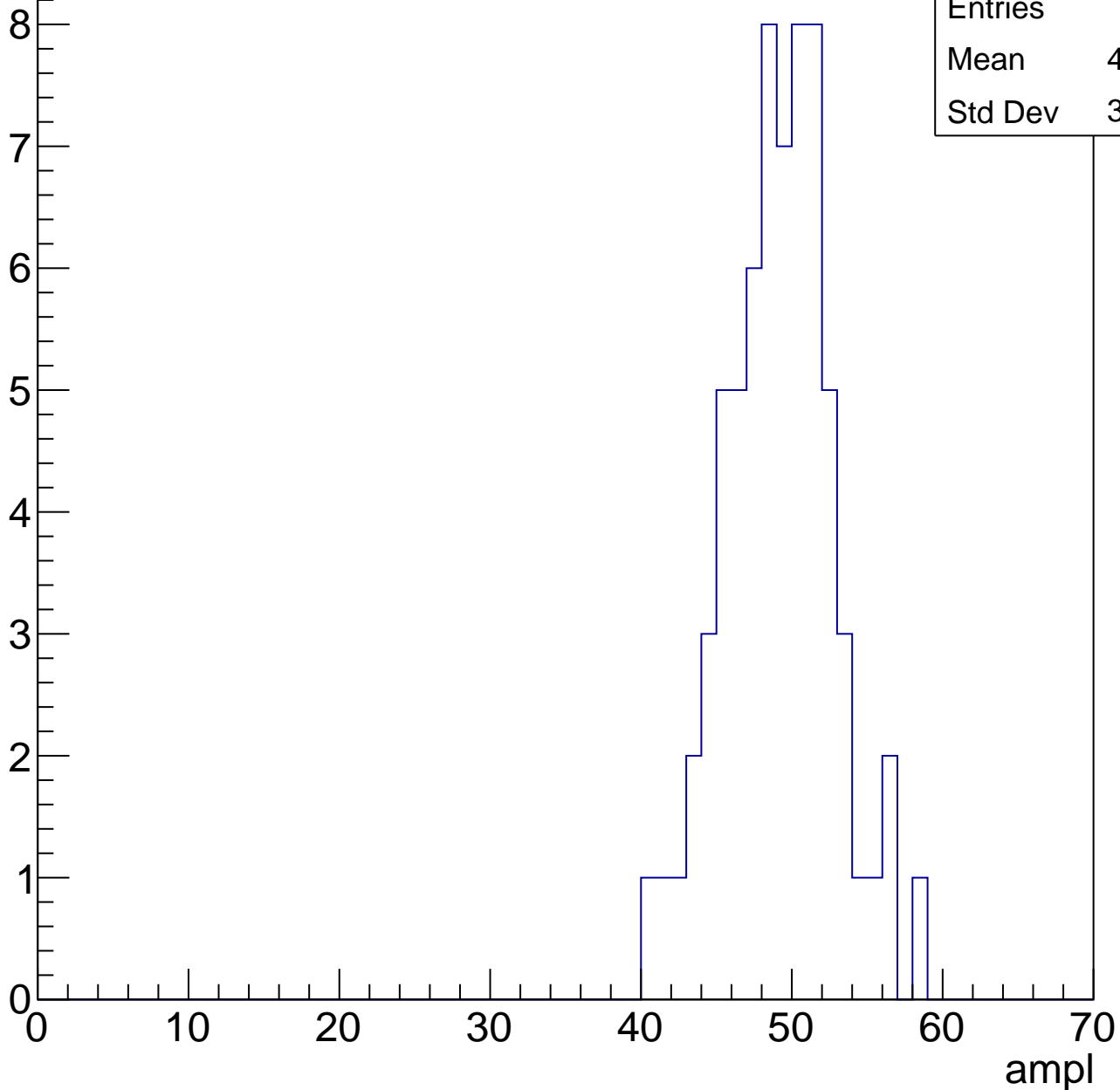


# B0L002S, U1-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 48.69 |
| Std Dev | 3.566 |

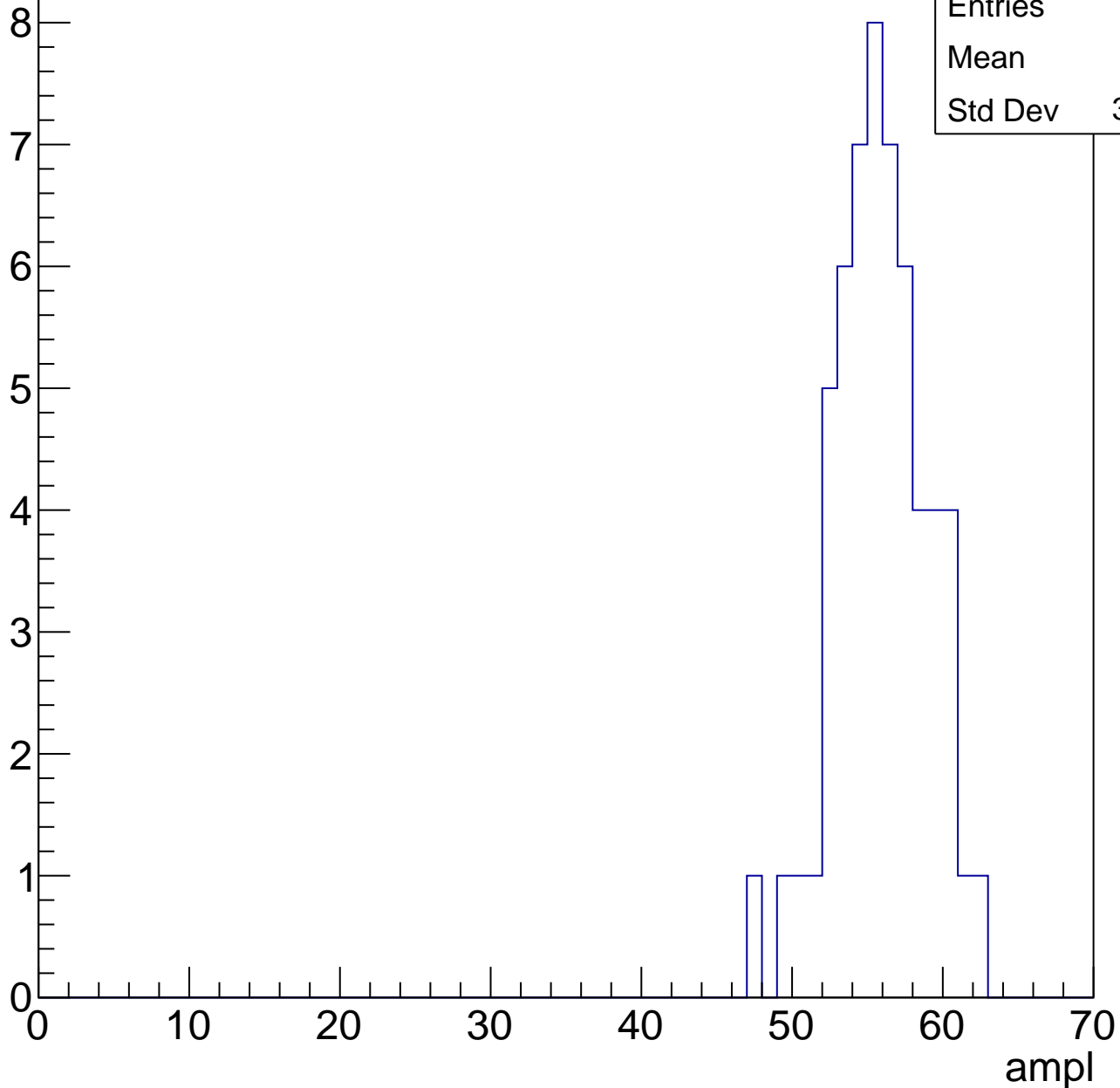


# B0L002S, U1-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 55.4  |
| Std Dev | 3.031 |

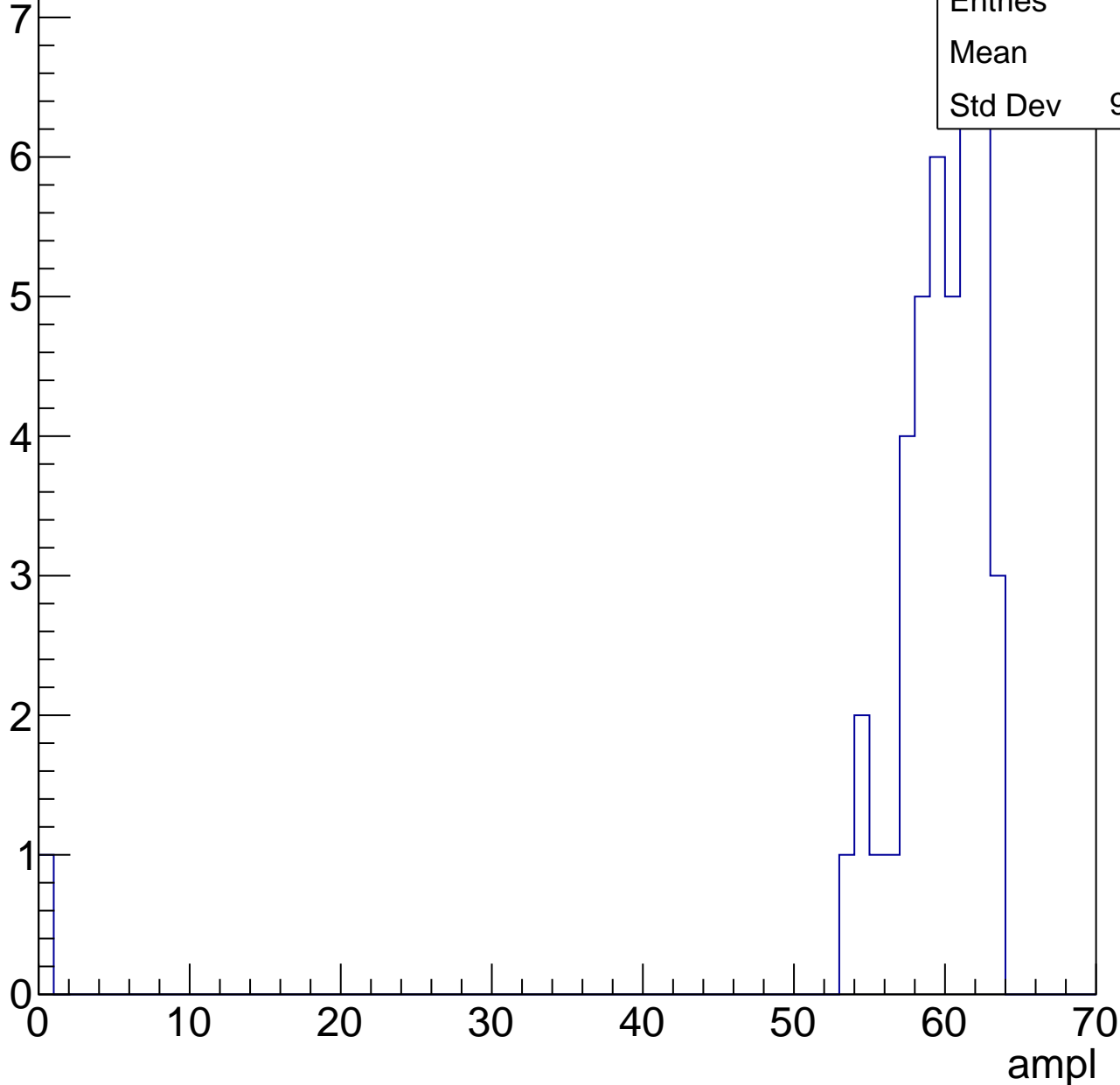


# B0L002S, U1-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 58    |
| Std Dev | 9.294 |

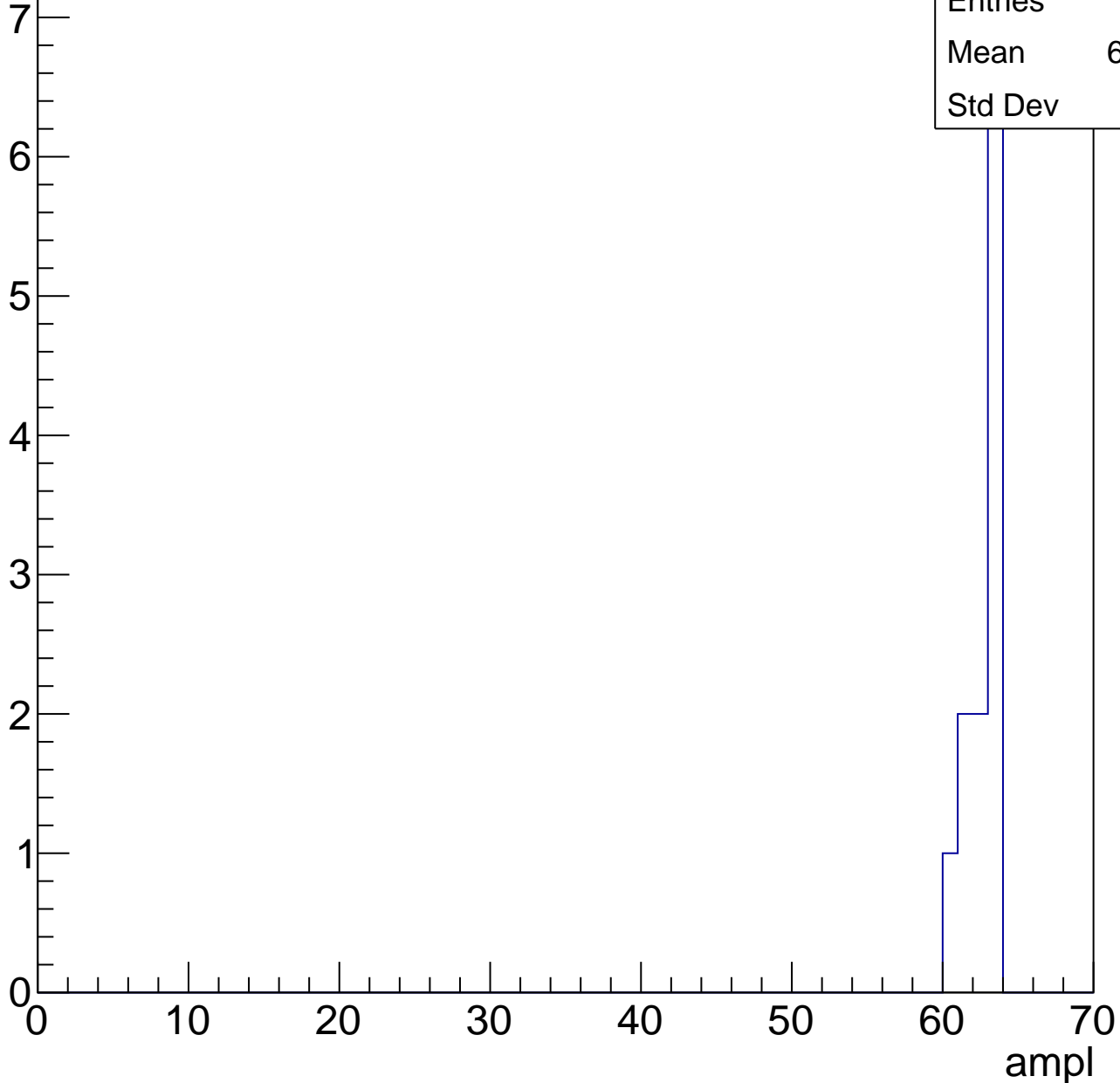


# B0L002S, U1-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 12    |
| Mean    | 62.25 |
| Std Dev | 1.01  |





# B0L002S, U1-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch107, adc0

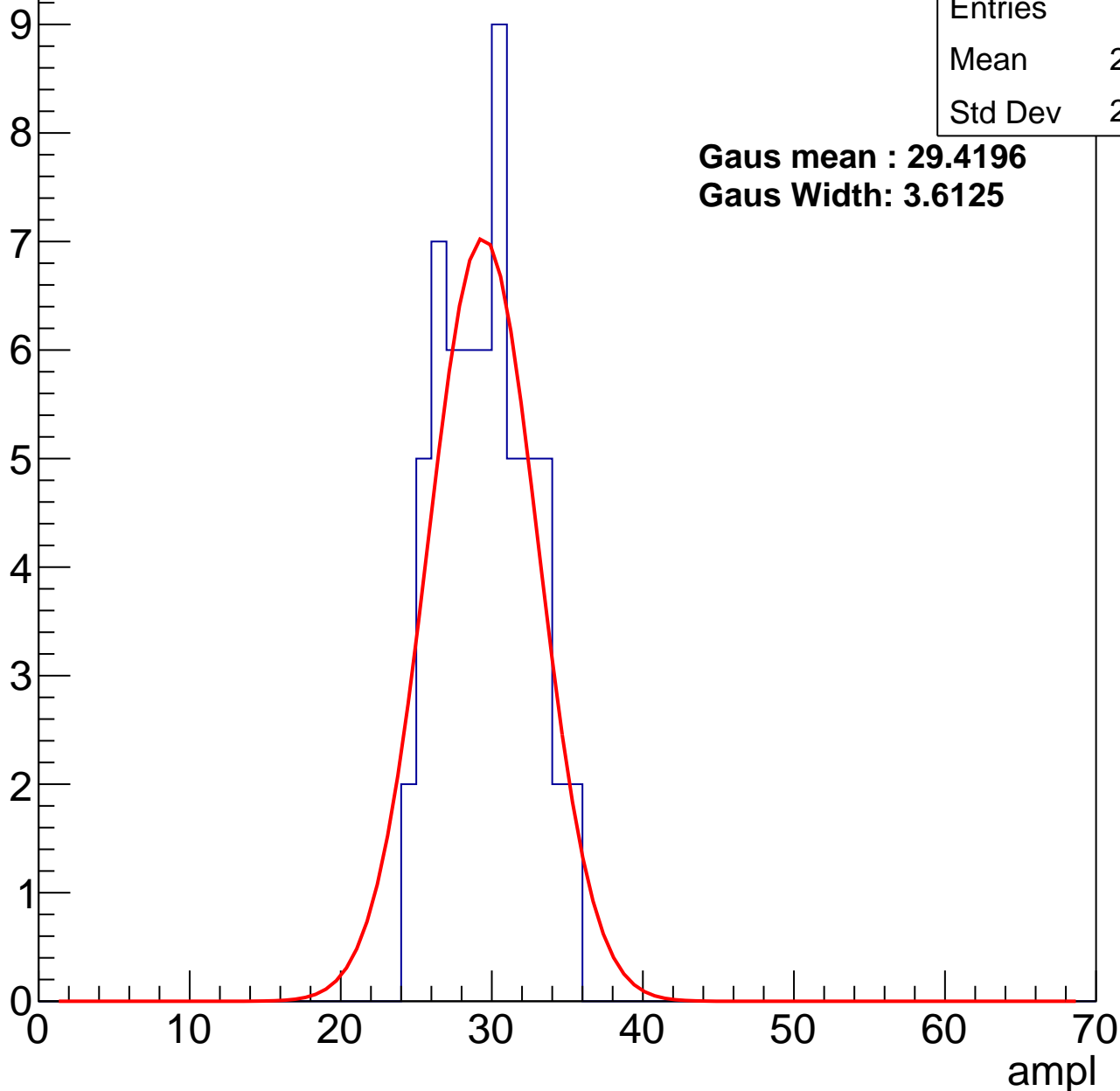
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 29.12 |
| Std Dev | 2.882 |

**Gaus mean : 29.4196**

**Gaus Width: 3.6125**



# B0L002S, U1-ch107, adc1

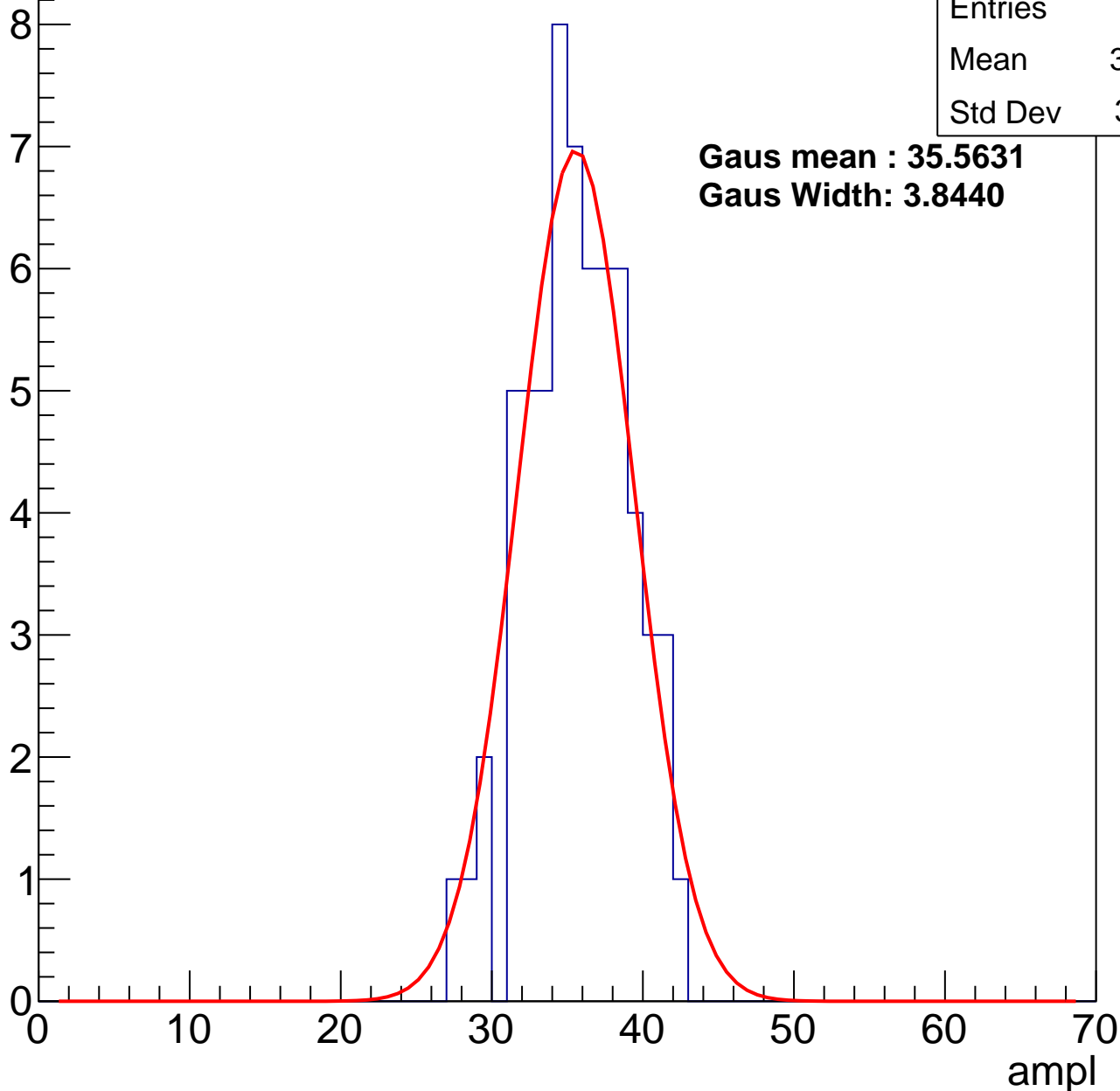
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 35.19 |
| Std Dev | 3.371 |

**Gaus mean : 35.5631**

**Gaus Width: 3.8440**



# B0L002S, U1-ch107, adc2

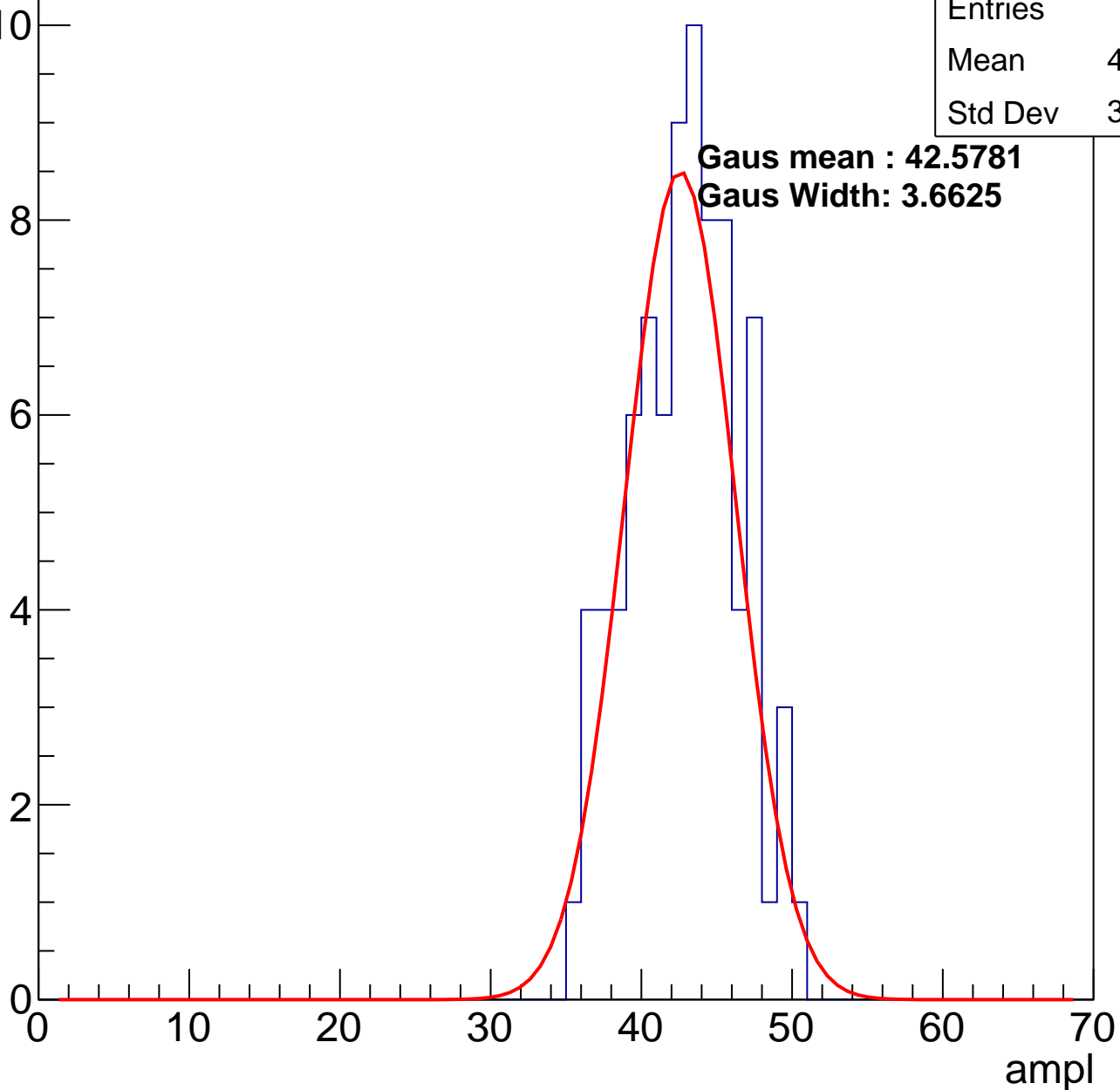
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 42.37 |
| Std Dev | 3.529 |

**Gaus mean : 42.5781**

**Gaus Width: 3.6625**

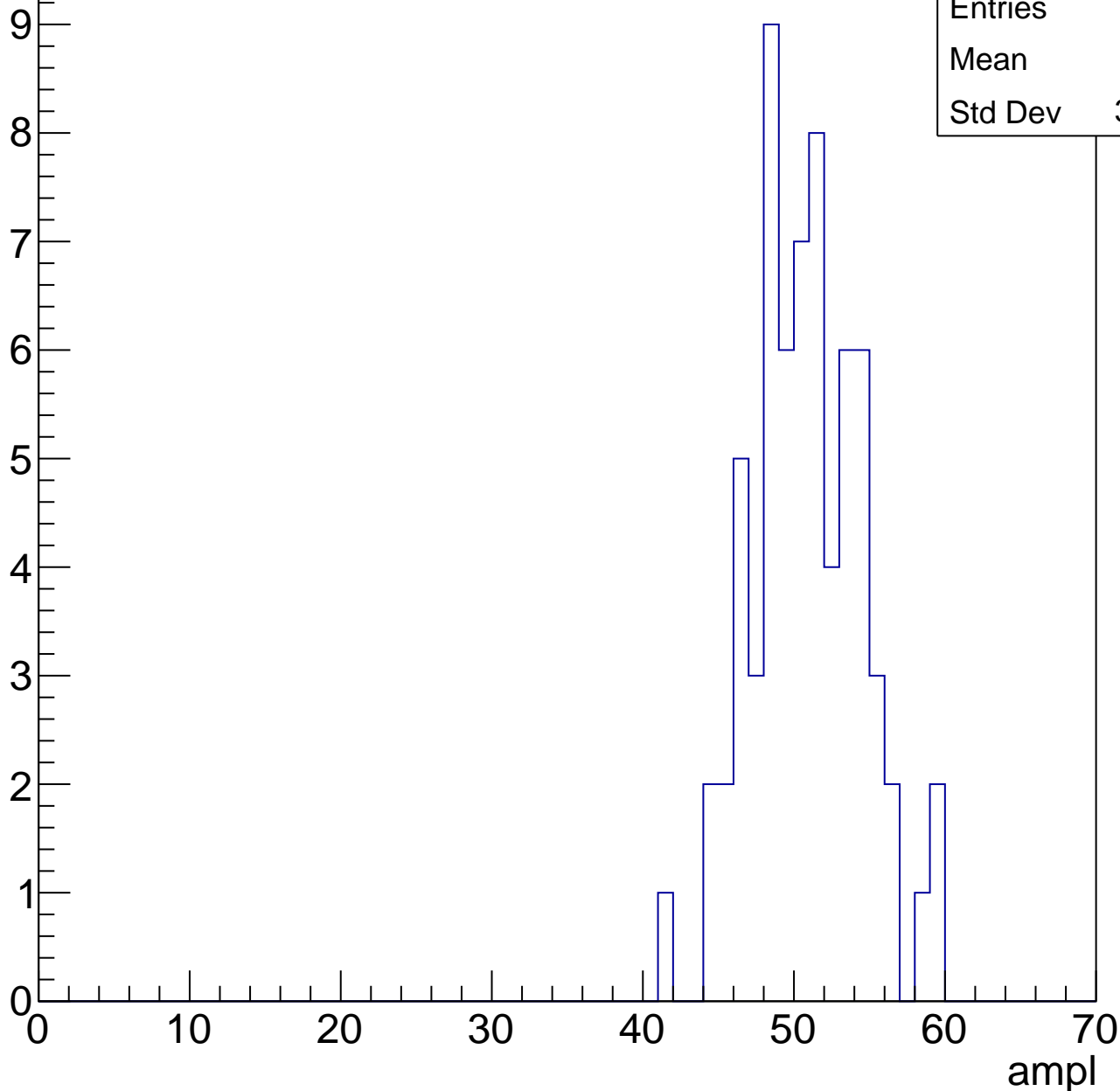


# B0L002S, U1-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 50.4  |
| Std Dev | 3.641 |

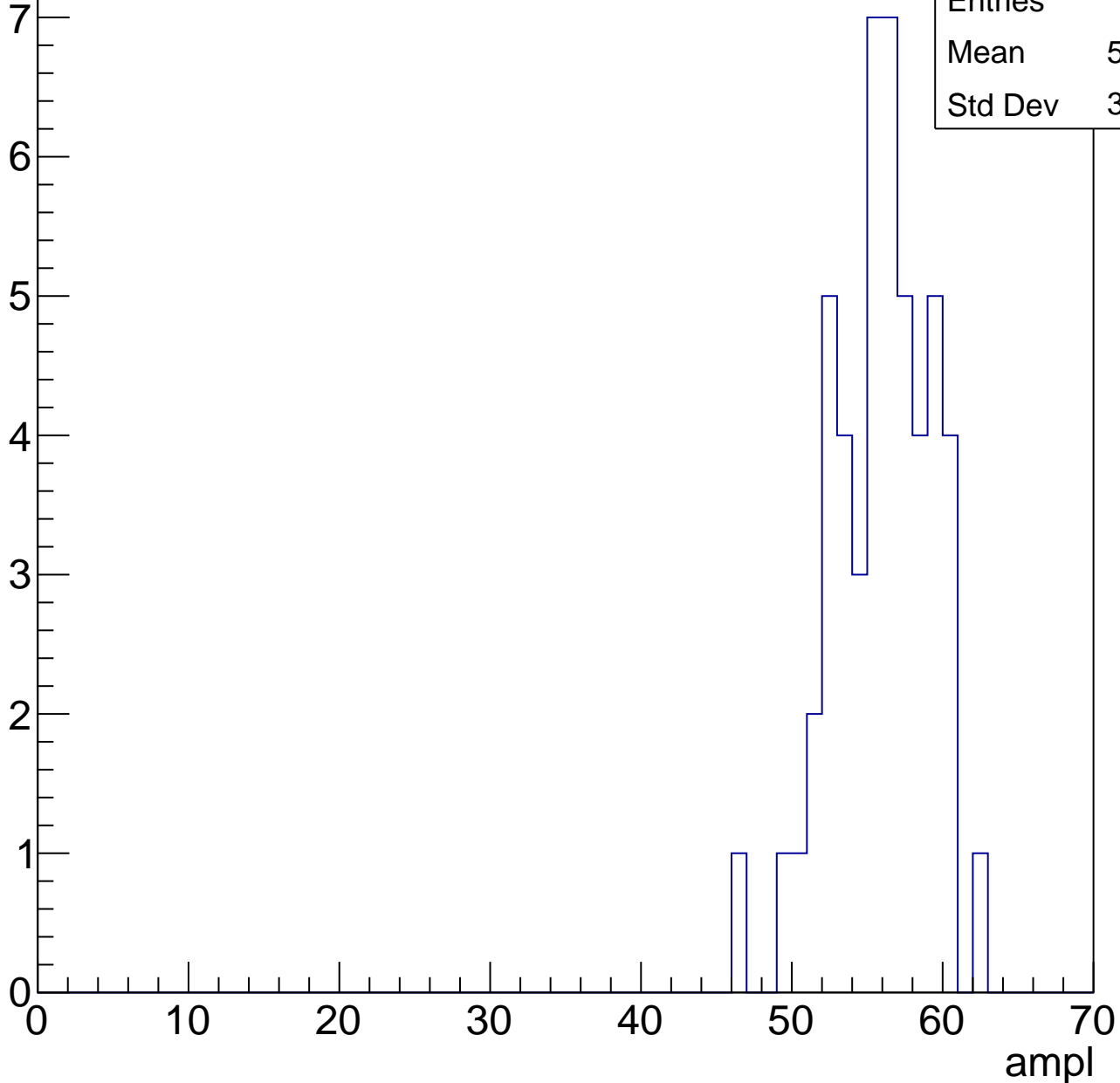


# B0L002S, U1-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 55.44 |
| Std Dev | 3.226 |

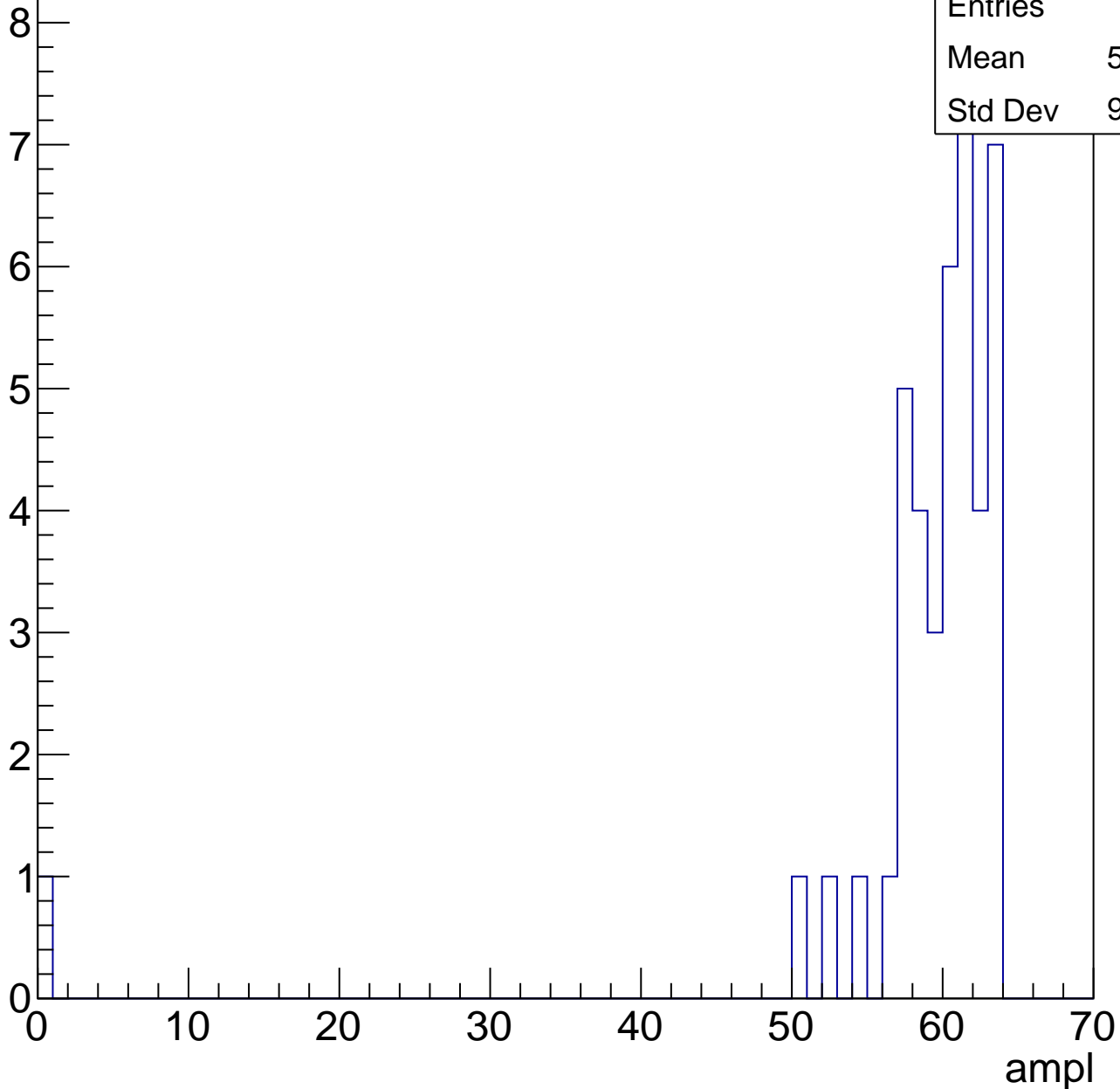


# B0L002S, U1-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 58.17 |
| Std Dev | 9.544 |

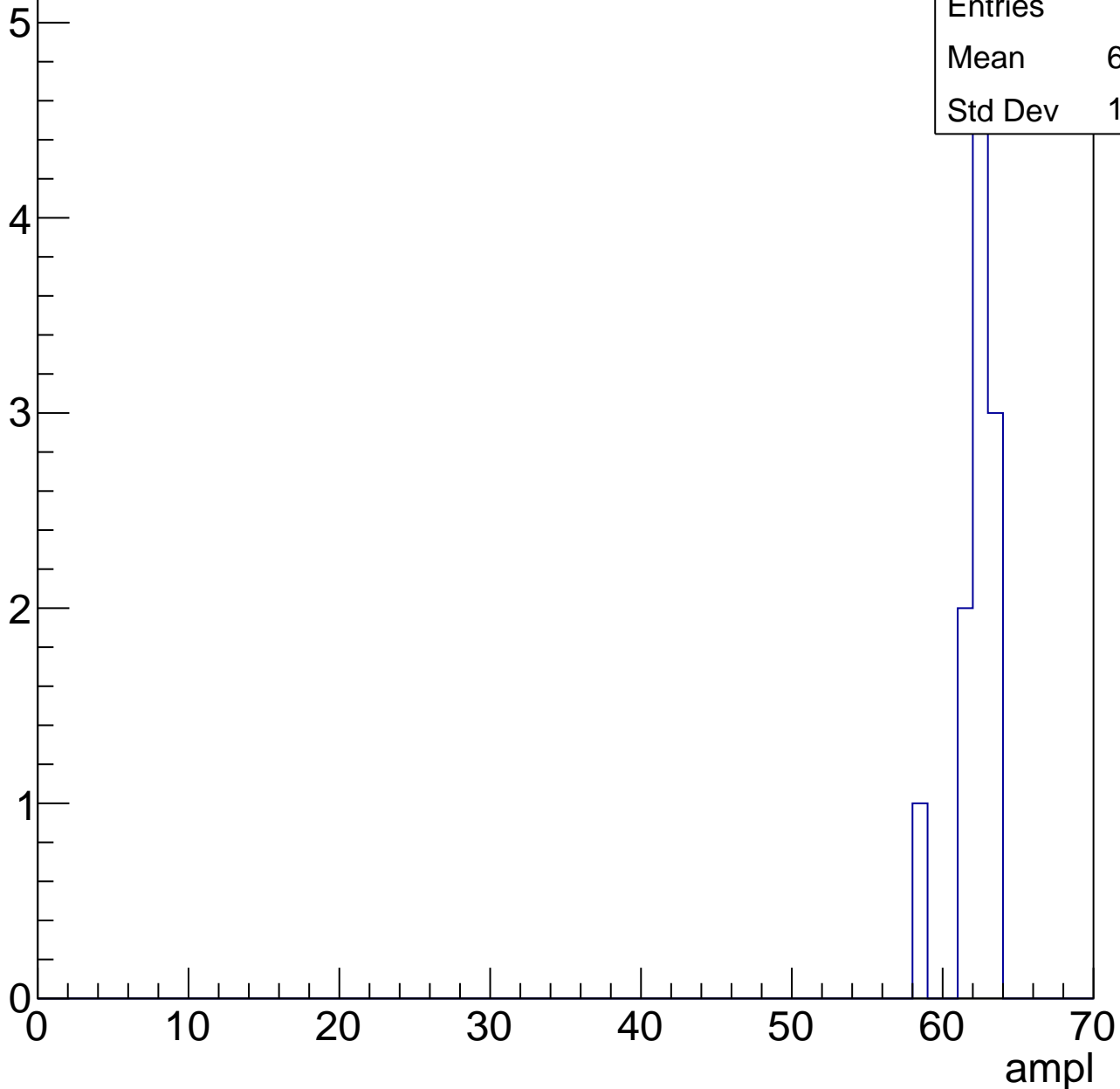


# B0L002S, U1-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 61.73 |
| Std Dev | 1.355 |





# B0L002S, U1-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch108, adc0

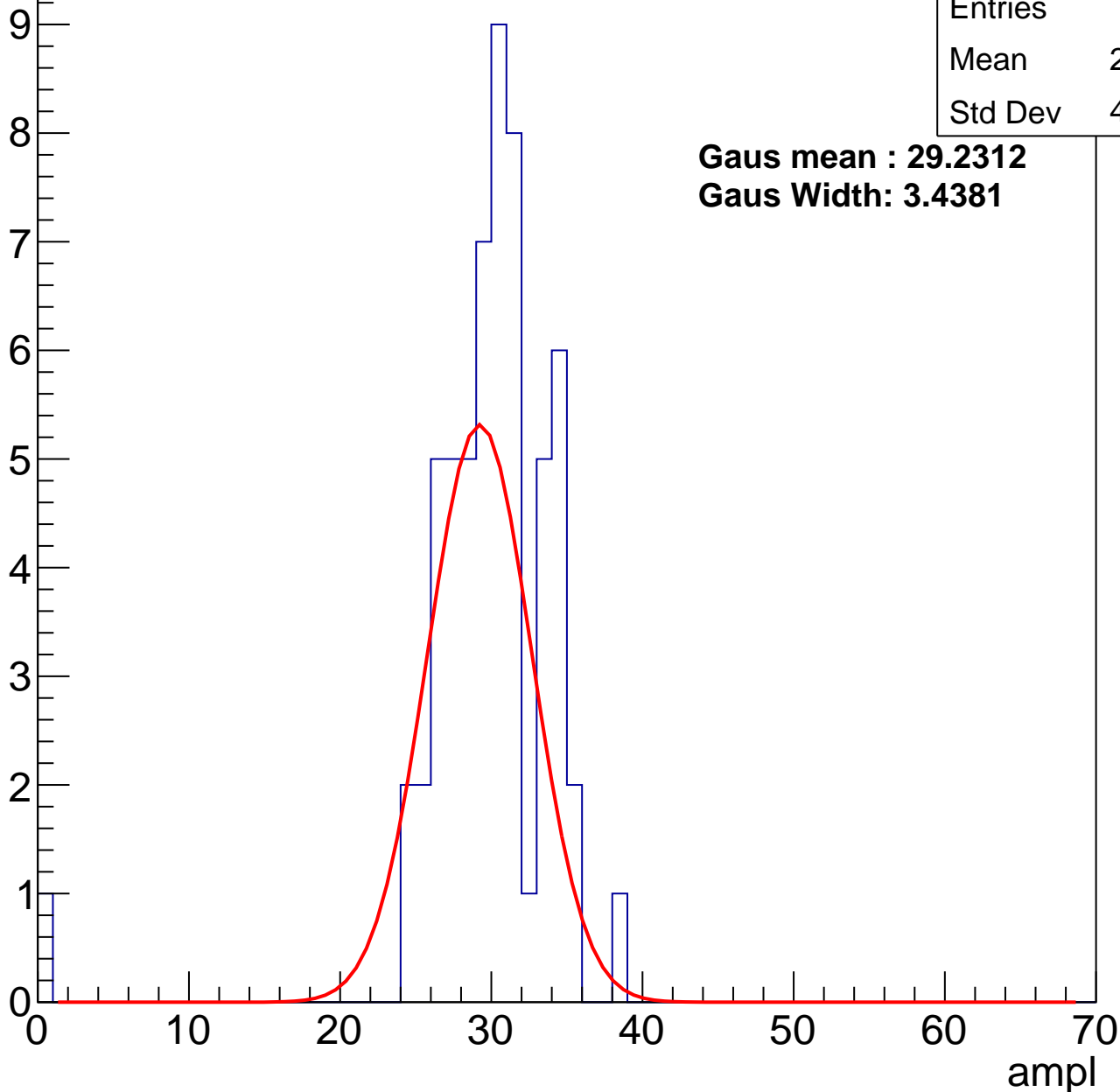
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 29.37 |
| Std Dev | 4.902 |

**Gaus mean : 29.2312**

**Gaus Width: 3.4381**



# B0L002S, U1-ch108, adc1

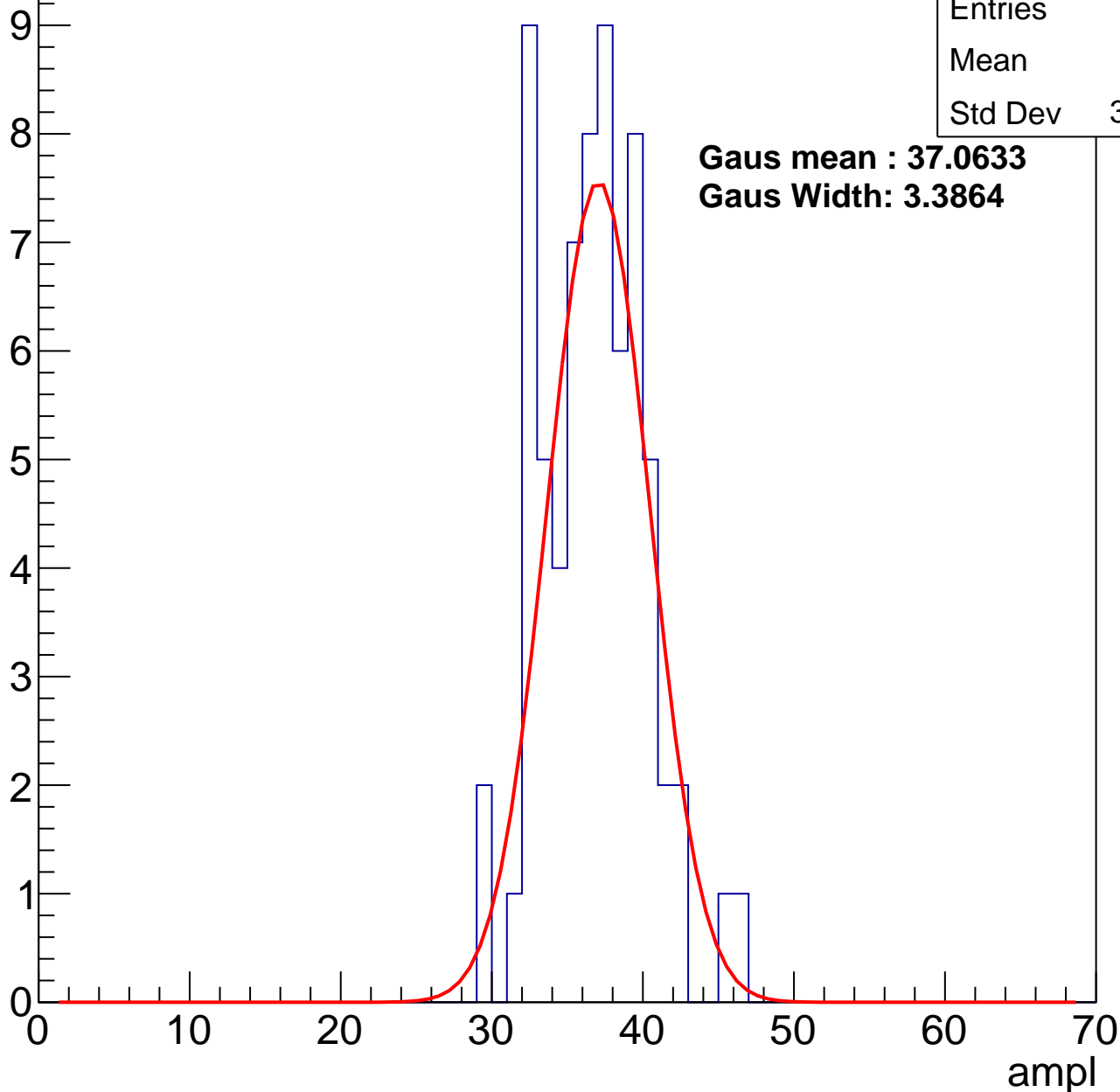
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 36.3  |
| Std Dev | 3.416 |

**Gaus mean : 37.0633**

**Gaus Width: 3.3864**



# B0L002S, U1-ch108, adc2

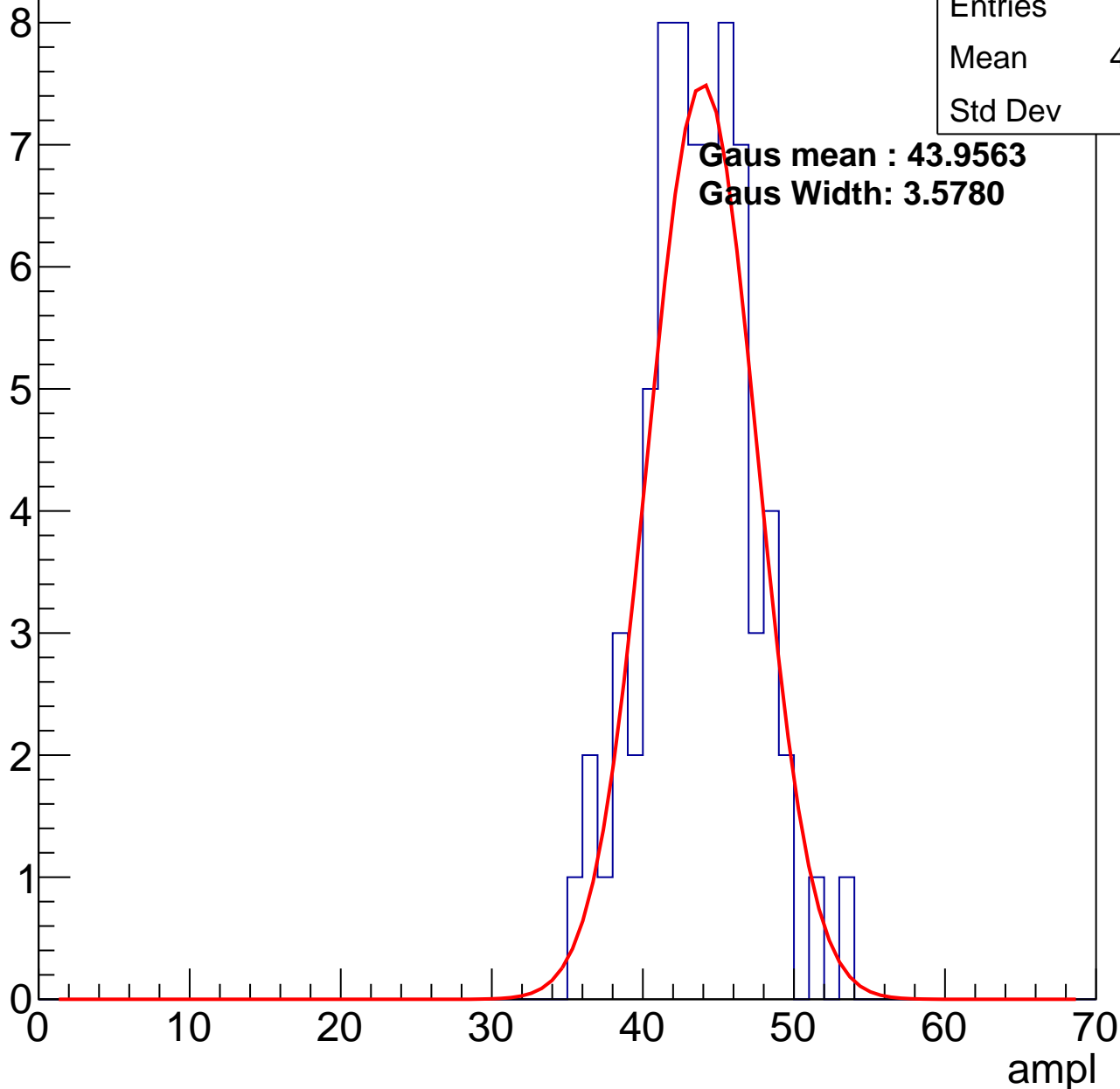
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 43.23 |
| Std Dev | 3.53  |

**Gaus mean : 43.9563**

**Gaus Width: 3.5780**

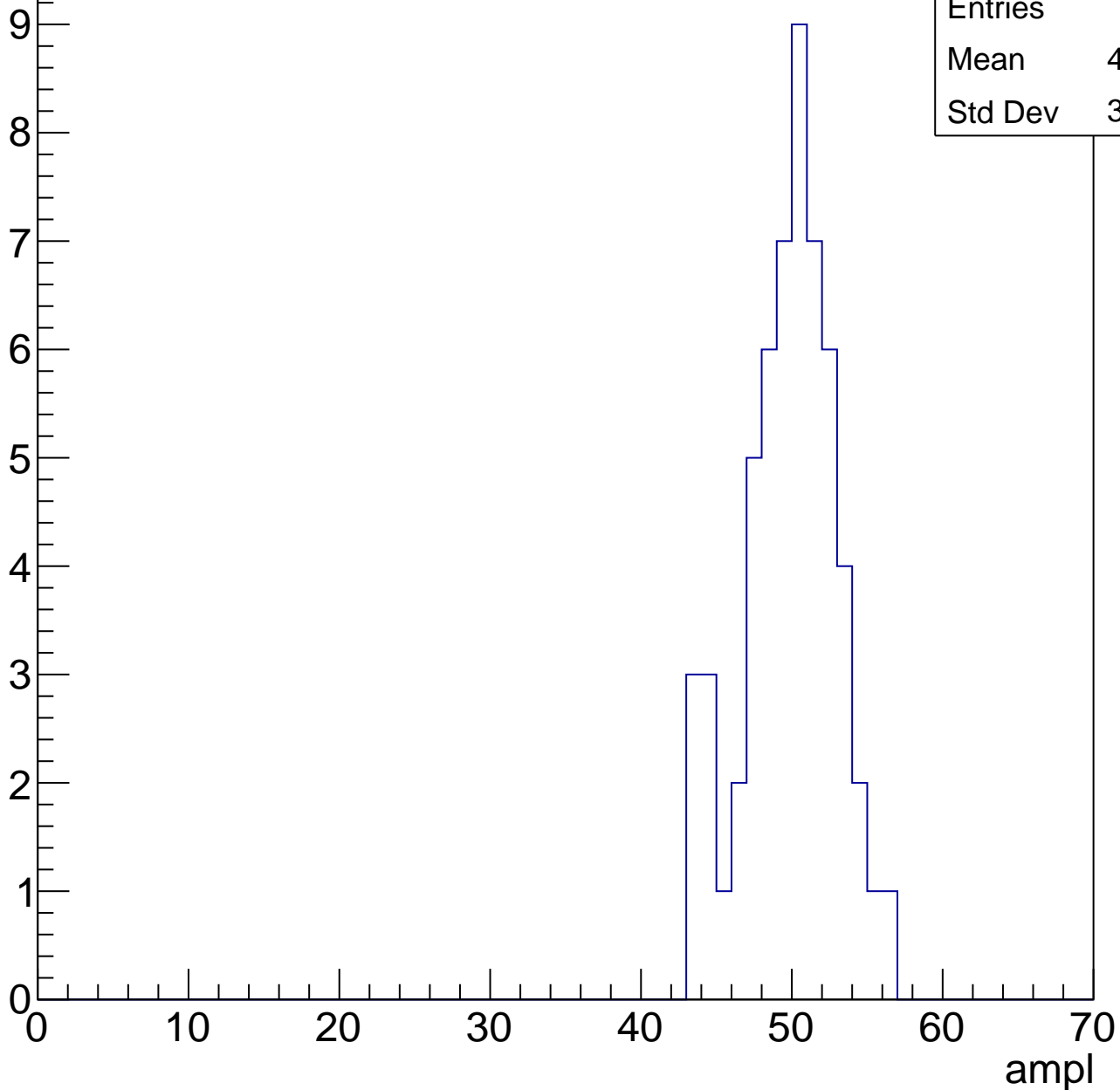


# B0L002S, U1-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 49.37 |
| Std Dev | 3.036 |

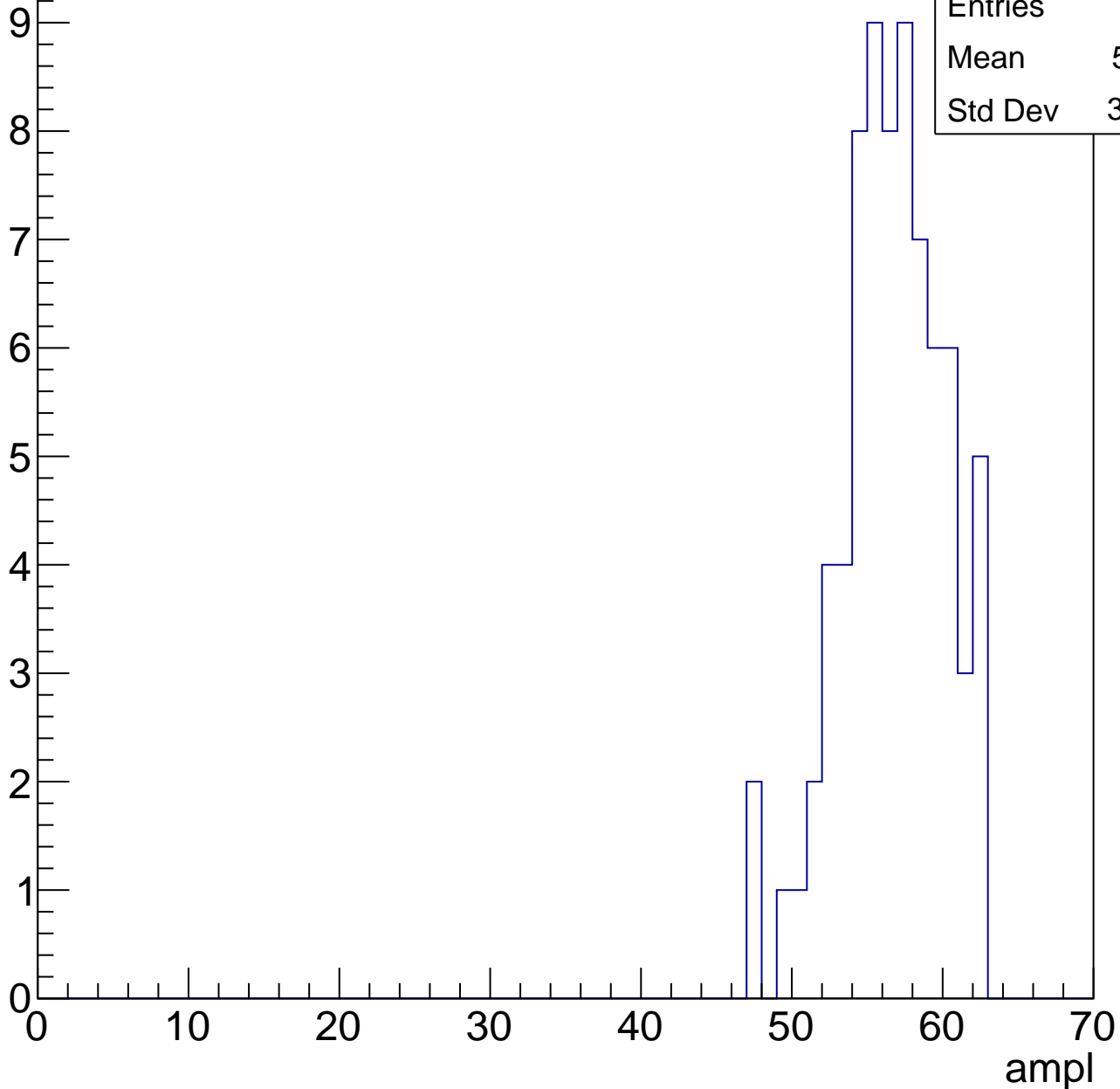


# B0L002S, U1-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 56.21 |
| Std Dev | 3.419 |

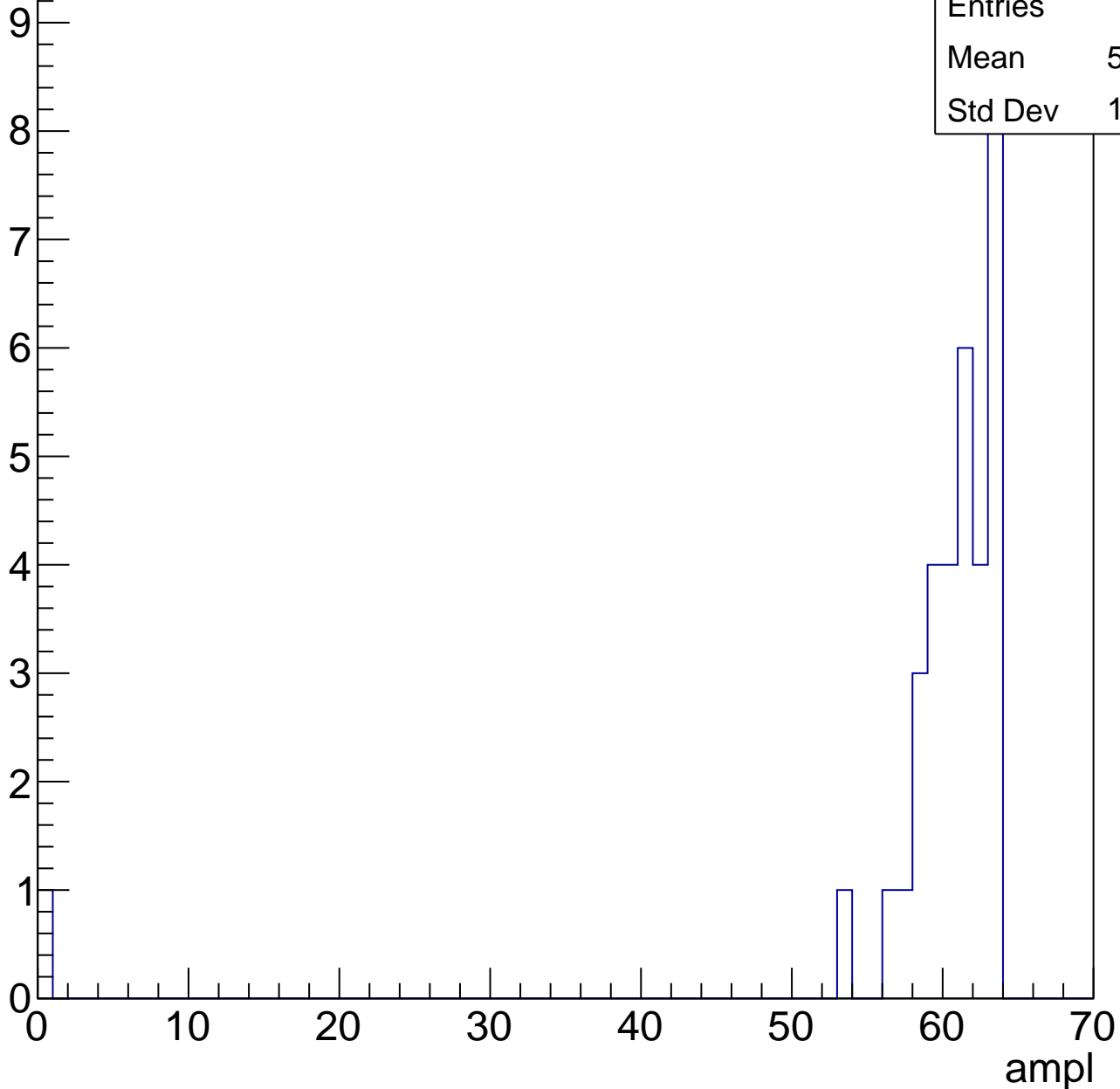


# B0L002S, U1-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 34    |
| Mean    | 58.74 |
| Std Dev | 10.49 |



# B0L002S, U1-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 28.68 |
| Std Dev | 3.885 |

**Gaus mean : 29.3701**

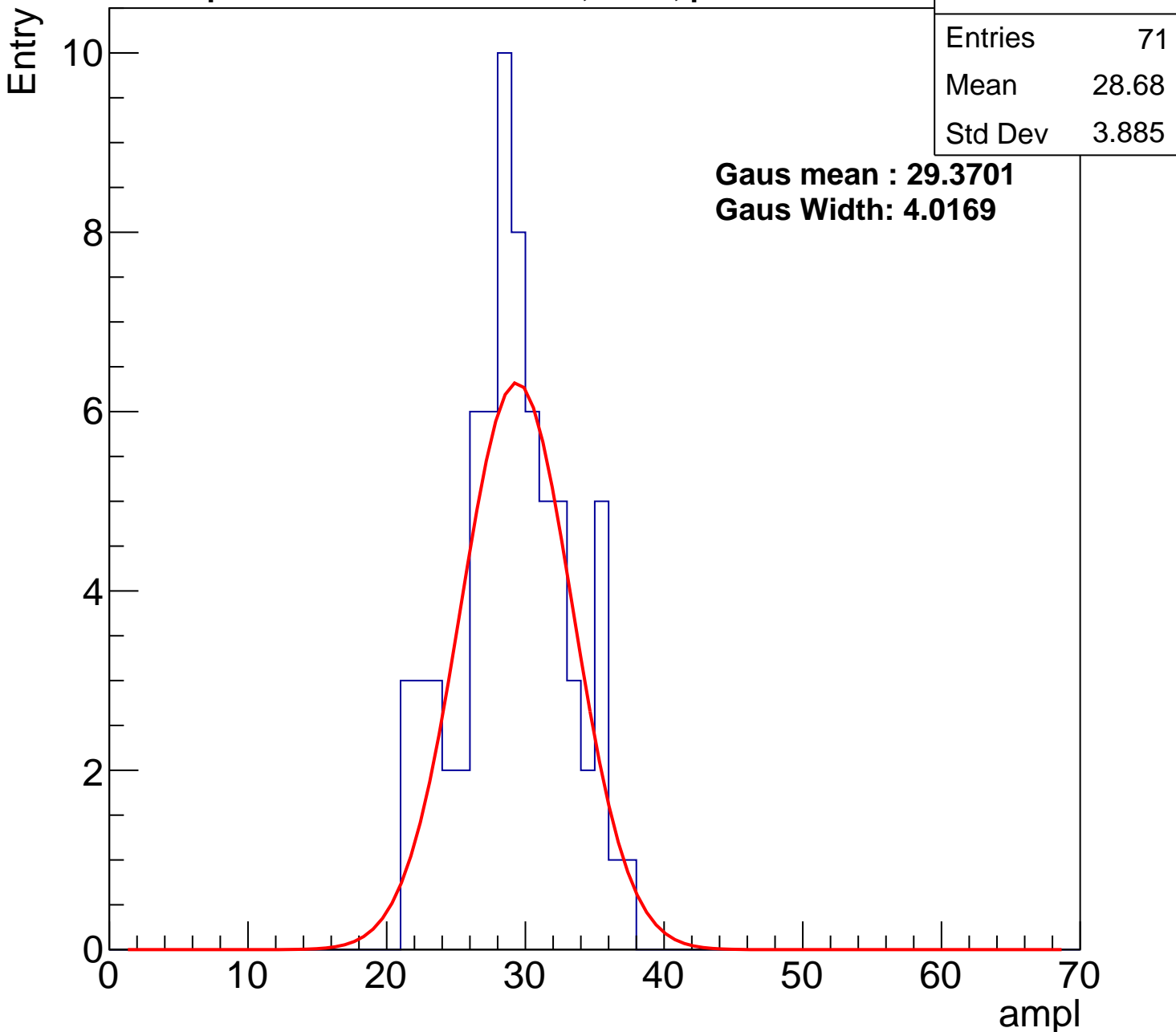
**Gaus Width: 4.0169**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch109, adc1

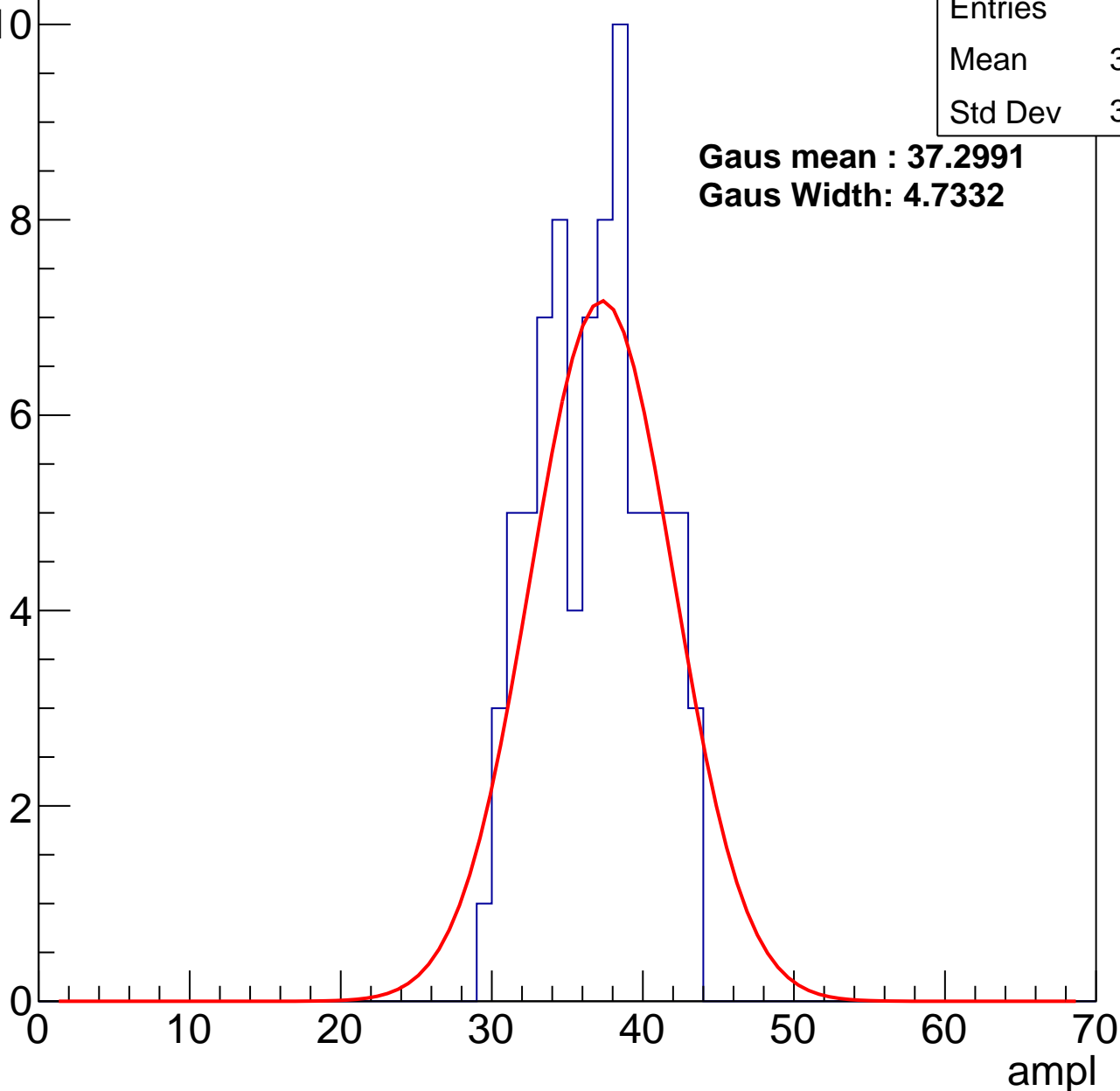
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 36.35 |
| Std Dev | 3.645 |

**Gaus mean : 37.2991**

**Gaus Width: 4.7332**



# B0L002S, U1-ch109, adc2

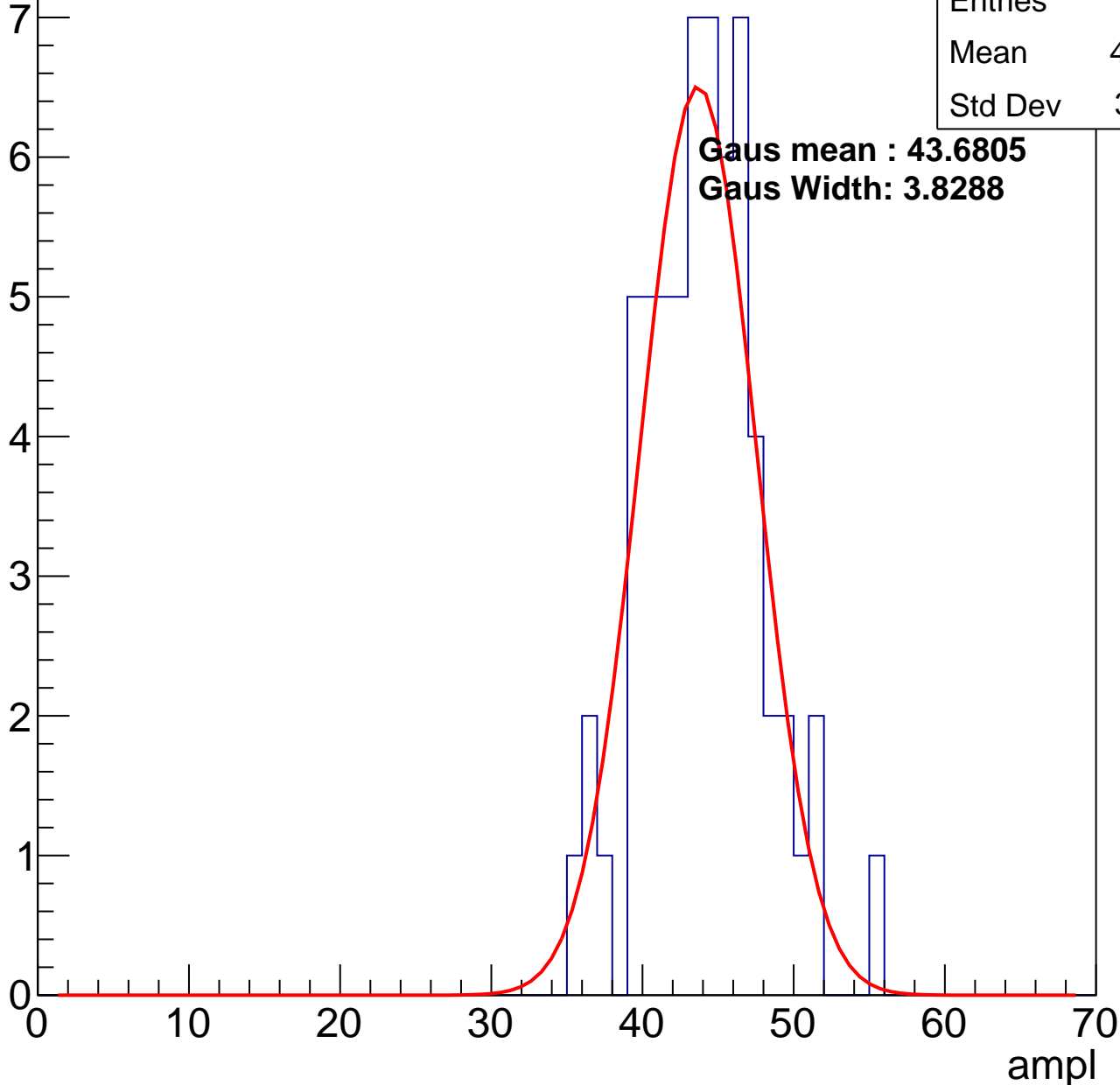
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 43.56 |
| Std Dev | 3.841 |

**Gaus mean : 43.6805**

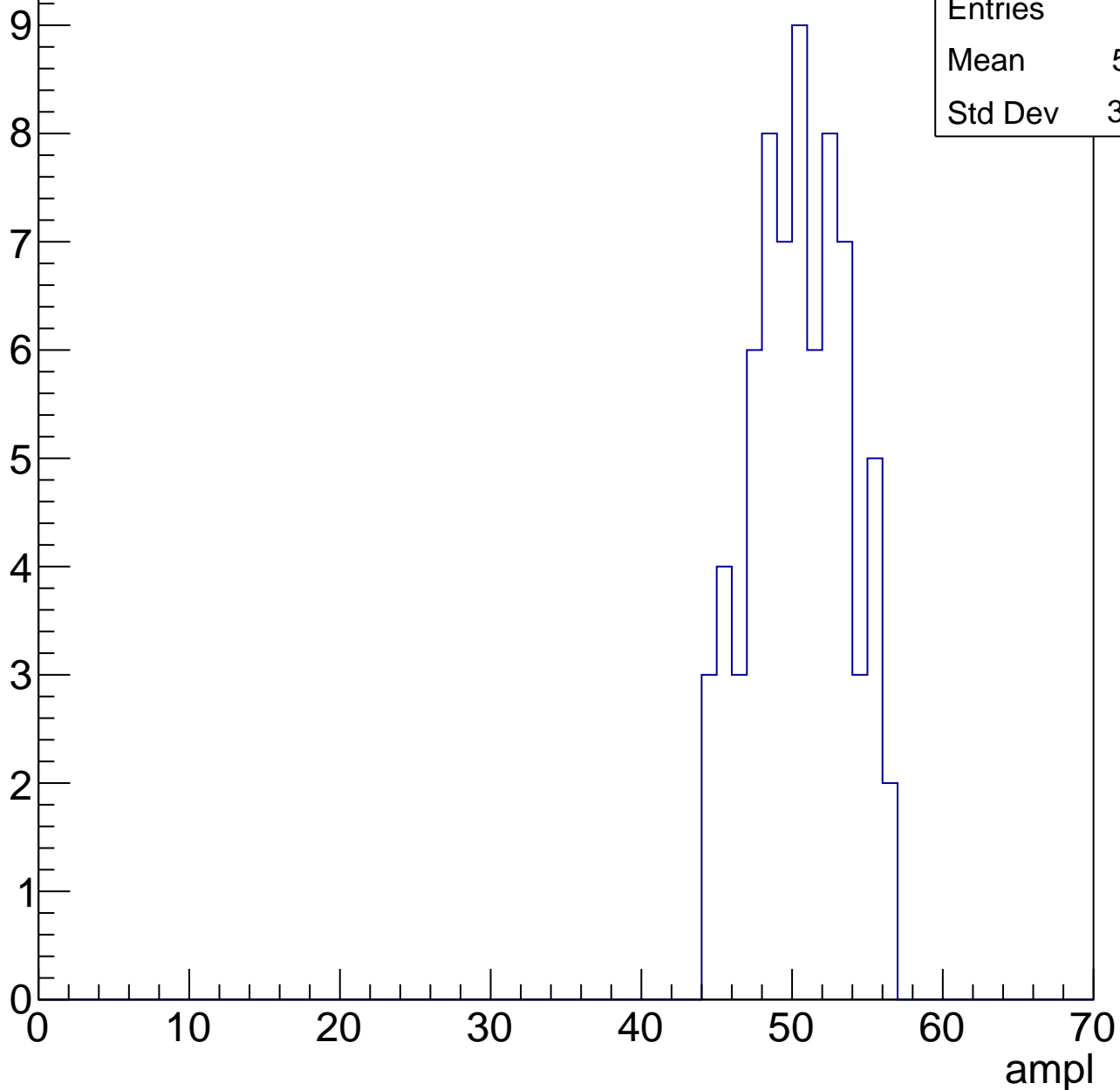
**Gaus Width: 3.8288**



# B0L002S, U1-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

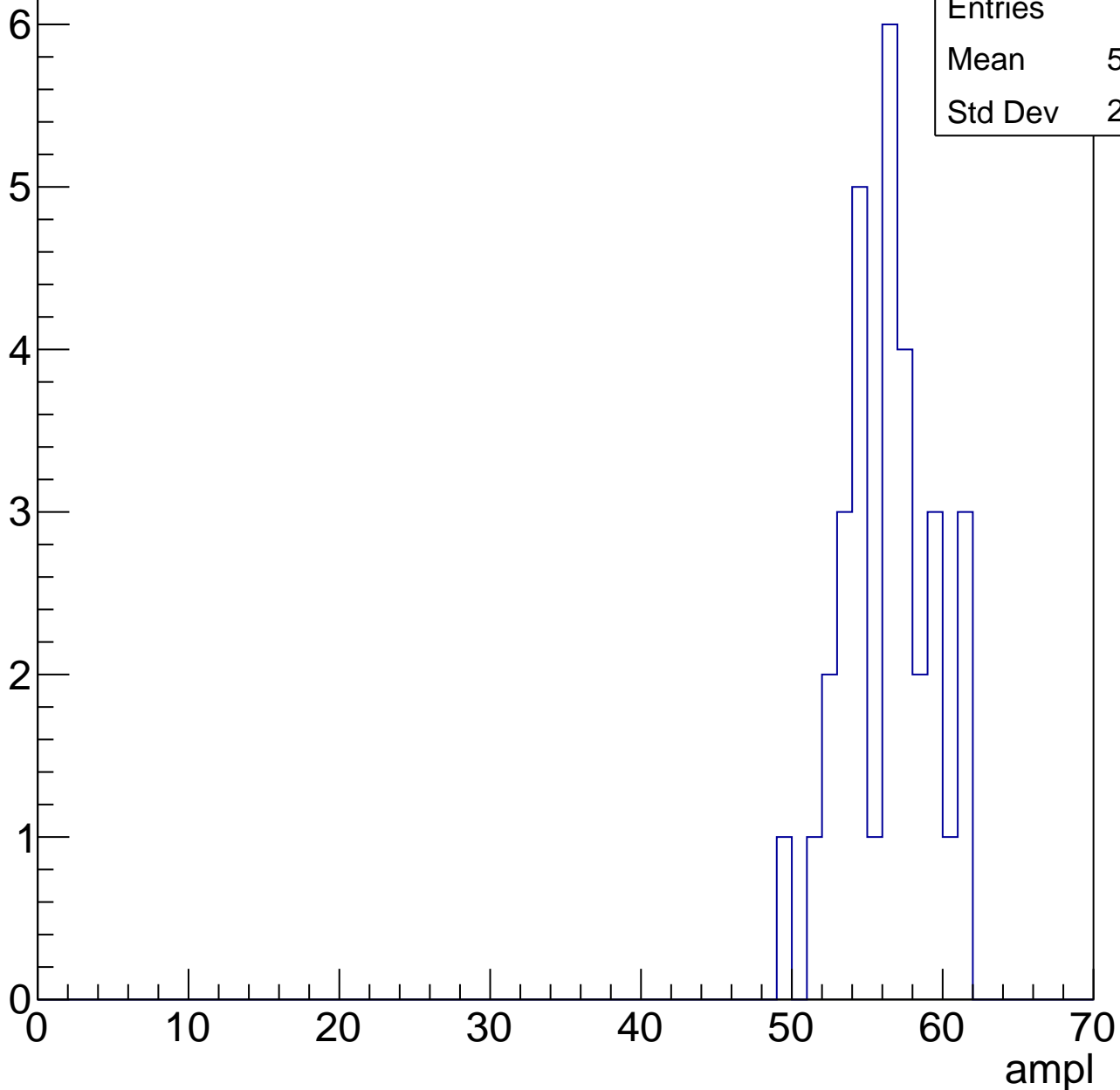


# B0L002S, U1-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 32    |
| Mean    | 55.88 |
| Std Dev | 2.976 |

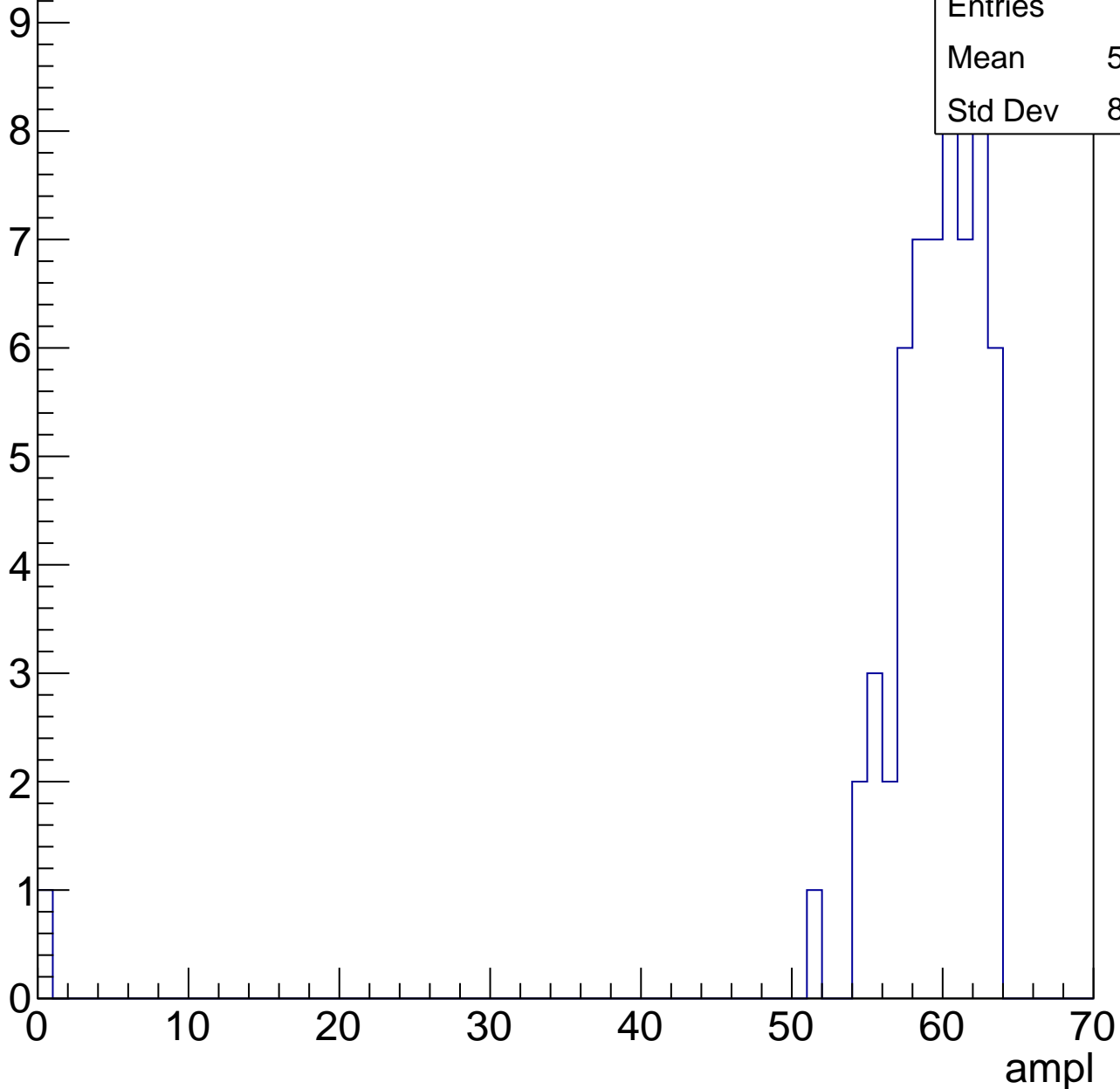


# B0L002S, U1-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

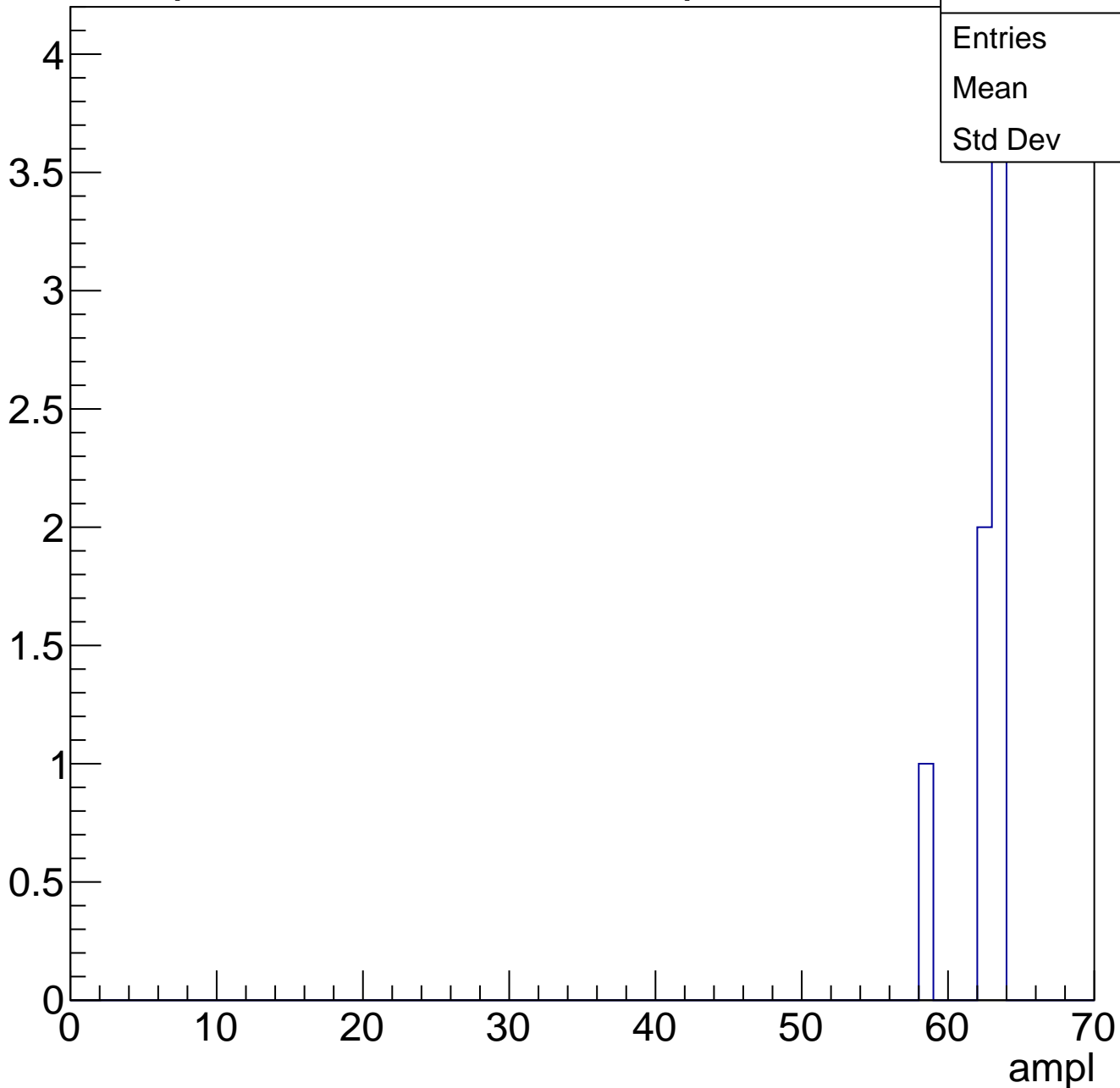
|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 58.27 |
| Std Dev | 8.094 |



# B0L002S, U1-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 7    |
| Mean    | 62   |
| Std Dev | 1.69 |



# B0L002S, U1-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 30.56 |
| Std Dev | 5.075 |

**Gaus mean : 31.9157**

**Gaus Width: 3.3709**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

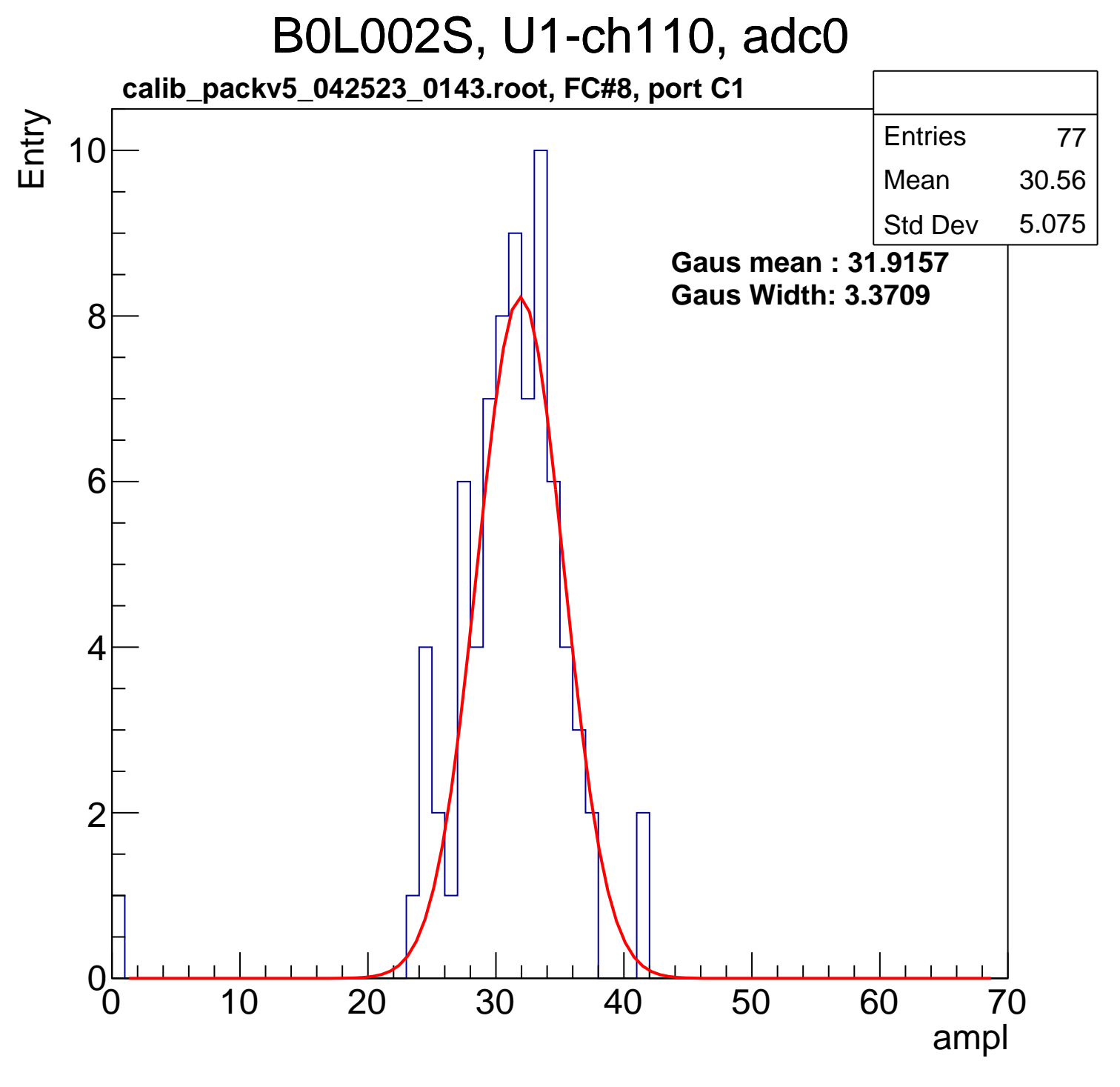
30

40

50

60

70



# B0L002S, U1-ch110, adc1

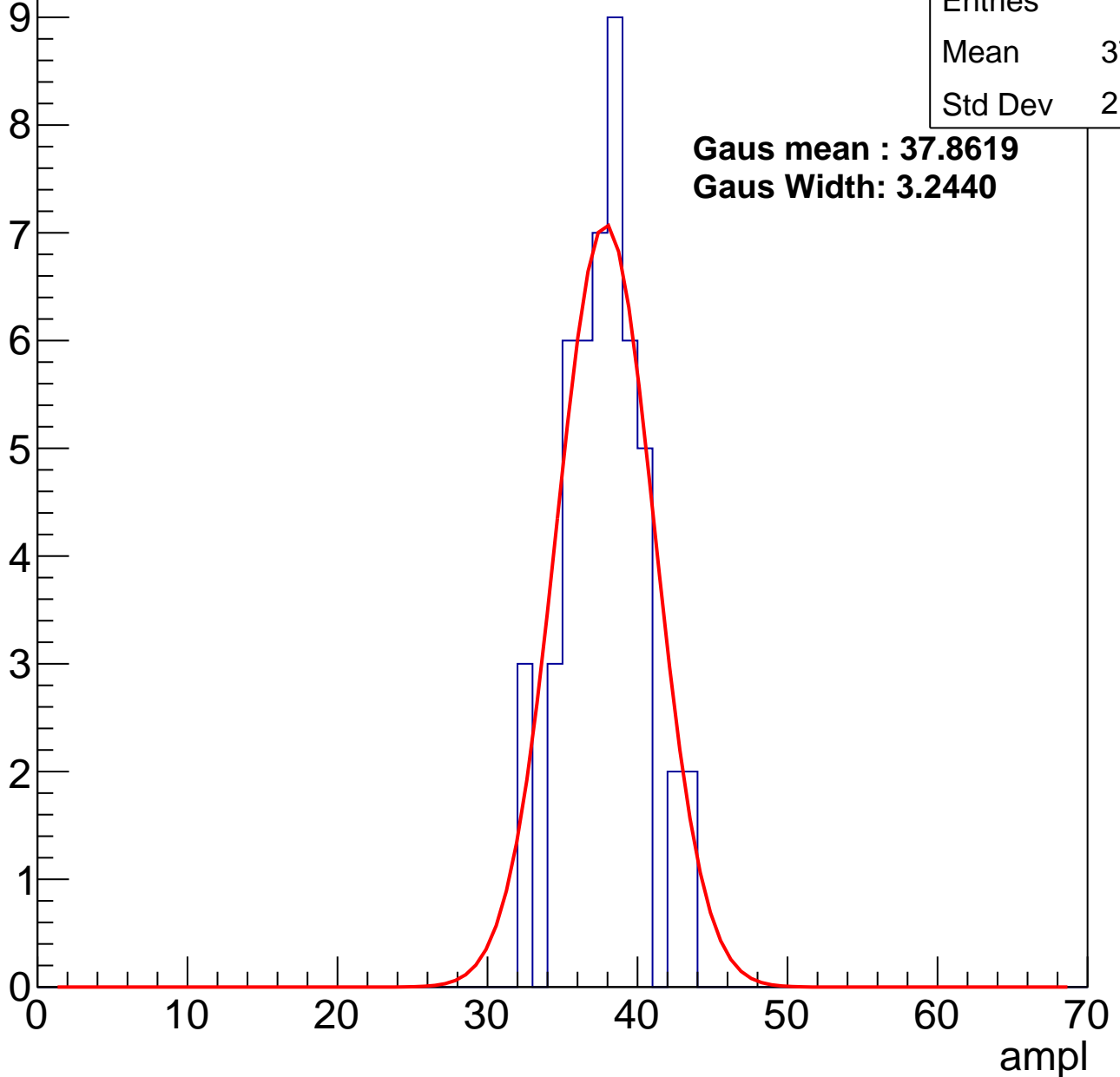
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 37.33 |
| Std Dev | 2.582 |

**Gaus mean : 37.8619**

**Gaus Width: 3.2440**



# B0L002S, U1-ch110, adc2

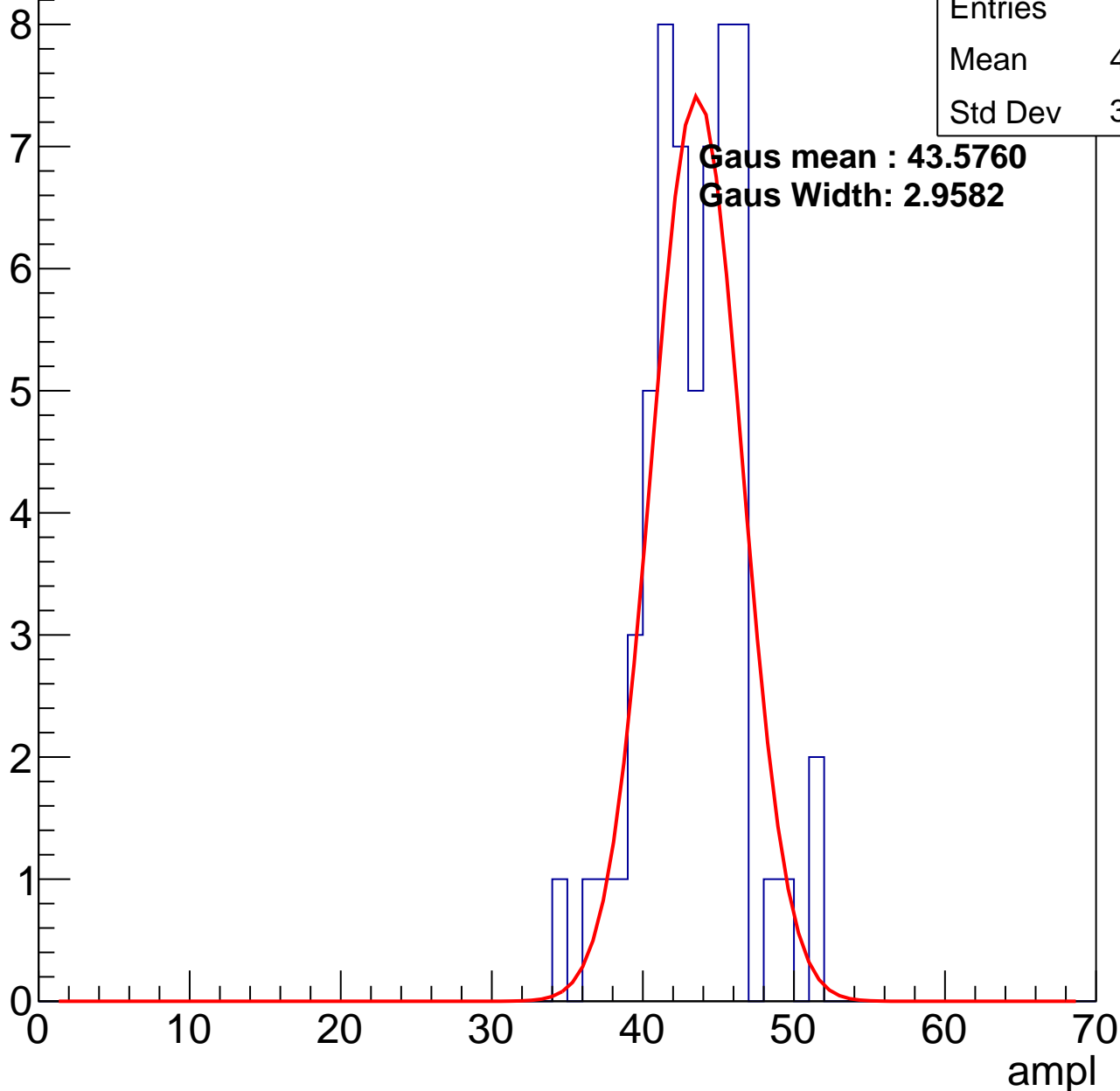
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 42.95 |
| Std Dev | 3.249 |

**Gaus mean : 43.5760**

**Gaus Width: 2.9582**



# B0L002S, U1-ch110, adc3

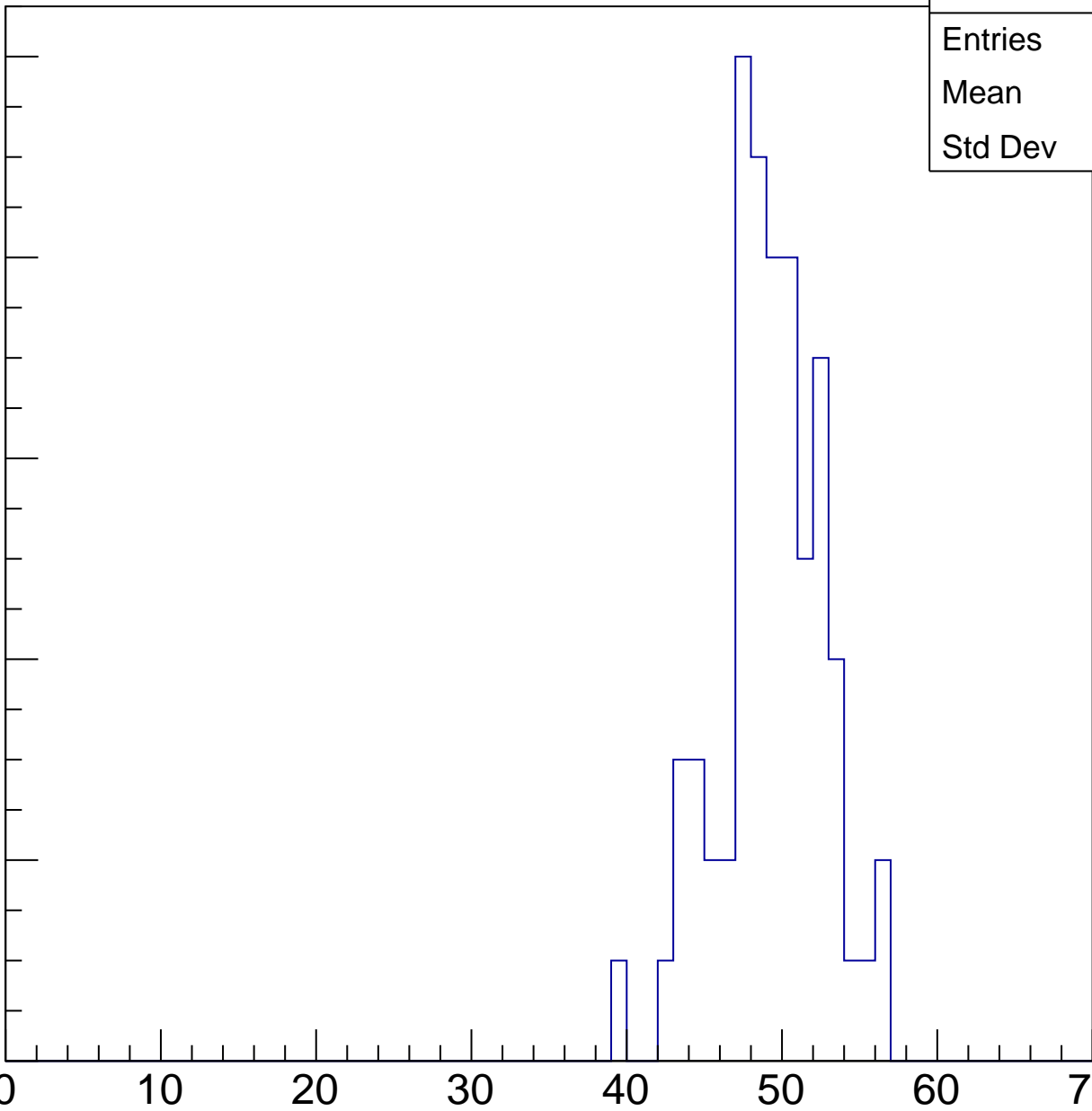
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 48.81 |
| Std Dev | 3.329 |

Entry

10  
8  
6  
4  
2  
0

ampl

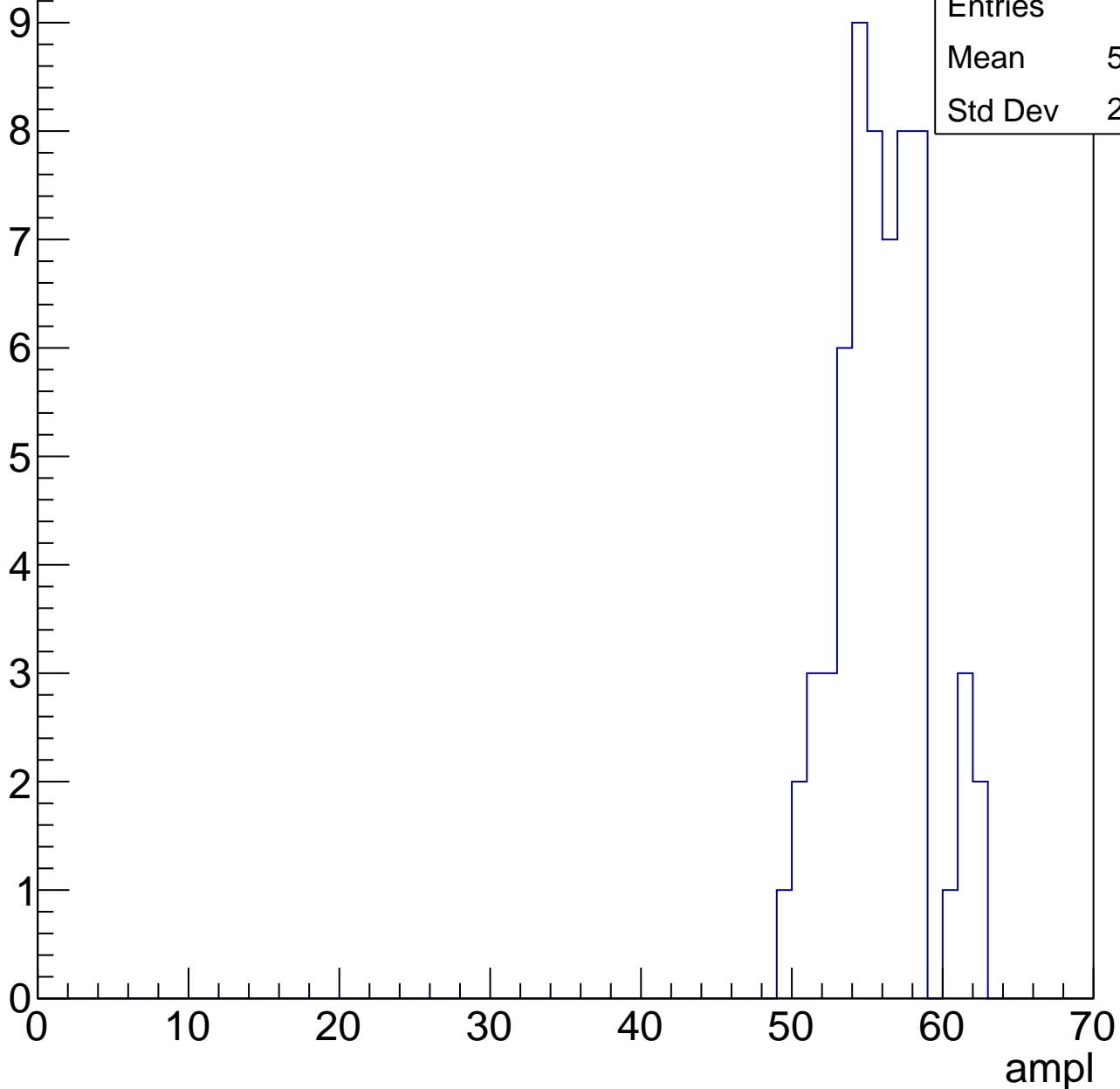


# B0L002S, U1-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 55.43 |
| Std Dev | 2.933 |



# B0L002S, U1-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

10

Entries 43

Mean 60.16

Std Dev 1.904

8

6

4

2

0

0

10

20

30

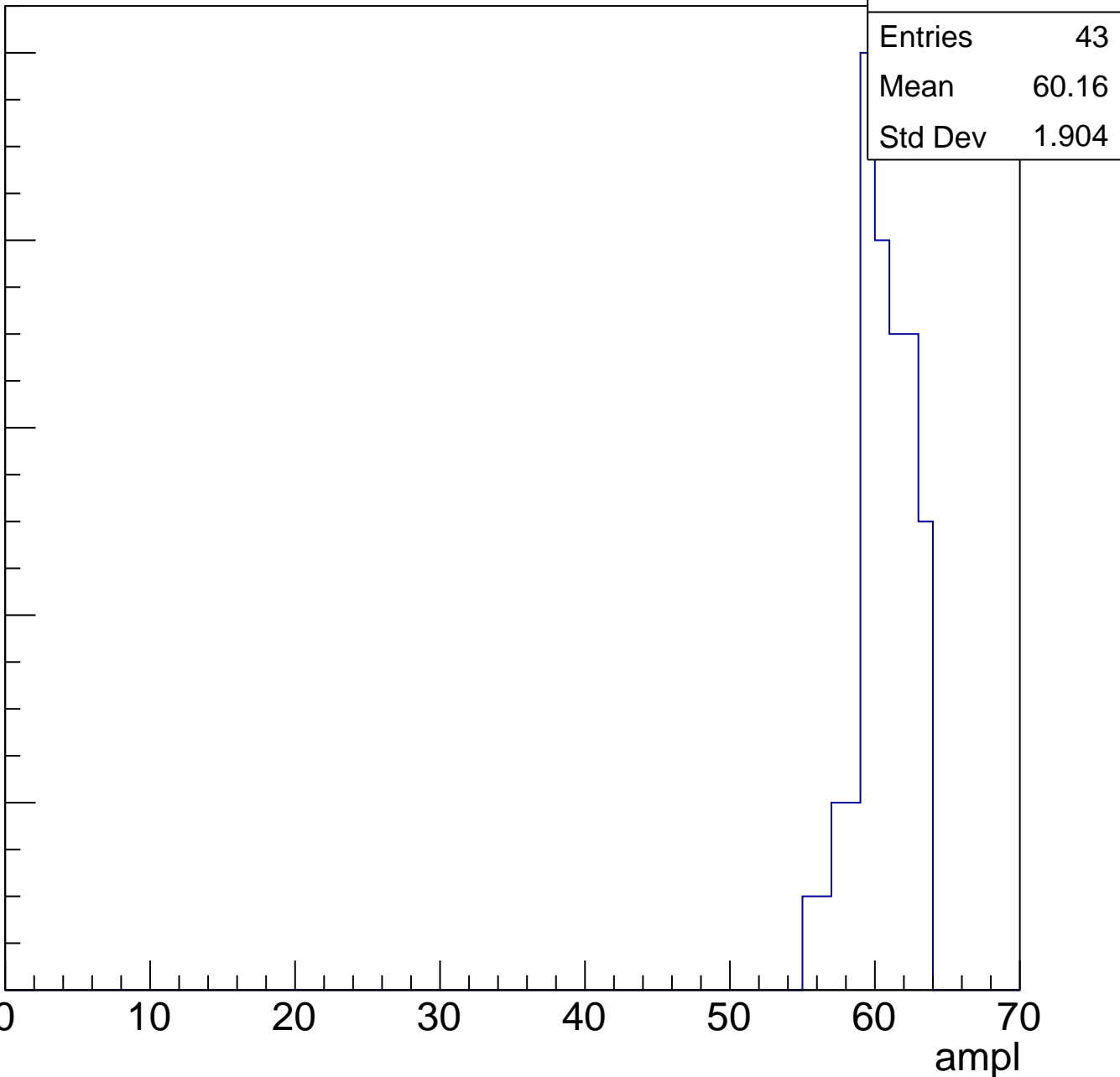
40

50

60

70

ampl

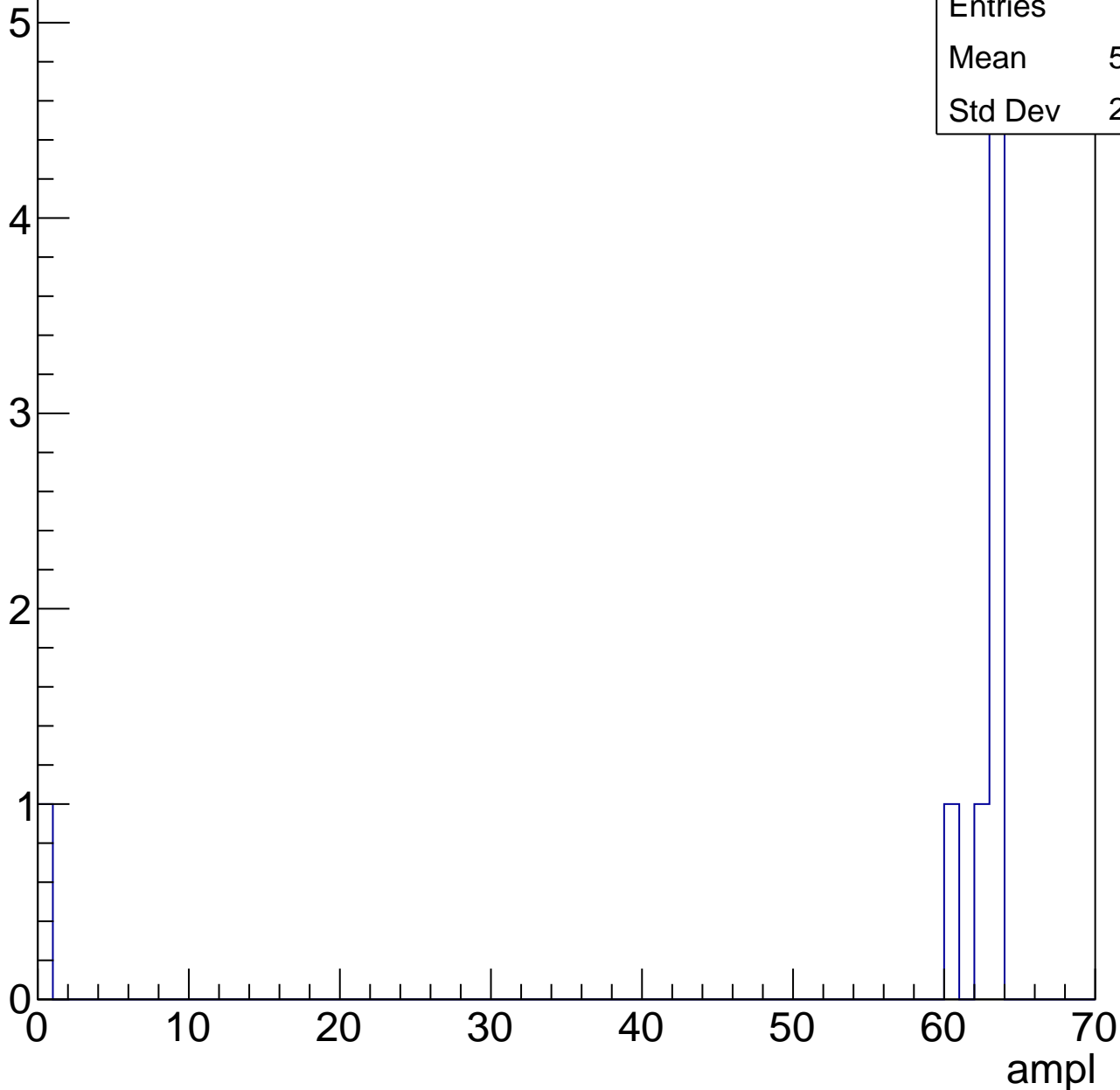


# B0L002S, U1-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 8     |
| Mean    | 54.62 |
| Std Dev | 20.67 |





# B0L002S, U1-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch111, adc0

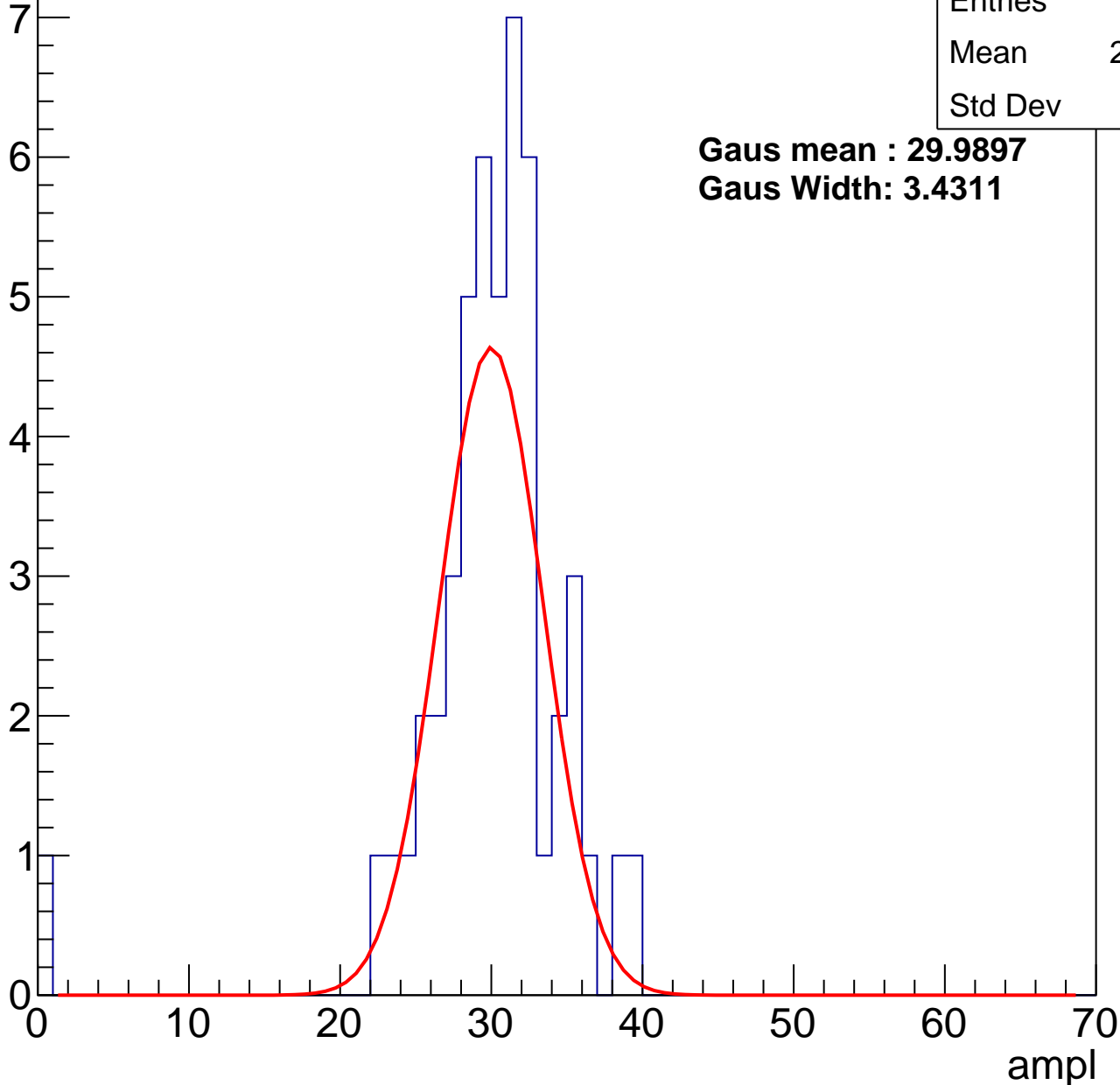
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 49    |
| Mean    | 29.47 |
| Std Dev | 5.53  |

**Gaus mean : 29.9897**

**Gaus Width: 3.4311**



# B0L002S, U1-ch111, adc1

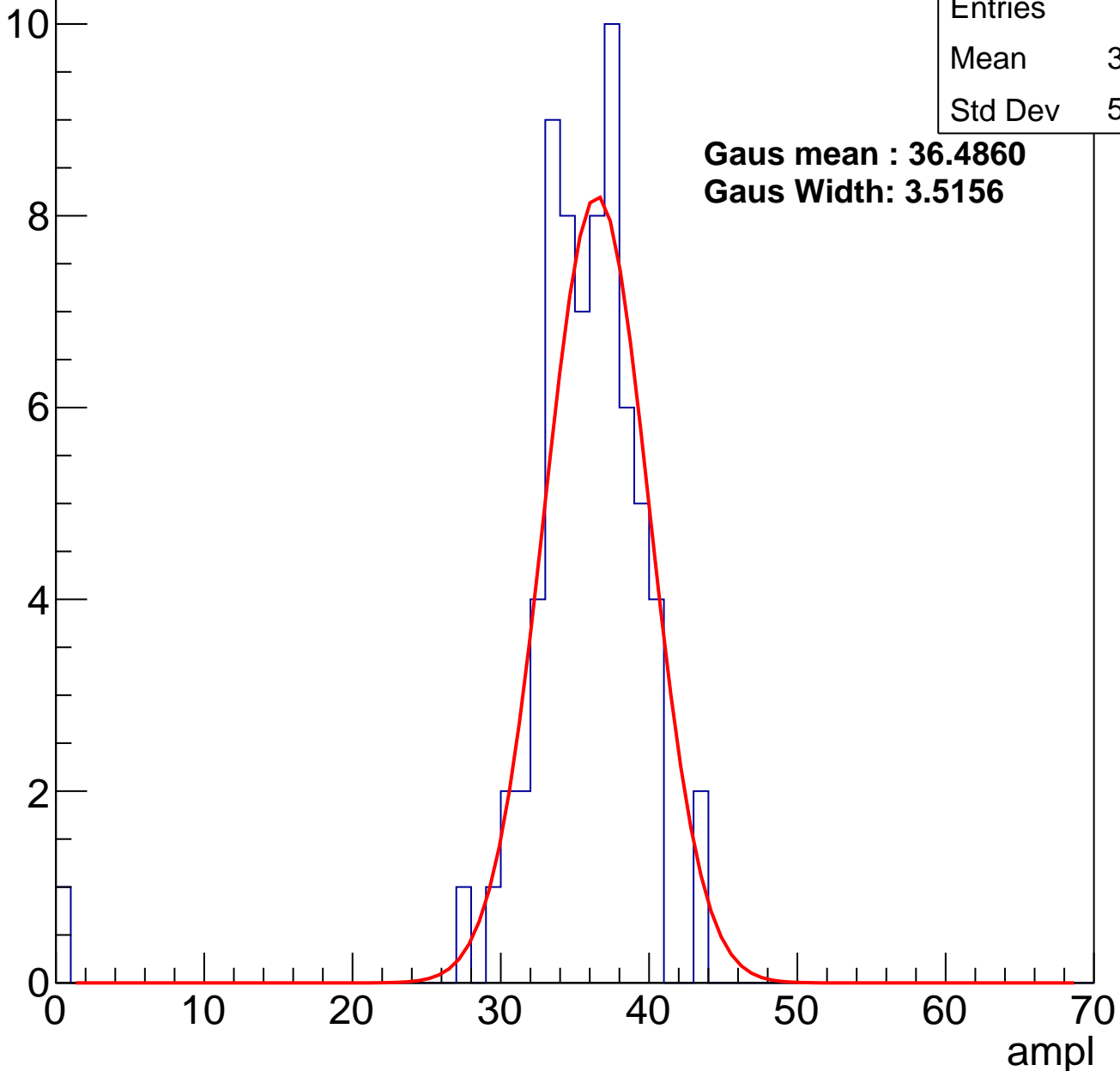
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 34.96 |
| Std Dev | 5.206 |

**Gaus mean : 36.4860**

**Gaus Width: 3.5156**

Entry



# B0L002S, U1-ch111, adc2

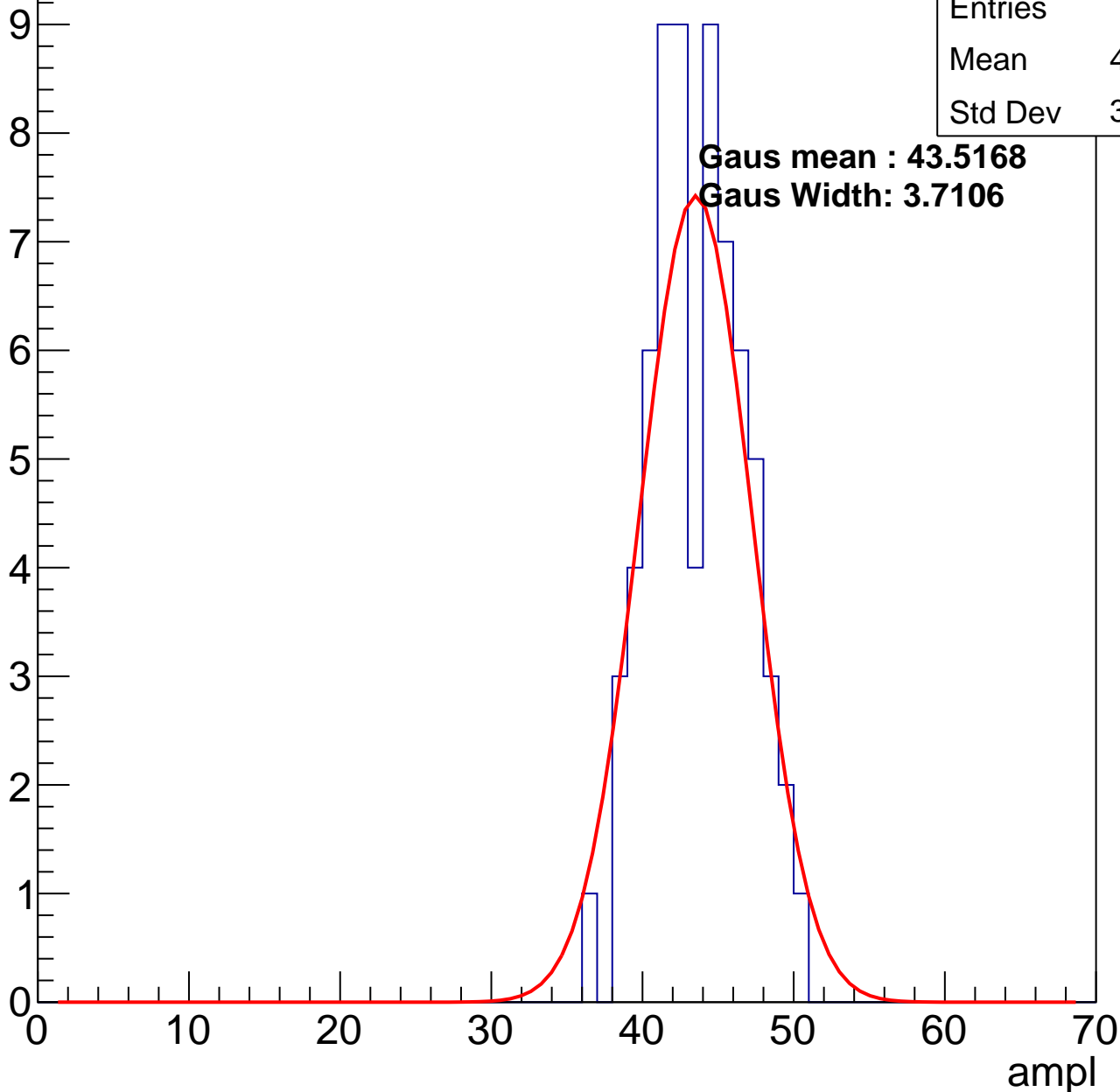
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 43.17 |
| Std Dev | 3.074 |

**Gaus mean : 43.5168**

**Gaus Width: 3.7106**

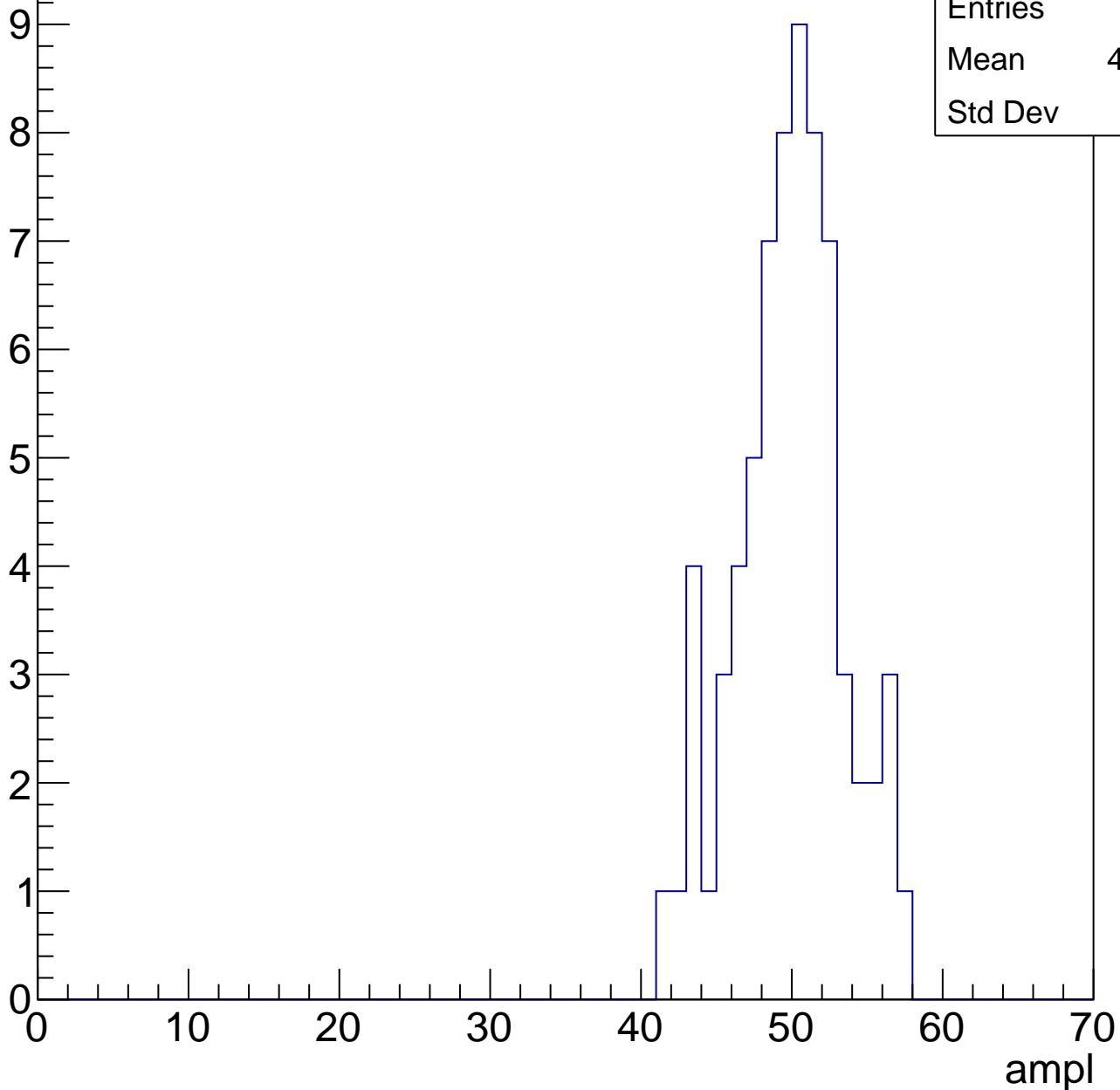


# B0L002S, U1-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 49.35 |
| Std Dev | 3.55  |

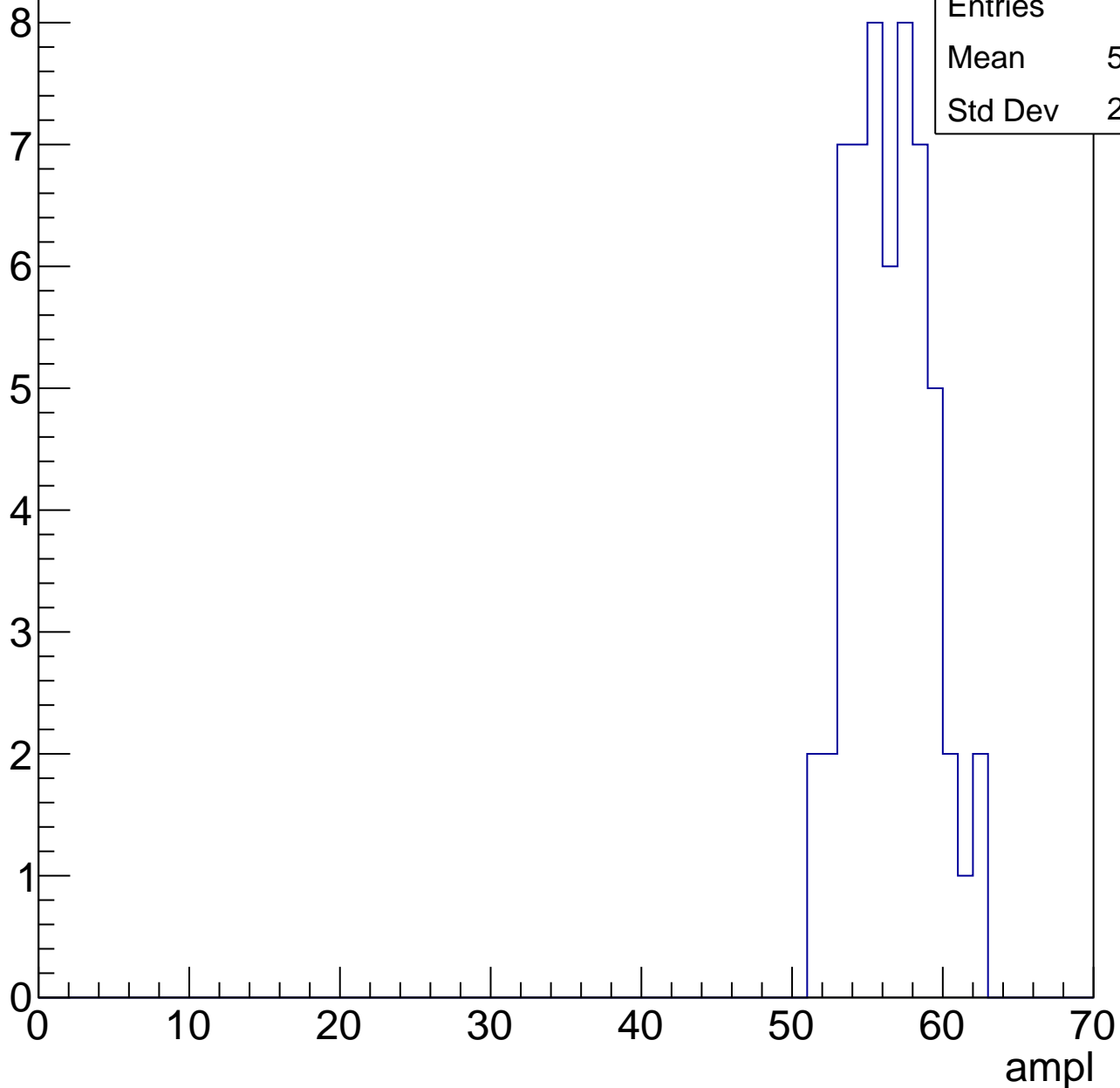


# B0L002S, U1-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 56.02 |
| Std Dev | 2.619 |

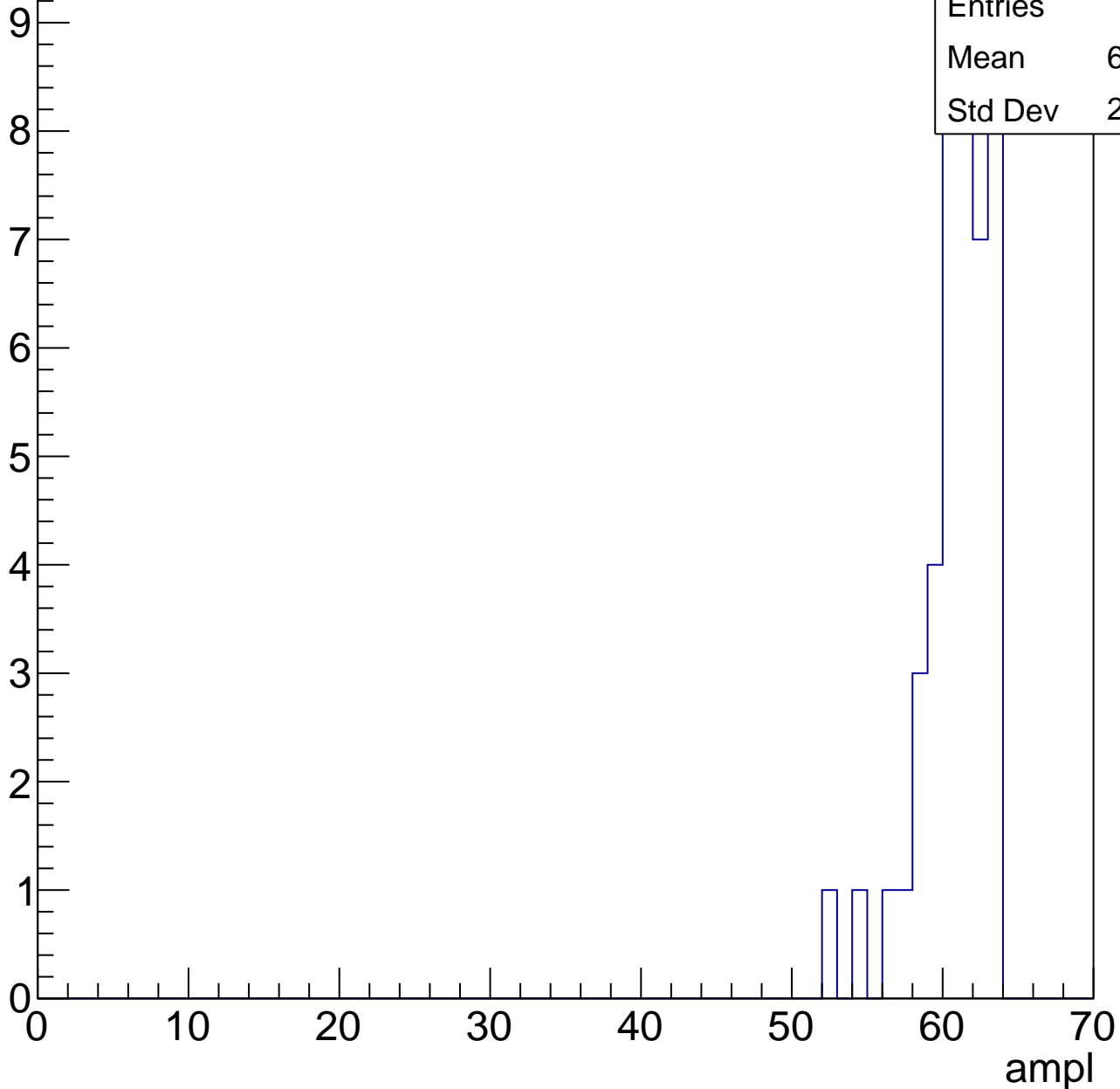


# B0L002S, U1-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

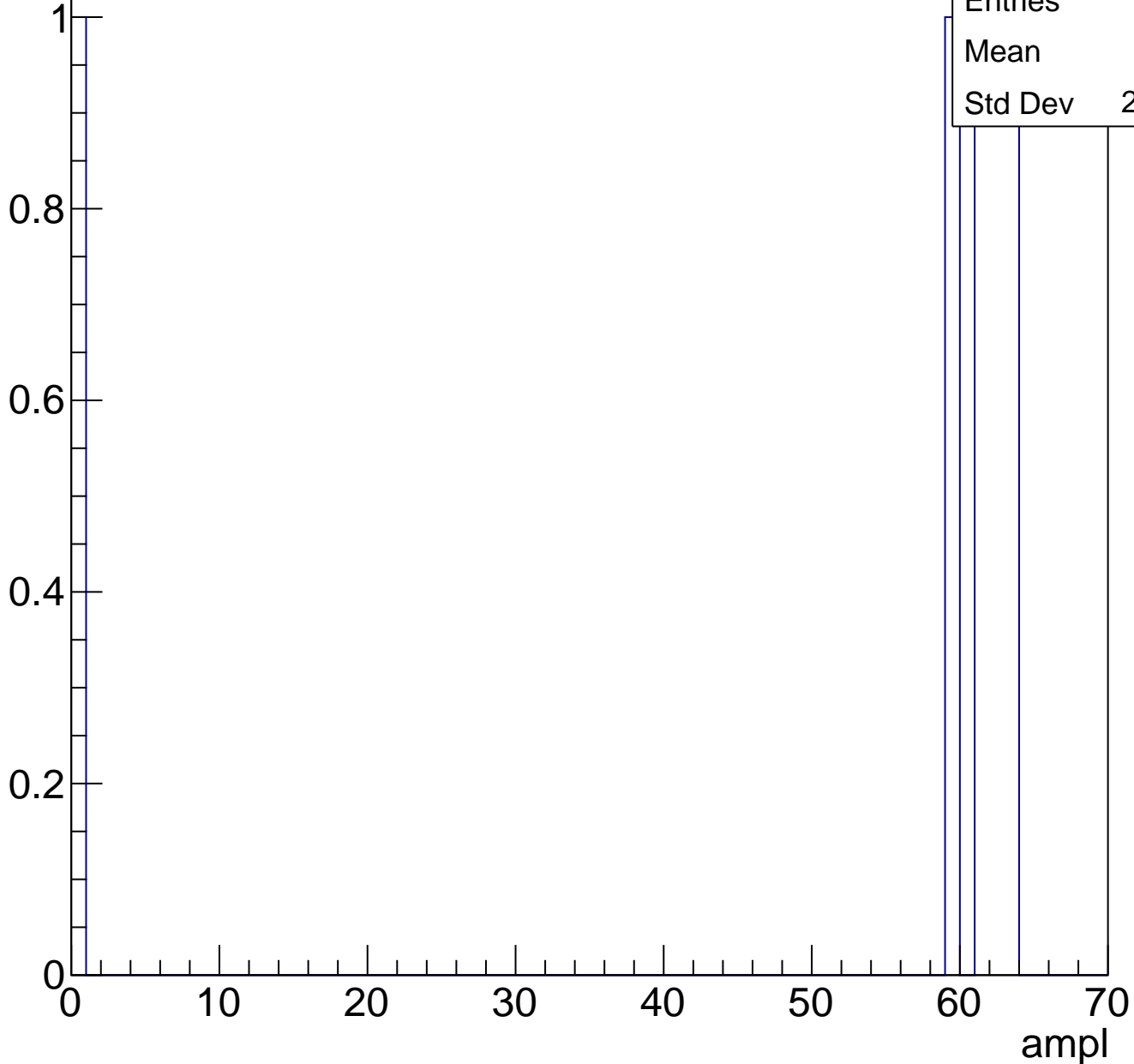
|         |       |
|---------|-------|
| Entries | 43    |
| Mean    | 60.42 |
| Std Dev | 2.413 |



# B0L002S, U1-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch112, adc0

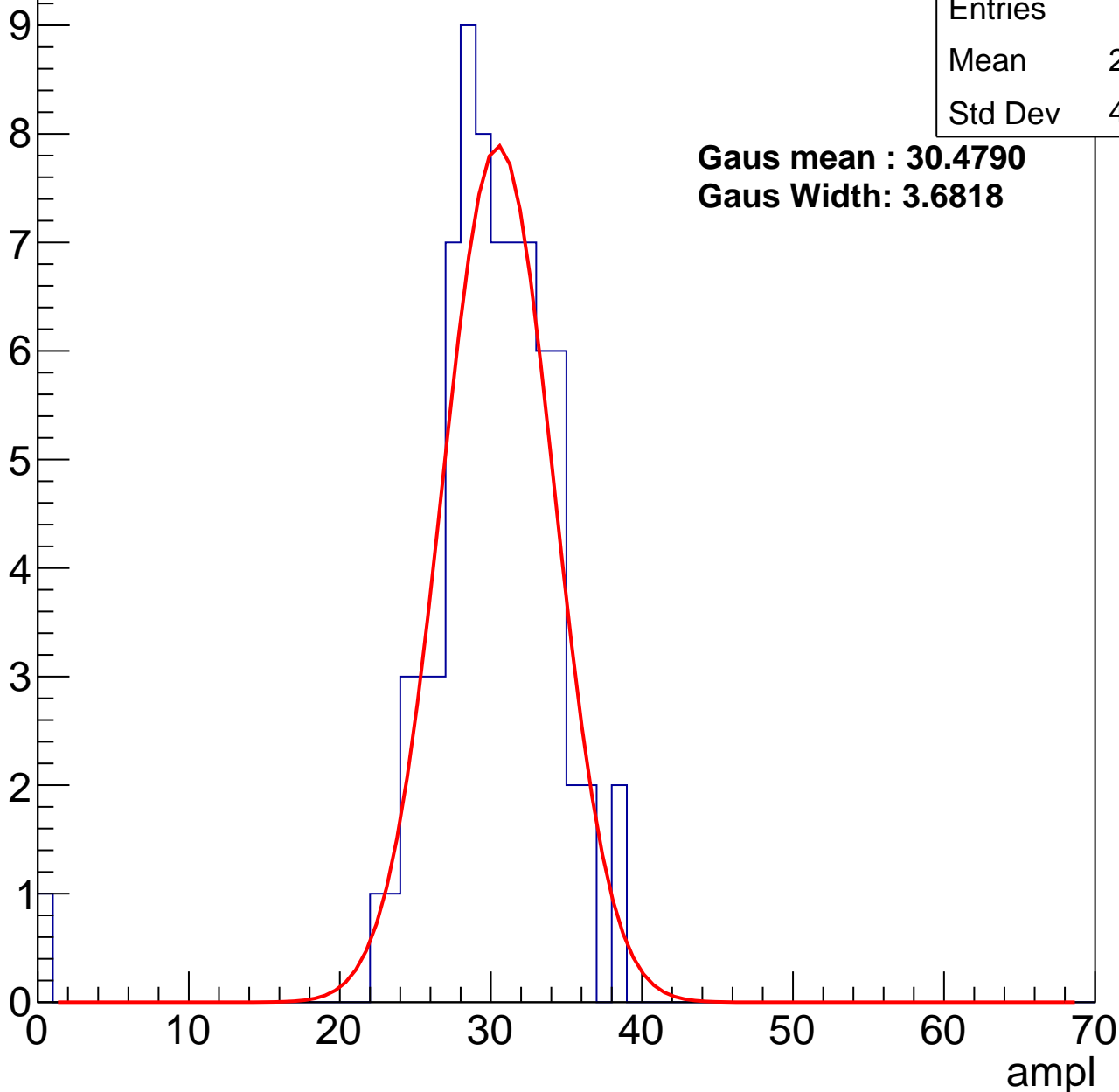
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 29.52 |
| Std Dev | 4.856 |

**Gaus mean : 30.4790**

**Gaus Width: 3.6818**



# B0L002S, U1-ch112, adc1

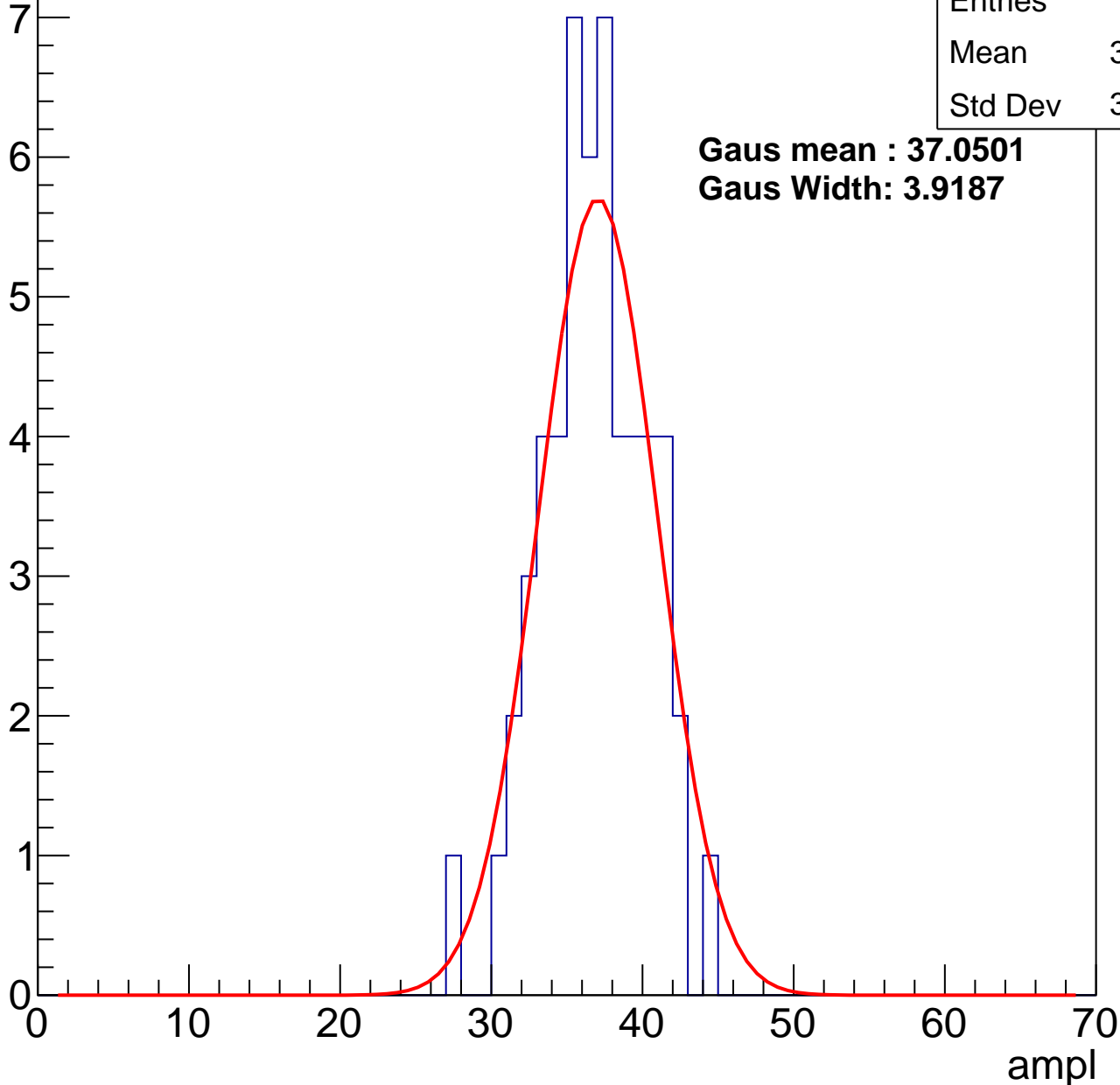
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 36.35 |
| Std Dev | 3.406 |

**Gaus mean : 37.0501**

**Gaus Width: 3.9187**



# B0L002S, U1-ch112, adc2

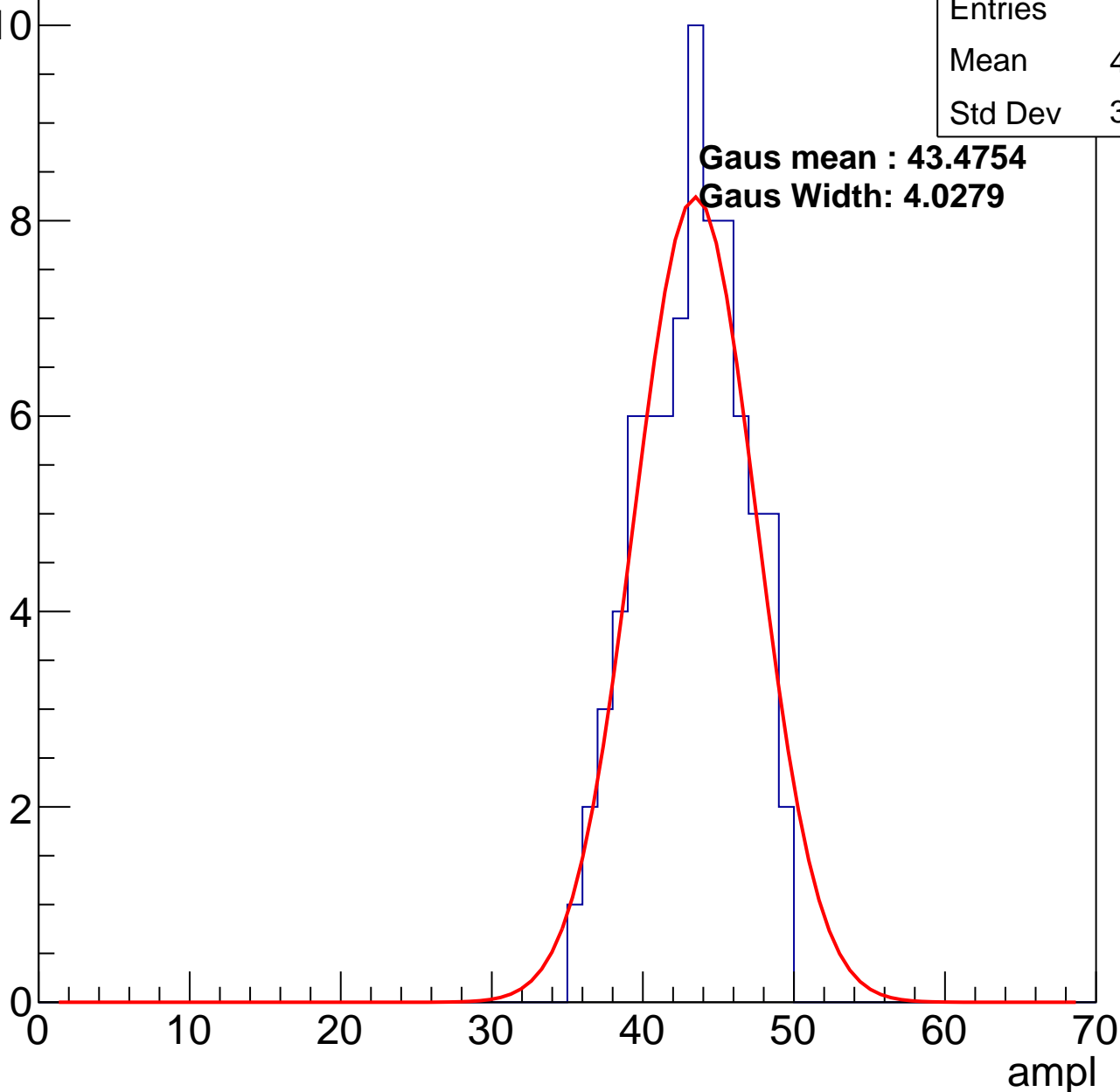
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 42.72 |
| Std Dev | 3.405 |

**Gaus mean : 43.4754**

**Gaus Width: 4.0279**

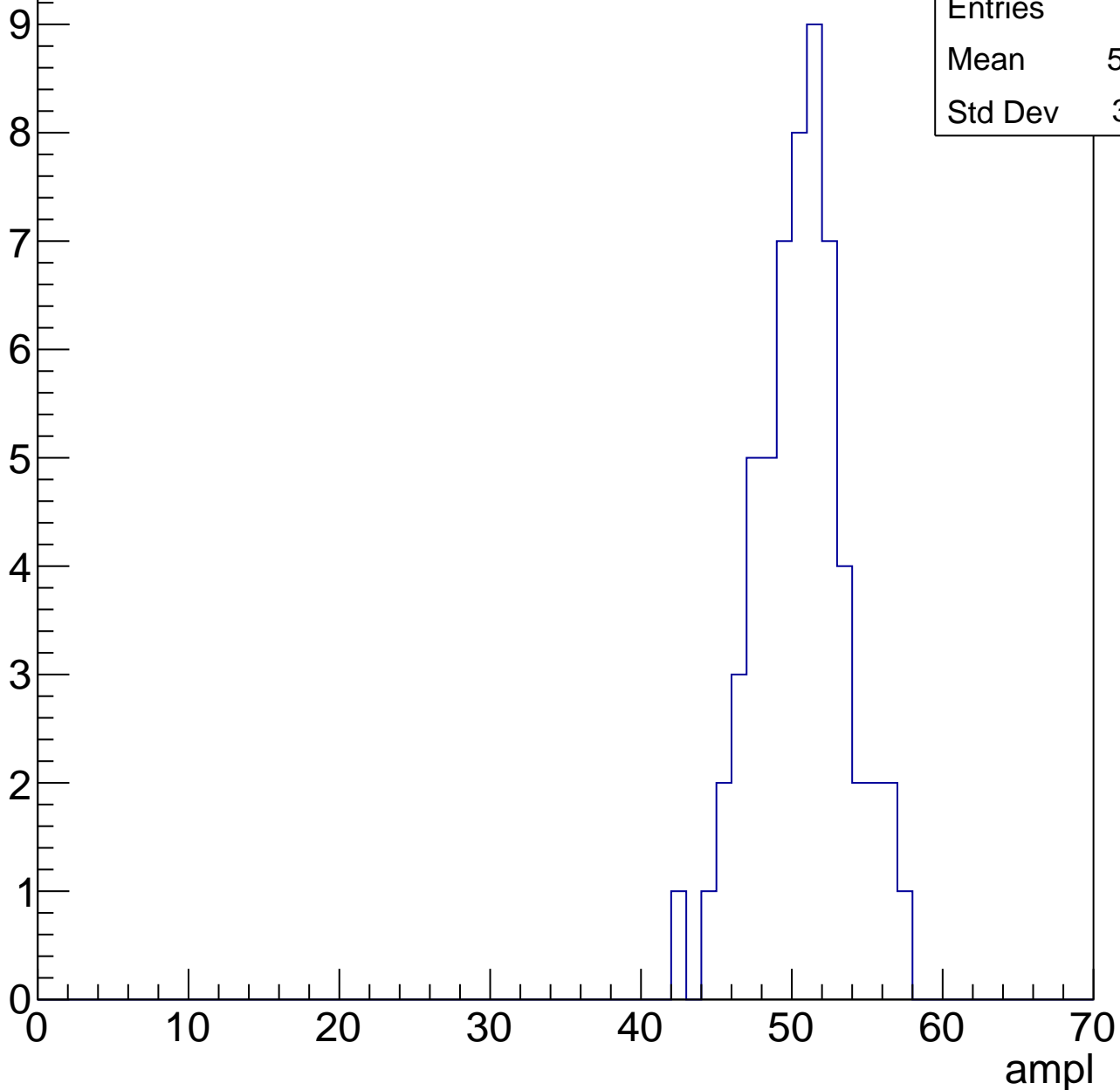


# B0L002S, U1-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 50.07 |
| Std Dev | 3.041 |

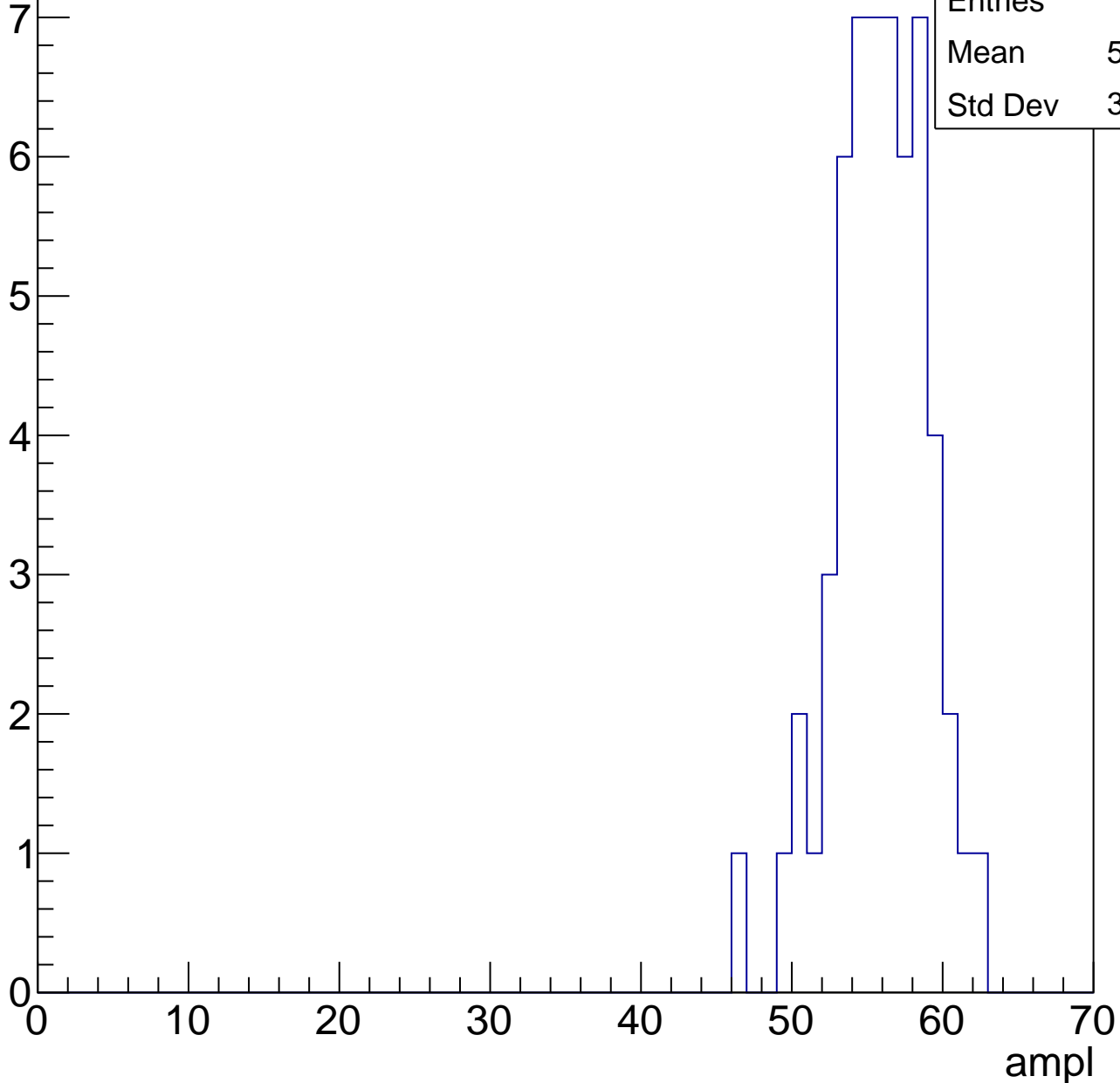


# B0L002S, U1-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 55.39 |
| Std Dev | 3.063 |

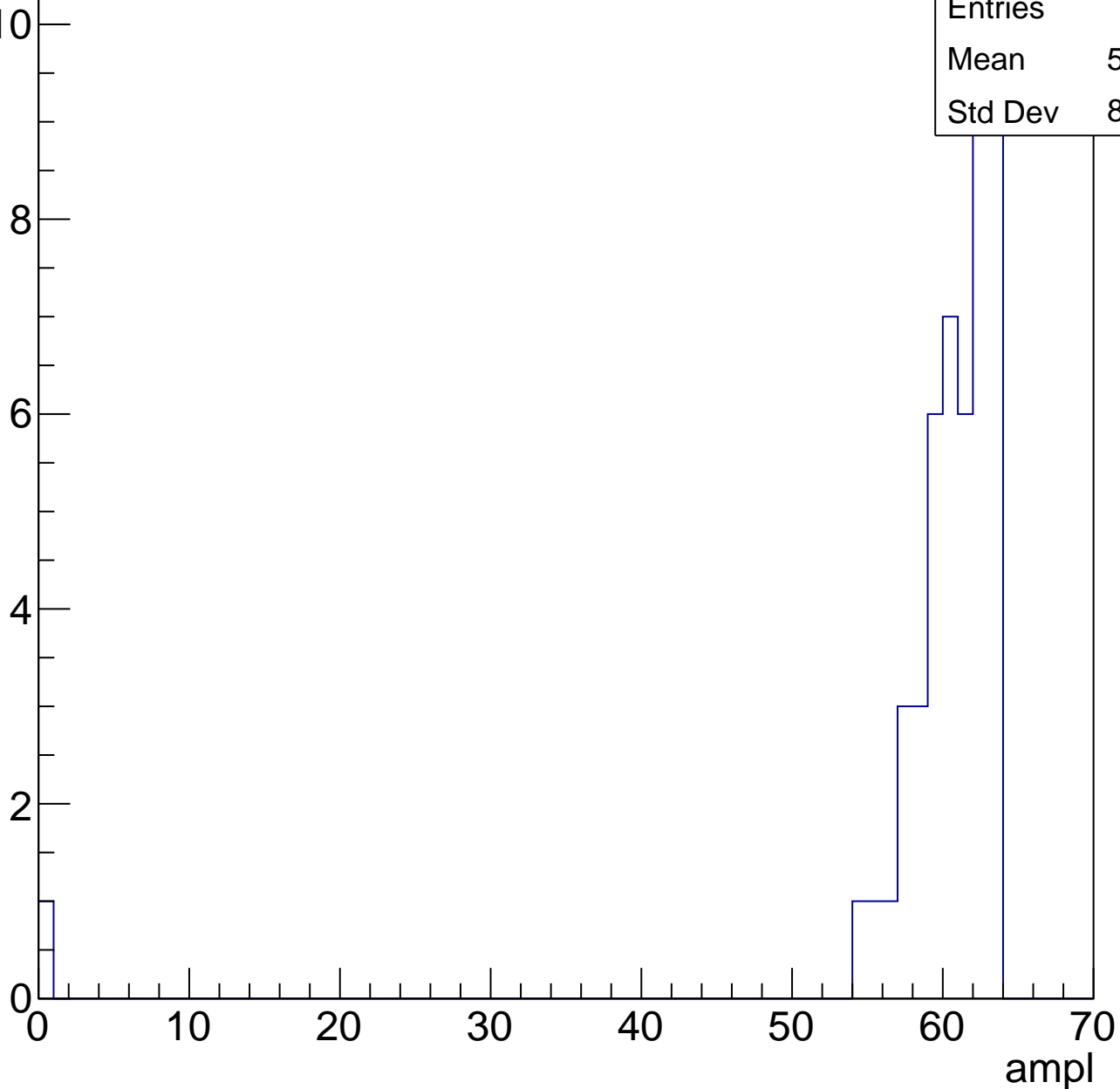


# B0L002S, U1-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

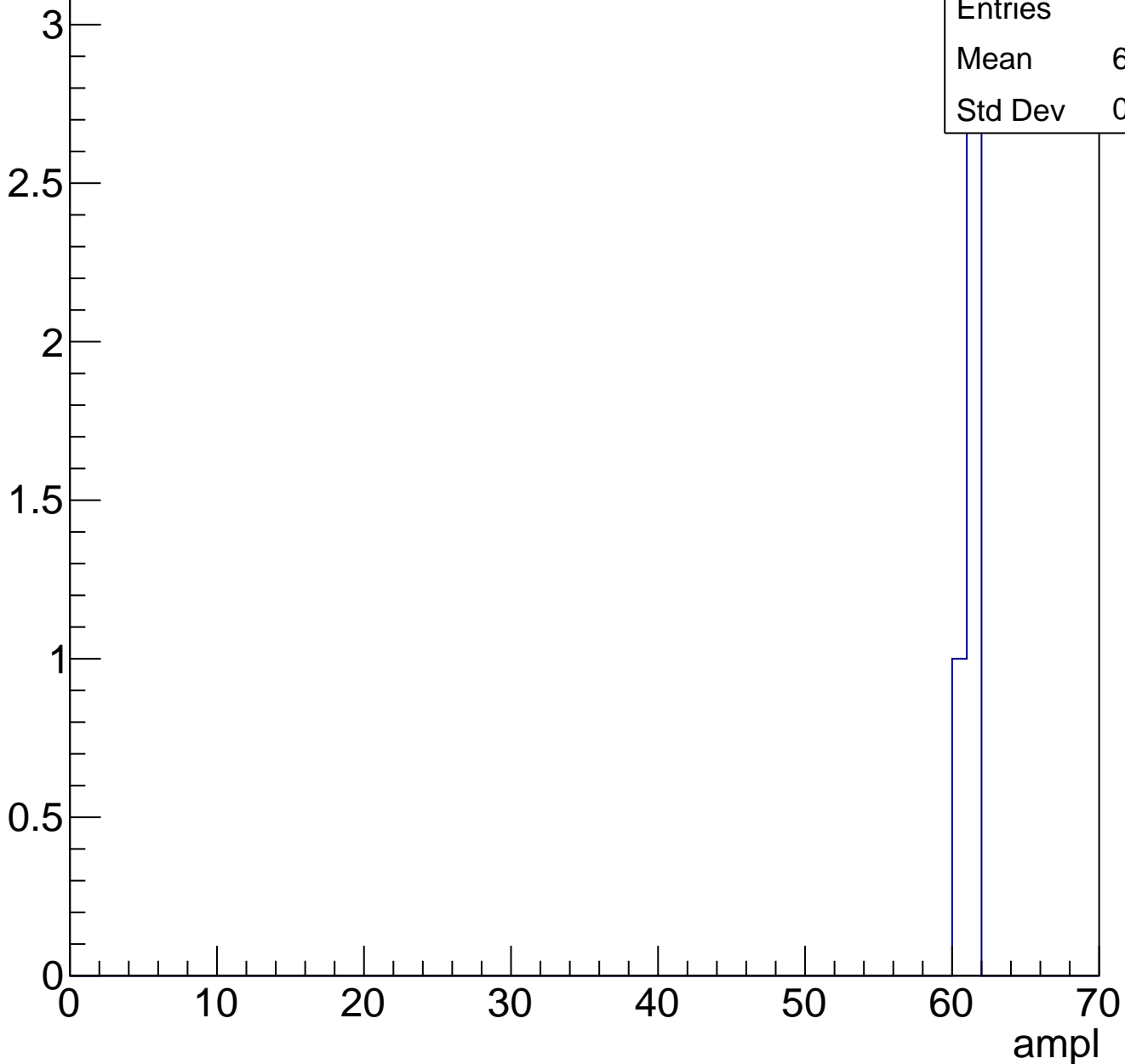
|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 59.12 |
| Std Dev | 8.918 |



# B0L002S, U1-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch113, adc0

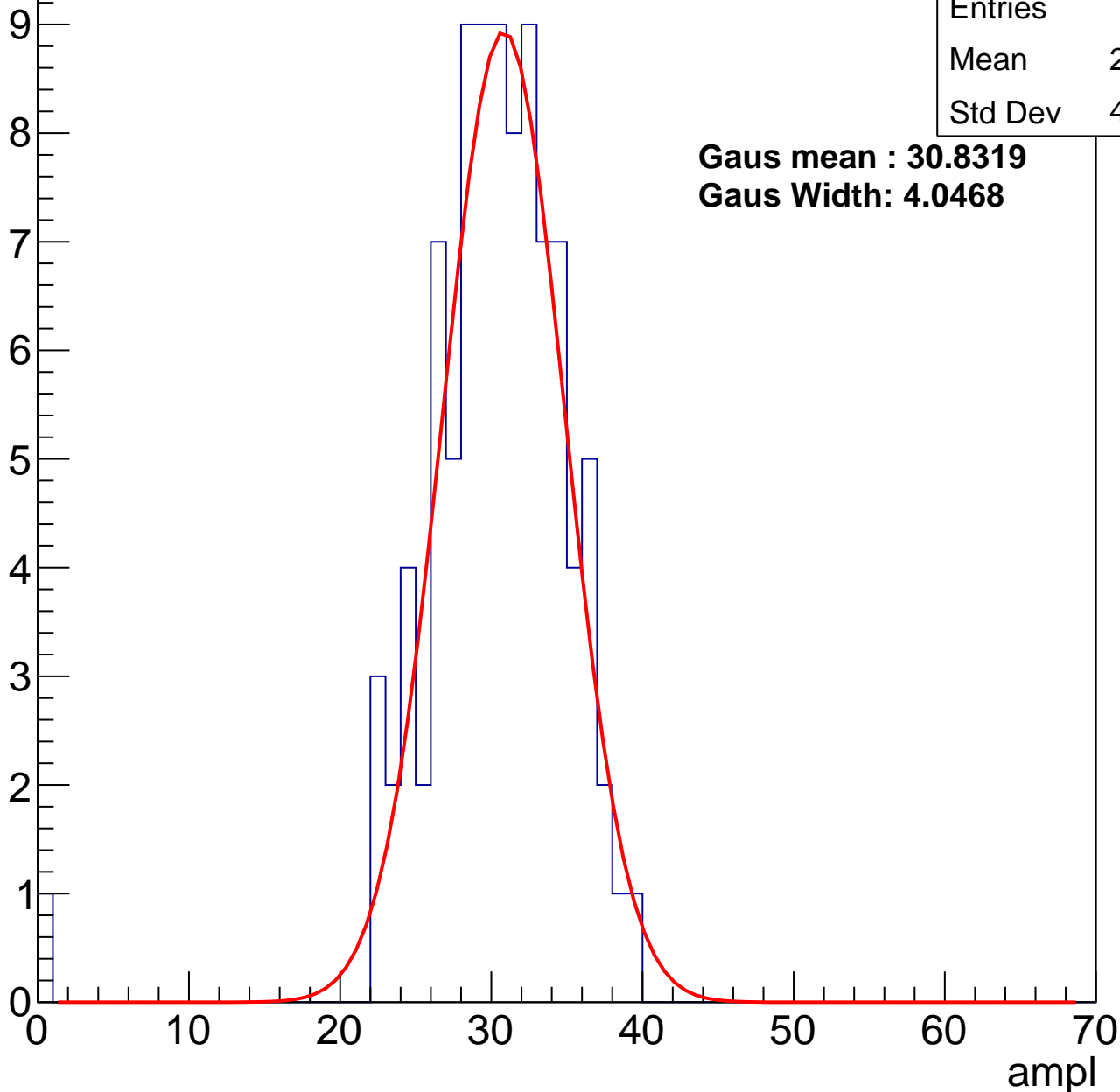
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 29.83 |
| Std Dev | 4.947 |

**Gaus mean : 30.8319**

**Gaus Width: 4.0468**



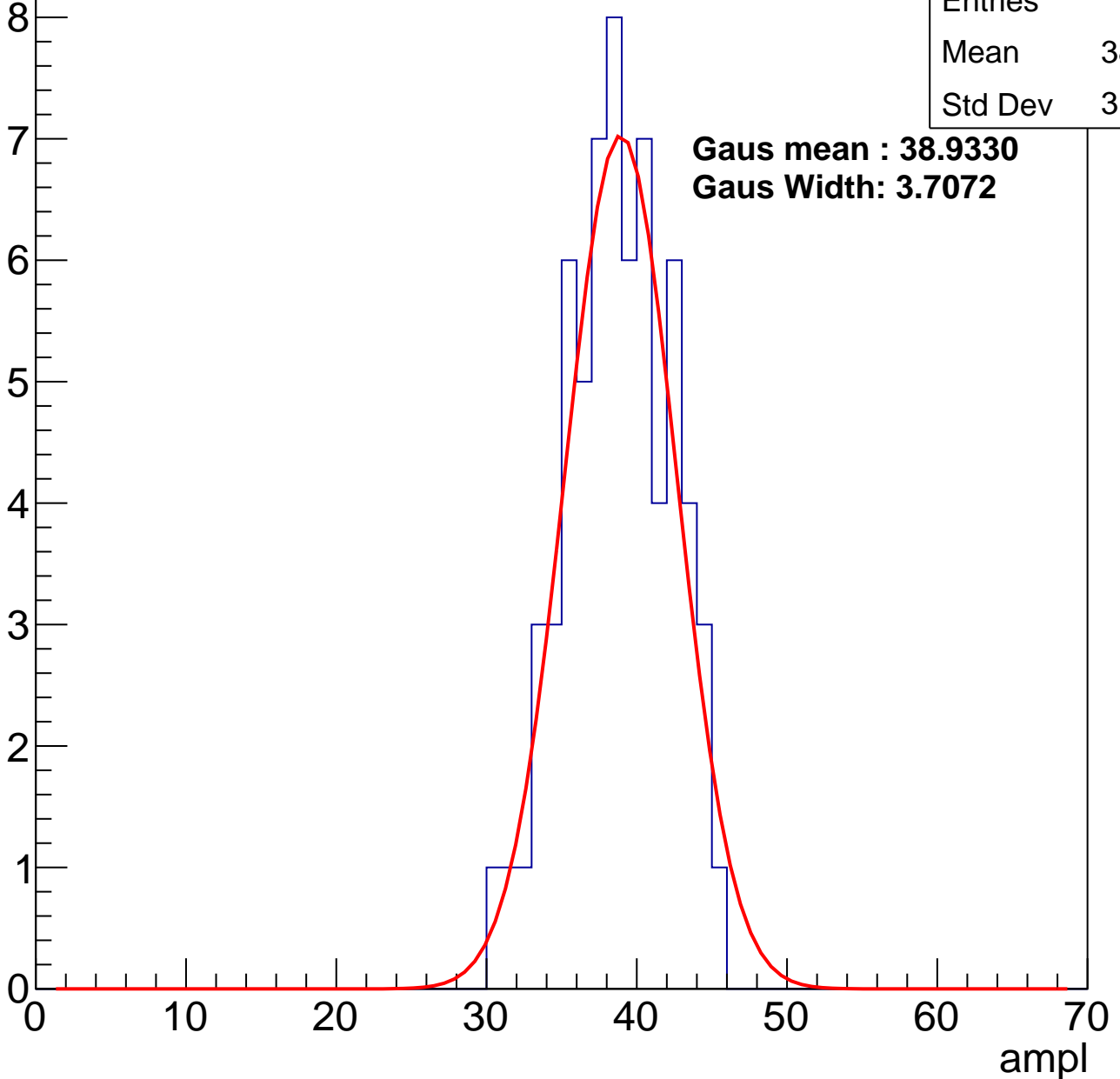
# B0L002S, U1-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 38.27 |
| Std Dev | 3.418 |

**Gaus mean : 38.9330**  
**Gaus Width: 3.7072**



# B0L002S, U1-ch113, adc2

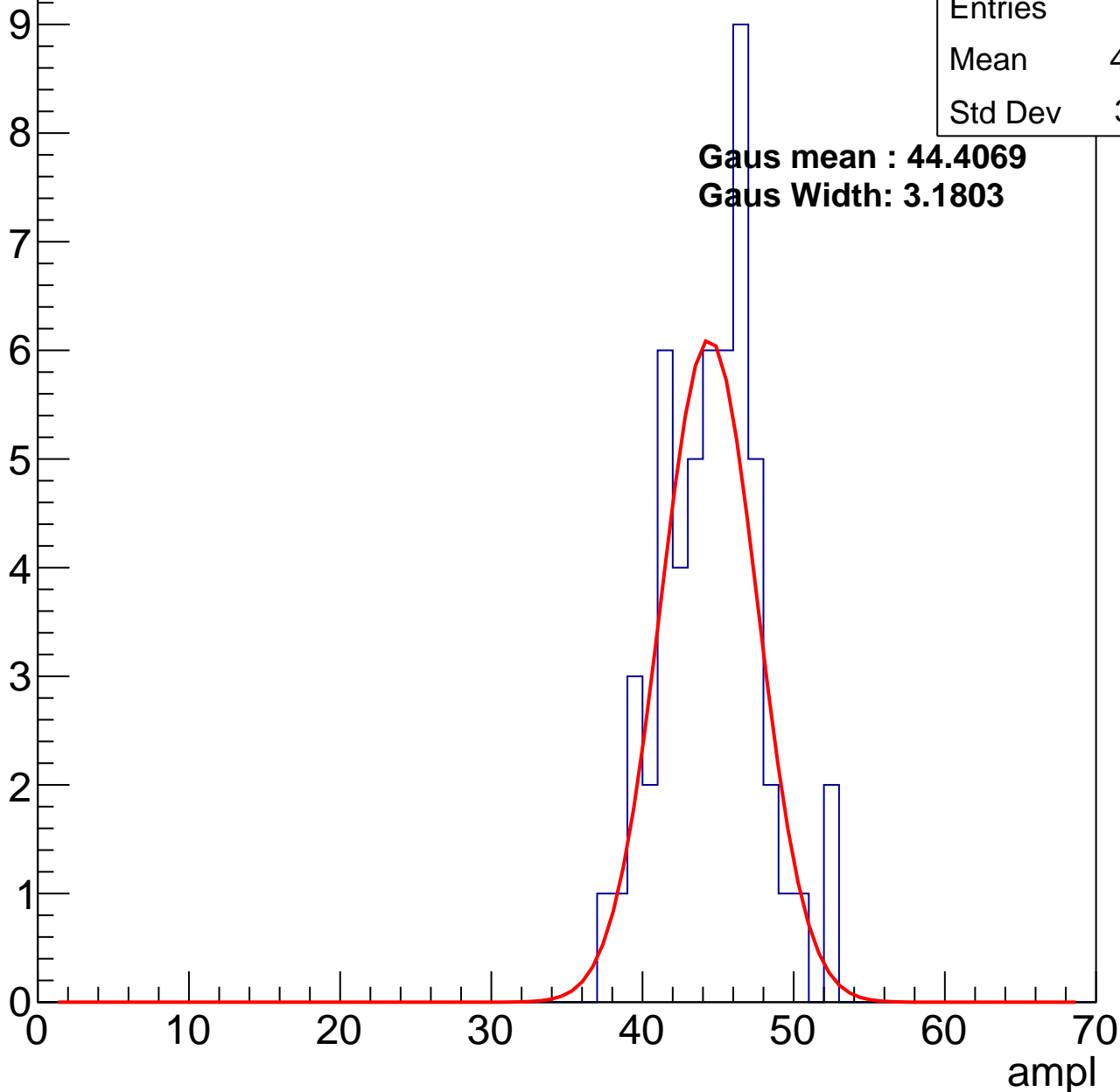
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 44.13 |
| Std Dev | 3.261 |

**Gaus mean : 44.4069**

**Gaus Width: 3.1803**

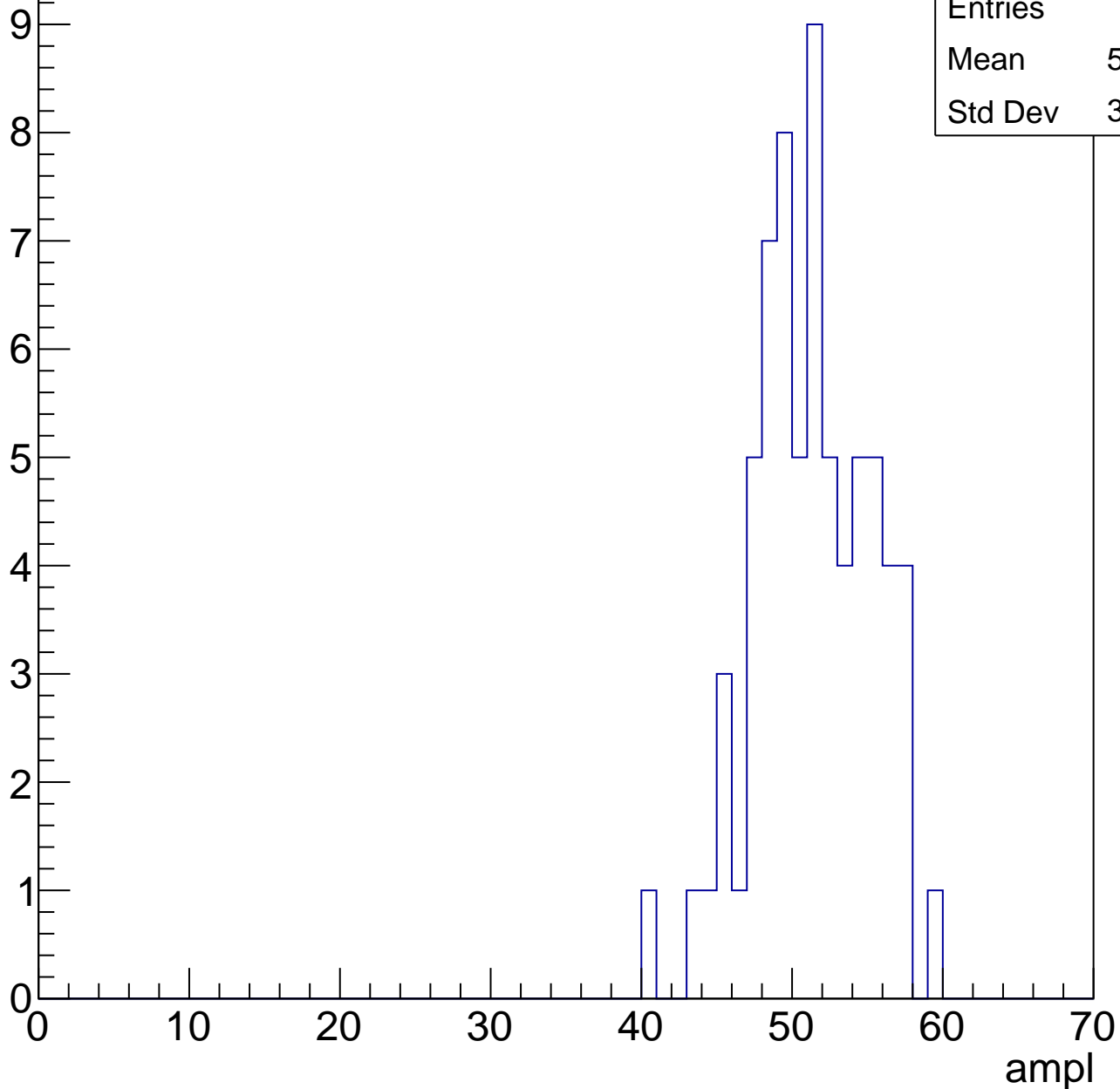


# B0L002S, U1-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 69    |
| Mean    | 50.84 |
| Std Dev | 3.806 |

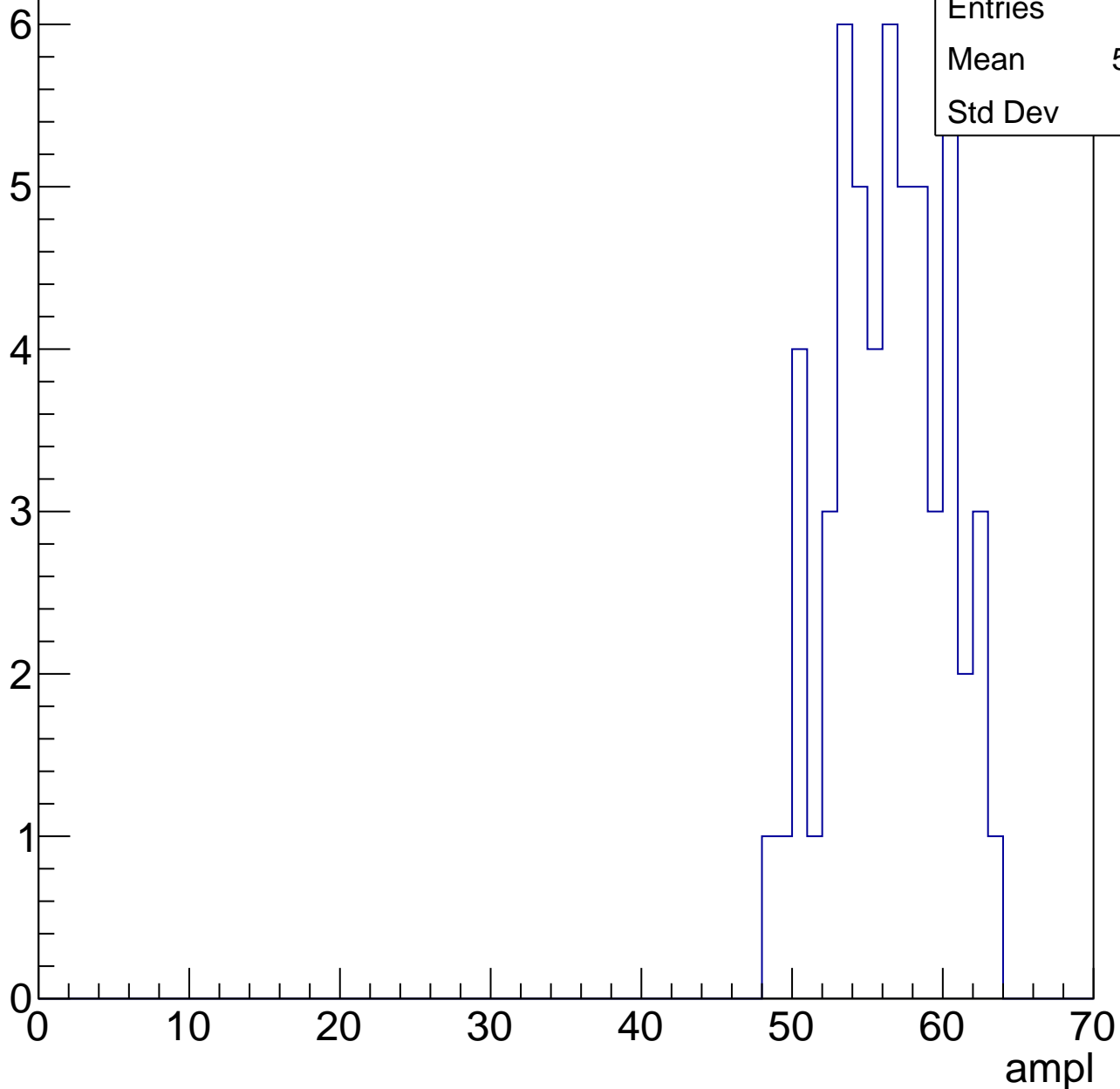


# B0L002S, U1-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 55.91 |
| Std Dev | 3.69  |

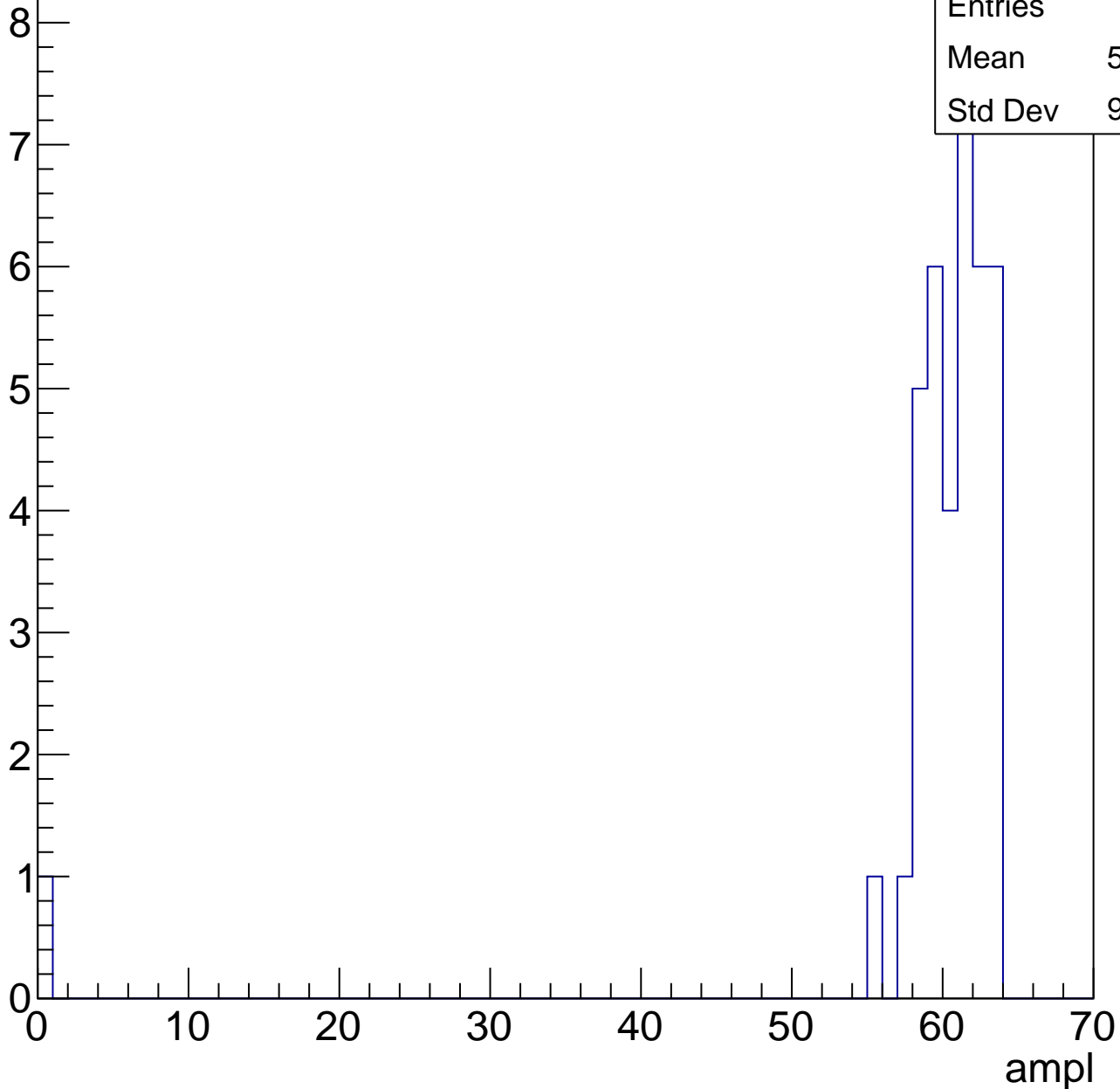


# B0L002S, U1-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 38    |
| Mean    | 58.79 |
| Std Dev | 9.855 |



# B0L002S, U1-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch114, adc0

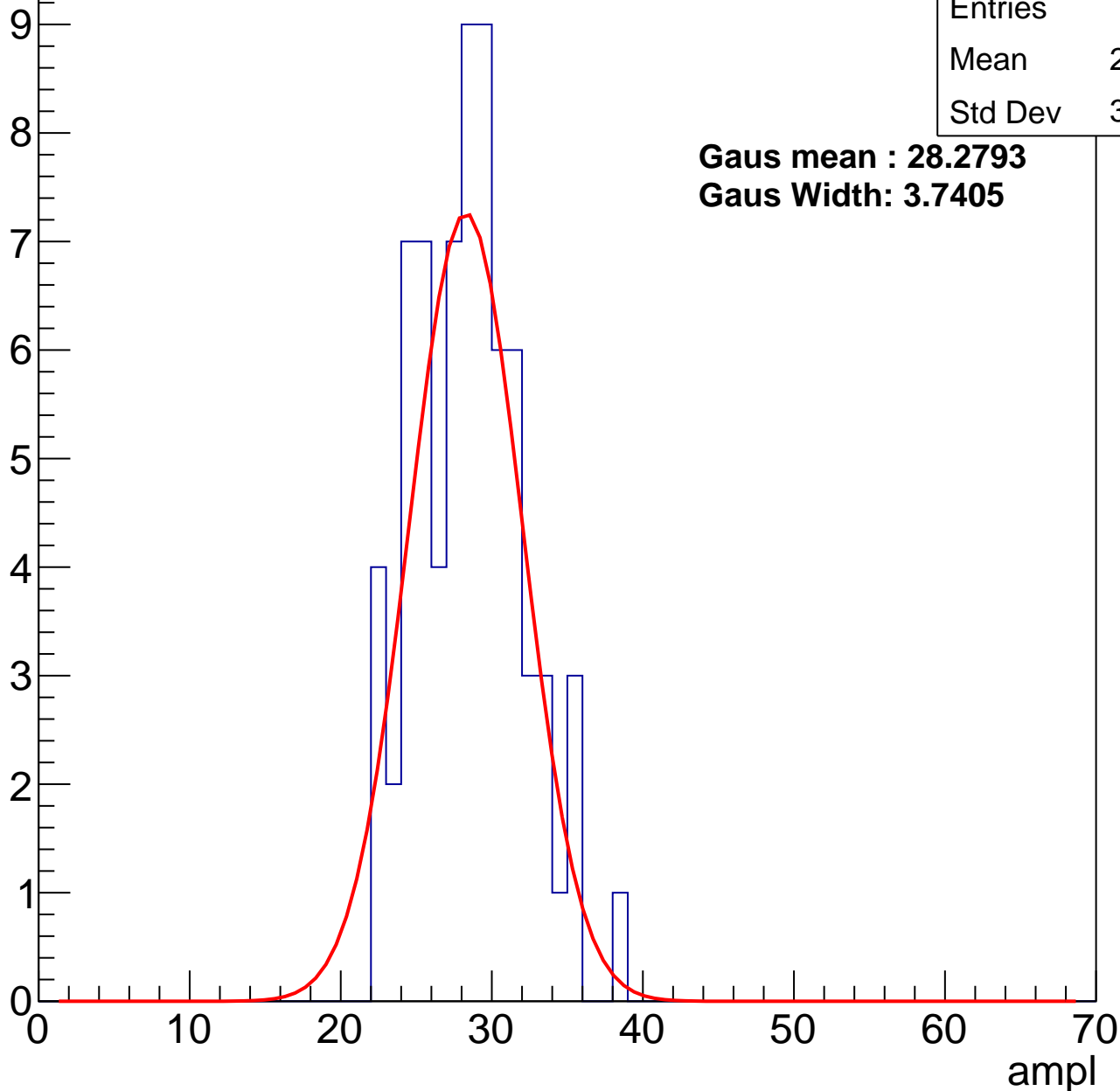
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 28.07 |
| Std Dev | 3.505 |

**Gaus mean : 28.2793**

**Gaus Width: 3.7405**



# B0L002S, U1-ch114, adc1

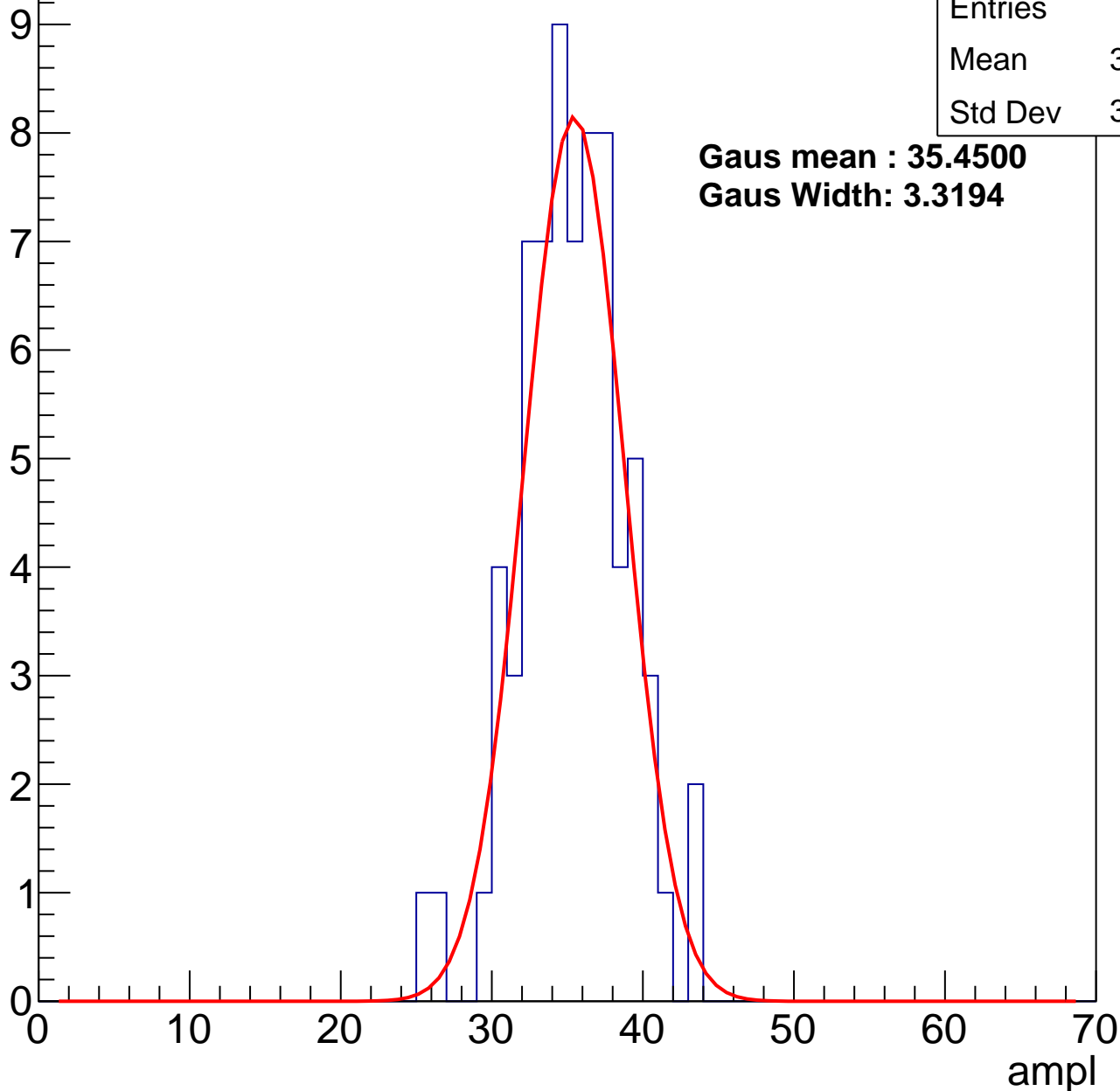
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 34.89 |
| Std Dev | 3.479 |

**Gaus mean : 35.4500**

**Gaus Width: 3.3194**



# B0L002S, U1-ch114, adc2

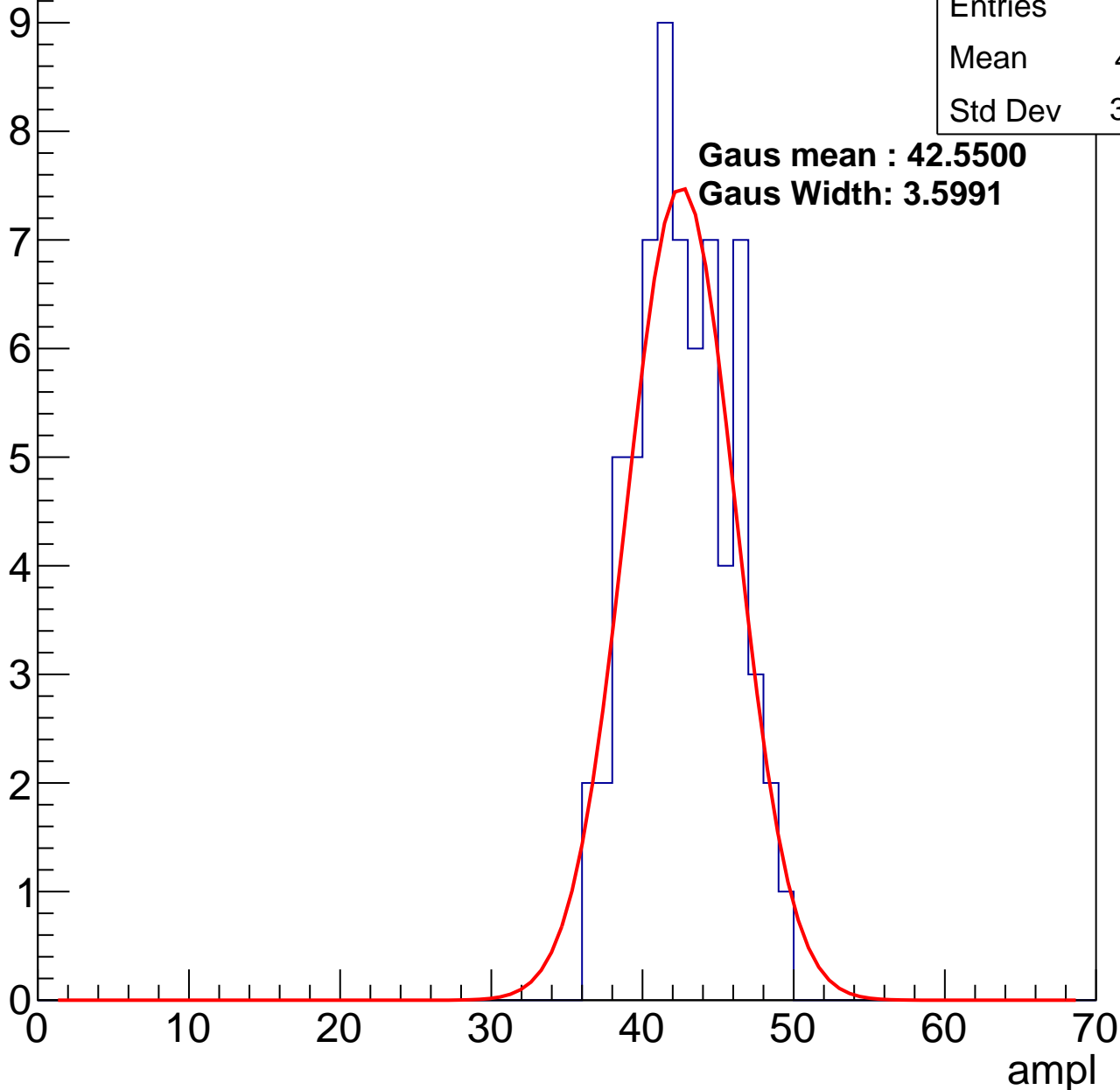
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 42.21 |
| Std Dev | 3.136 |

**Gaus mean : 42.5500**

**Gaus Width: 3.5991**

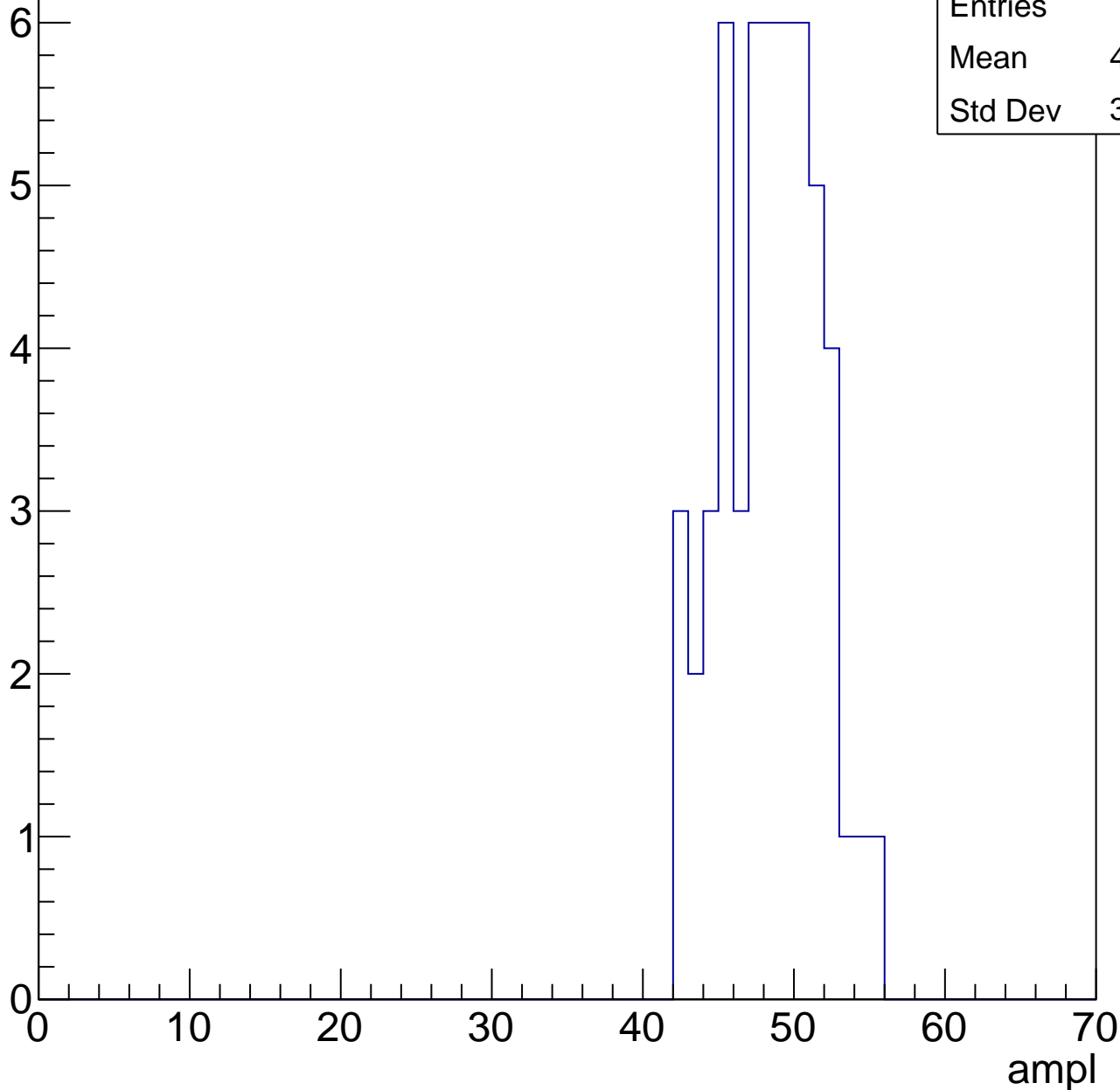


# B0L002S, U1-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 47.94 |
| Std Dev | 3.153 |

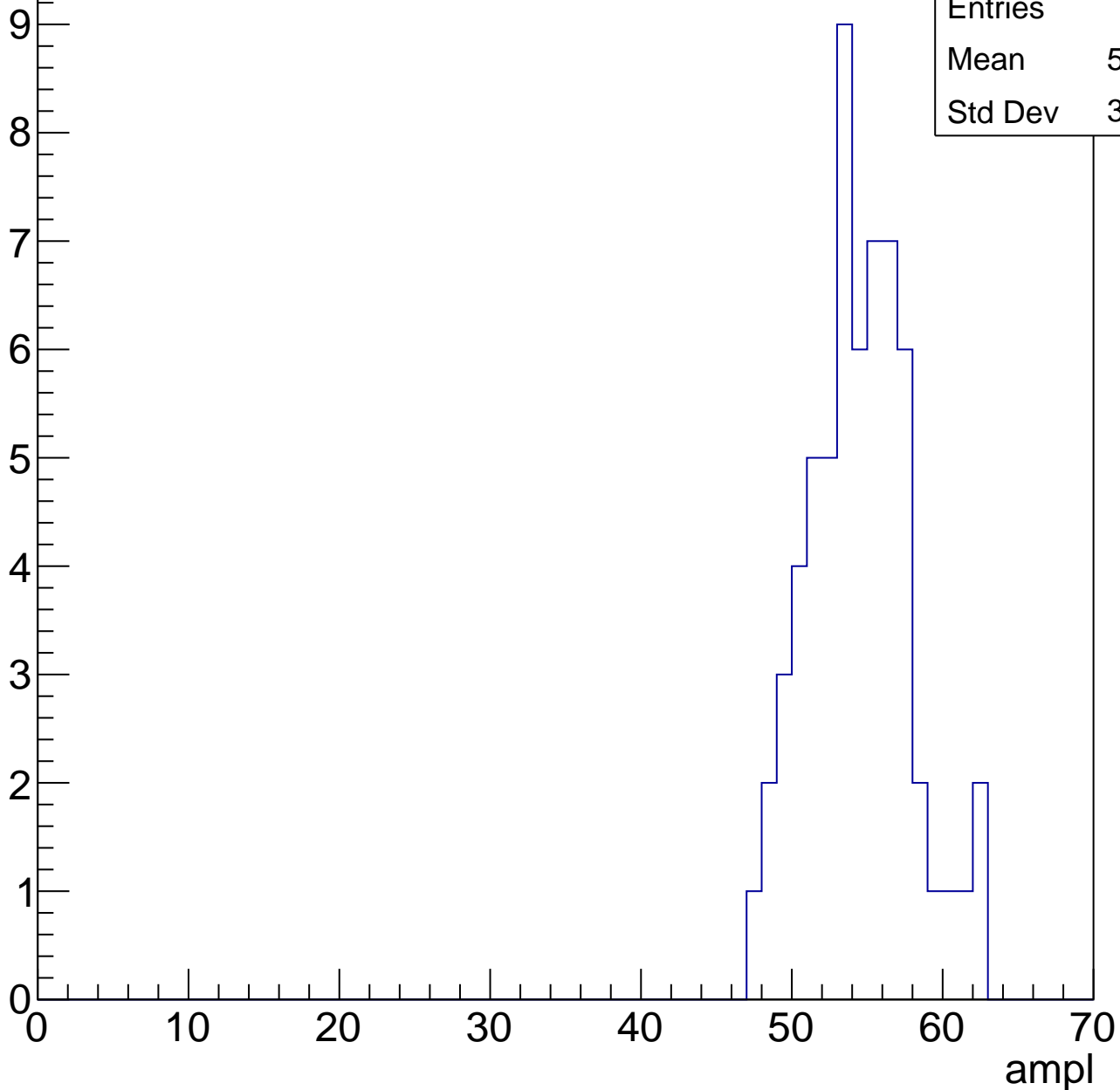


# B0L002S, U1-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

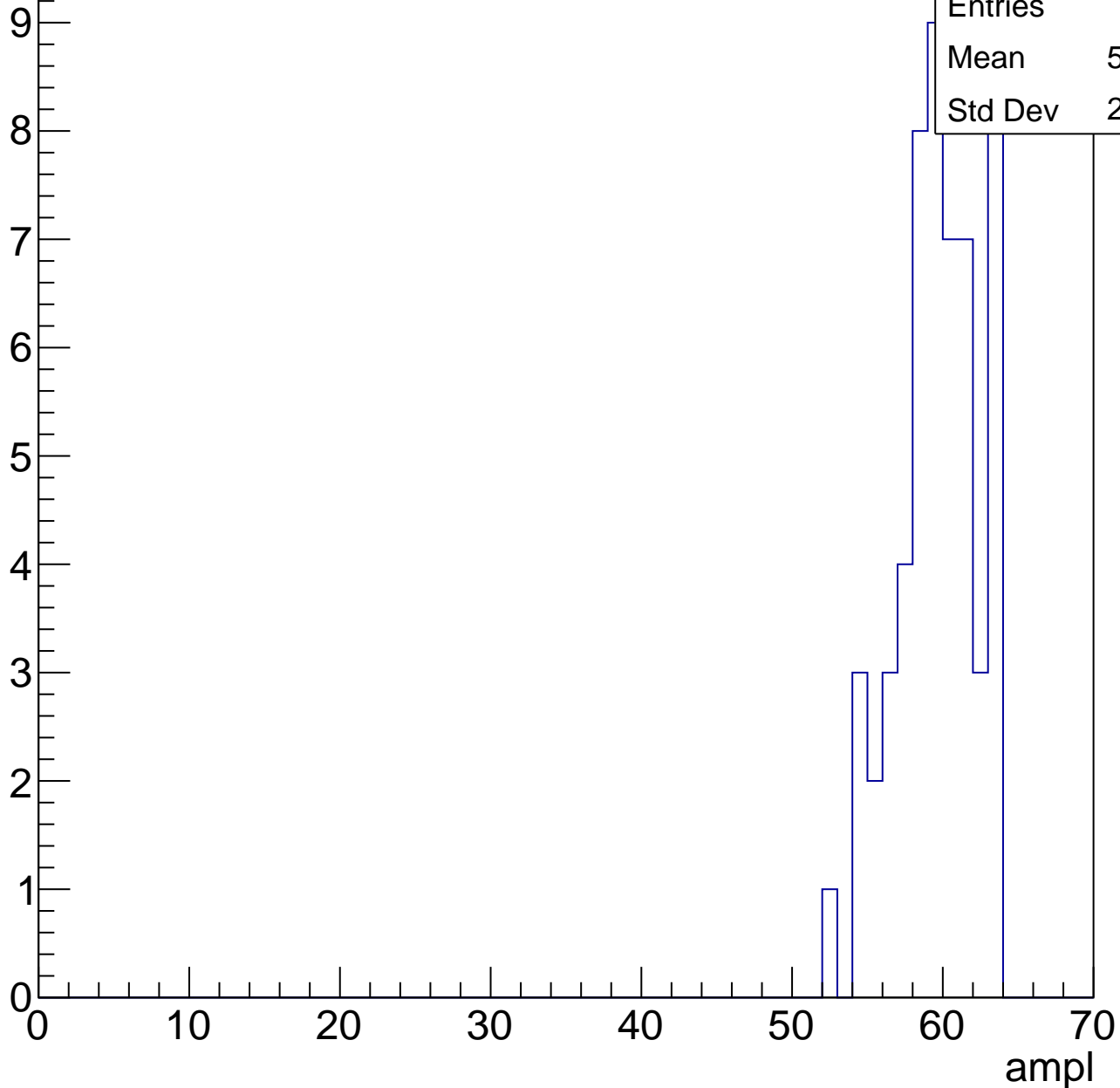
|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 53.95 |
| Std Dev | 3.343 |



# B0L002S, U1-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

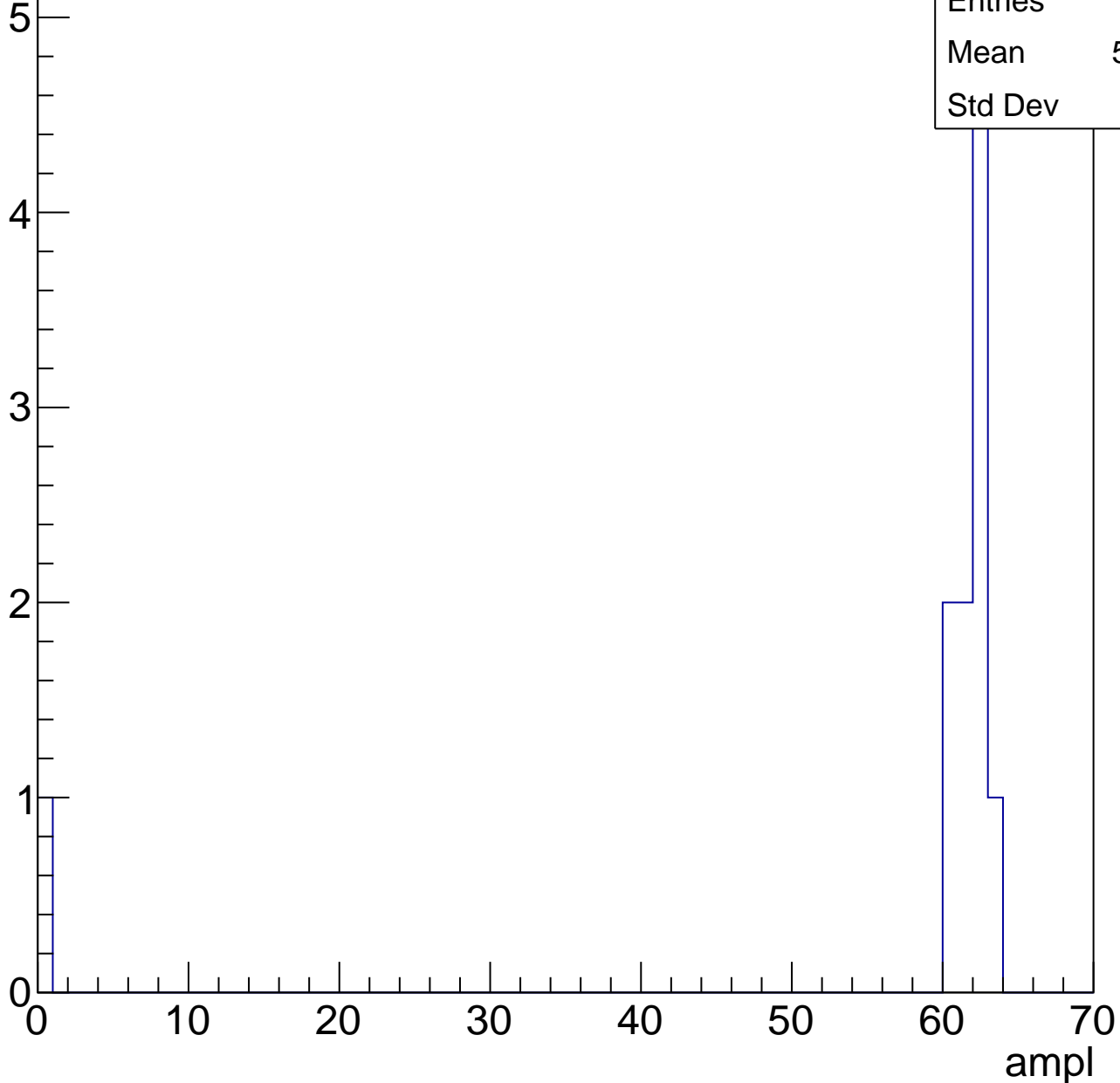


# B0L002S, U1-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 11    |
| Mean    | 55.91 |
| Std Dev | 17.7  |





# B0L002S, U1-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 41.5 |
| Std Dev | 21.5 |

ampl

# B0L002S, U1-ch115, adc0

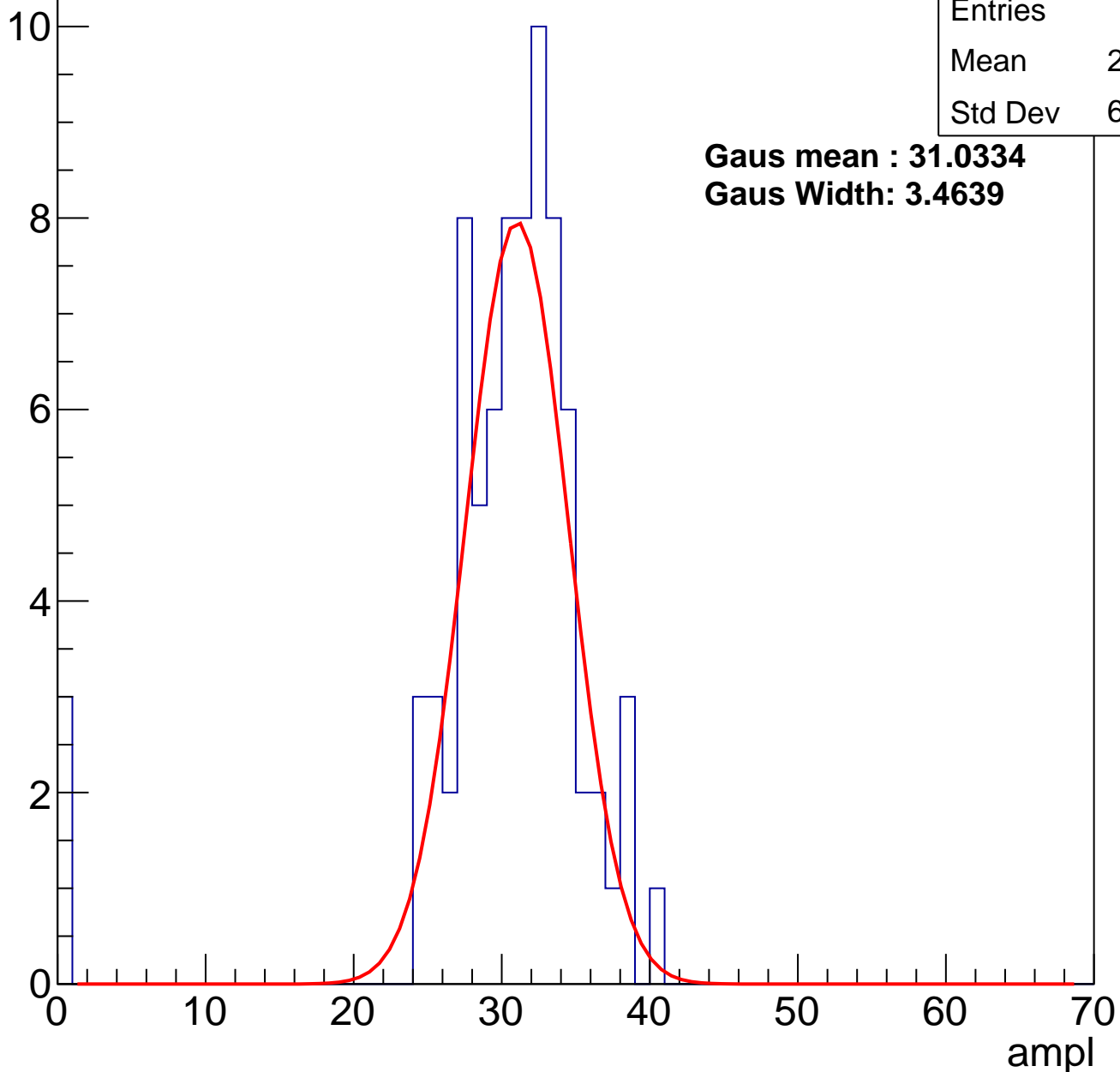
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 79    |
| Mean    | 29.59 |
| Std Dev | 6.824 |

**Gaus mean : 31.0334**

**Gaus Width: 3.4639**

Entry



# B0L002S, U1-ch115, adc1

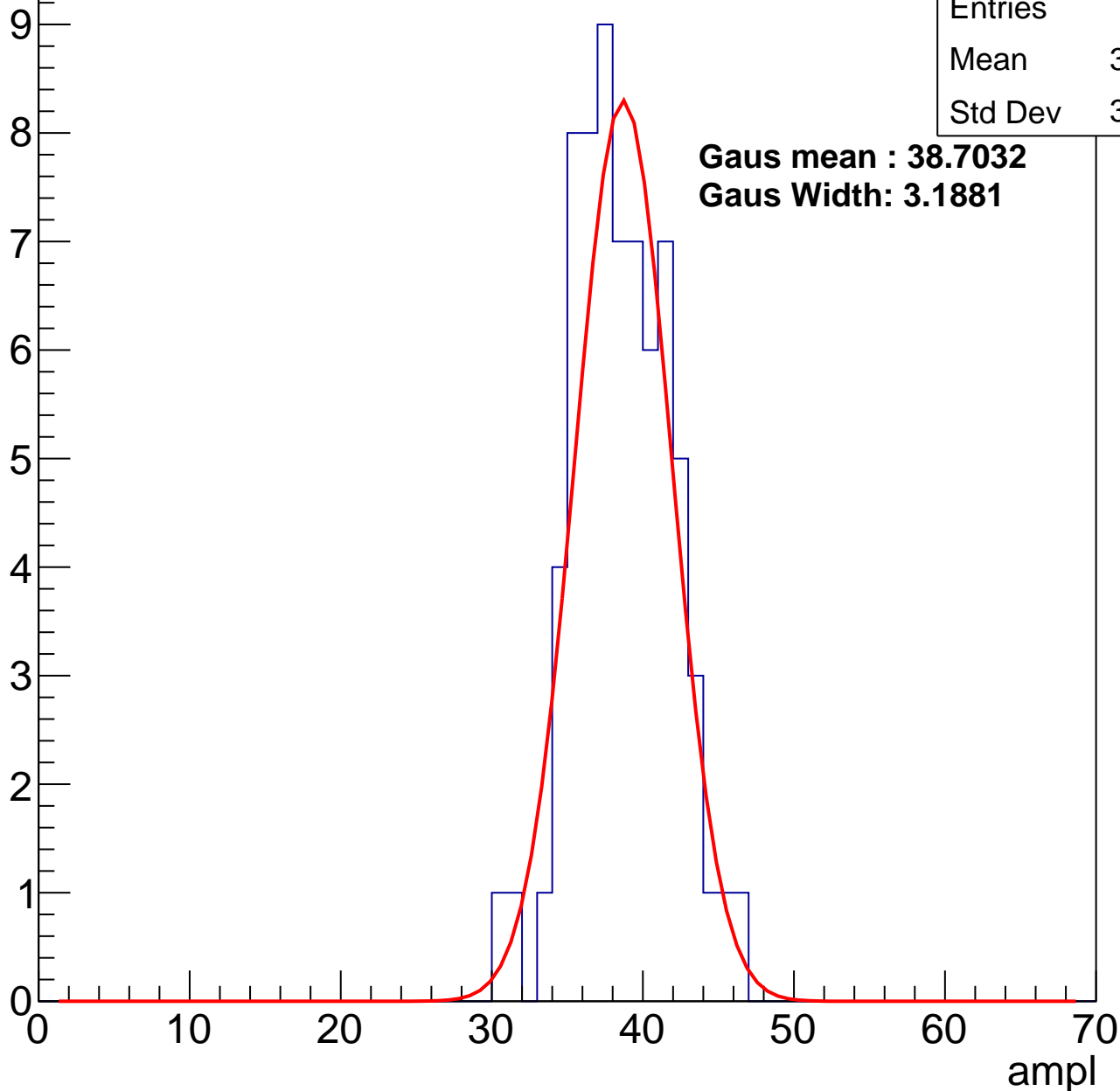
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 38.16 |
| Std Dev | 3.188 |

**Gaus mean : 38.7032**

**Gaus Width: 3.1881**



# B0L002S, U1-ch115, adc2

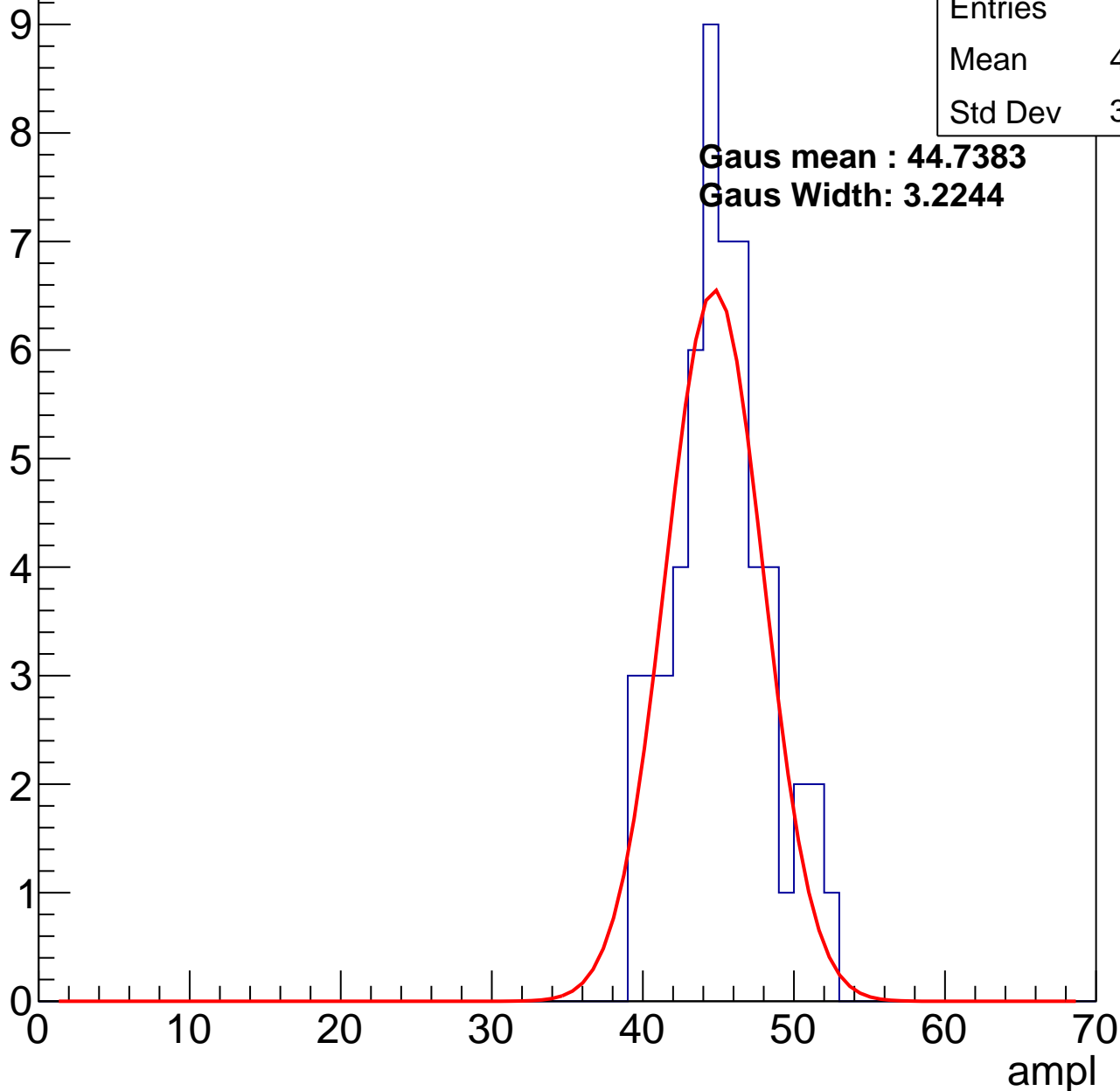
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 44.68 |
| Std Dev | 3.106 |

**Gaus mean : 44.7383**

**Gaus Width: 3.2244**

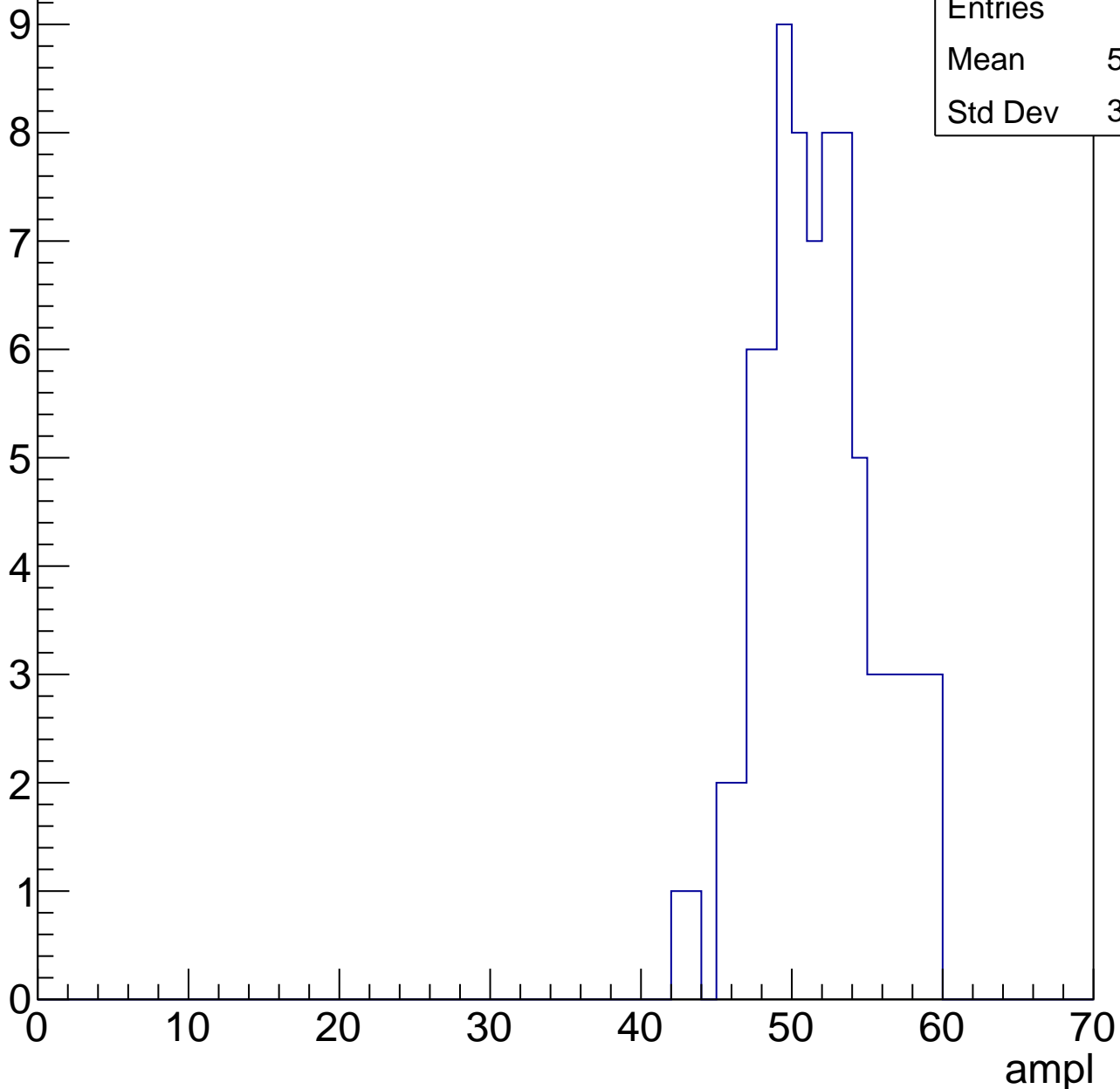


# B0L002S, U1-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 51.28 |
| Std Dev | 3.772 |

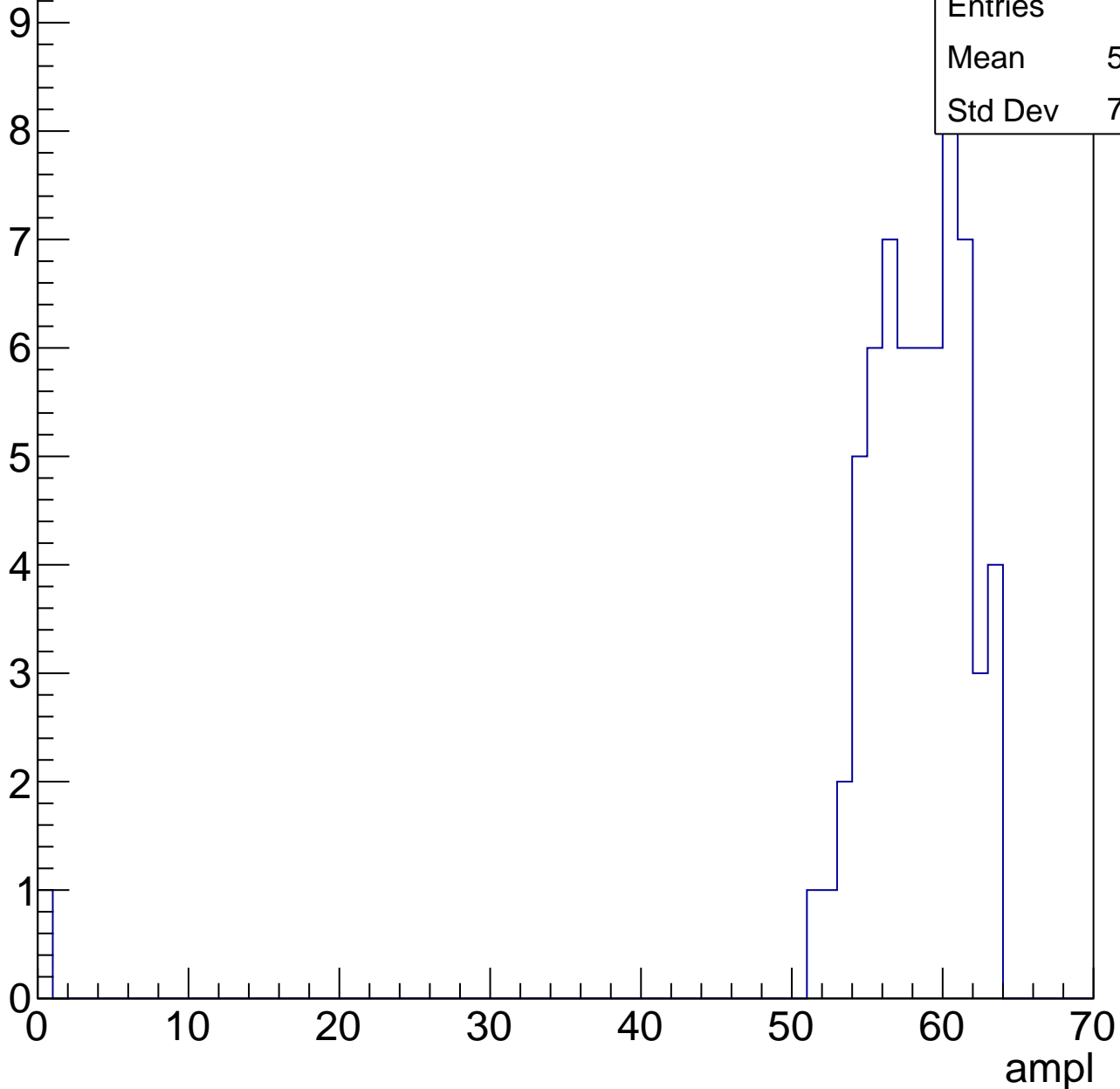


# B0L002S, U1-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 64    |
| Mean    | 57.03 |
| Std Dev | 7.766 |

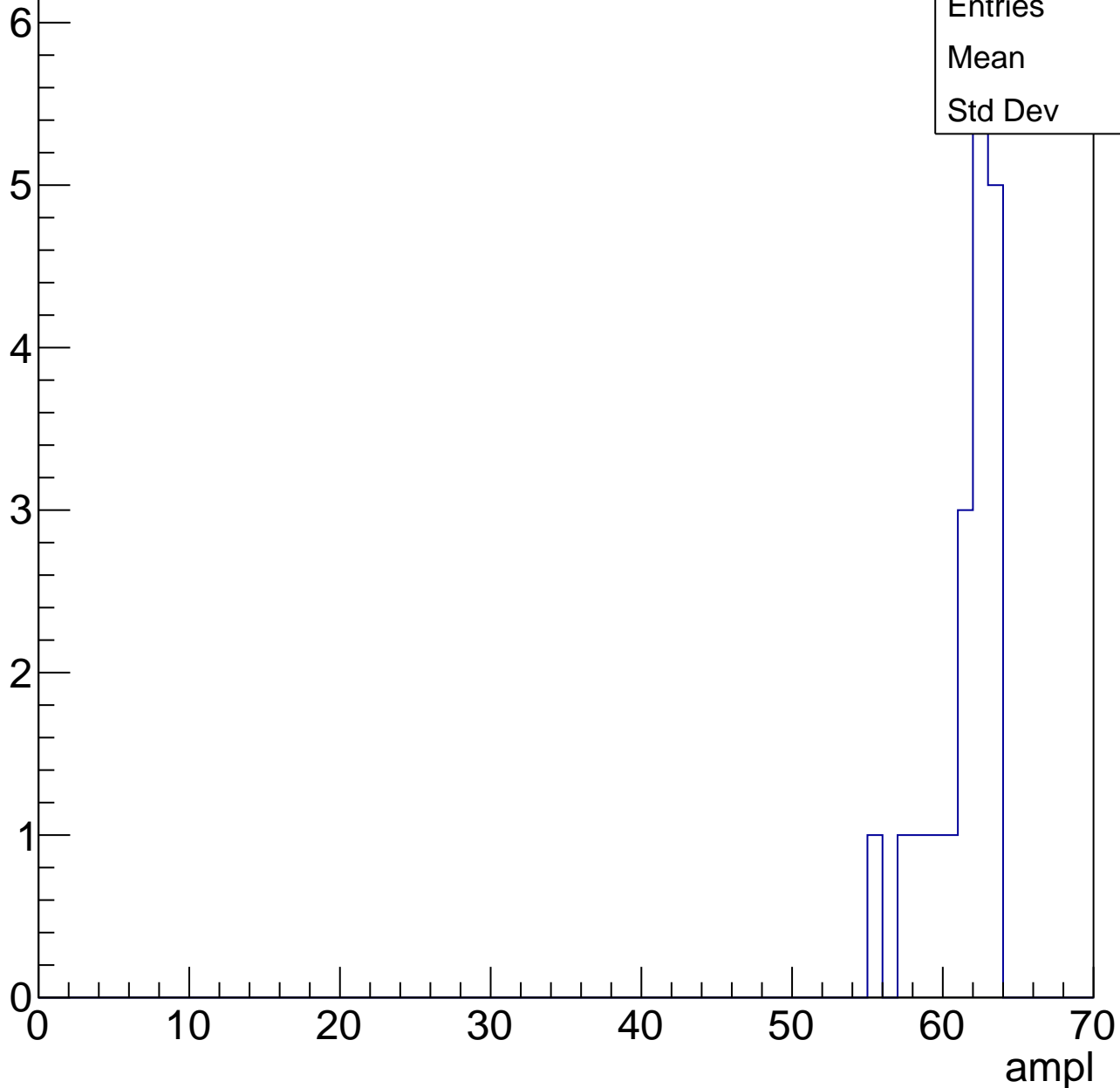


# B0L002S, U1-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |     |
|---------|-----|
| Entries | 19  |
| Mean    | 61  |
| Std Dev | 2.2 |



# B0L002S, U1-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch116, adc0

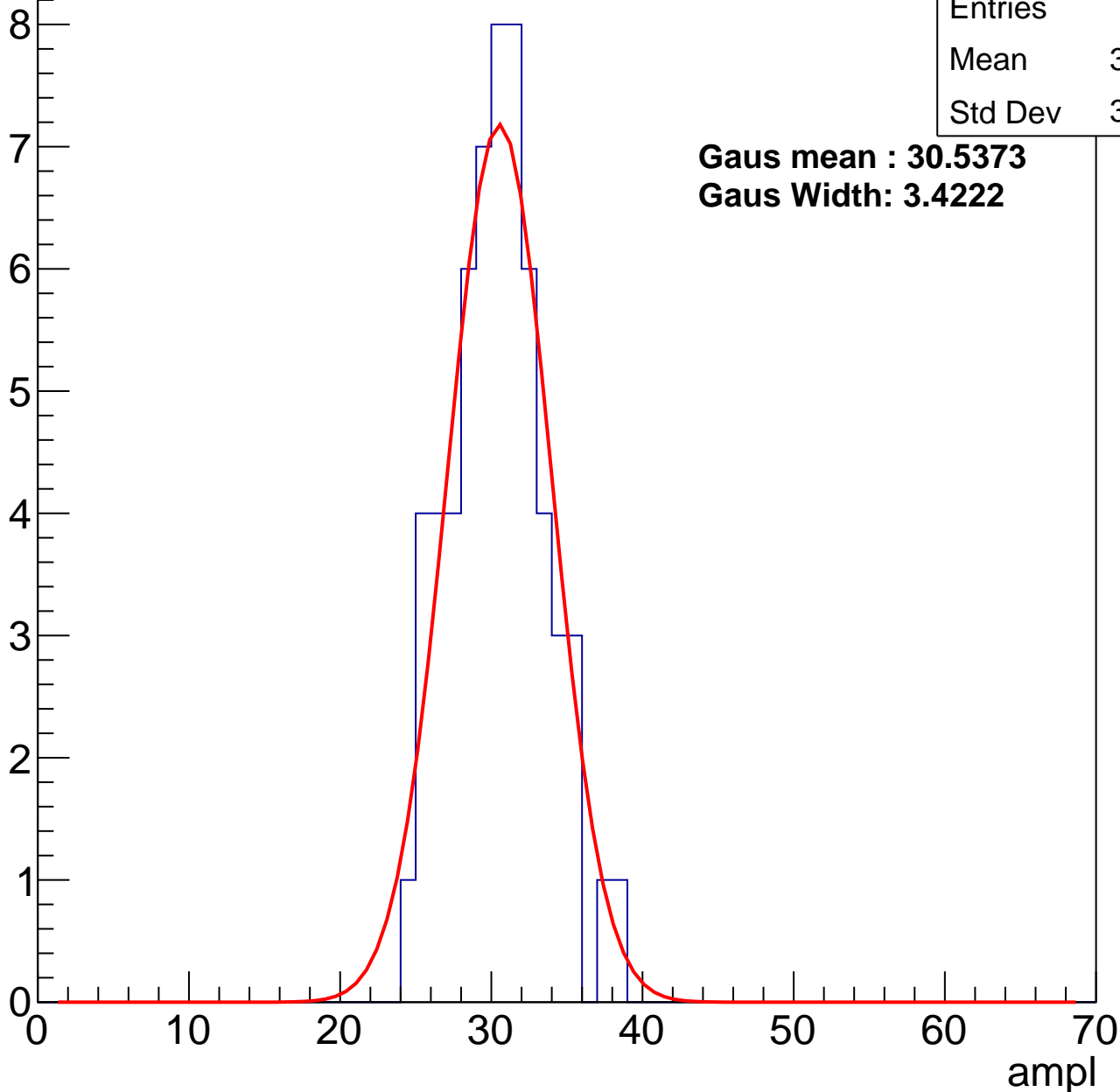
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 30.02 |
| Std Dev | 3.085 |

**Gaus mean : 30.5373**

**Gaus Width: 3.4222**



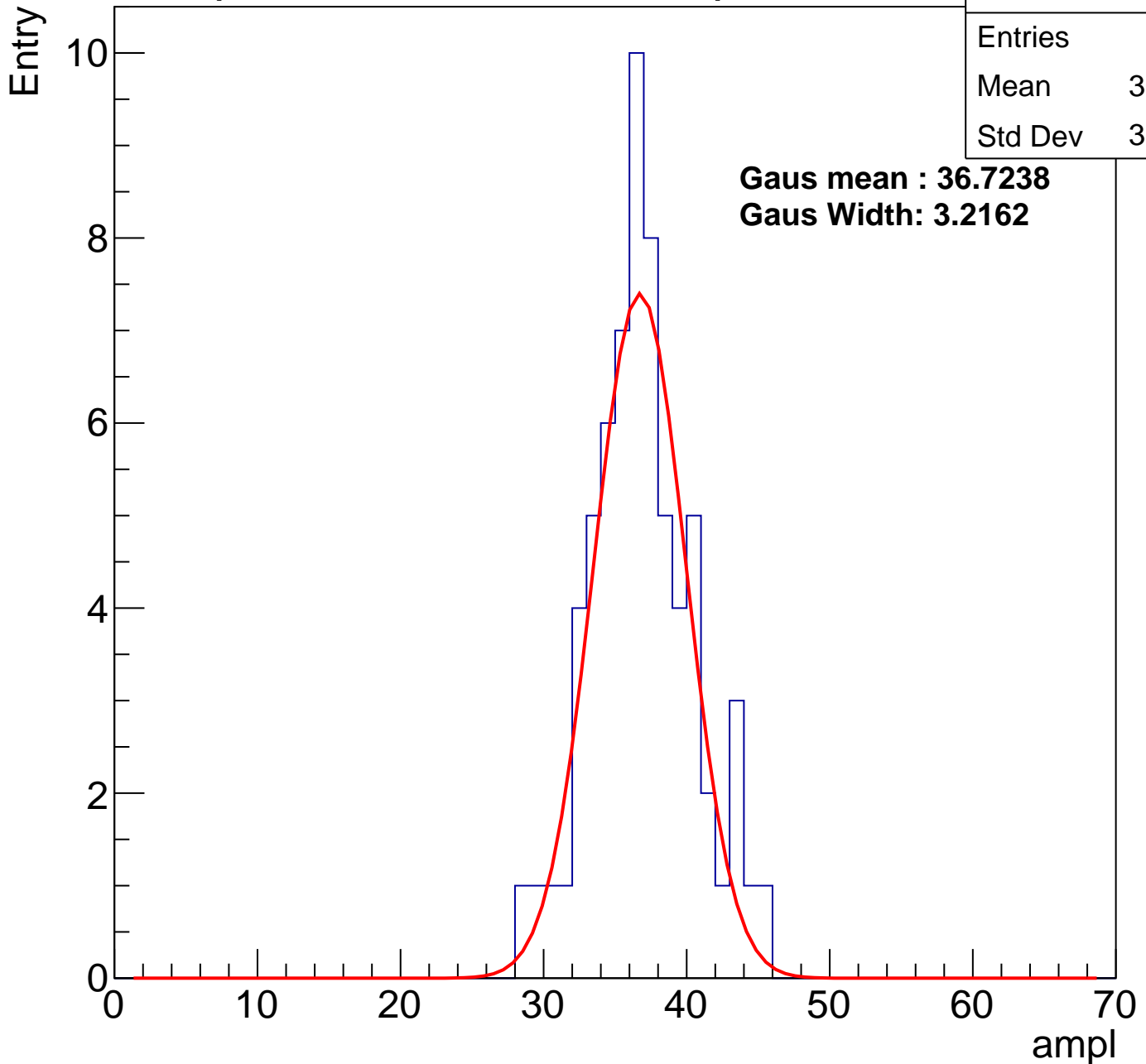
# B0L002S, U1-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 36.42 |
| Std Dev | 3.534 |

**Gaus mean : 36.7238**

**Gaus Width: 3.2162**



# B0L002S, U1-ch116, adc2

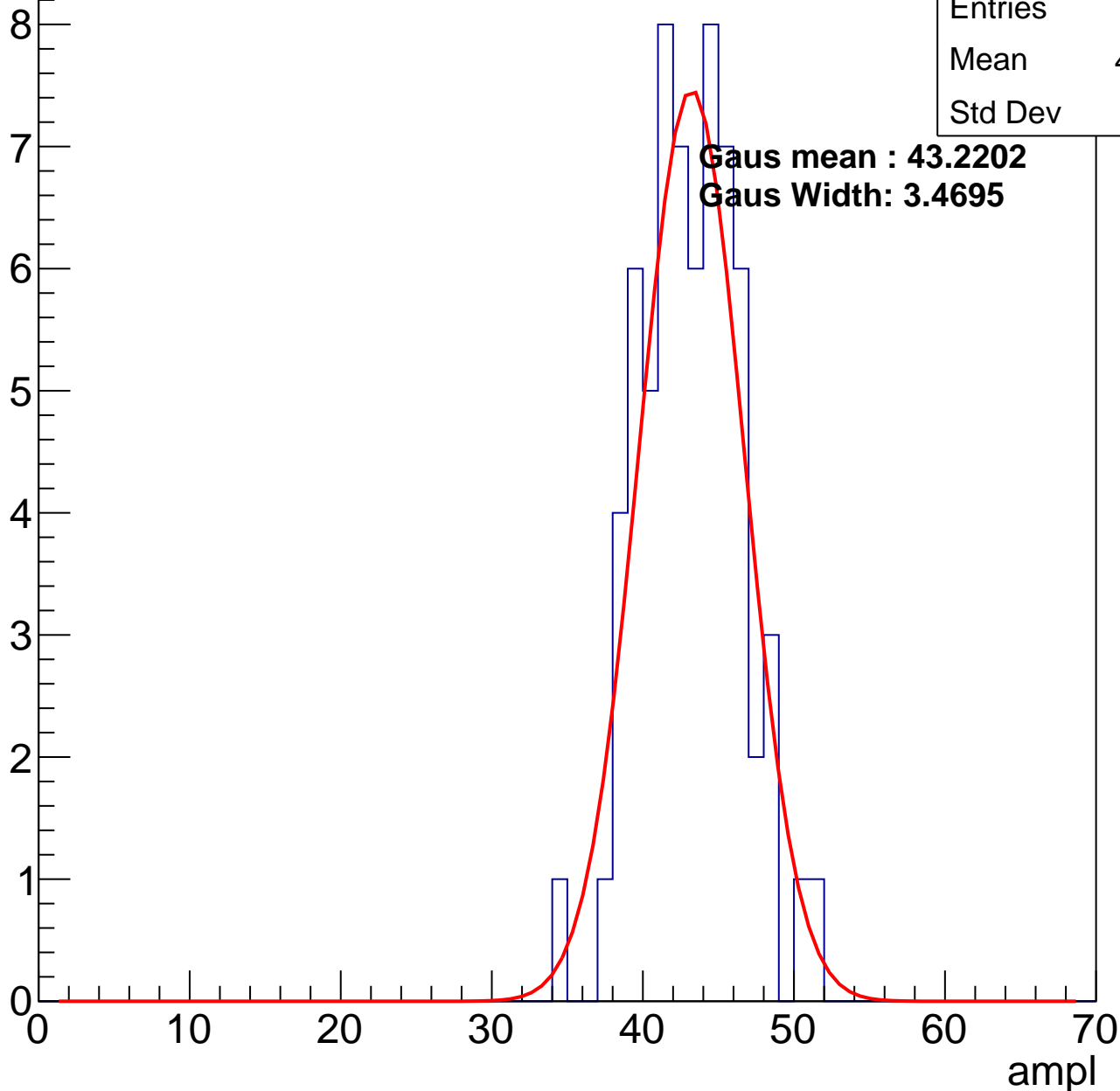
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 42.71 |
| Std Dev | 3.26  |

**Gaus mean : 43.2202**

**Gaus Width: 3.4695**

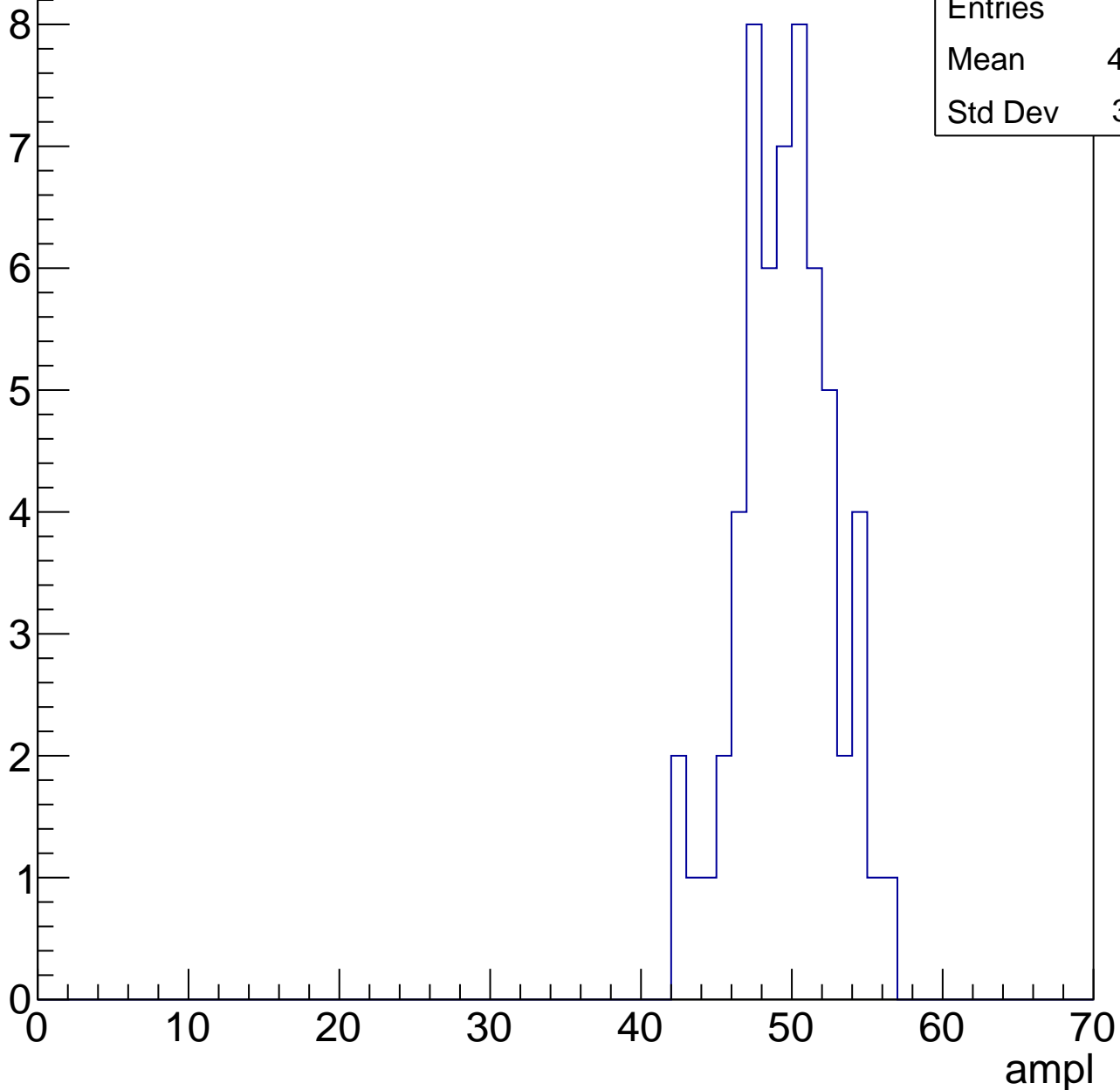


# B0L002S, U1-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 49.16 |
| Std Dev | 3.101 |

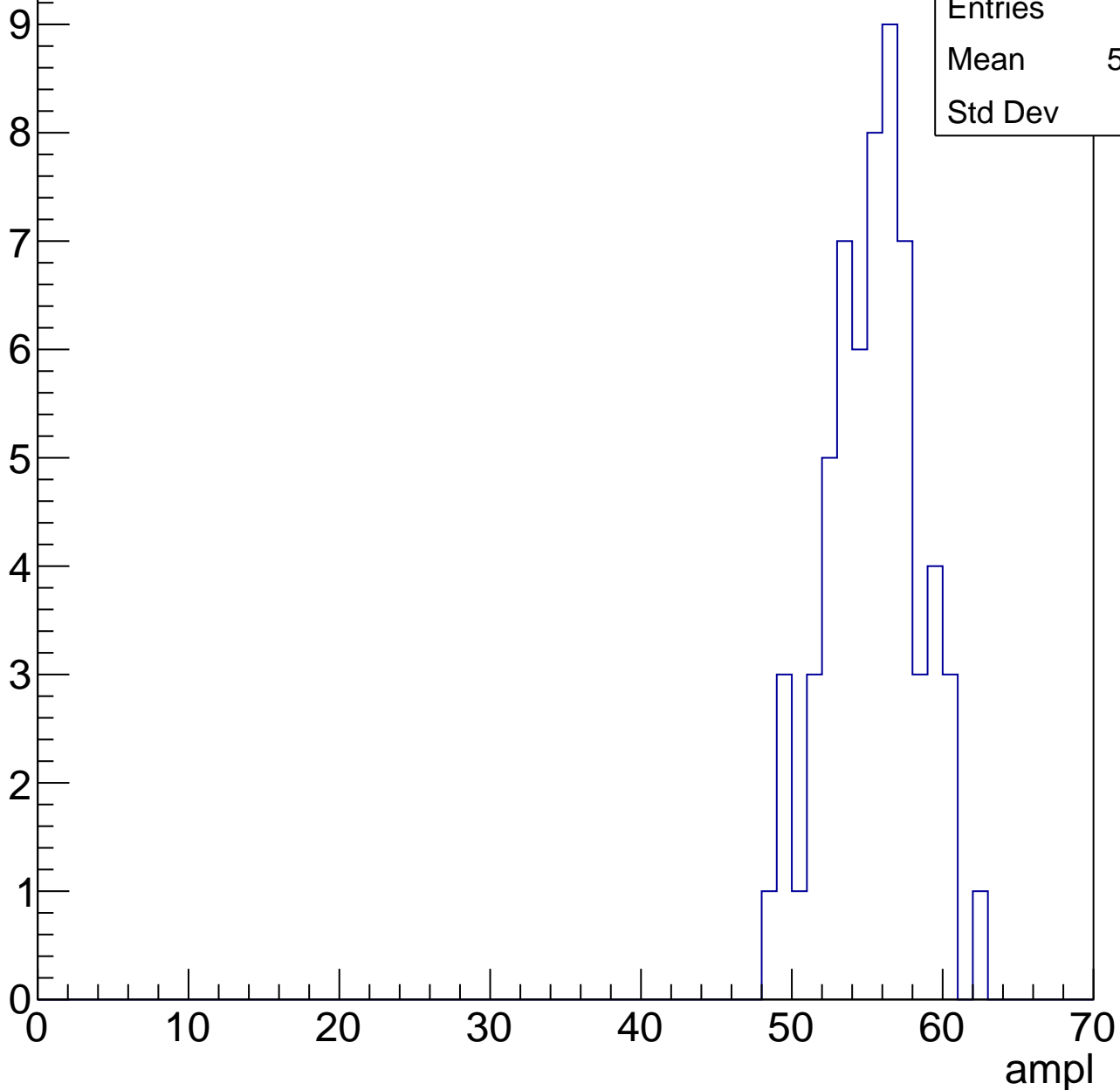


# B0L002S, U1-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 54.89 |
| Std Dev | 3.03  |

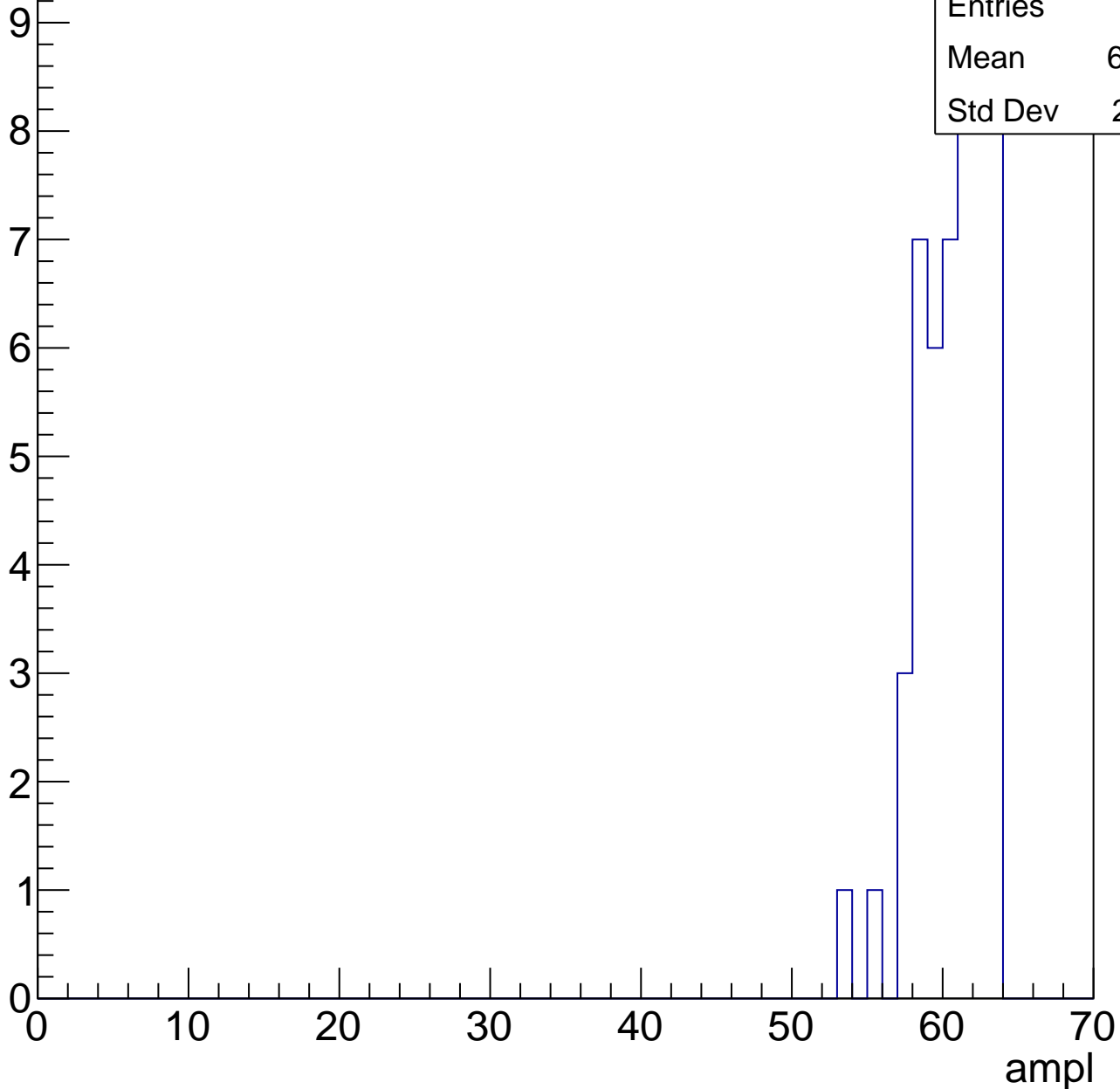


# B0L002S, U1-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 50    |
| Mean    | 60.16 |
| Std Dev | 2.221 |



# B0L002S, U1-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch117, adc0

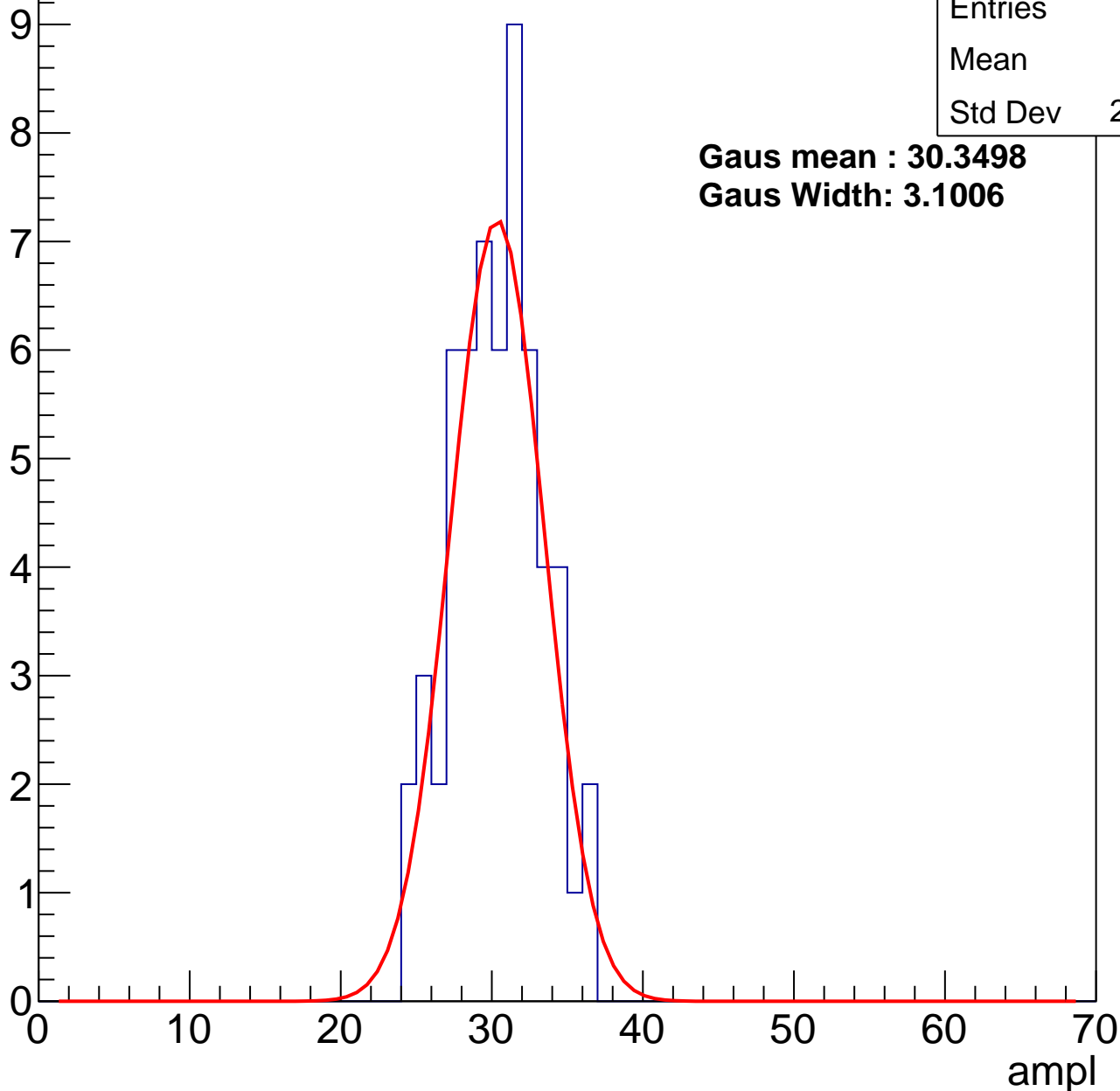
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 29.9  |
| Std Dev | 2.917 |

**Gaus mean : 30.3498**

**Gaus Width: 3.1006**



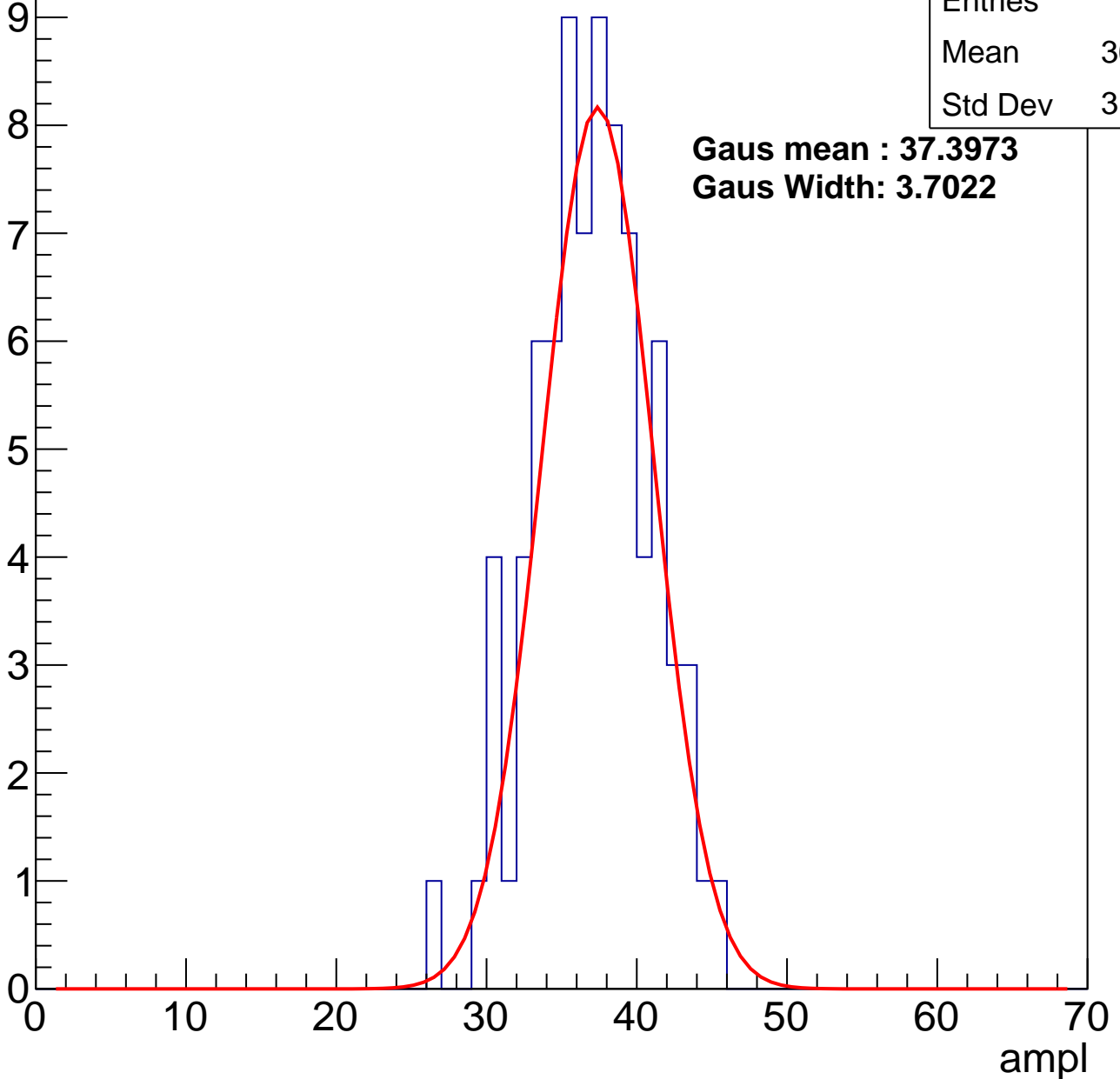
# B0L002S, U1-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 81    |
| Mean    | 36.58 |
| Std Dev | 3.784 |

**Gaus mean : 37.3973**  
**Gaus Width: 3.7022**



# B0L002S, U1-ch117, adc2

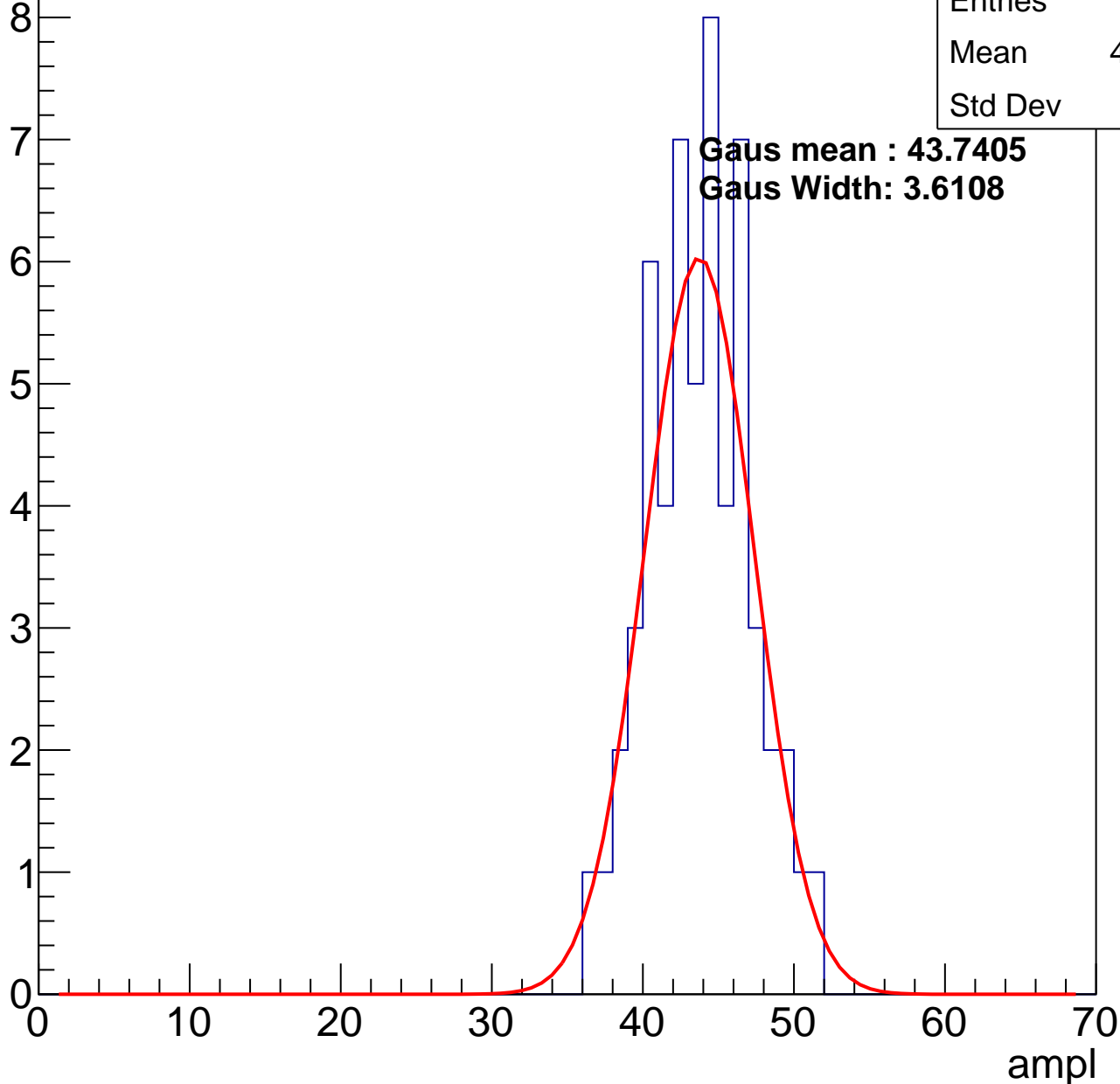
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 43.32 |
| Std Dev | 3.31  |

**Gaus mean : 43.7405**

**Gaus Width: 3.6108**

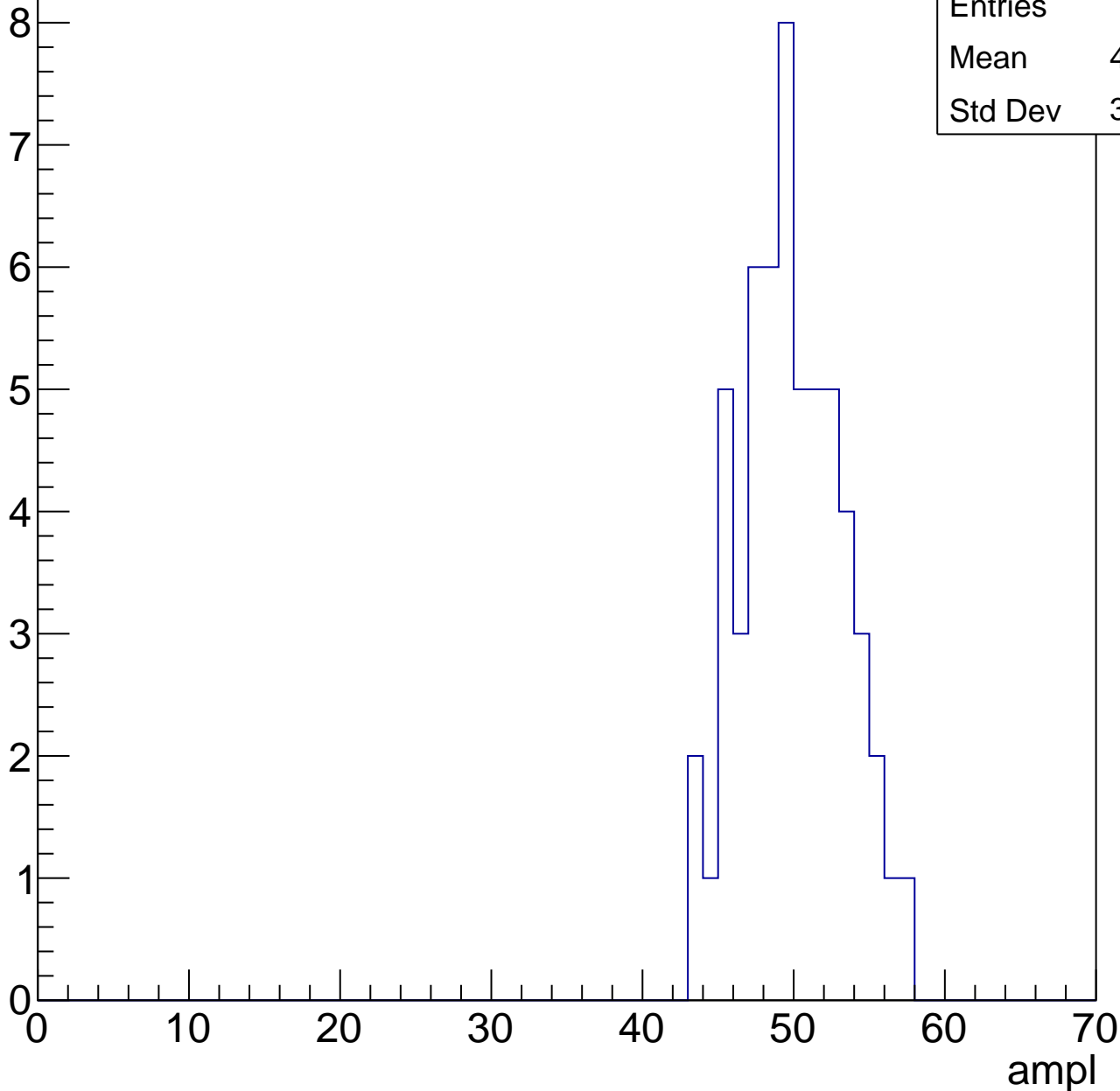


# B0L002S, U1-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 57    |
| Mean    | 49.42 |
| Std Dev | 3.292 |

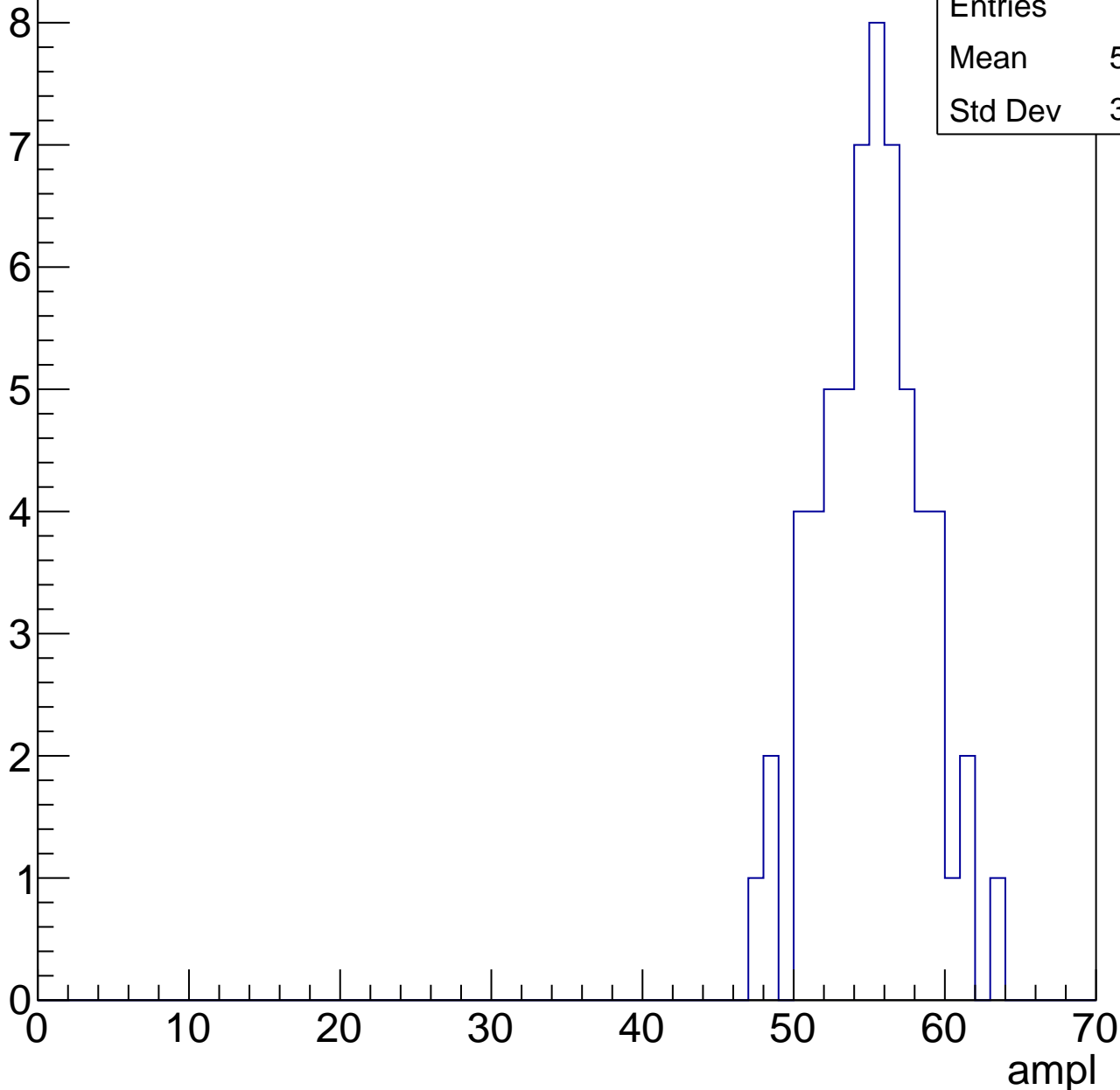


# B0L002S, U1-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 60    |
| Mean    | 54.67 |
| Std Dev | 3.365 |

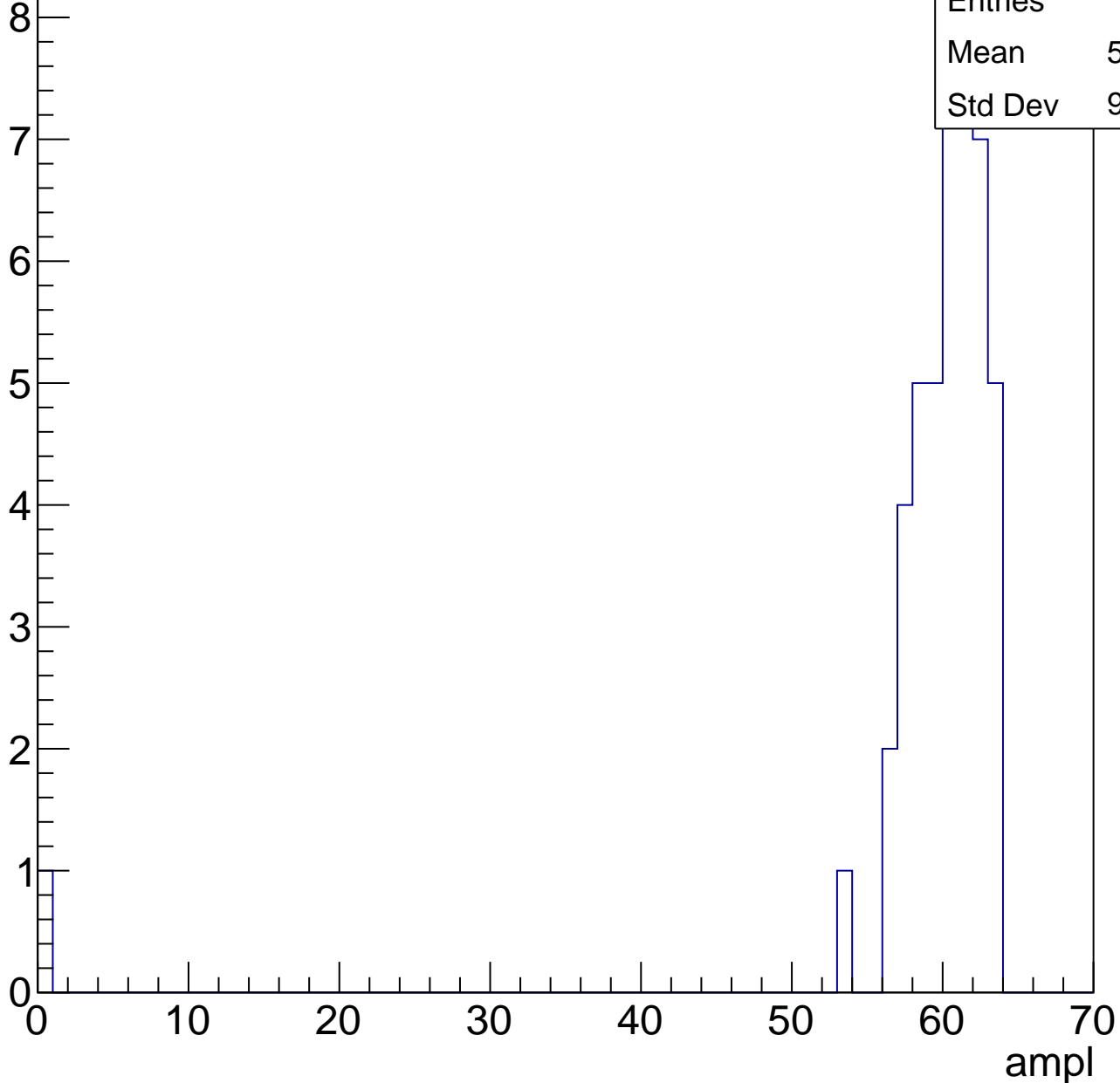


# B0L002S, U1-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

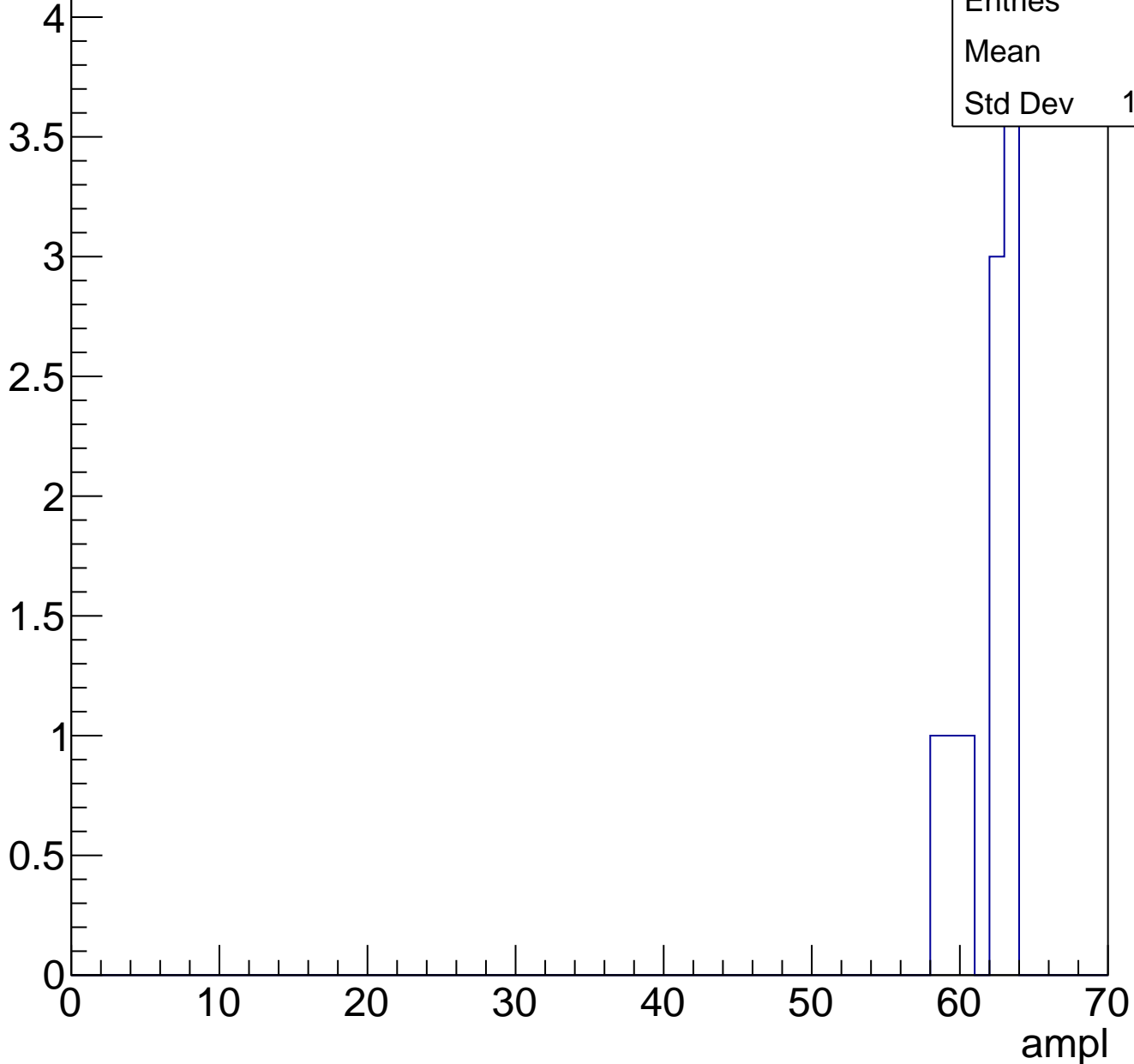
|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 58.59 |
| Std Dev | 9.006 |



# B0L002S, U1-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 2    |
| Mean    | 11.5 |
| Std Dev | 11.5 |

# B0L002S, U1-ch118, adc0

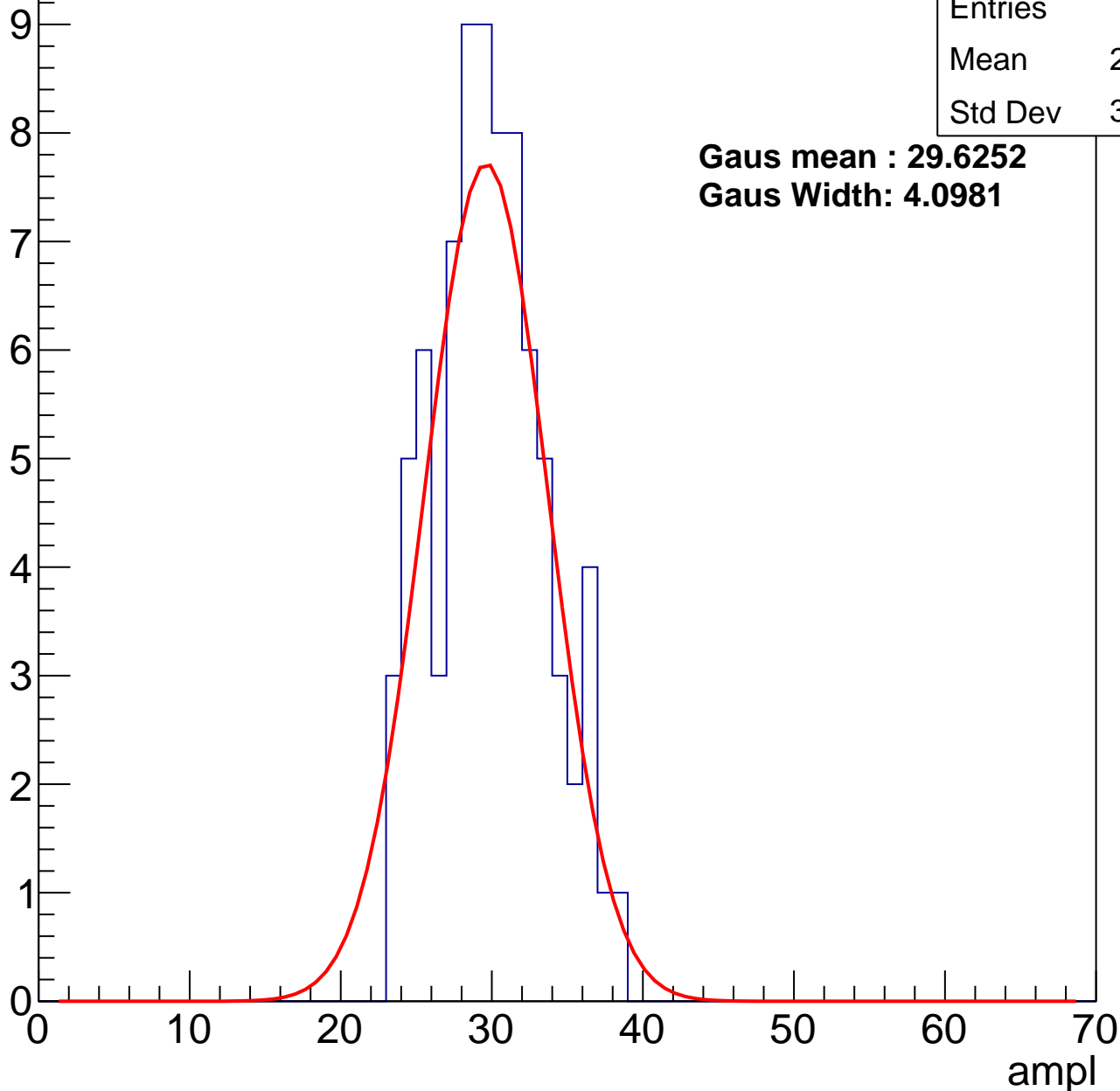
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 80    |
| Mean    | 29.44 |
| Std Dev | 3.605 |

**Gaus mean : 29.6252**

**Gaus Width: 4.0981**



# B0L002S, U1-ch118, adc1

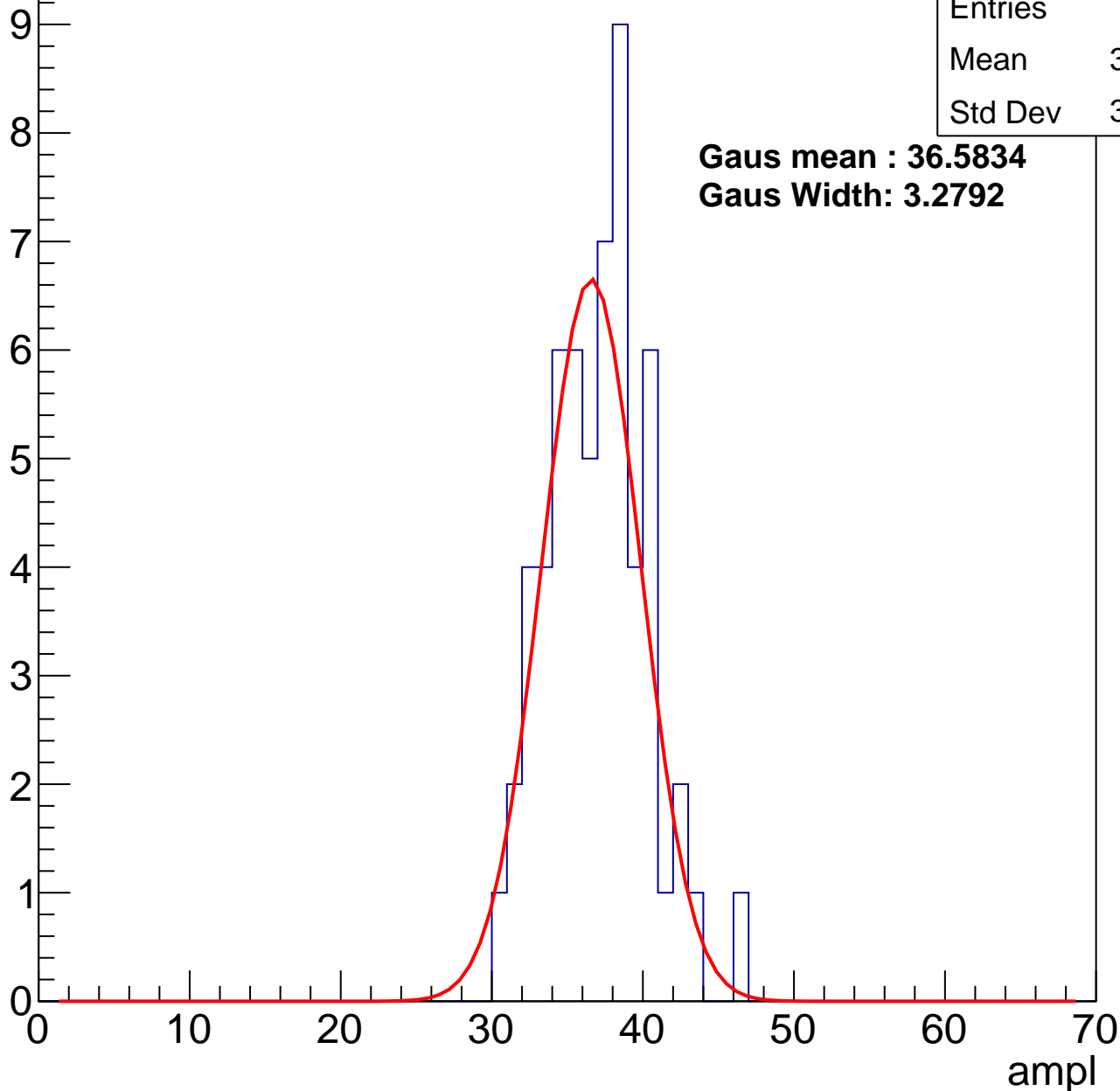
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 36.56 |
| Std Dev | 3.238 |

**Gaus mean : 36.5834**

**Gaus Width: 3.2792**



# B0L002S, U1-ch118, adc2

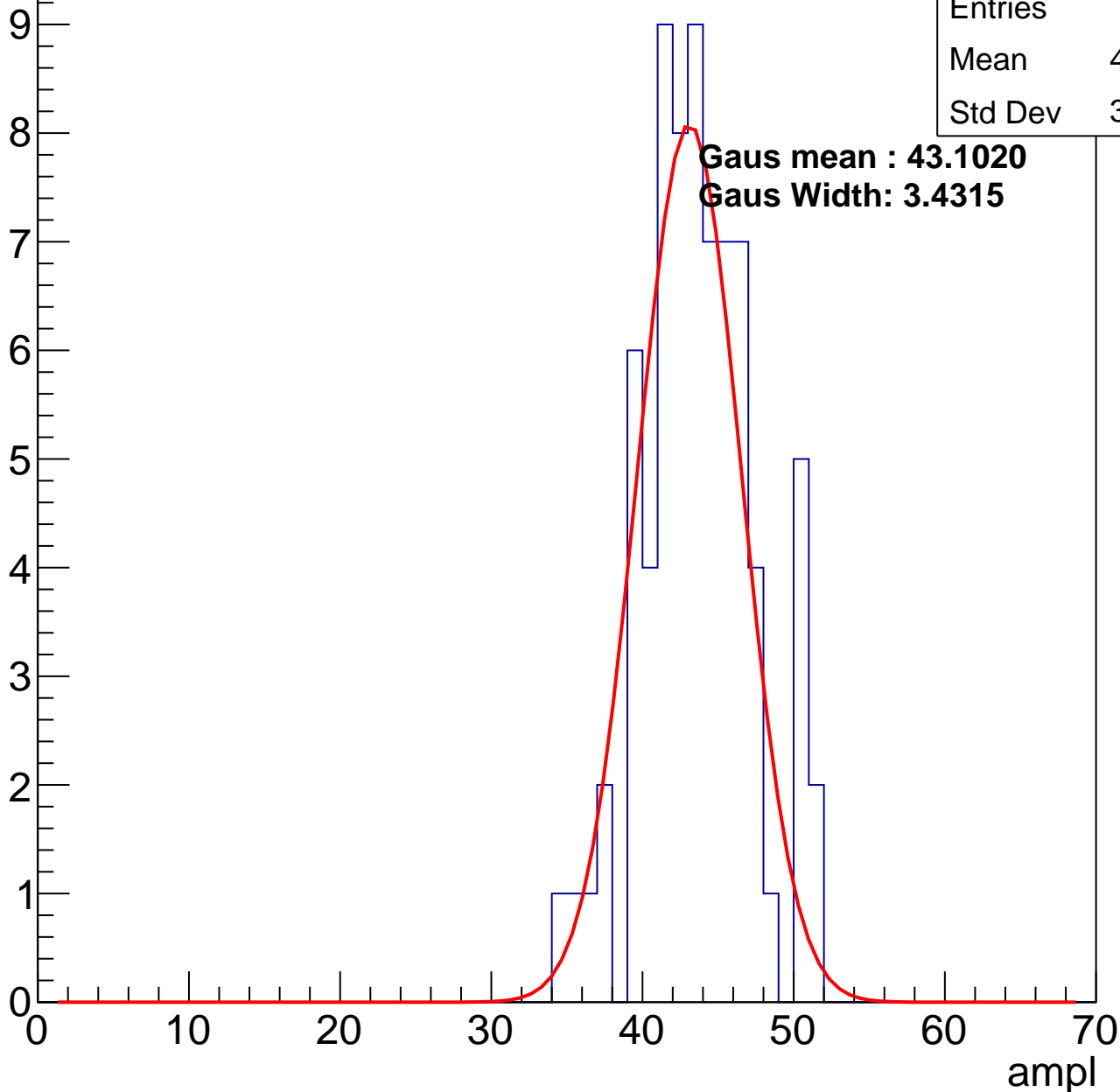
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 43.22 |
| Std Dev | 3.673 |

**Gaus mean : 43.1020**

**Gaus Width: 3.4315**

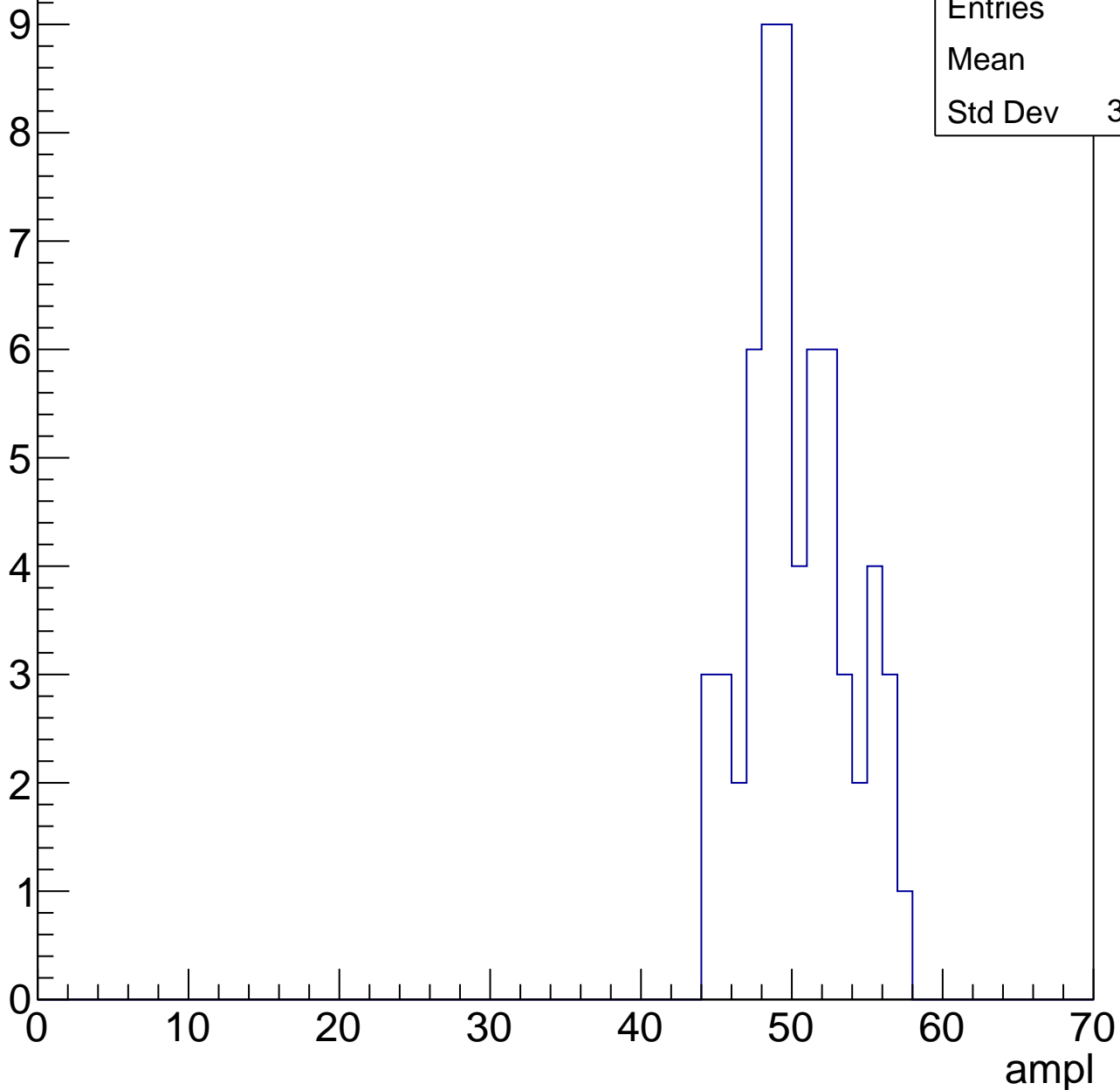


# B0L002S, U1-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

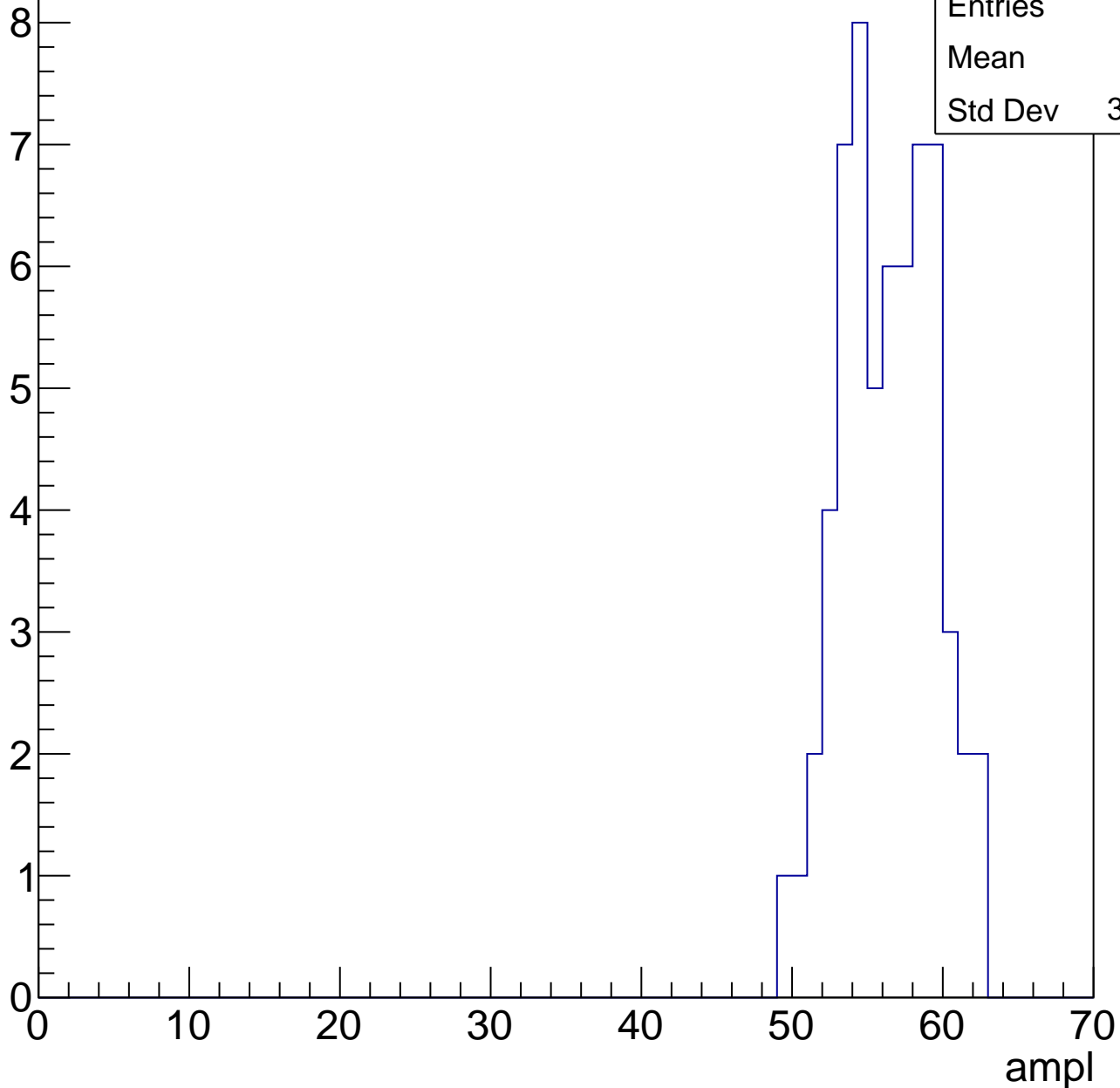
|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 49.9  |
| Std Dev | 3.288 |



# B0L002S, U1-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

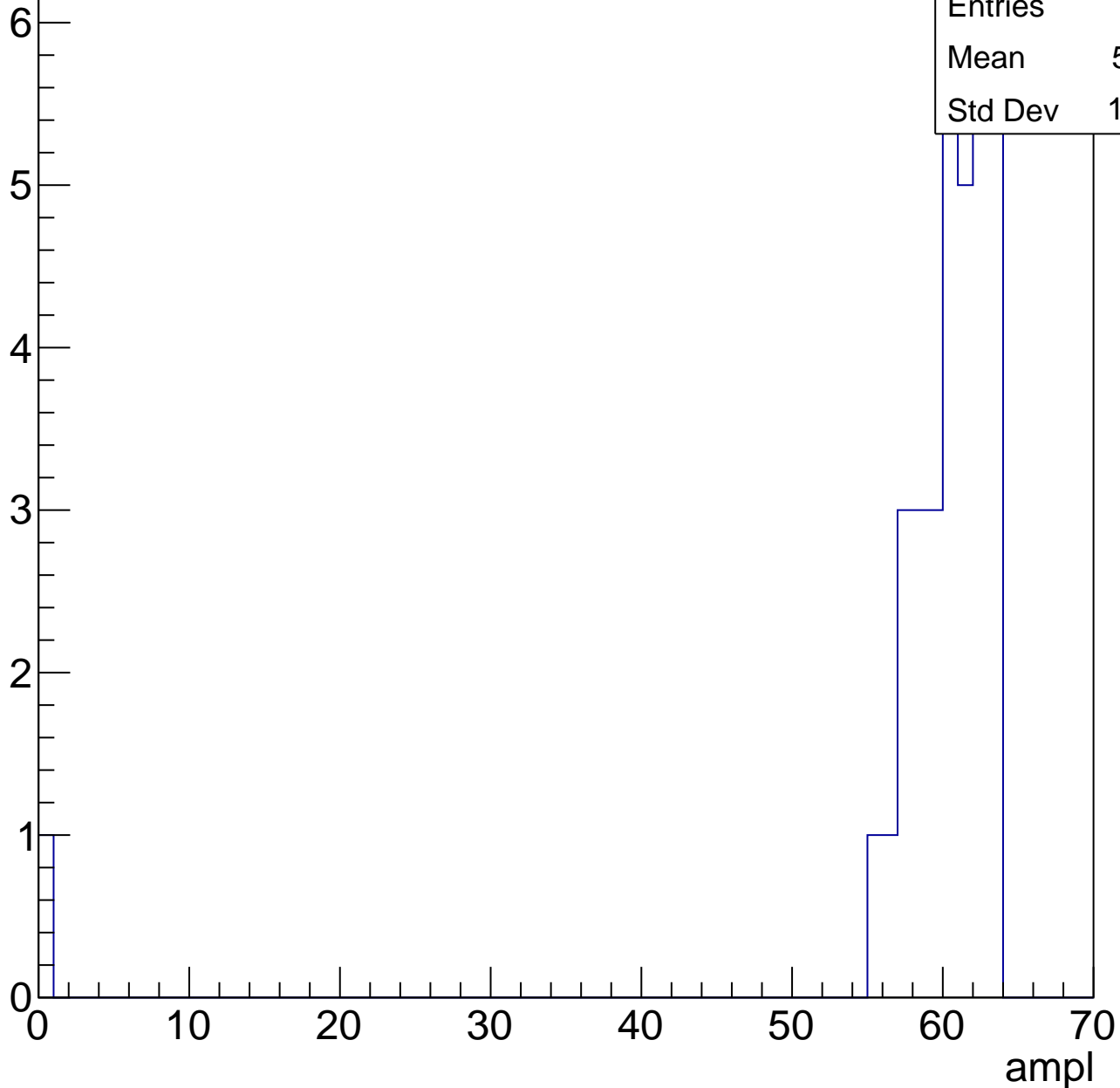


|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 55.9  |
| Std Dev | 3.045 |

# B0L002S, U1-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

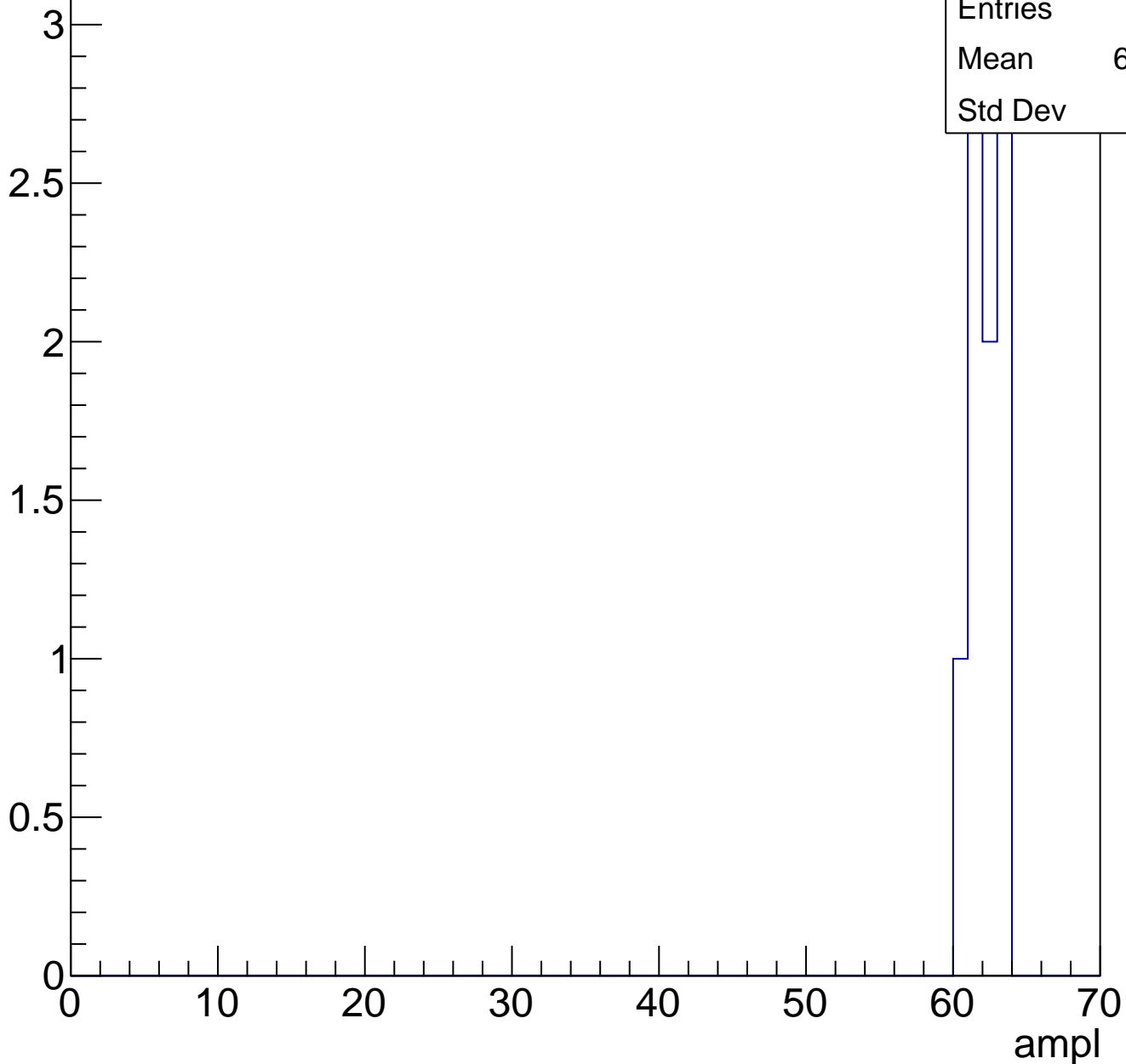
Entry



# B0L002S, U1-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |      |
|---------|------|
| Entries | 3    |
| Mean    | 21   |
| Std Dev | 29.7 |

# B0L002S, U1-ch119, adc0

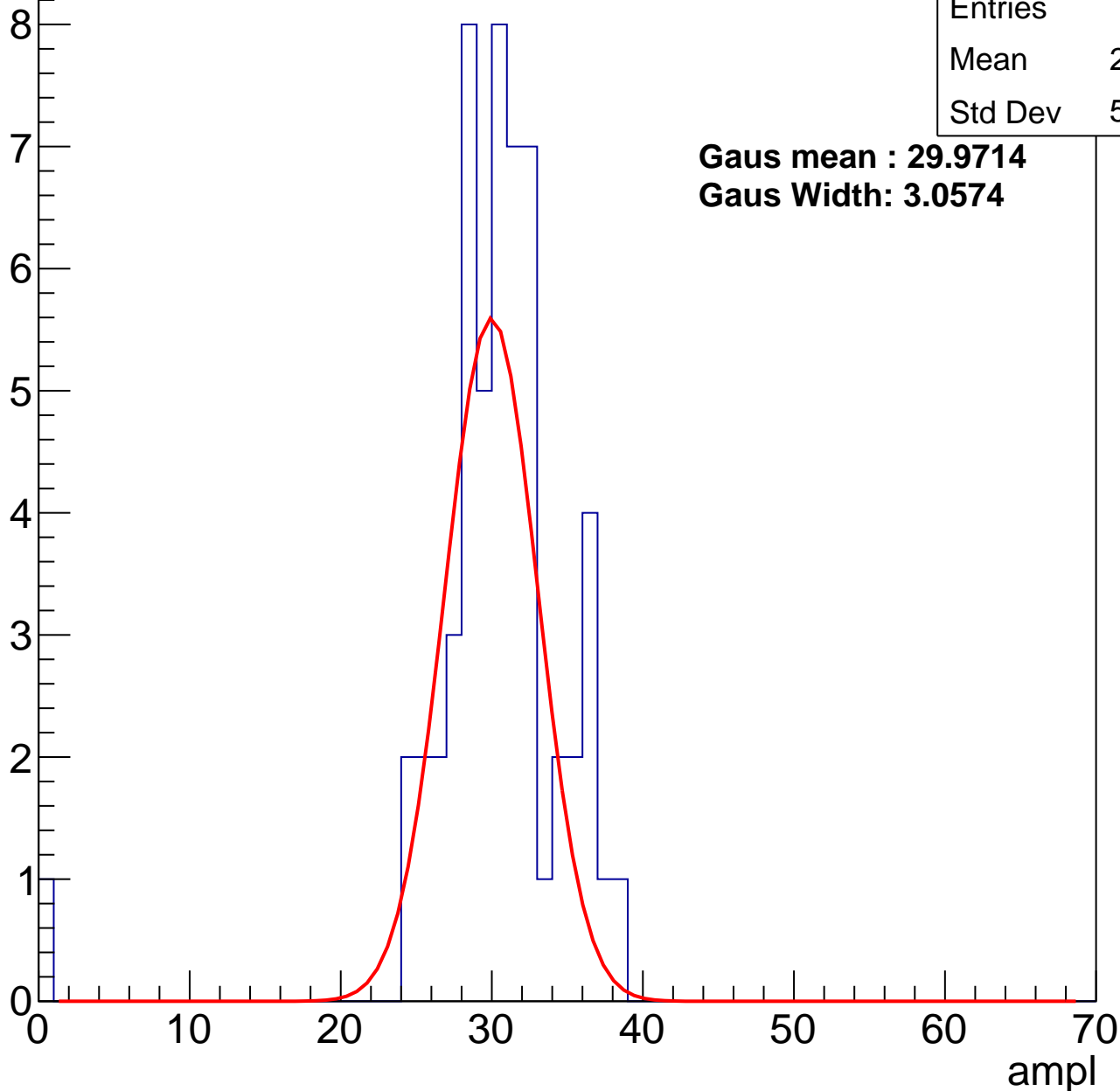
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 29.84 |
| Std Dev | 5.175 |

**Gaus mean : 29.9714**

**Gaus Width: 3.0574**



# B0L002S, U1-ch119, adc1

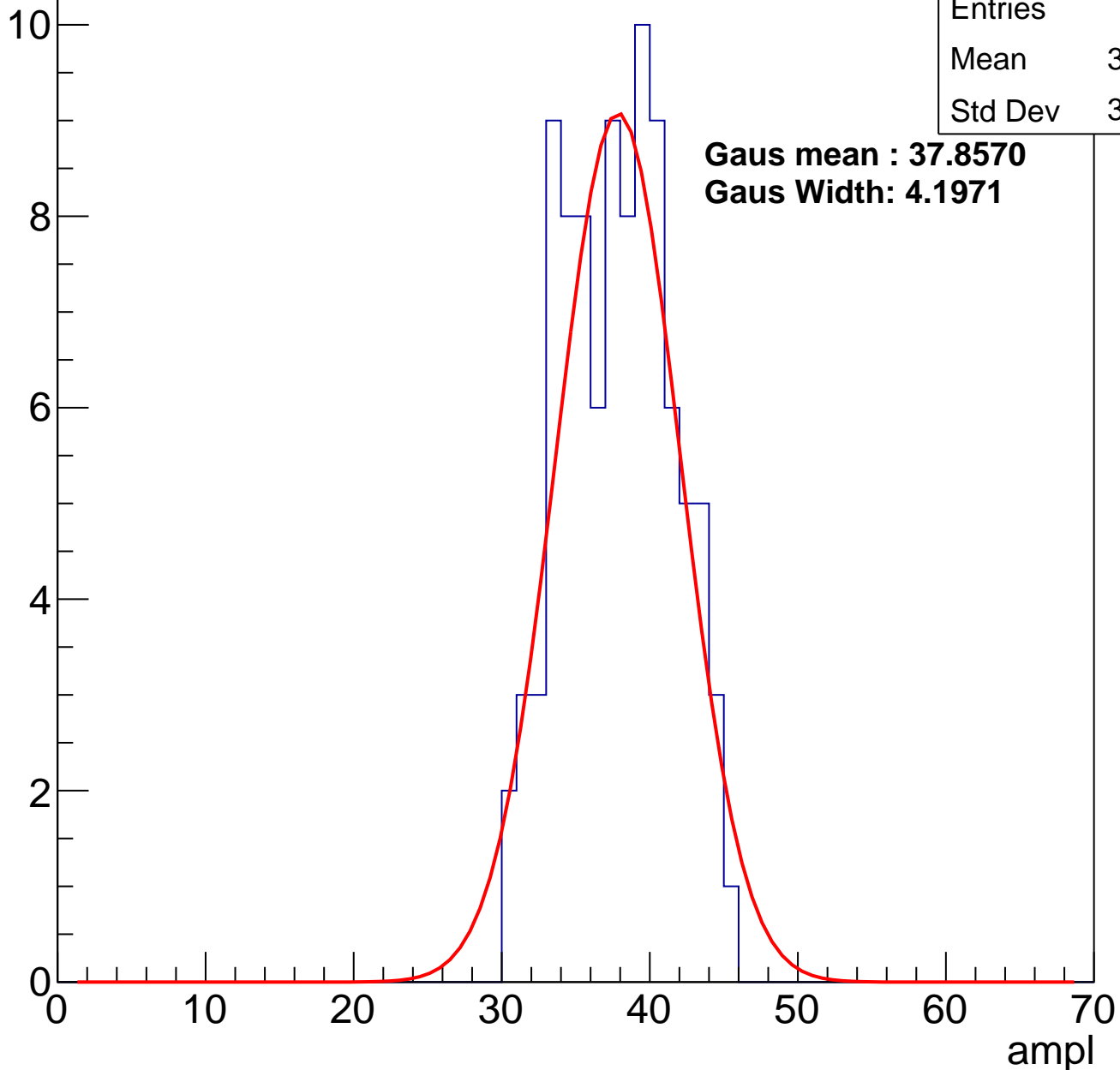
calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 95    |
| Mean    | 37.36 |
| Std Dev | 3.647 |

**Gaus mean : 37.8570**

**Gaus Width: 4.1971**

Entry



# B0L002S, U1-ch119, adc2

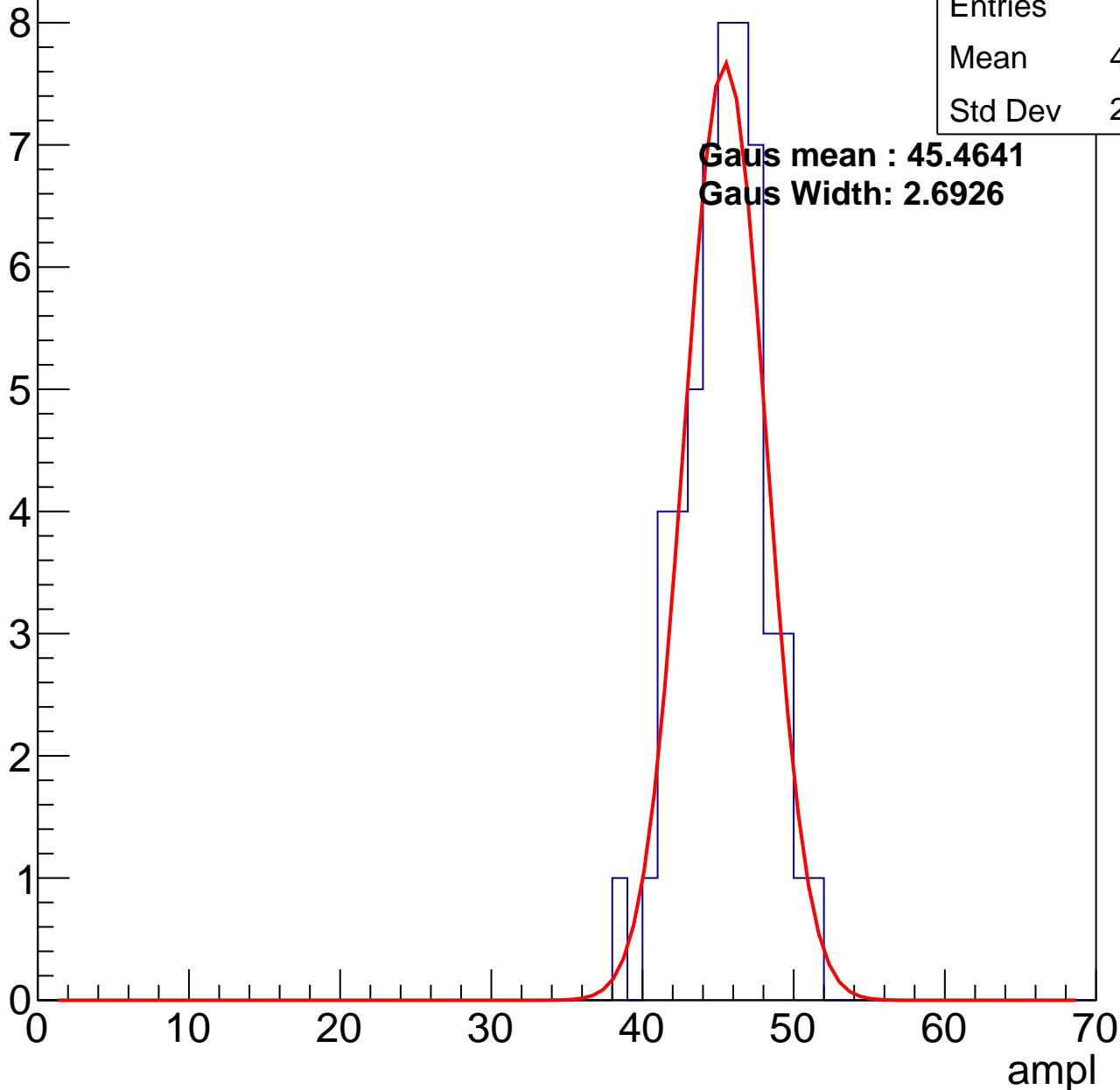
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 44.94 |
| Std Dev | 2.652 |

**Gaus mean : 45.4641**

**Gaus Width: 2.6926**

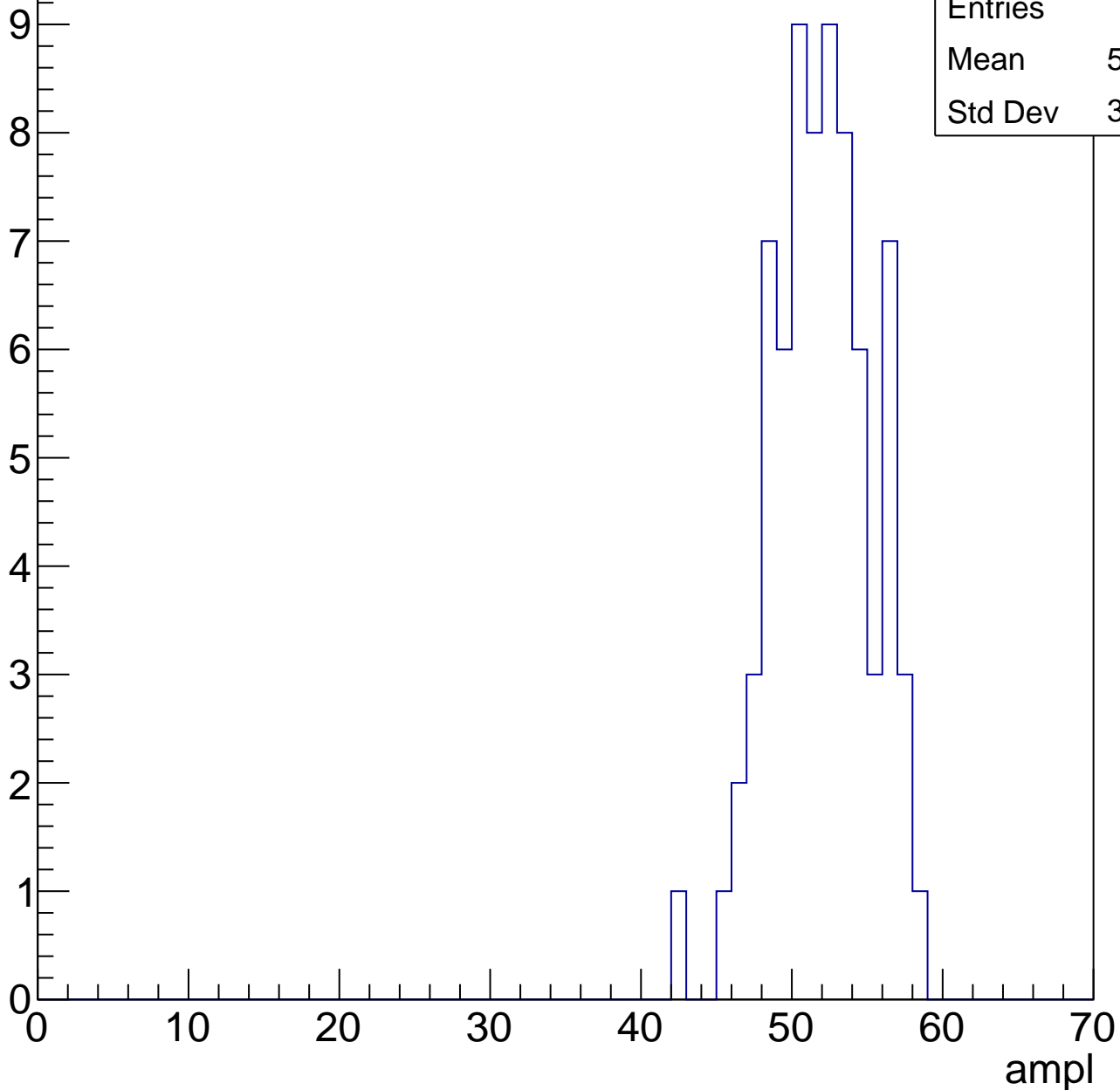


# B0L002S, U1-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 51.49 |
| Std Dev | 3.223 |

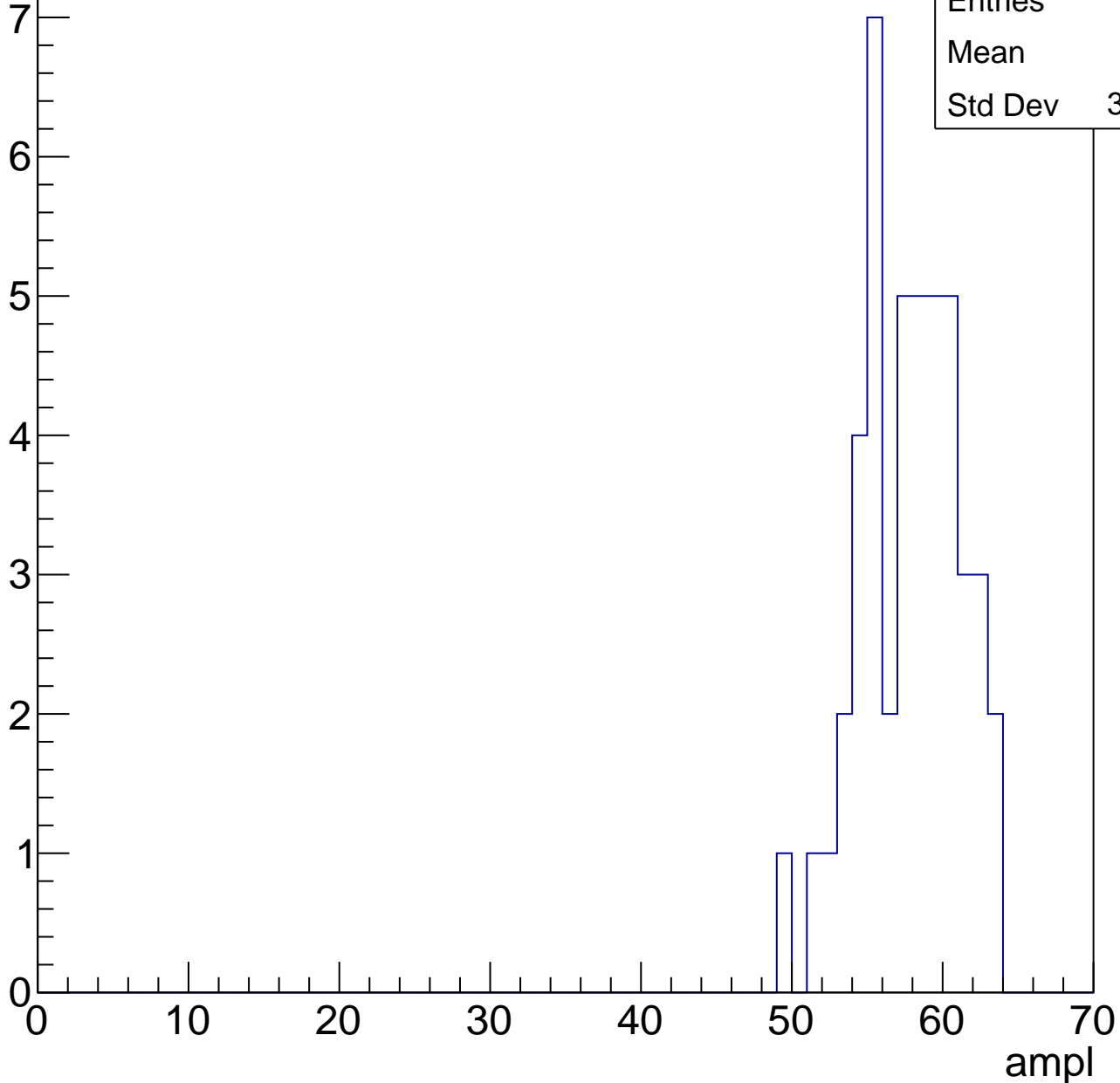


# B0L002S, U1-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 46    |
| Mean    | 57.3  |
| Std Dev | 3.243 |

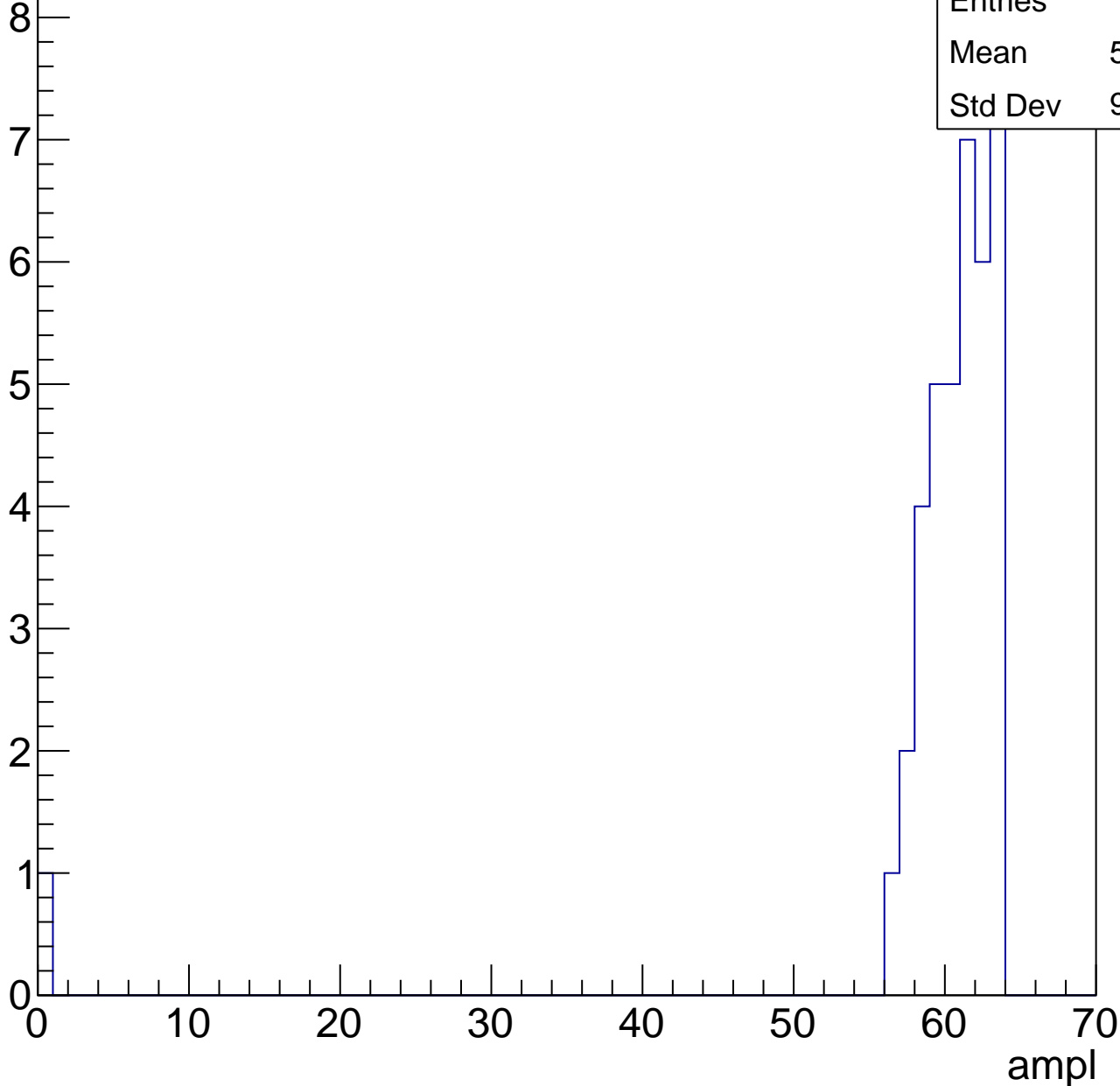


# B0L002S, U1-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 39    |
| Mean    | 58.97 |
| Std Dev | 9.763 |



# B0L002S, U1-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 62 |
| Std Dev | 0  |



# B0L002S, U1-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

|         |    |
|---------|----|
| Entries | 1  |
| Mean    | 23 |
| Std Dev | 0  |

ampl

0 10 20 30 40 50 60 70

# B0L002S, U1-ch120, adc0

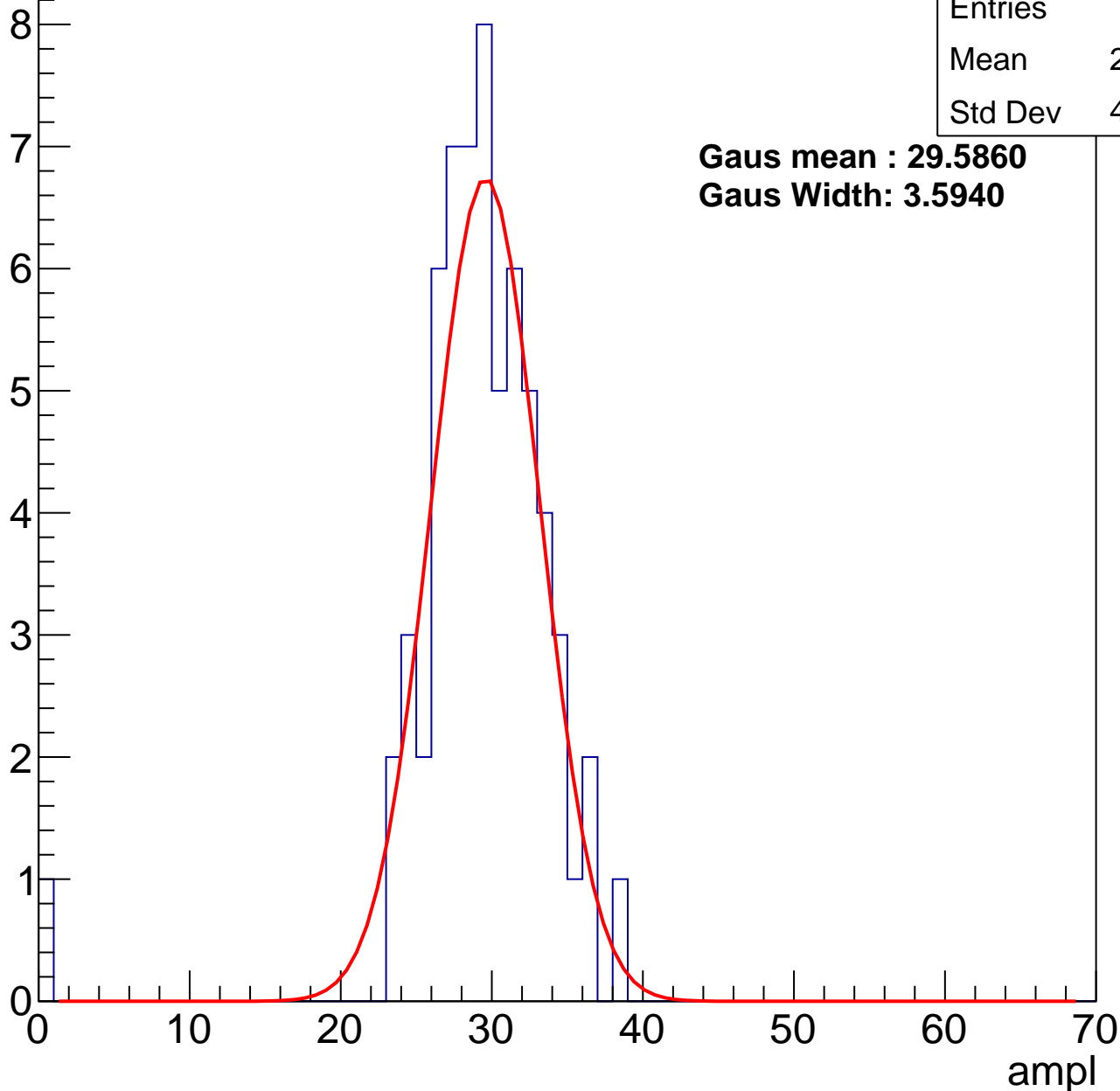
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 28.83 |
| Std Dev | 4.936 |

**Gaus mean : 29.5860**

**Gaus Width: 3.5940**



# B0L002S, U1-ch120, adc1

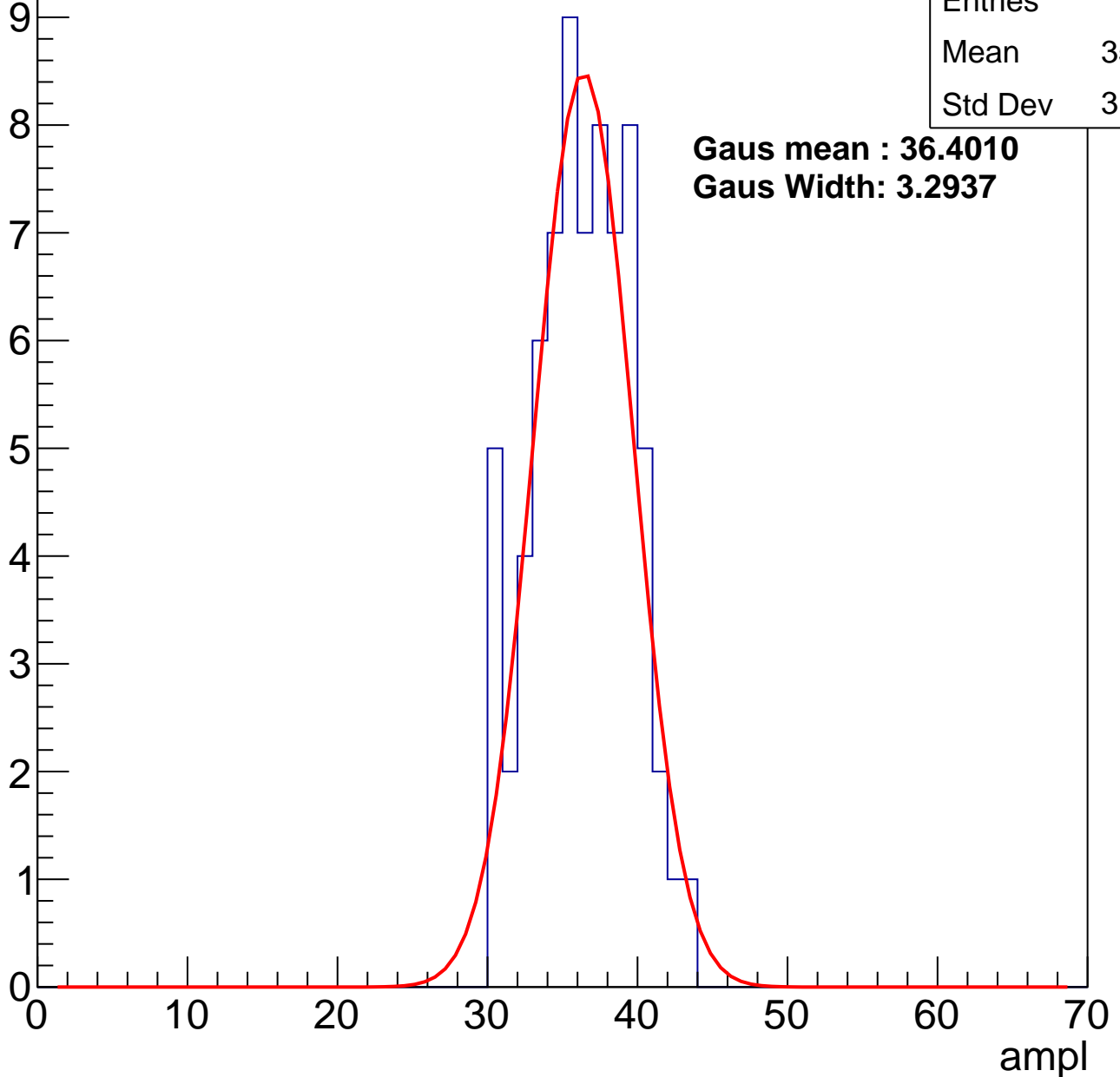
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 72    |
| Mean    | 35.89 |
| Std Dev | 3.134 |

**Gaus mean : 36.4010**

**Gaus Width: 3.2937**



# B0L002S, U1-ch120, adc2

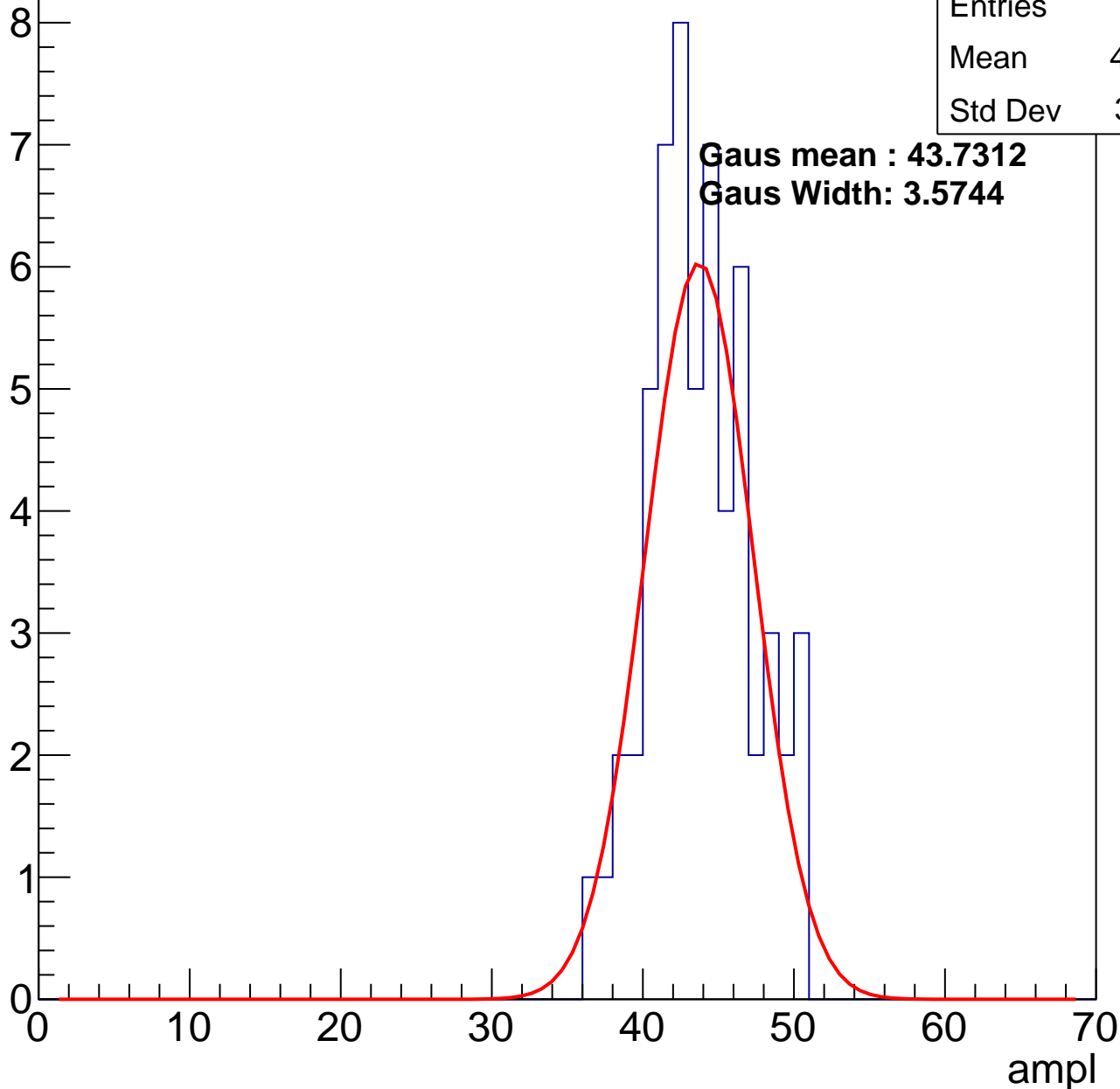
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 43.36 |
| Std Dev | 3.331 |

**Gaus mean : 43.7312**

**Gaus Width: 3.5744**

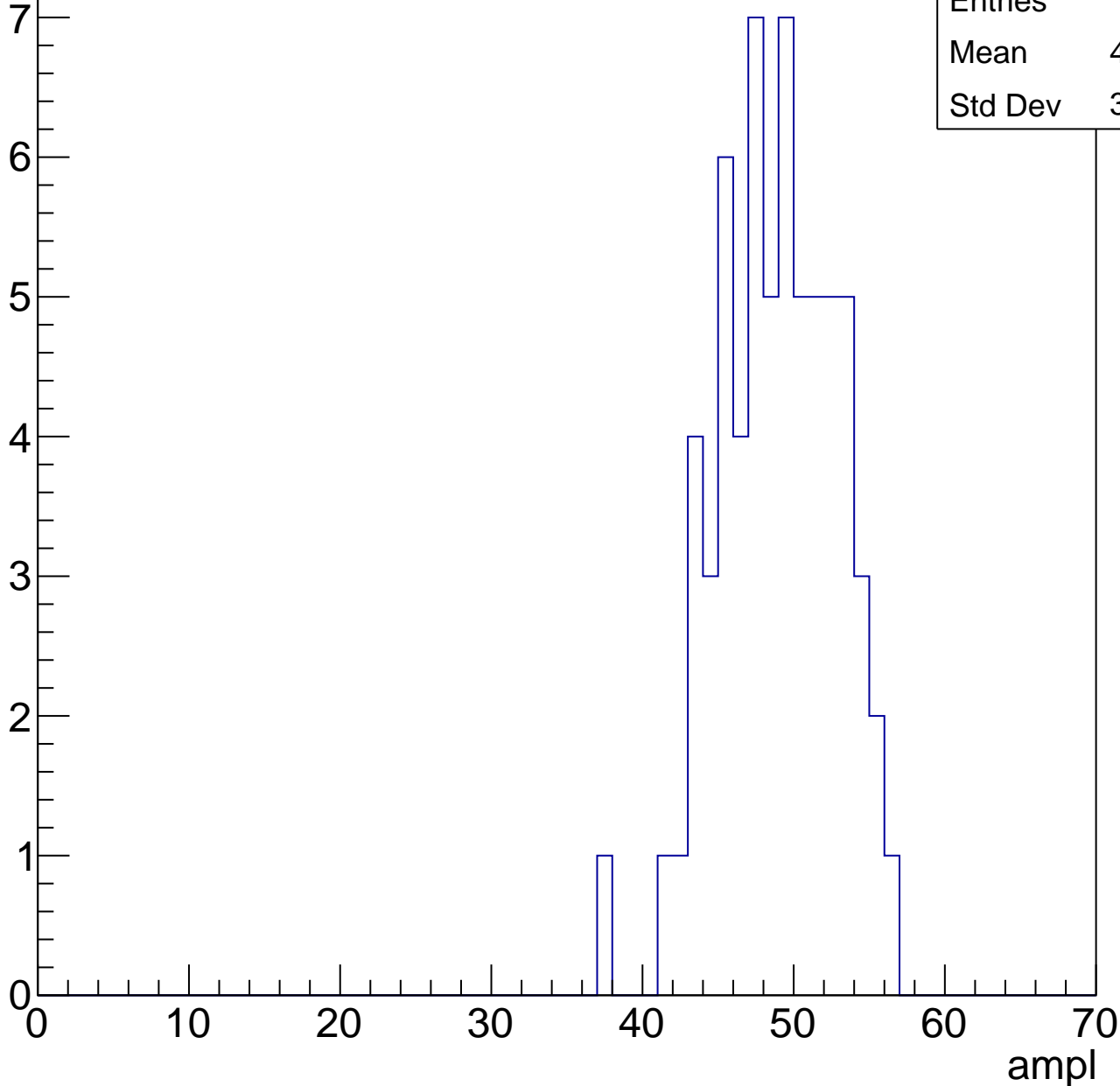


# B0L002S, U1-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 48.43 |
| Std Dev | 3.855 |

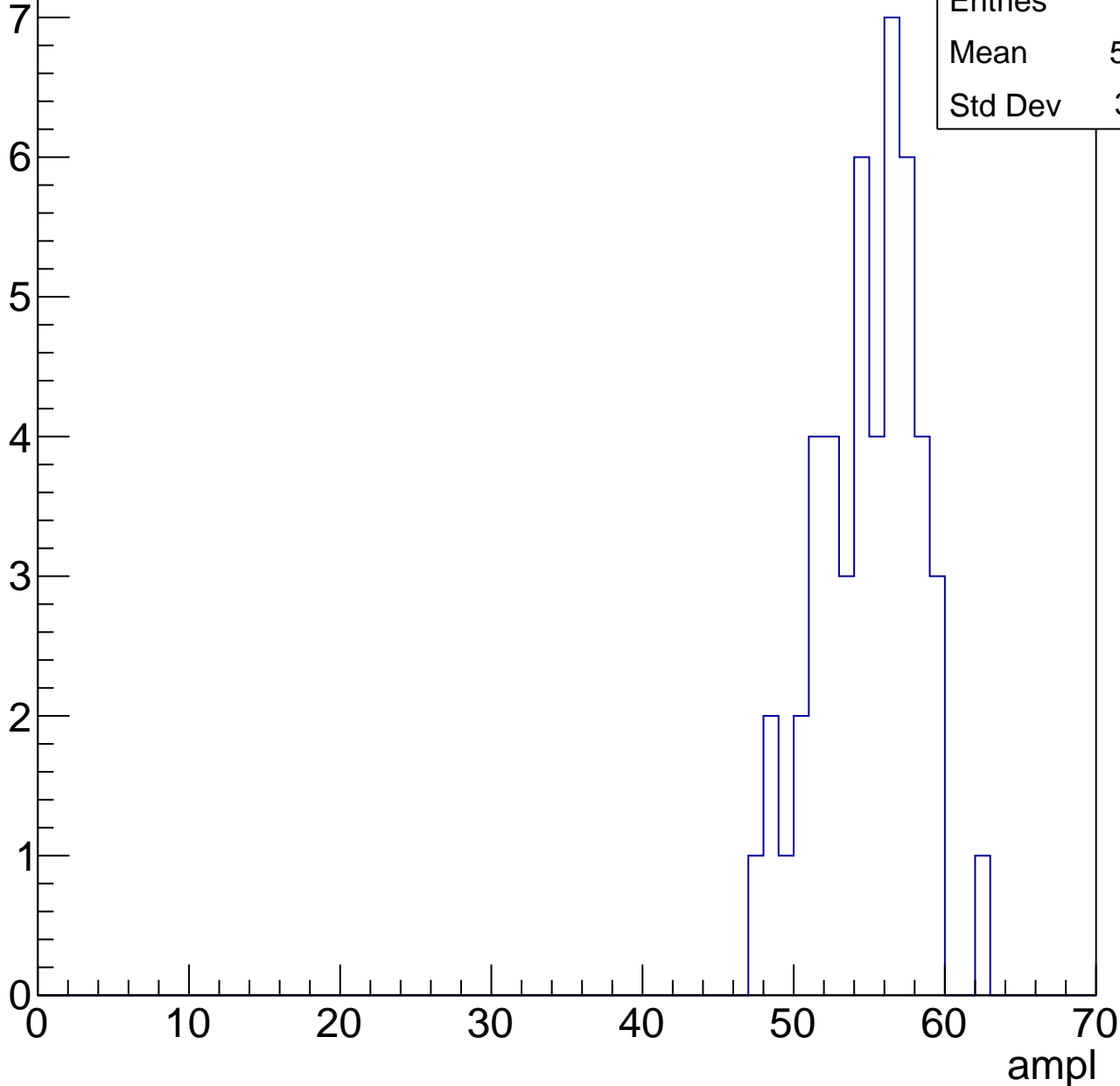


# B0L002S, U1-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 48    |
| Mean    | 54.42 |
| Std Dev | 3.271 |

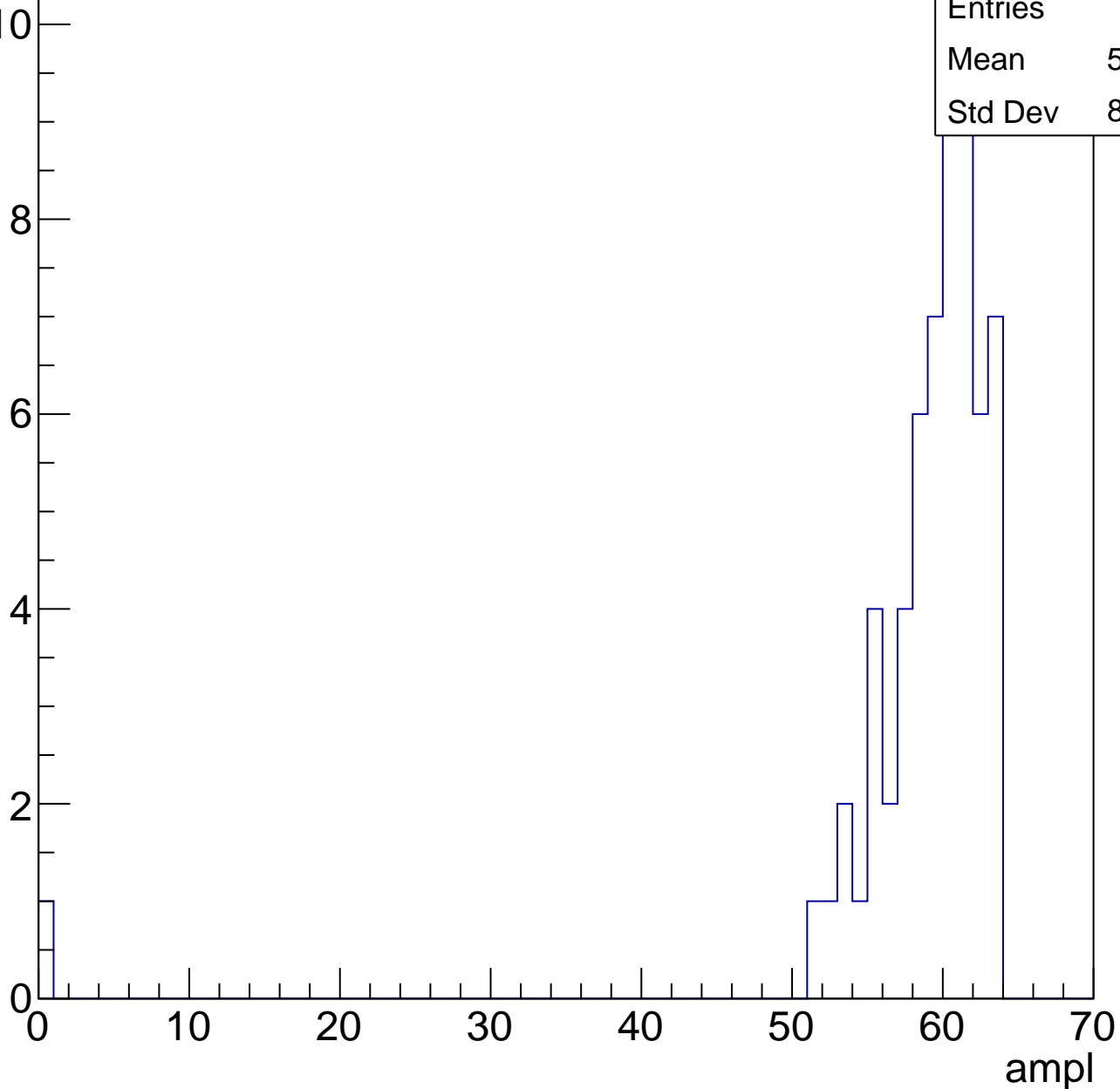


# B0L002S, U1-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 61    |
| Mean    | 58.13 |
| Std Dev | 8.058 |



# B0L002S, U1-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |    |
|---------|----|
| Entries | 2  |
| Mean    | 11 |
| Std Dev | 11 |

# B0L002S, U1-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 30.23 |
| Std Dev | 5.058 |

**Gaus mean : 31.2098**

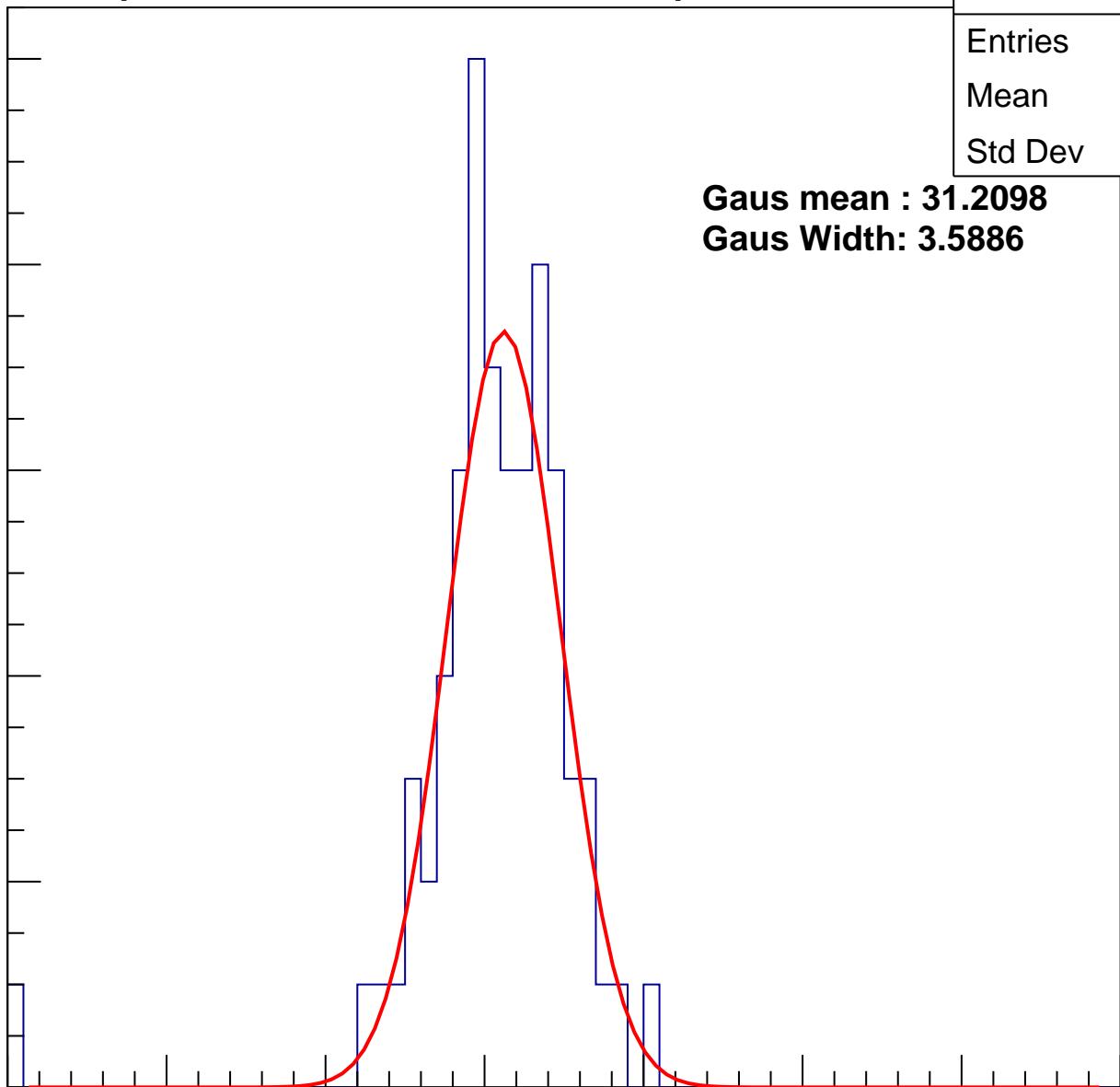
**Gaus Width: 3.5886**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch121, adc1

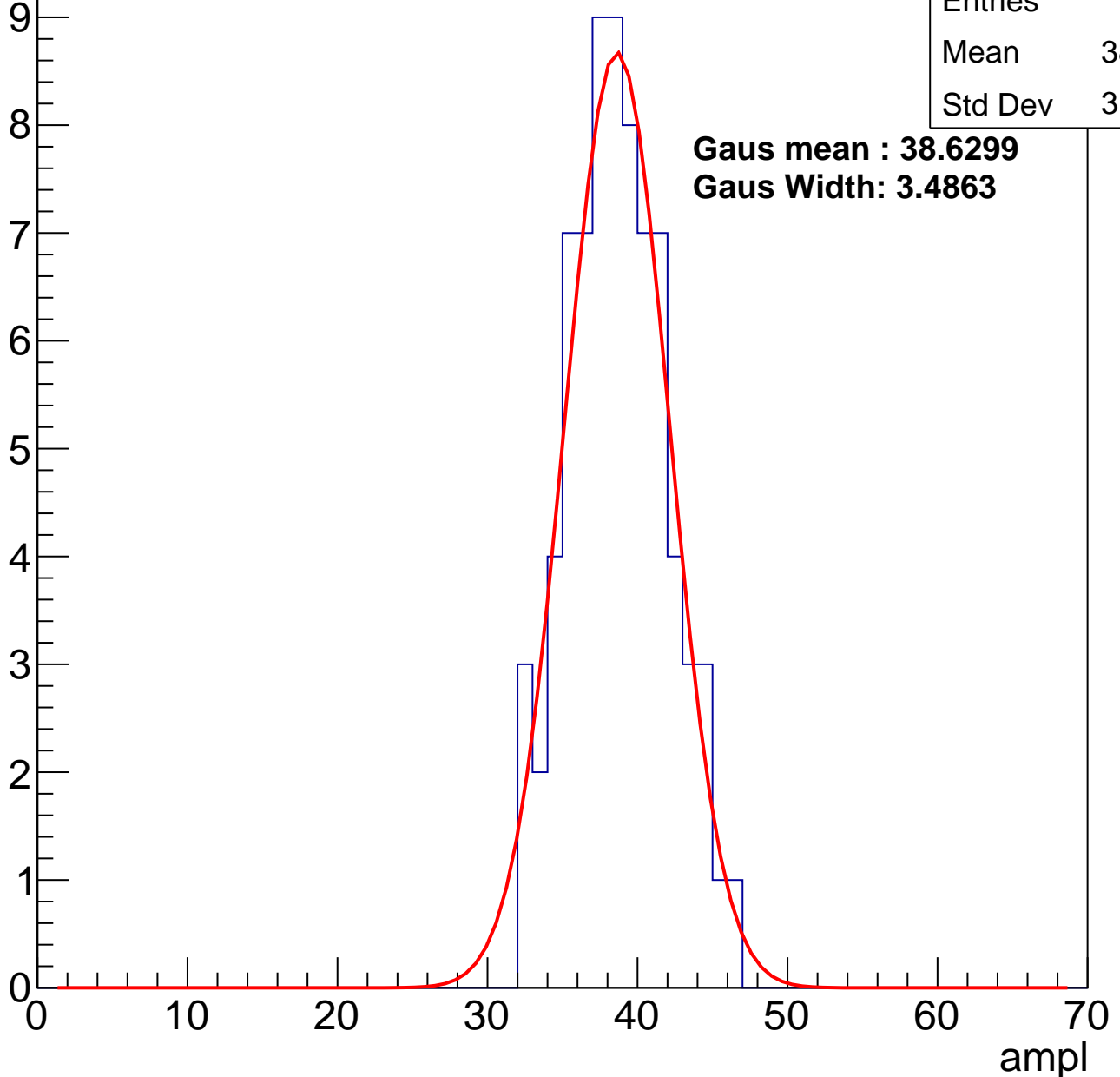
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 38.25 |
| Std Dev | 3.217 |

**Gaus mean : 38.6299**

**Gaus Width: 3.4863**



# B0L002S, U1-ch121, adc2

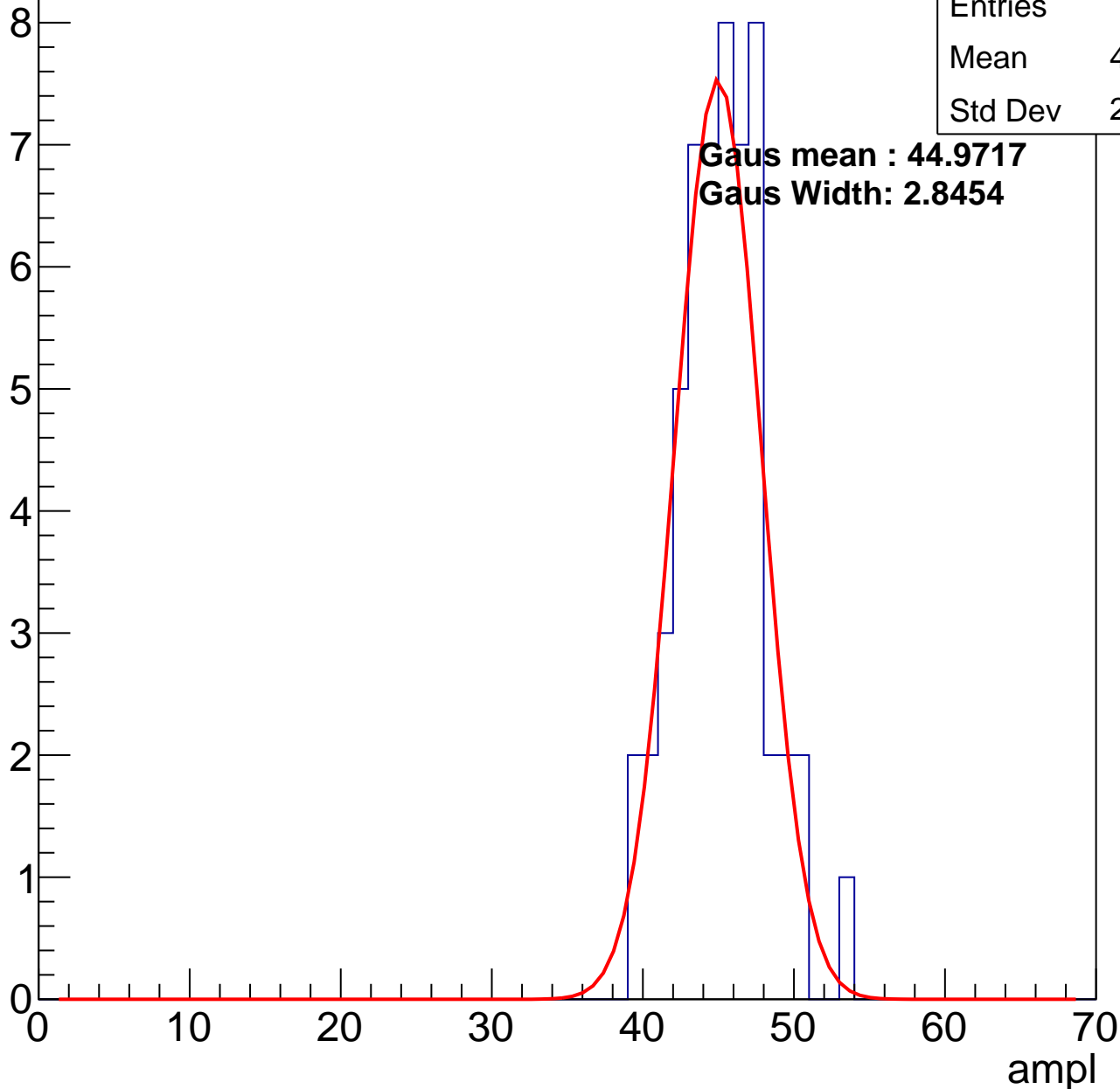
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 44.73 |
| Std Dev | 2.832 |

**Gaus mean : 44.9717**

**Gaus Width: 2.8454**

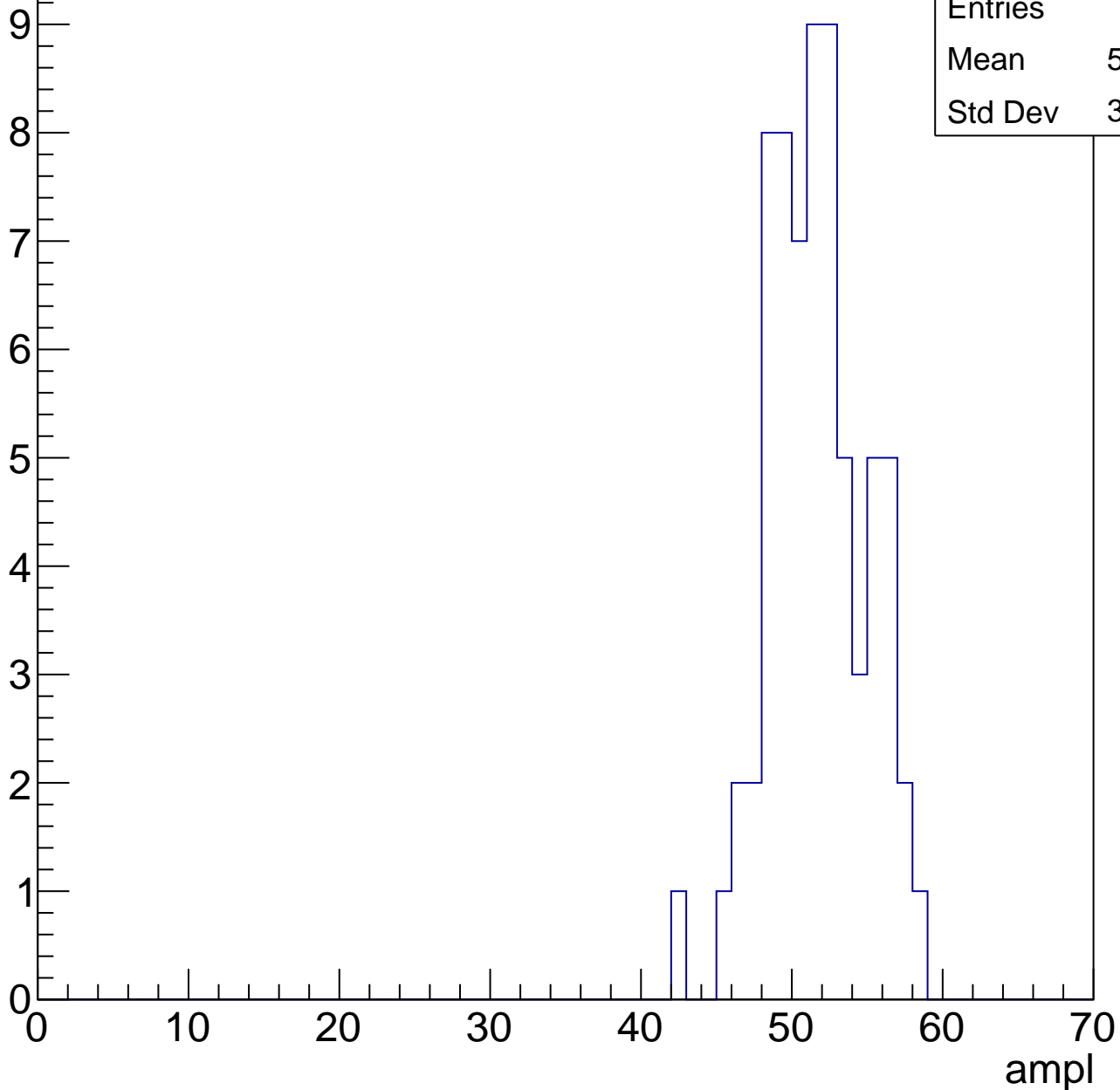


# B0L002S, U1-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 51.18 |
| Std Dev | 3.185 |

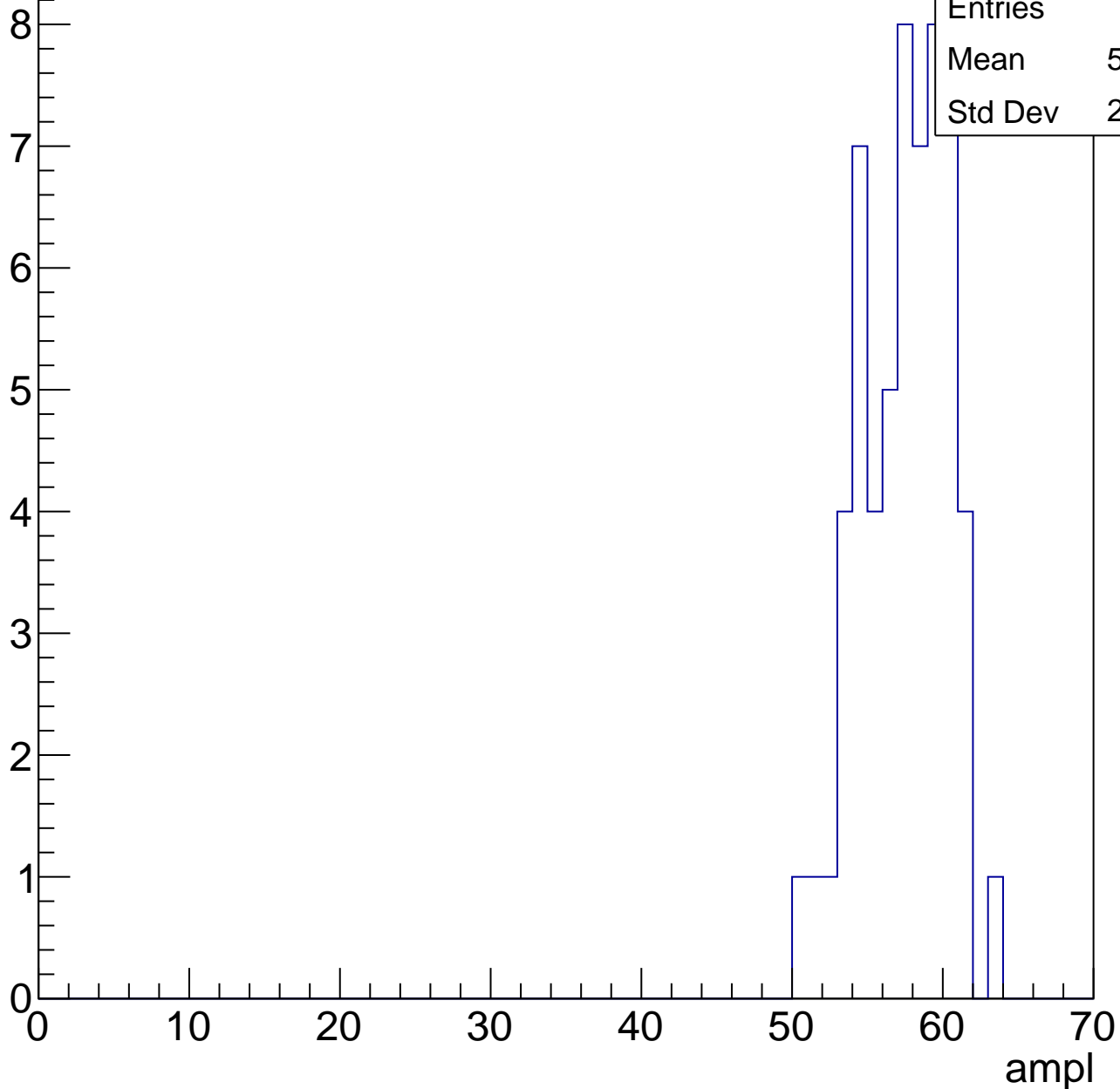


# B0L002S, U1-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 57.02 |
| Std Dev | 2.819 |

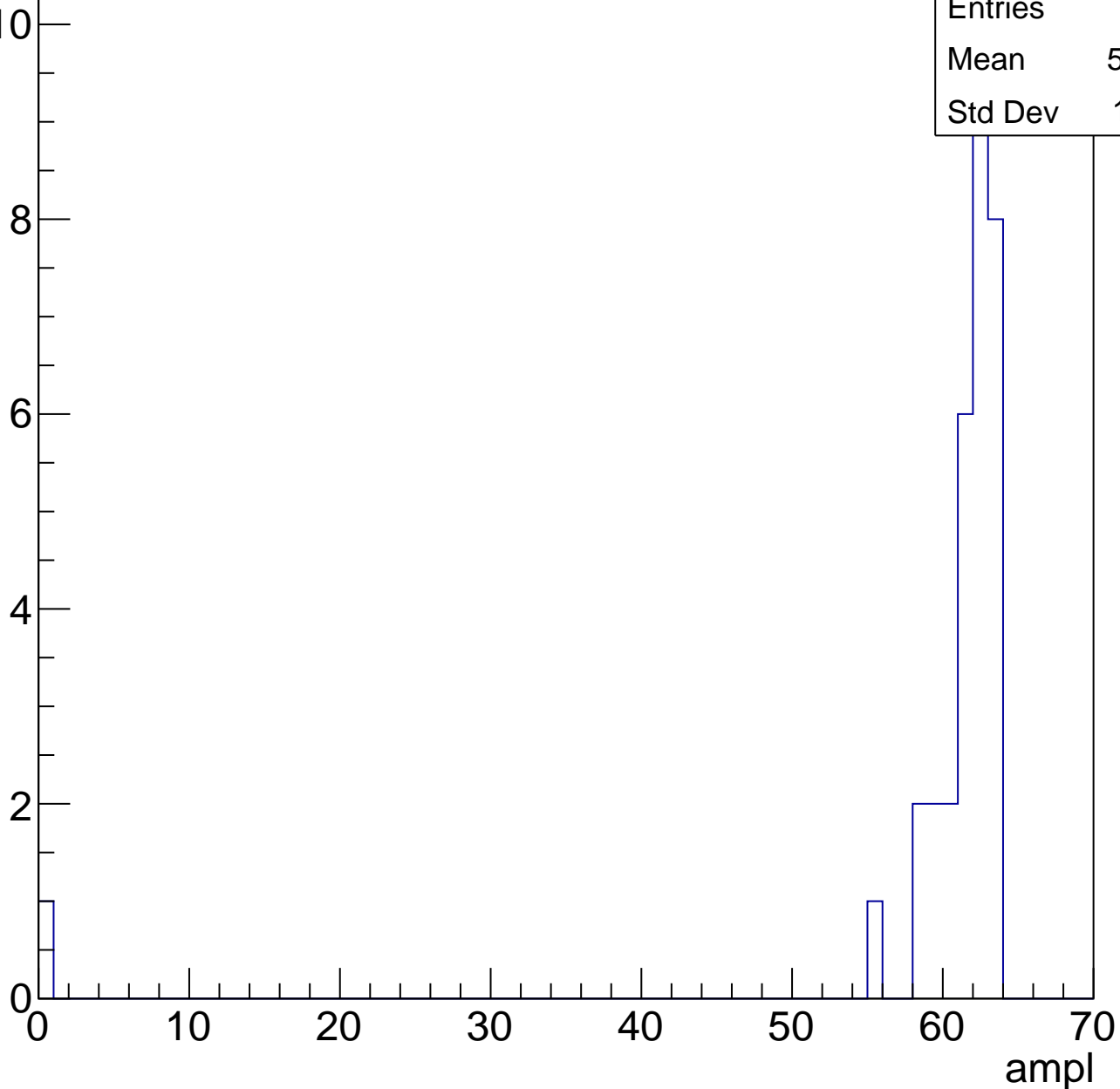


# B0L002S, U1-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 32    |
| Mean    | 59.34 |
| Std Dev | 10.81 |



# B0L002S, U1-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch122, adc0

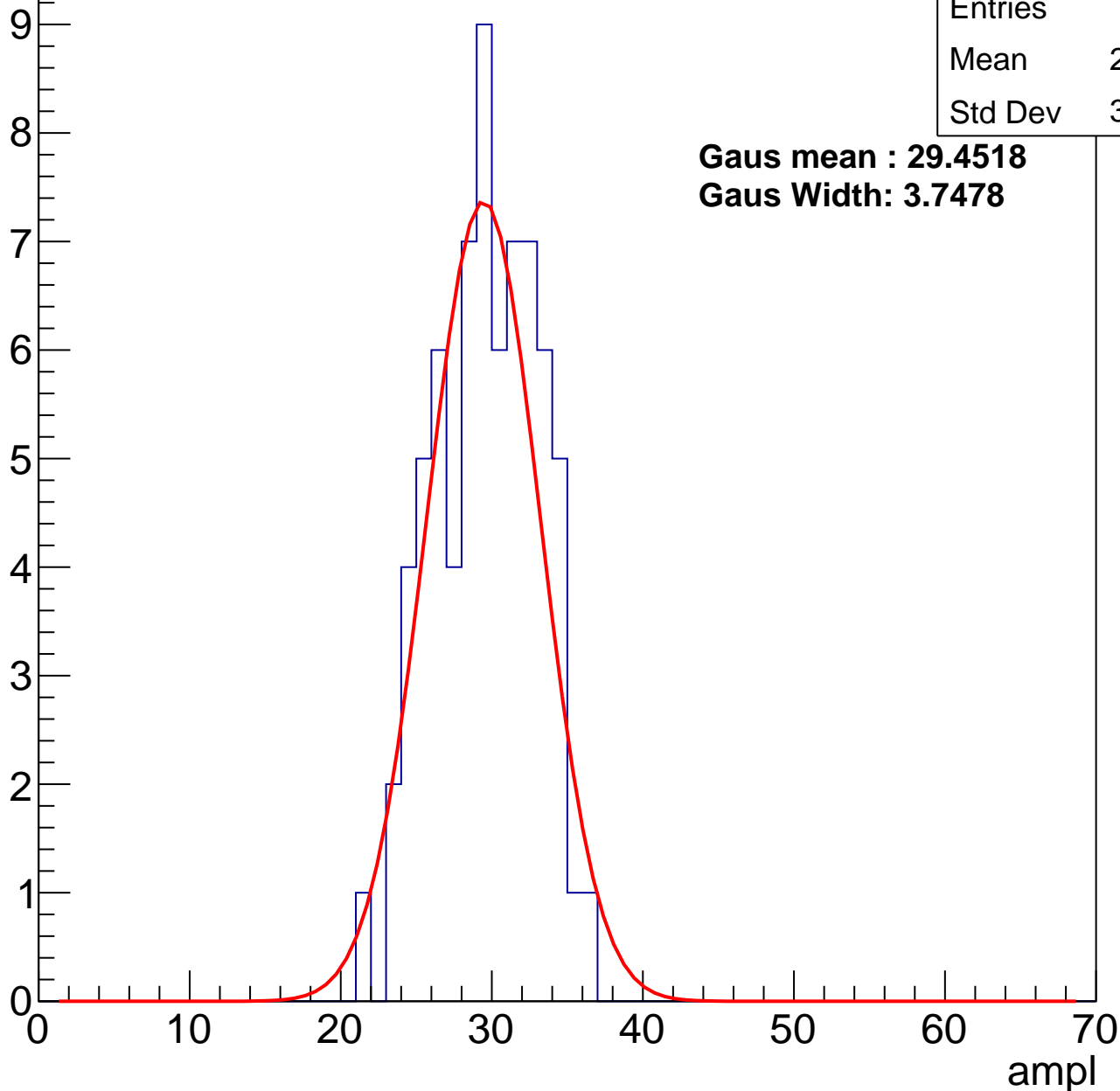
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 29.14 |
| Std Dev | 3.345 |

**Gaus mean : 29.4518**

**Gaus Width: 3.7478**



# B0L002S, U1-ch122, adc1

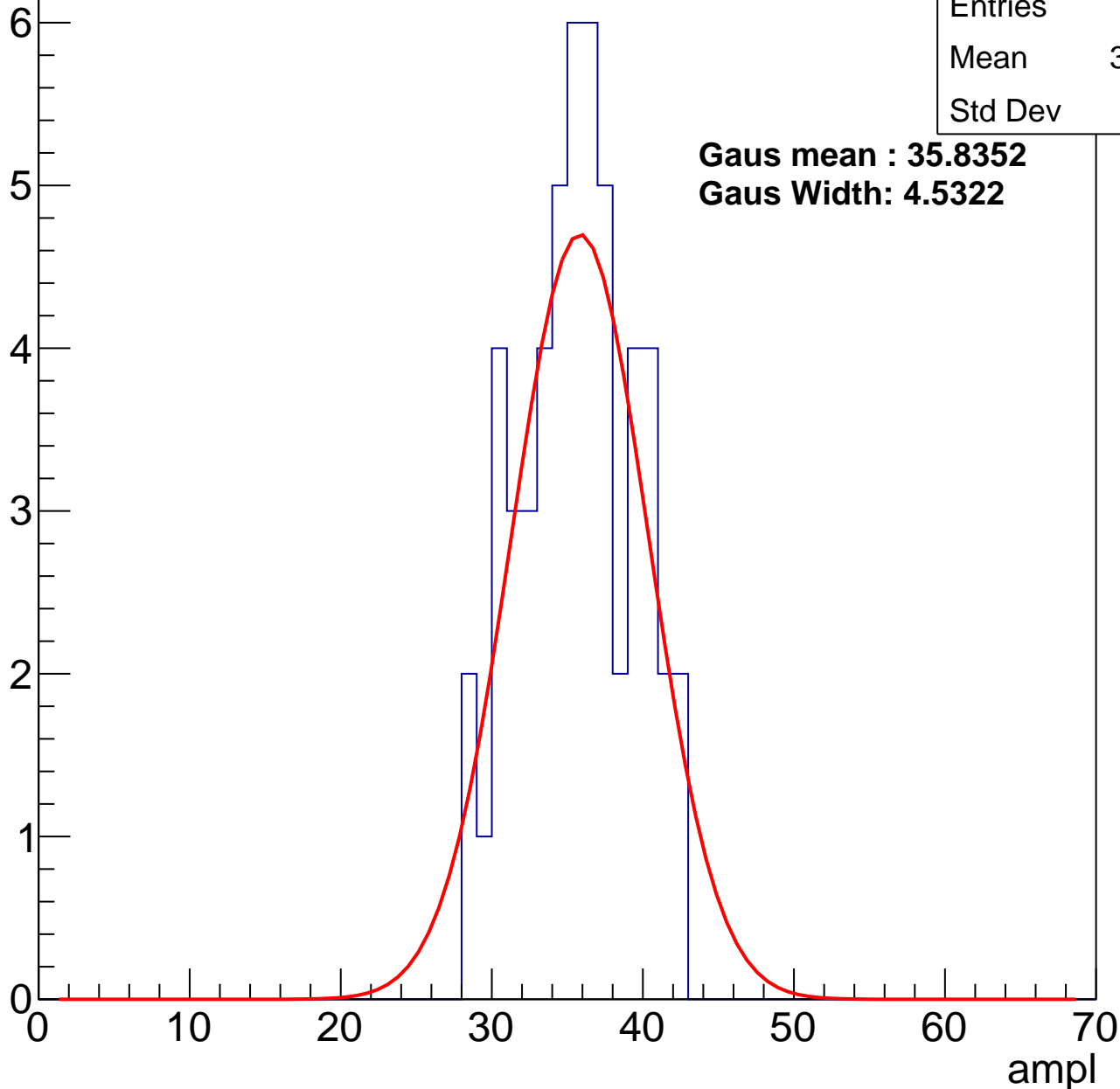
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 53    |
| Mean    | 35.19 |
| Std Dev | 3.65  |

**Gaus mean : 35.8352**

**Gaus Width: 4.5322**



# B0L002S, U1-ch122, adc2

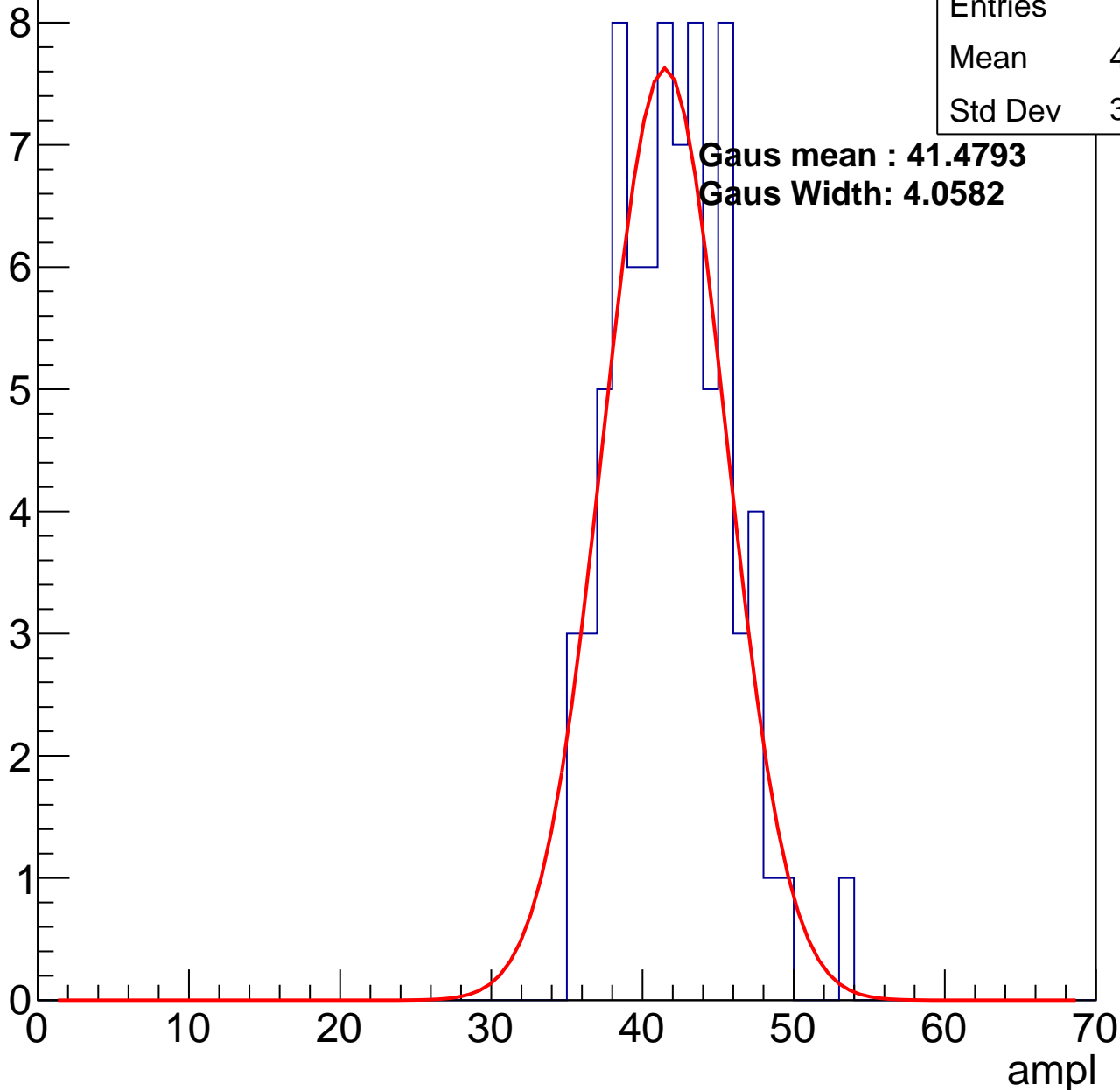
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 77    |
| Mean    | 41.53 |
| Std Dev | 3.659 |

**Gaus mean : 41.4793**

**Gaus Width: 4.0582**

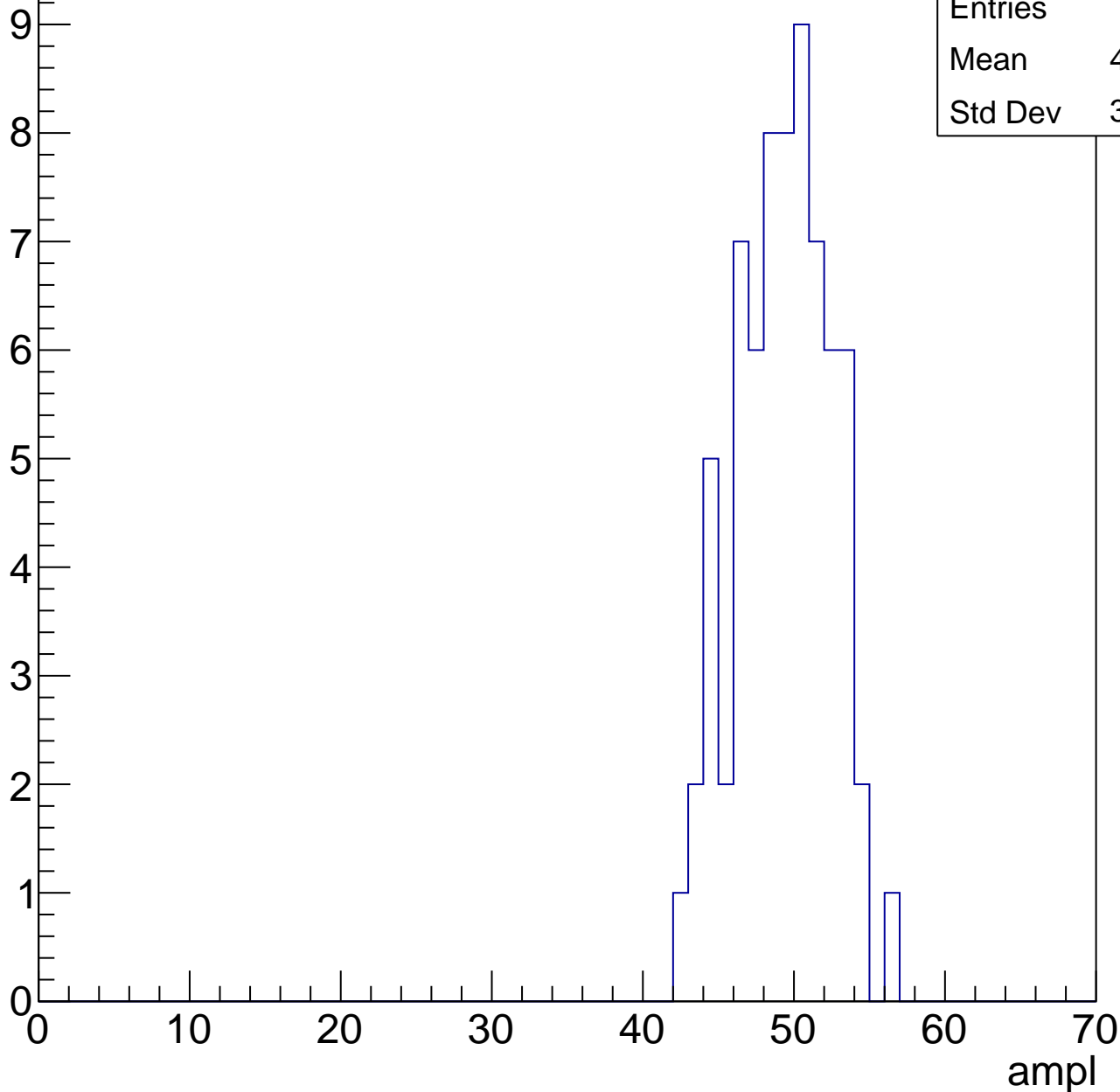


# B0L002S, U1-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 48.84 |
| Std Dev | 3.064 |



# B0L002S, U1-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

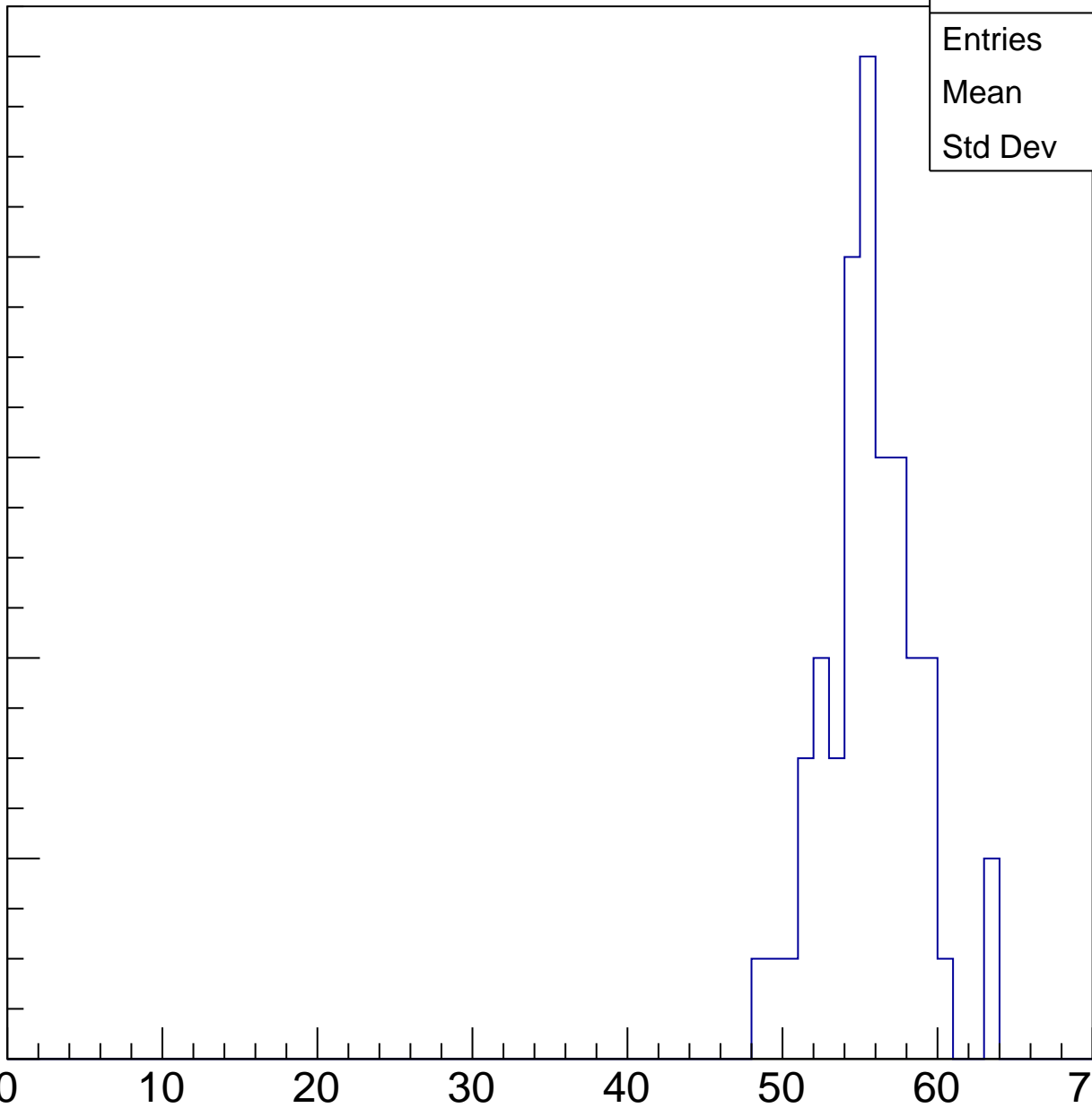
|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 55.2  |
| Std Dev | 3.027 |

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch122, adc5

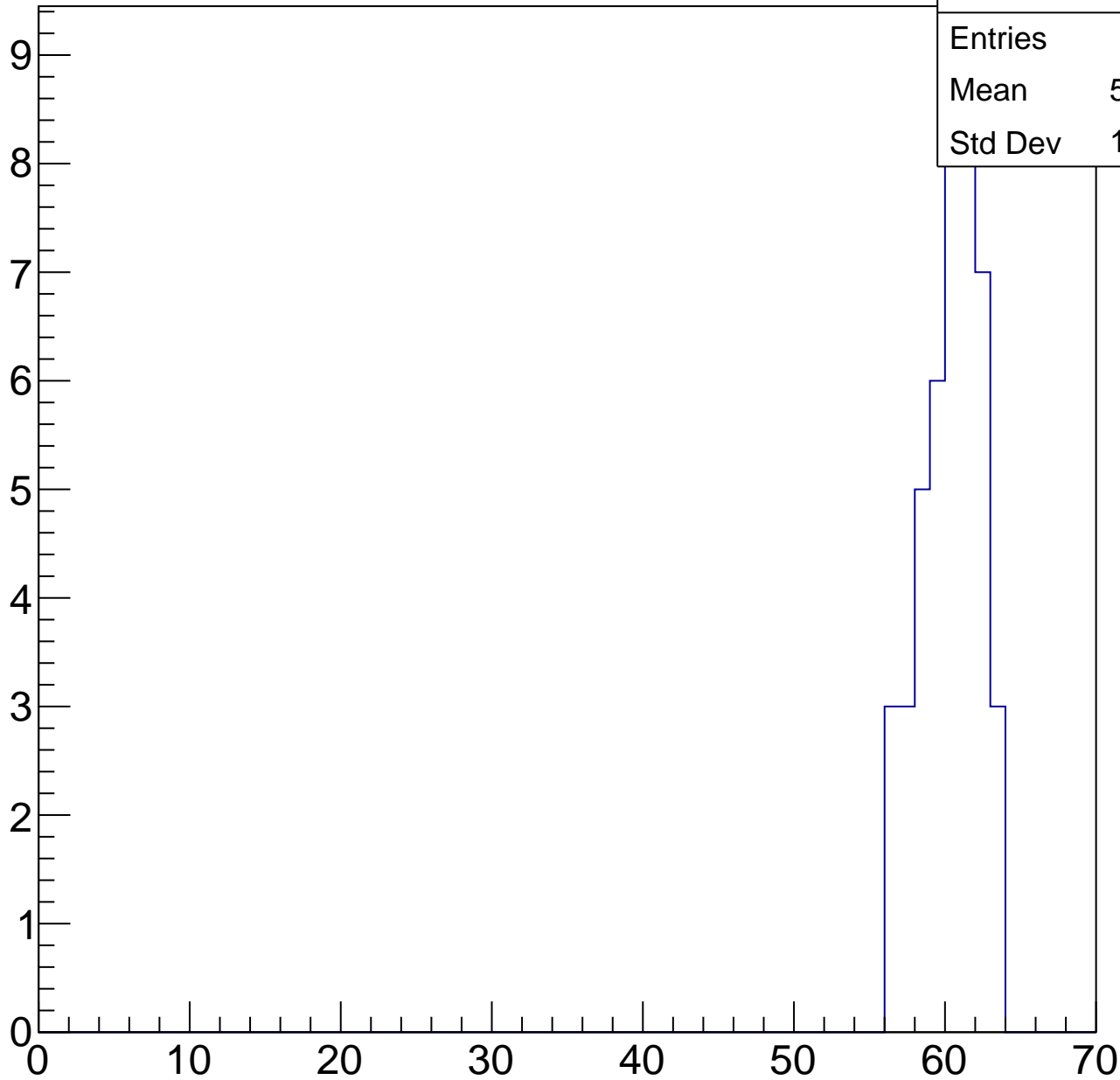
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

|         |       |
|---------|-------|
| Entries | 44    |
| Mean    | 59.89 |
| Std Dev | 1.933 |

ampl

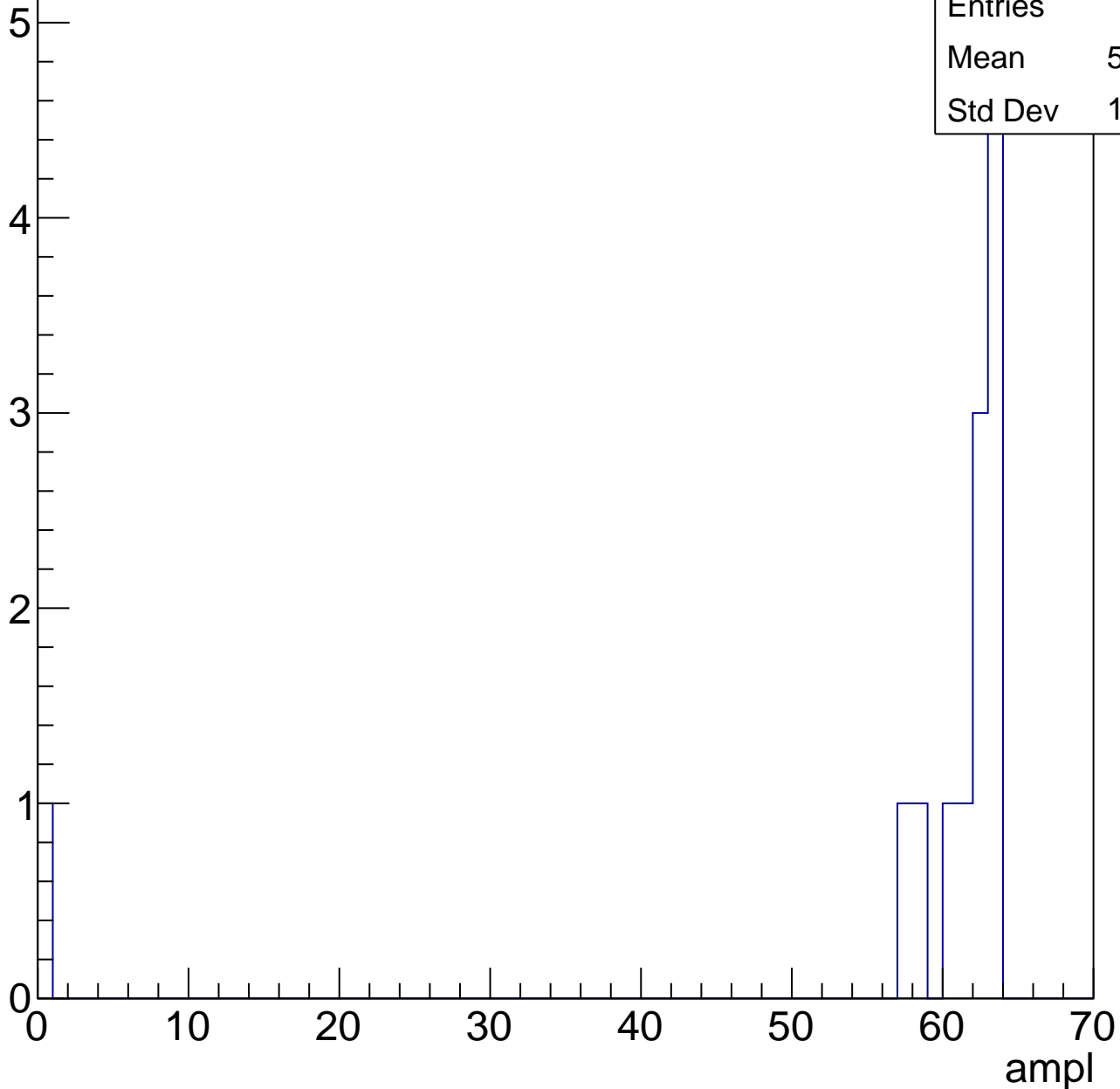


# B0L002S, U1-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 13    |
| Mean    | 56.69 |
| Std Dev | 16.48 |





# B0L002S, U1-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch123, adc0

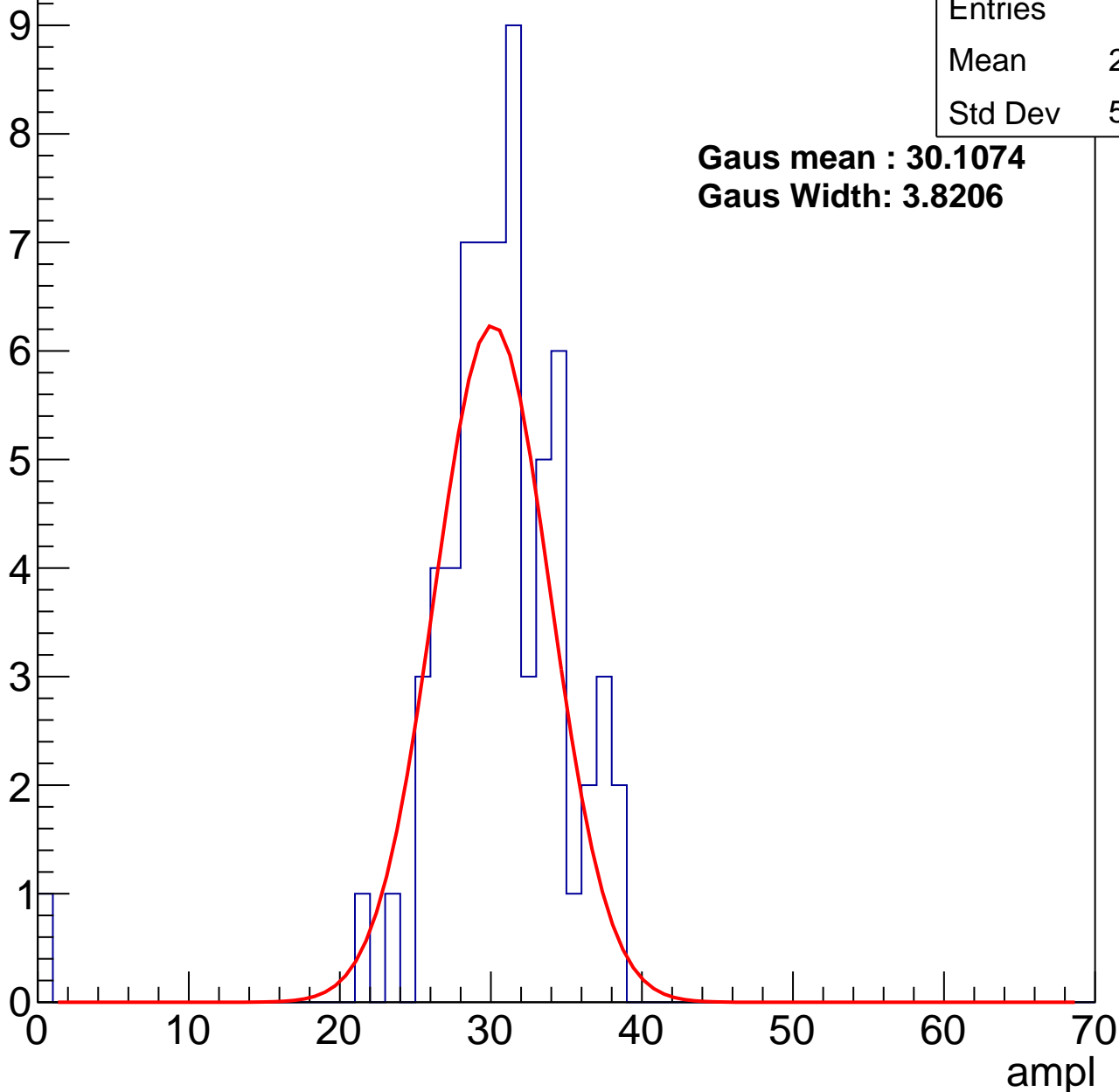
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 66    |
| Mean    | 29.97 |
| Std Dev | 5.184 |

**Gaus mean : 30.1074**

**Gaus Width: 3.8206**



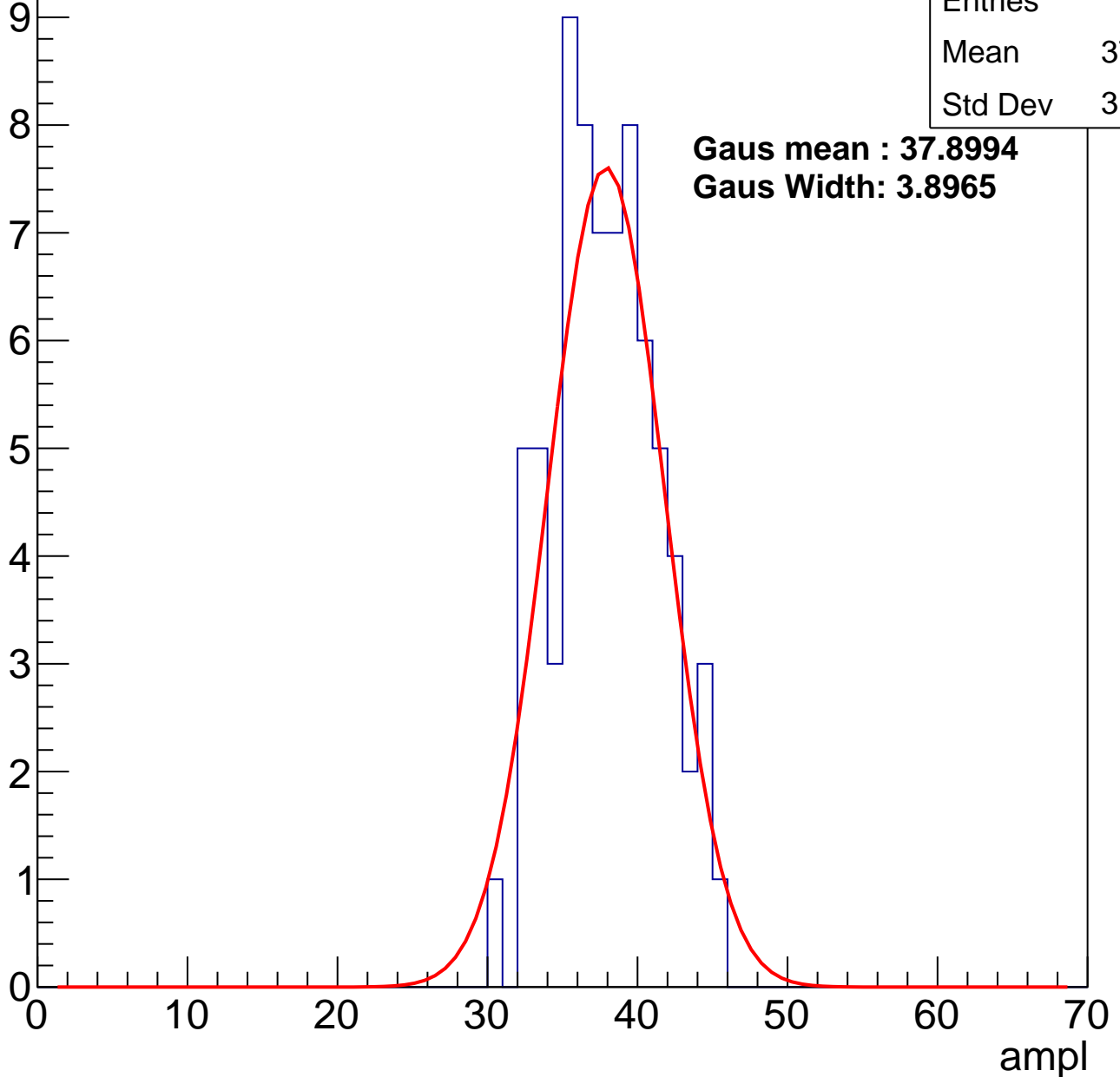
# B0L002S, U1-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 74    |
| Mean    | 37.47 |
| Std Dev | 3.418 |

**Gaus mean : 37.8994**  
**Gaus Width: 3.8965**



# B0L002S, U1-ch123, adc2

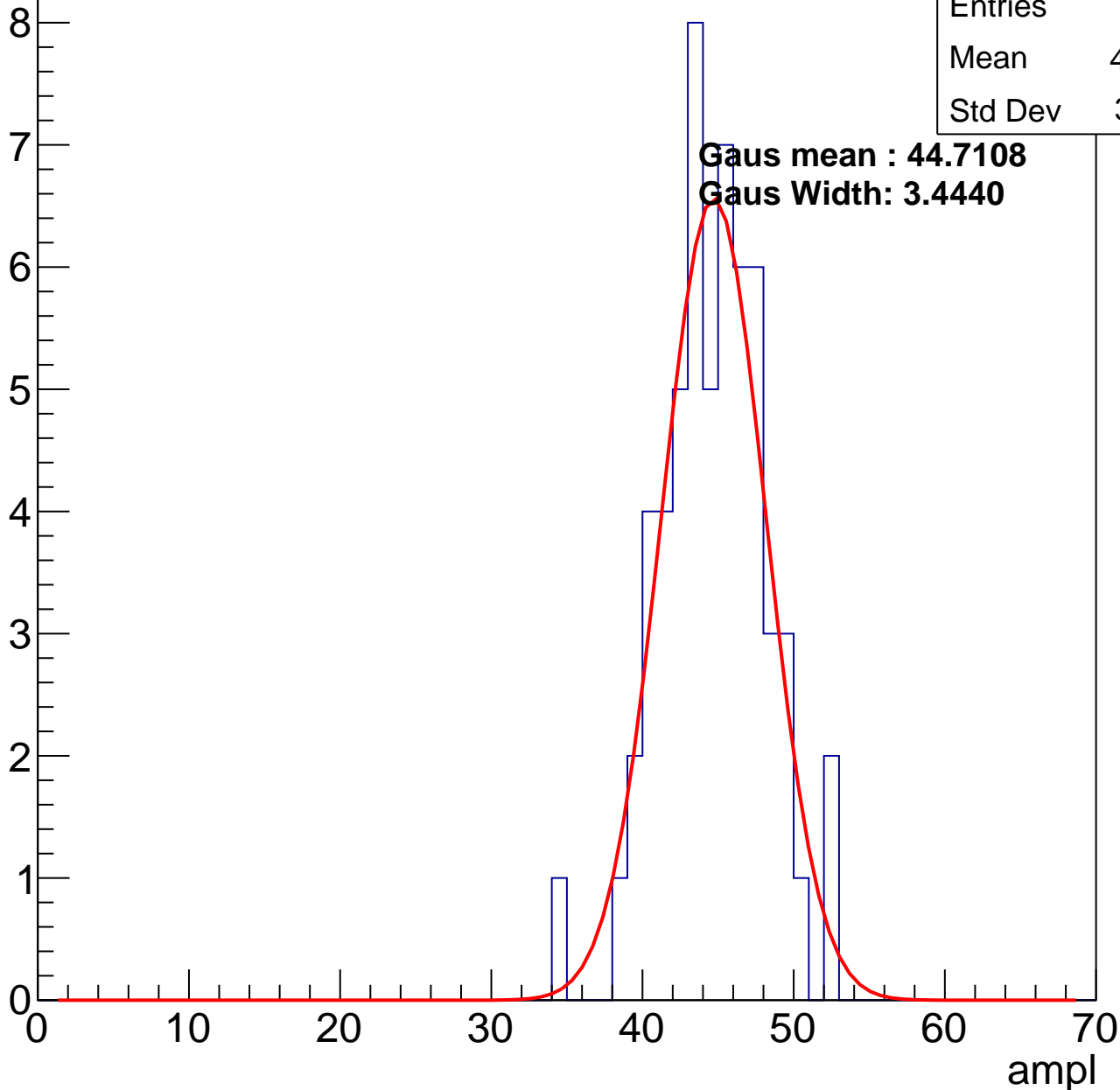
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 58    |
| Mean    | 44.24 |
| Std Dev | 3.431 |

**Gaus mean : 44.7108**

**Gaus Width: 3.4440**

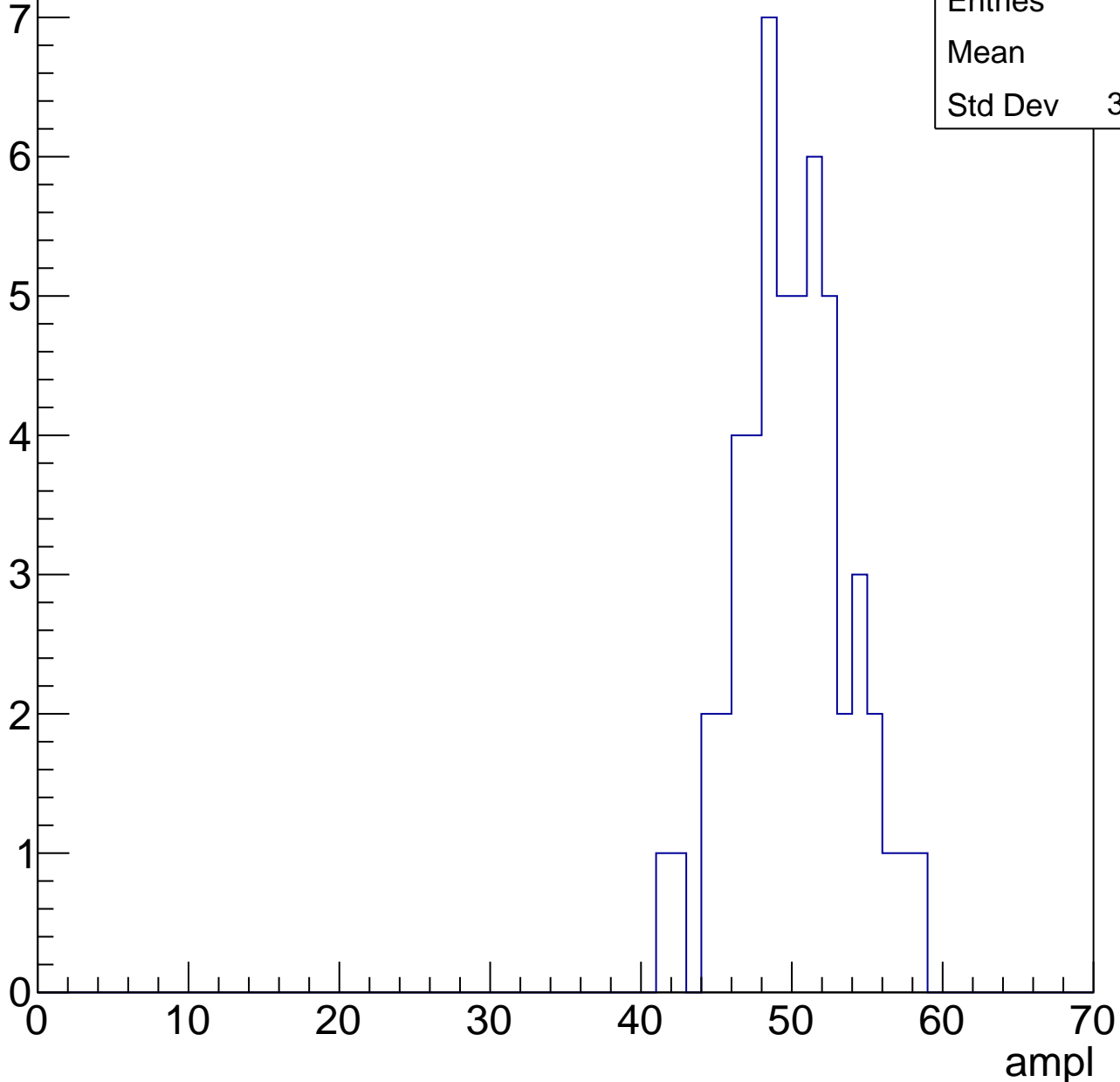


# B0L002S, U1-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 49.6  |
| Std Dev | 3.623 |

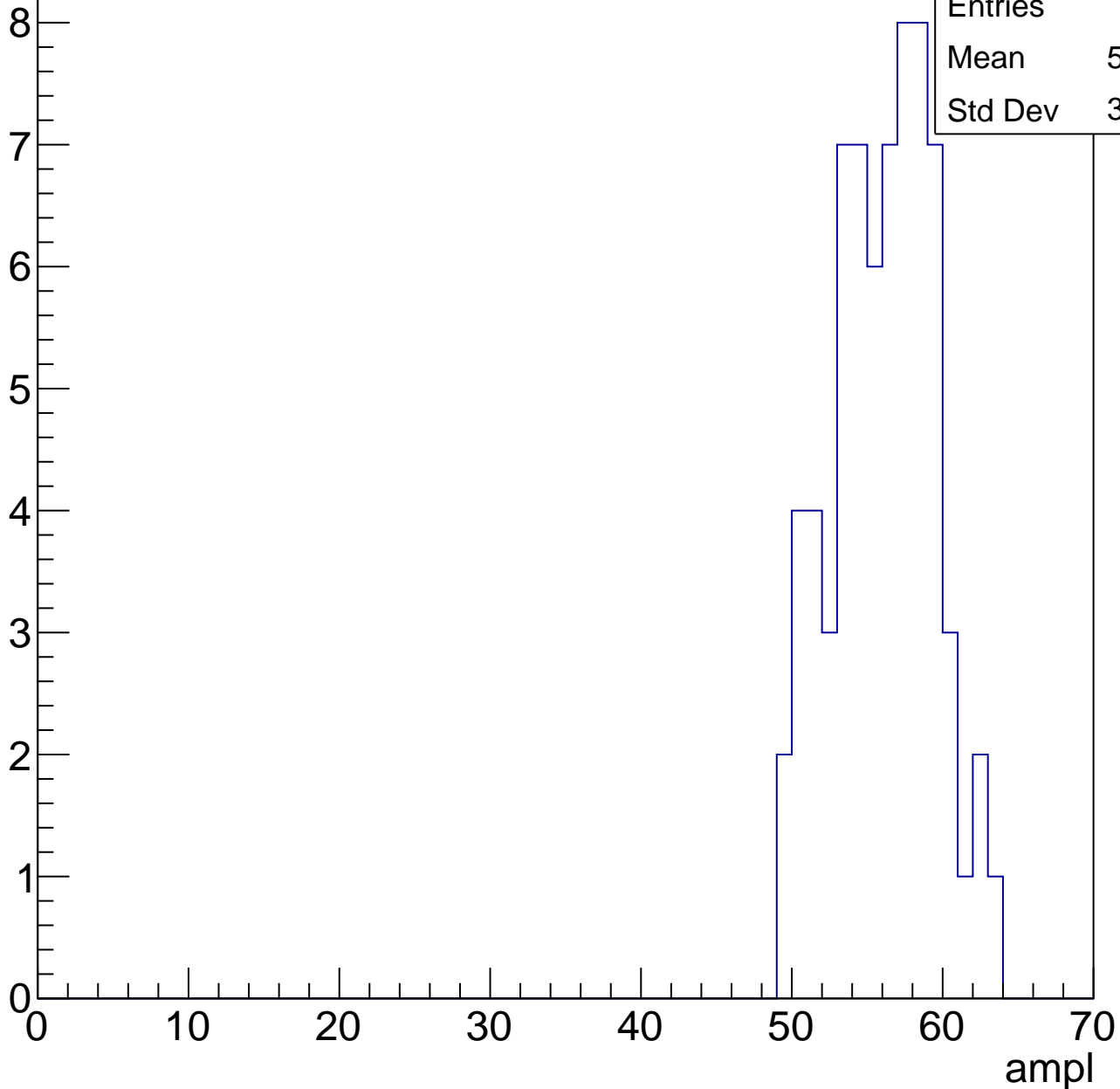


# B0L002S, U1-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 70    |
| Mean    | 55.57 |
| Std Dev | 3.319 |

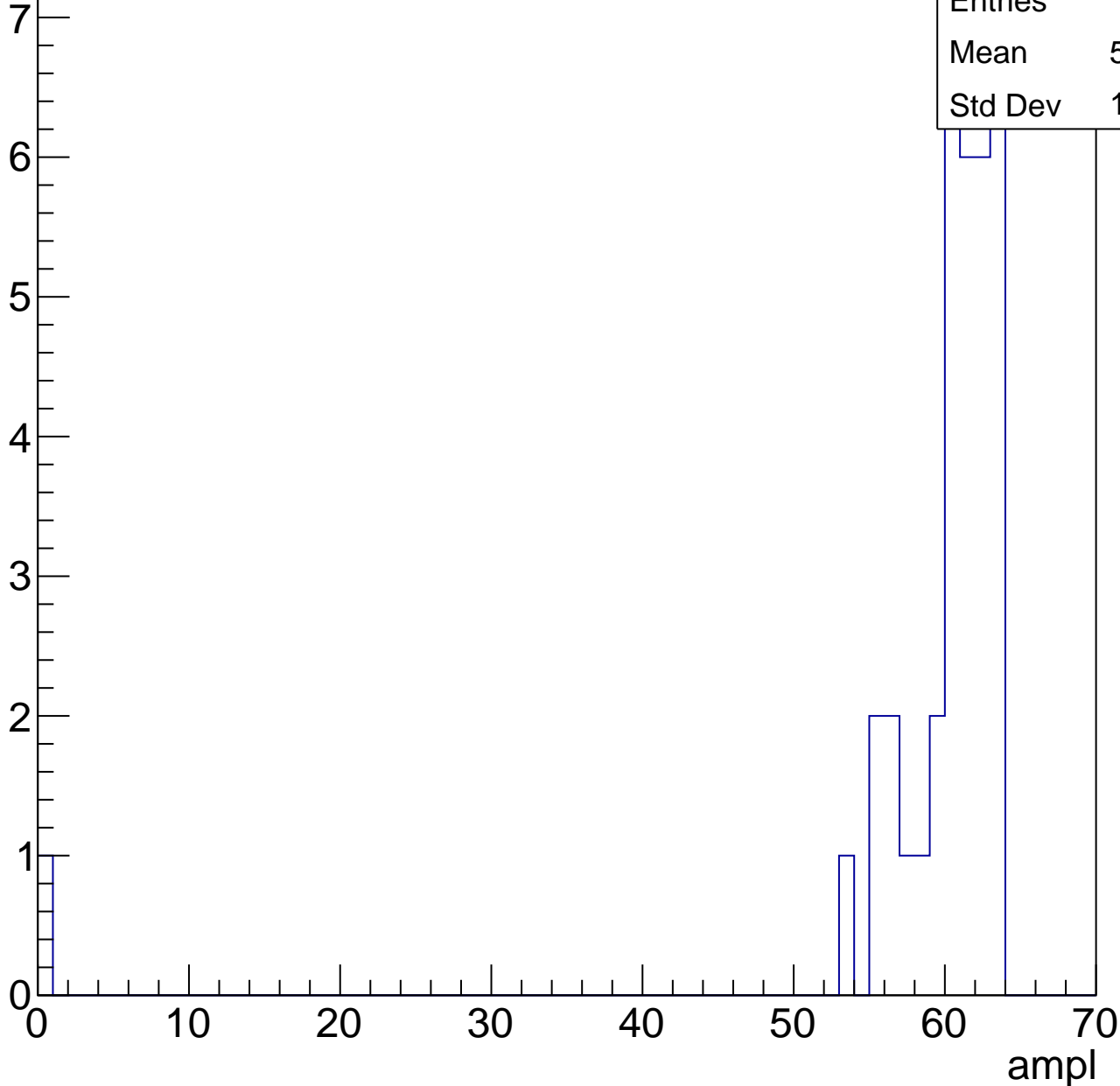


# B0L002S, U1-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

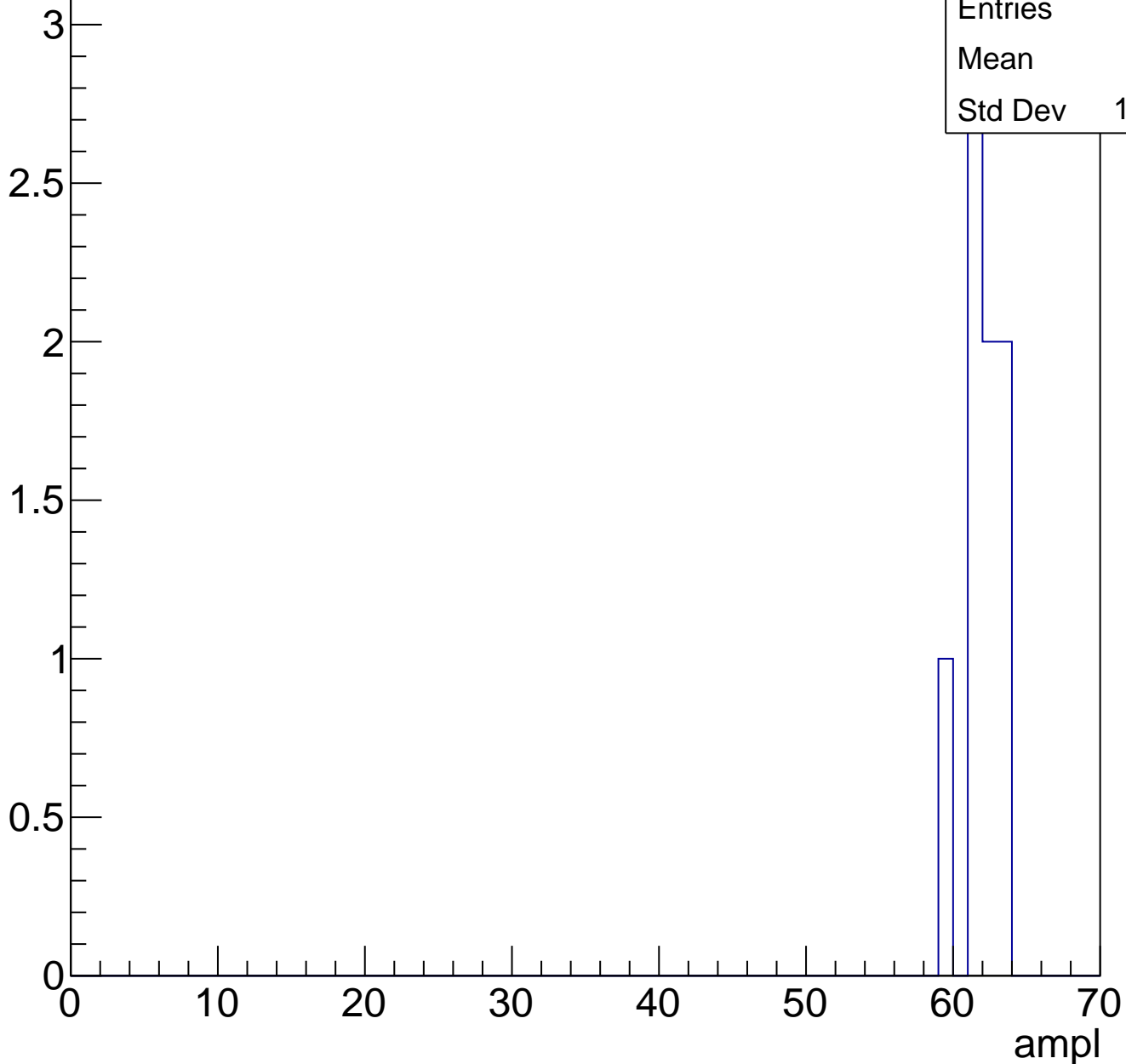
|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 58.53 |
| Std Dev | 10.22 |



# B0L002S, U1-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 2 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch124, adc0

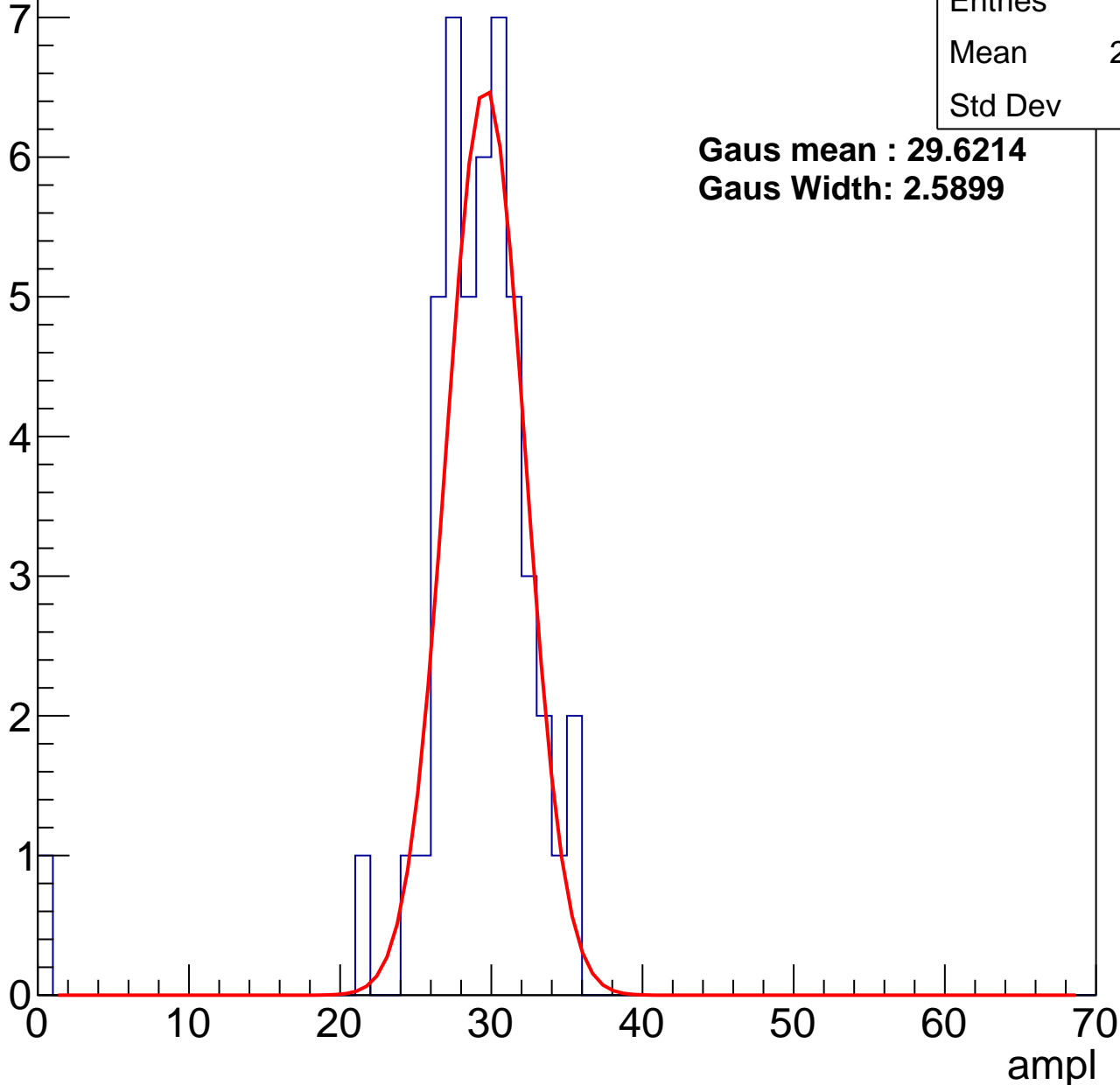
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 28.38 |
| Std Dev | 5.03  |

**Gaus mean : 29.6214**

**Gaus Width: 2.5899**



# B0L002S, U1-ch124, adc1

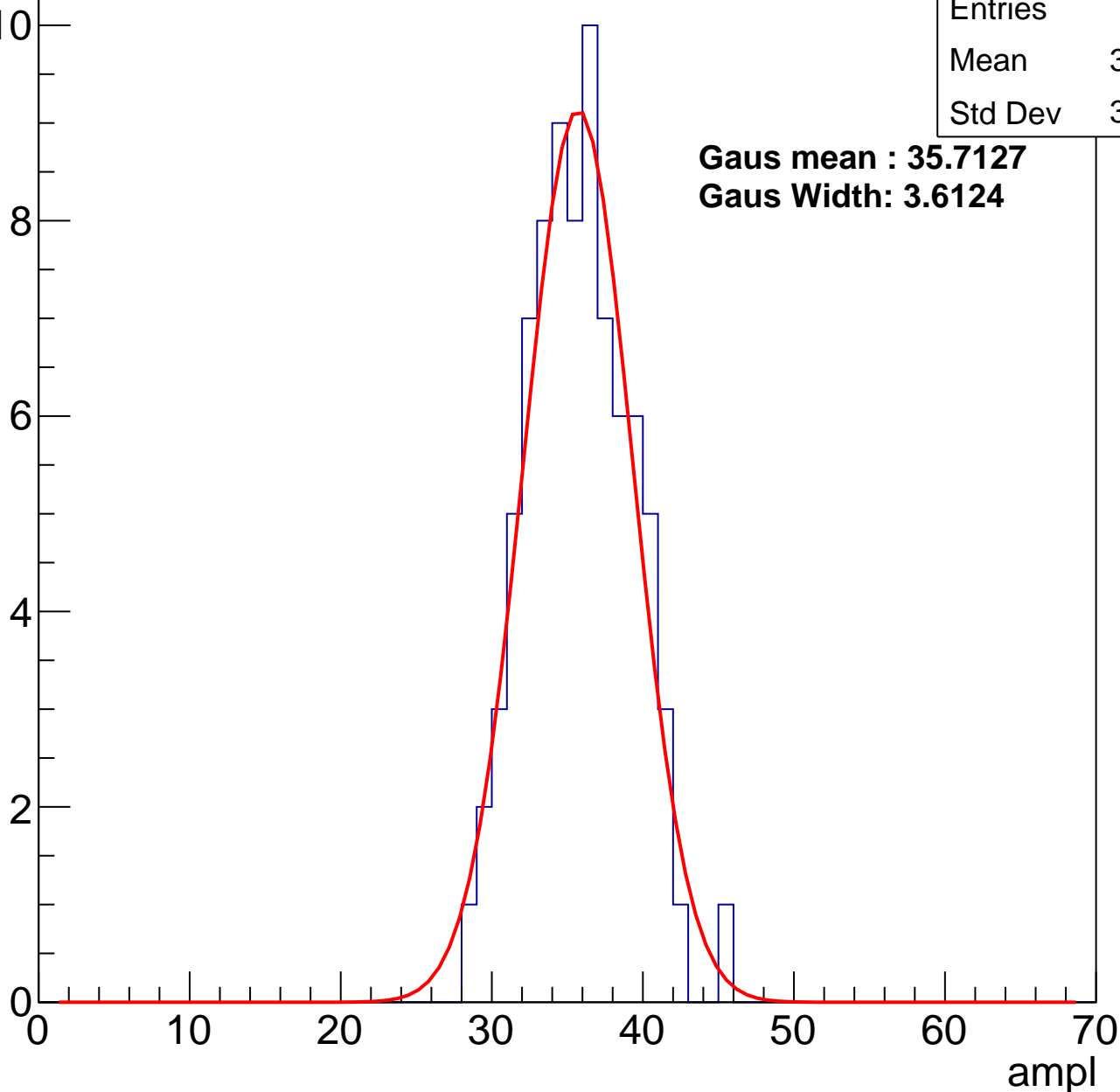
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 82    |
| Mean    | 35.32 |
| Std Dev | 3.389 |

**Gaus mean : 35.7127**

**Gaus Width: 3.6124**



# B0L002S, U1-ch124, adc2

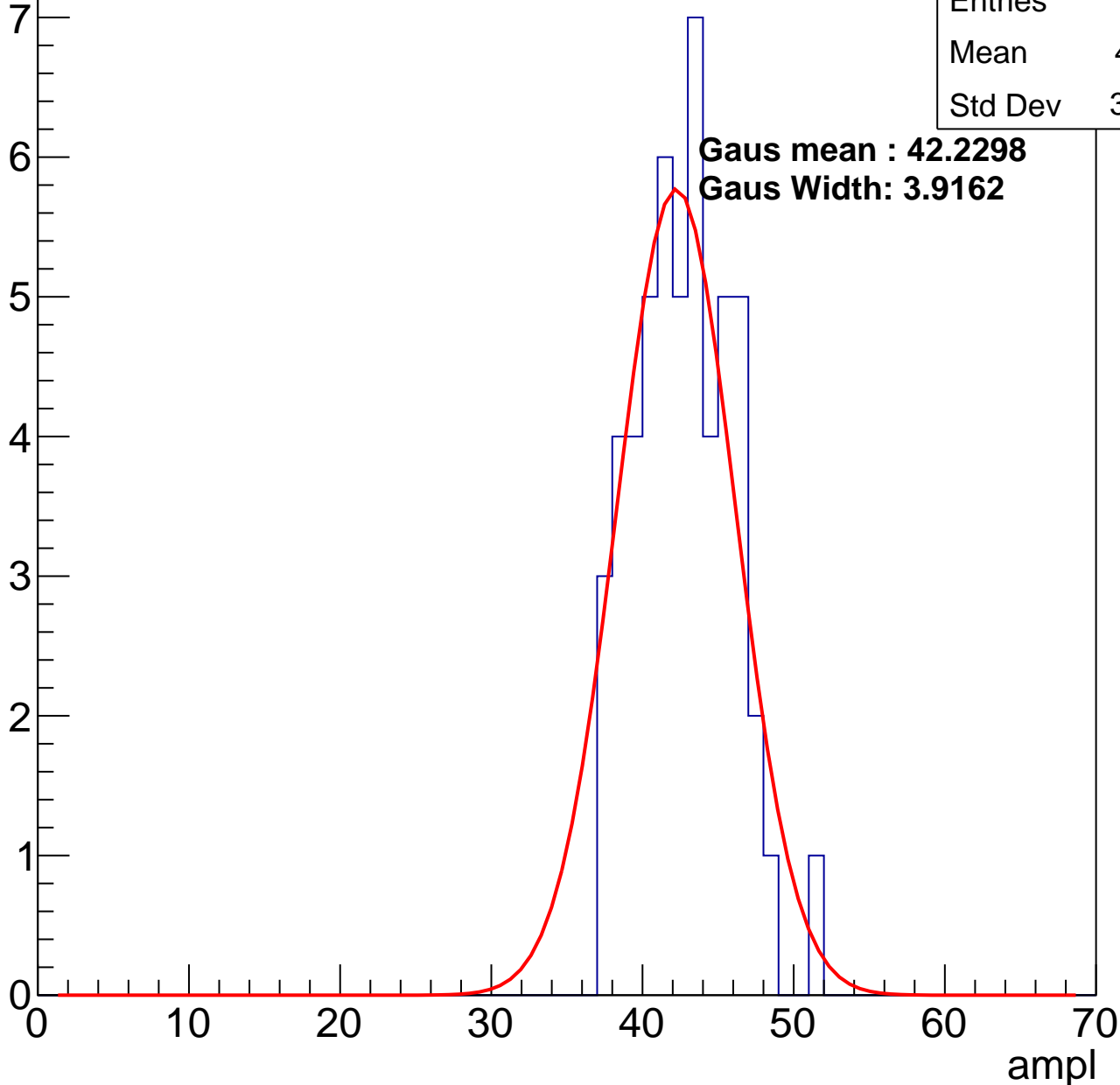
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 52    |
| Mean    | 42.31 |
| Std Dev | 3.135 |

**Gaus mean : 42.2298**

**Gaus Width: 3.9162**

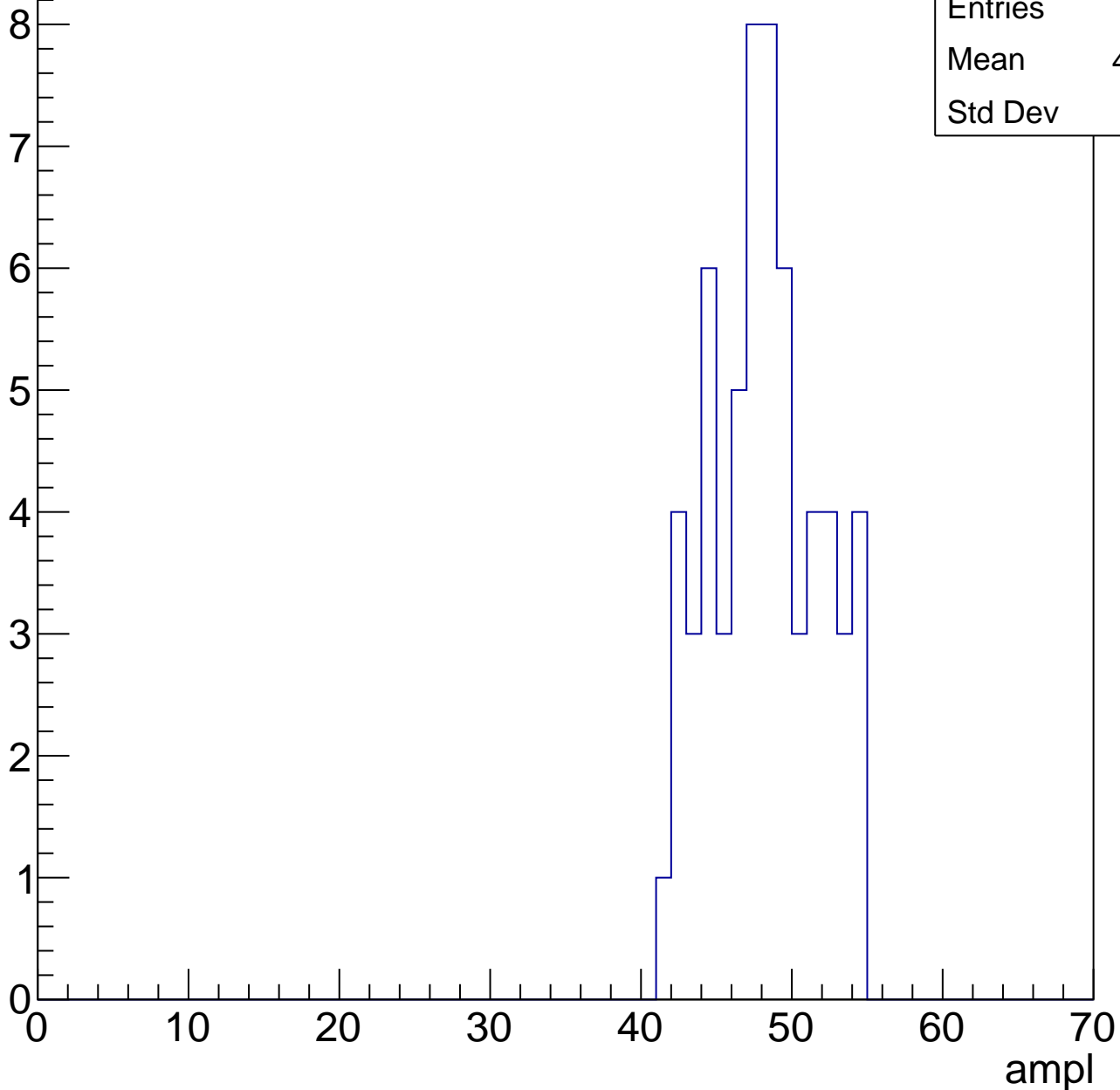


# B0L002S, U1-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 62    |
| Mean    | 47.71 |
| Std Dev | 3.48  |

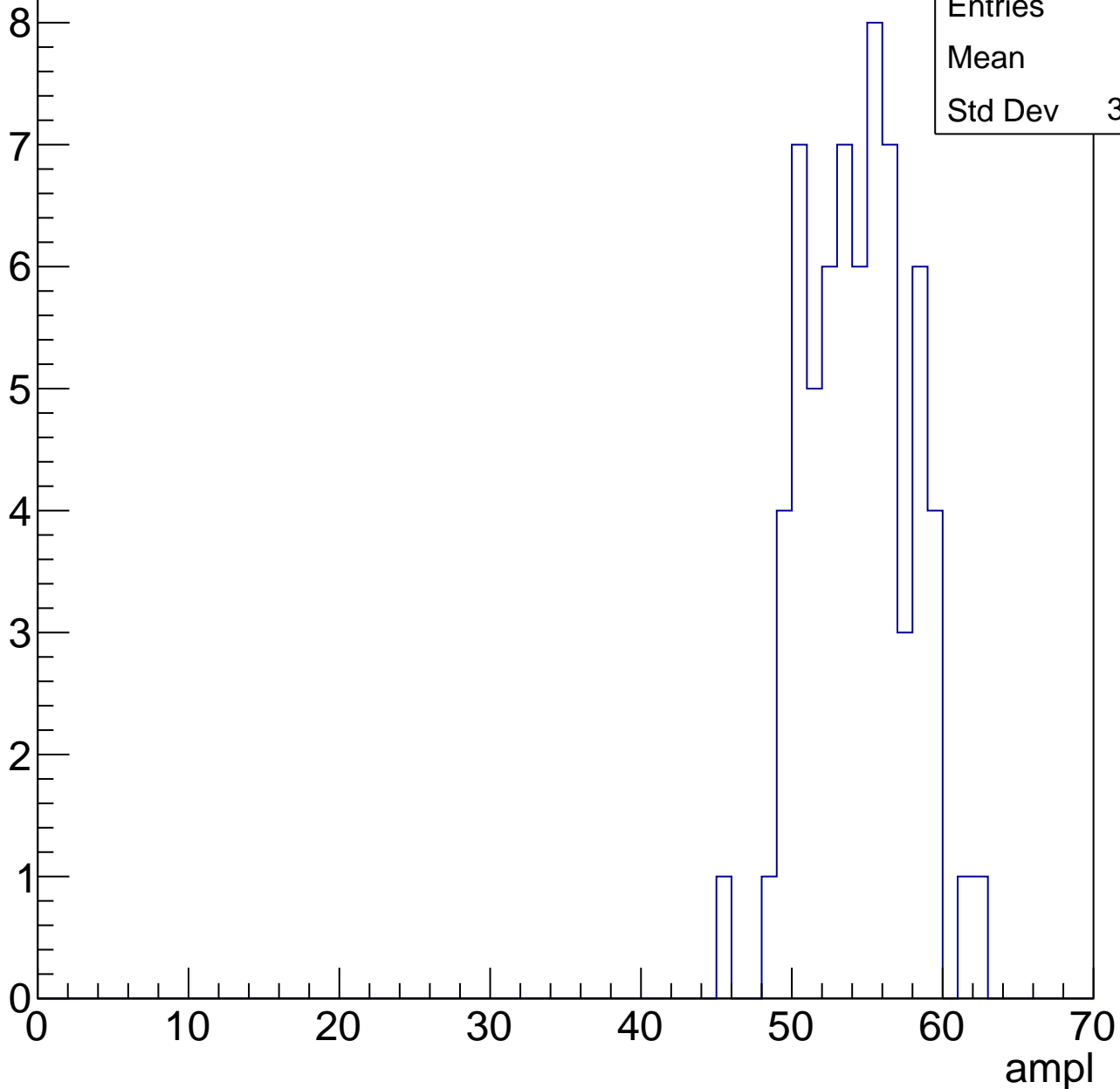


# B0L002S, U1-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

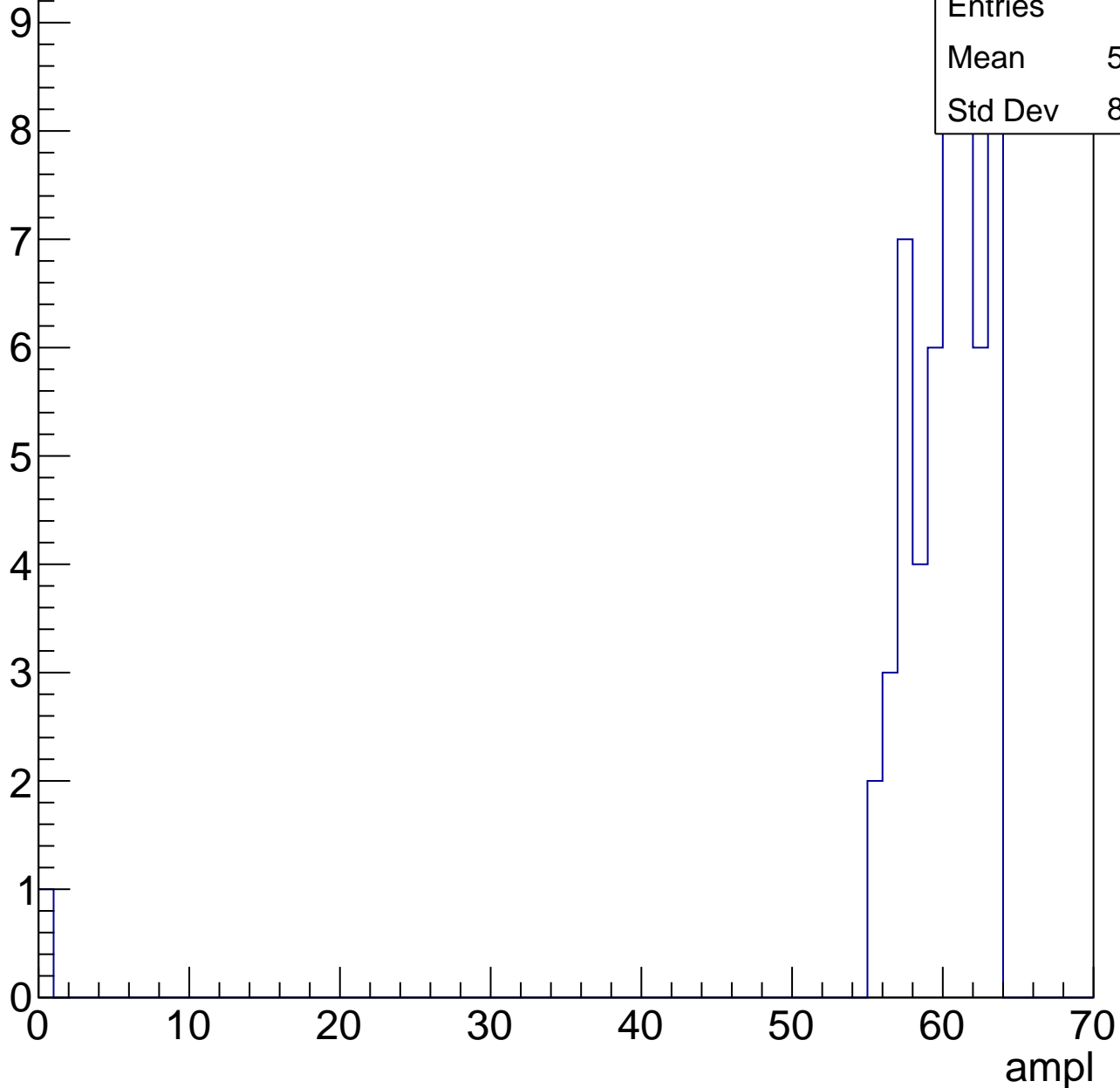
|         |       |
|---------|-------|
| Entries | 67    |
| Mean    | 53.9  |
| Std Dev | 3.404 |



# B0L002S, U1-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 1 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch125, adc0

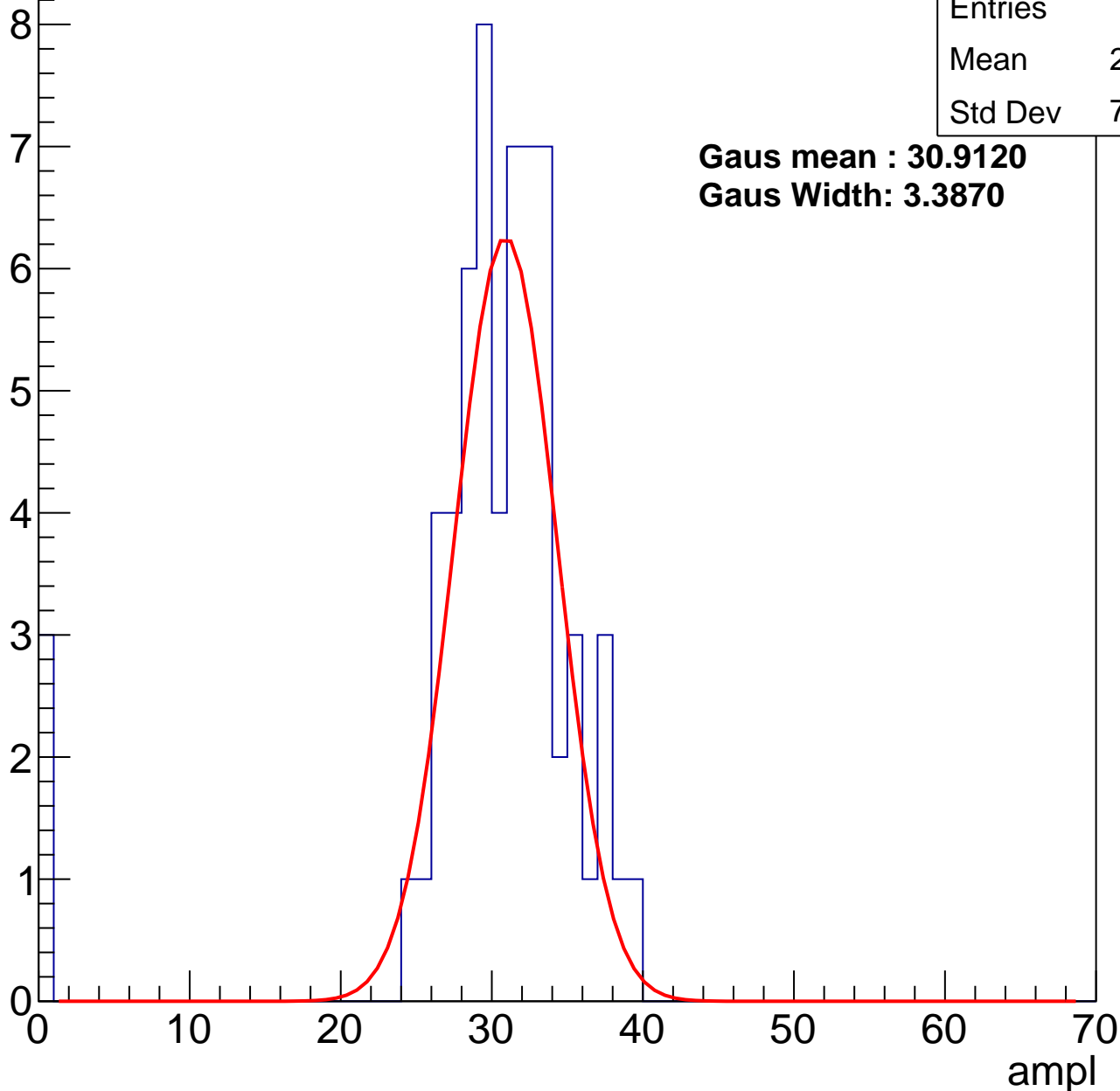
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 63    |
| Mean    | 29.37 |
| Std Dev | 7.347 |

**Gaus mean : 30.9120**

**Gaus Width: 3.3870**



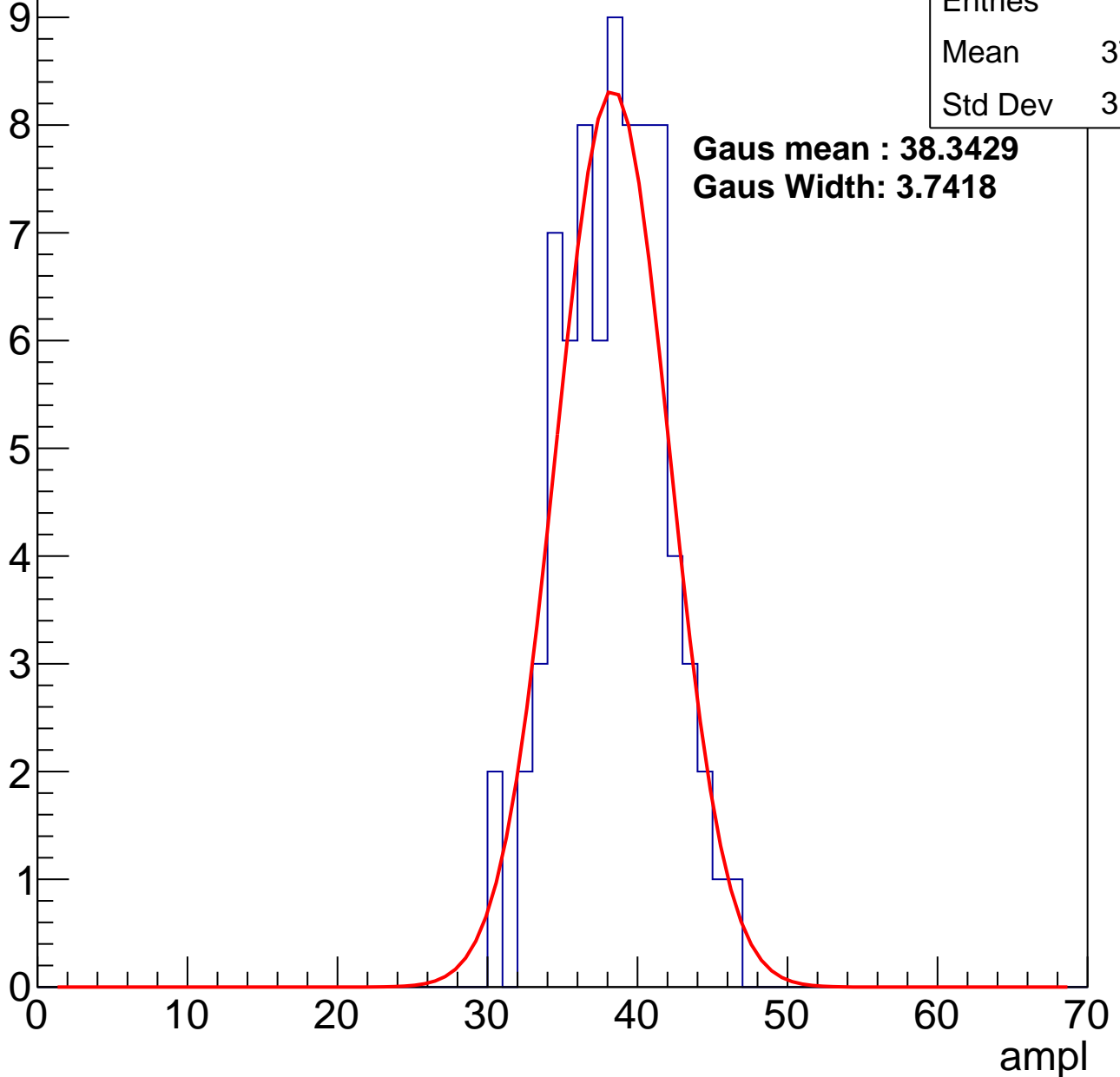
# B0L002S, U1-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 78    |
| Mean    | 37.94 |
| Std Dev | 3.424 |

**Gaus mean : 38.3429**  
**Gaus Width: 3.7418**



# B0L002S, U1-ch125, adc2

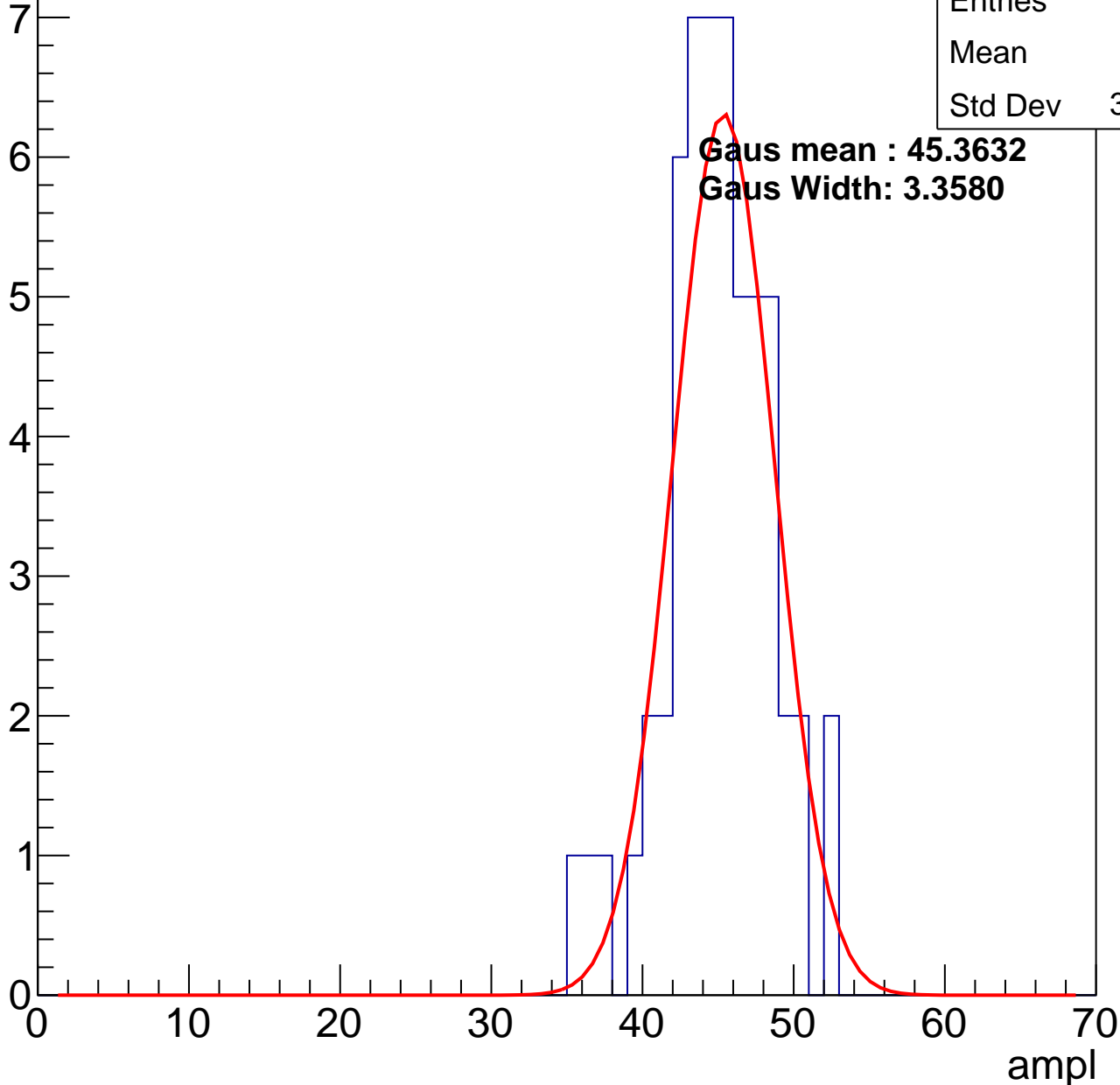
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 56    |
| Mean    | 44.5  |
| Std Dev | 3.505 |

**Gaus mean : 45.3632**

**Gaus Width: 3.3580**

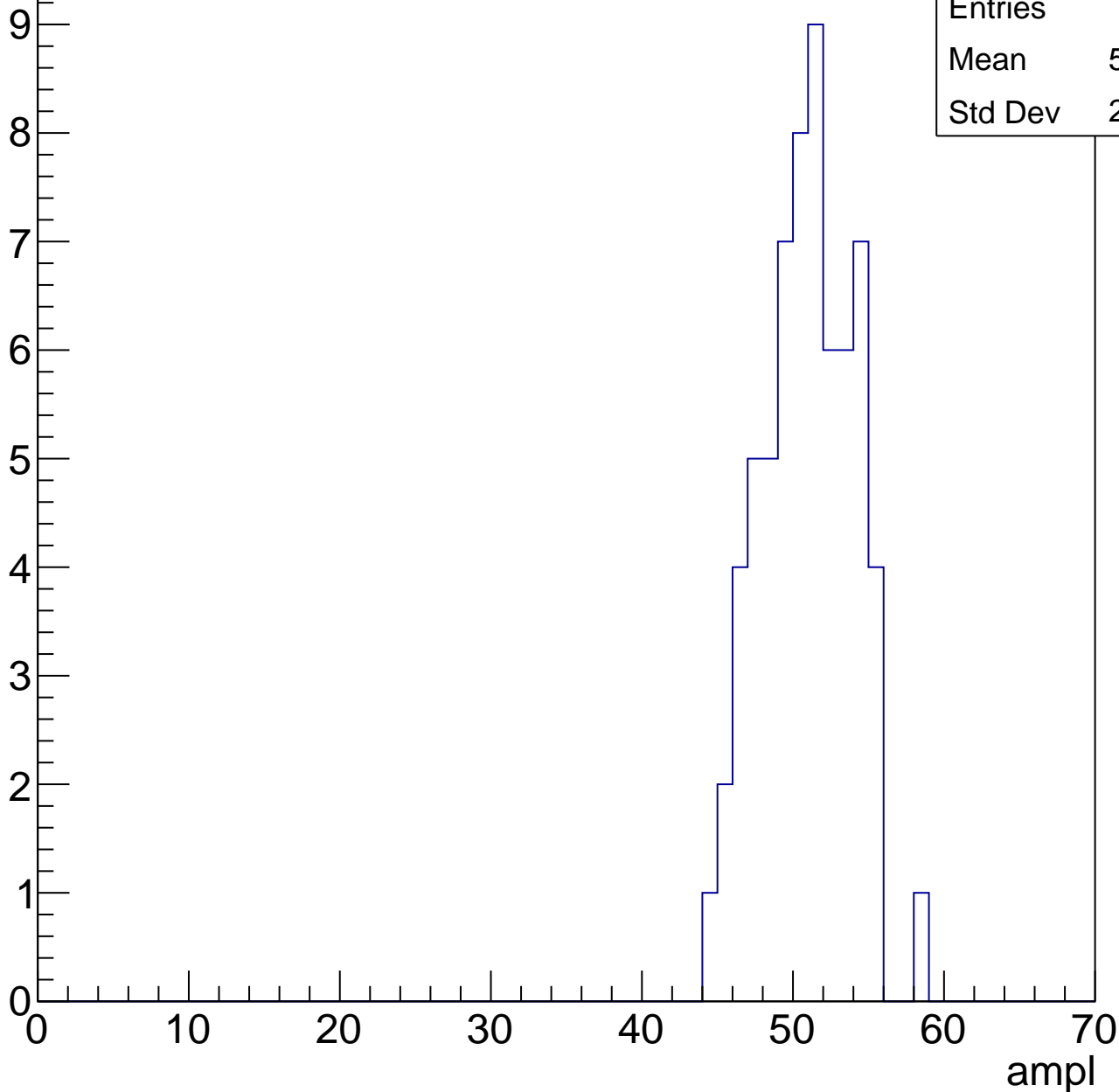


# B0L002S, U1-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 65    |
| Mean    | 50.48 |
| Std Dev | 2.962 |

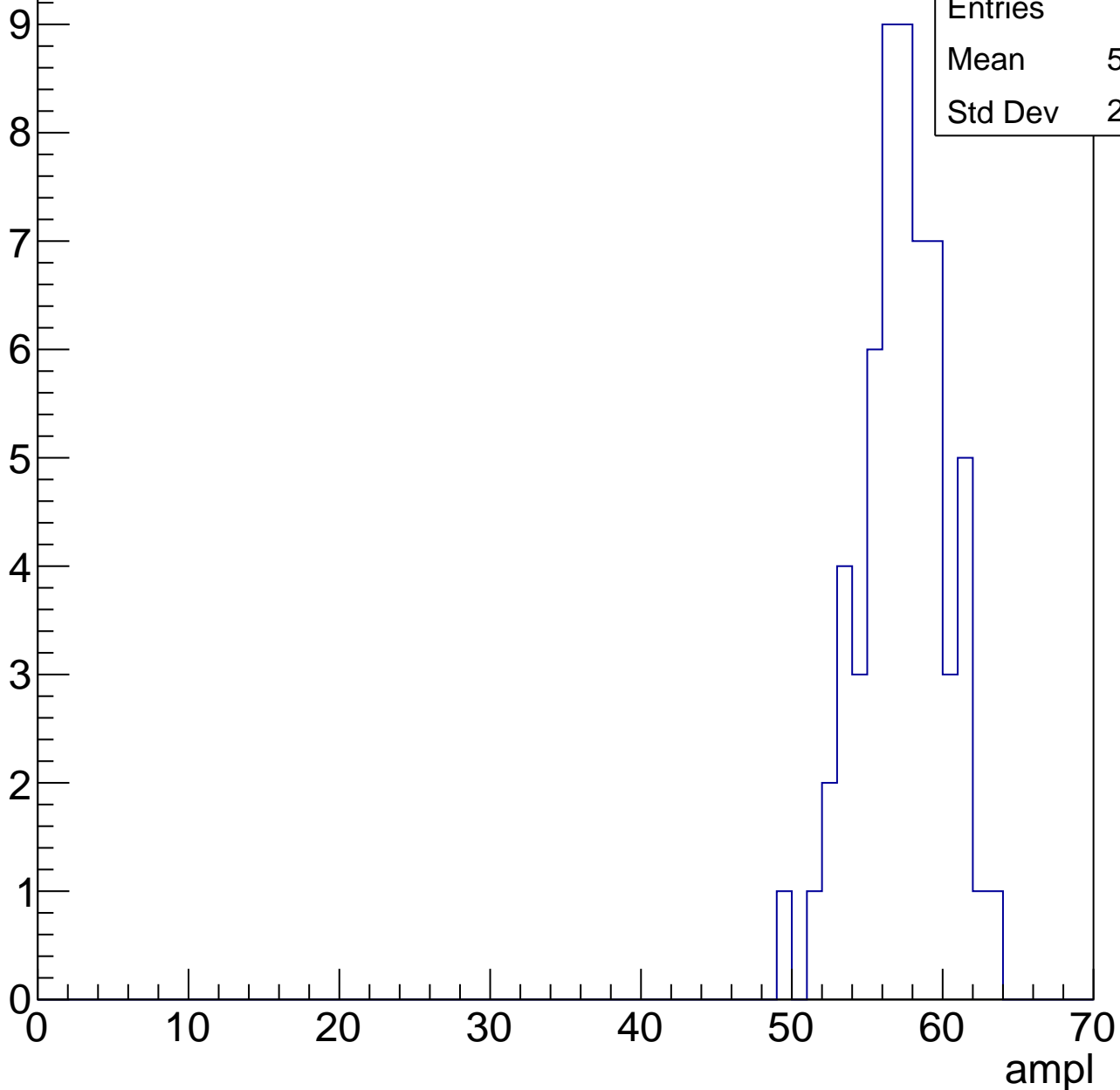


# B0L002S, U1-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 59    |
| Mean    | 56.85 |
| Std Dev | 2.839 |

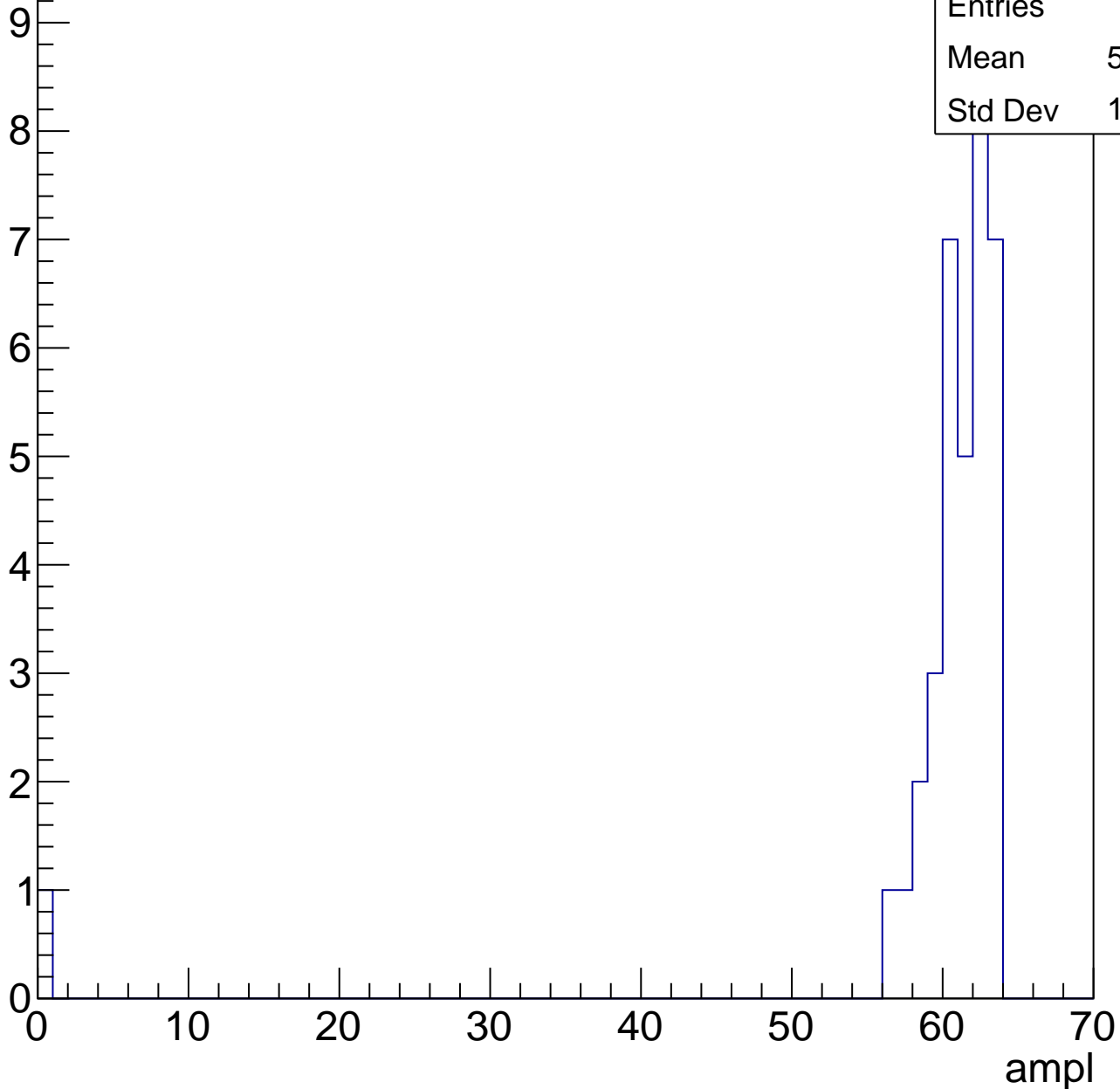


# B0L002S, U1-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 36    |
| Mean    | 59.17 |
| Std Dev | 10.16 |



# B0L002S, U1-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch126, adc0

calib\_packv5\_042523\_0143.root, FC#8, port C1

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 30.93 |
| Std Dev | 3.031 |

**Gaus mean : 31.1656**

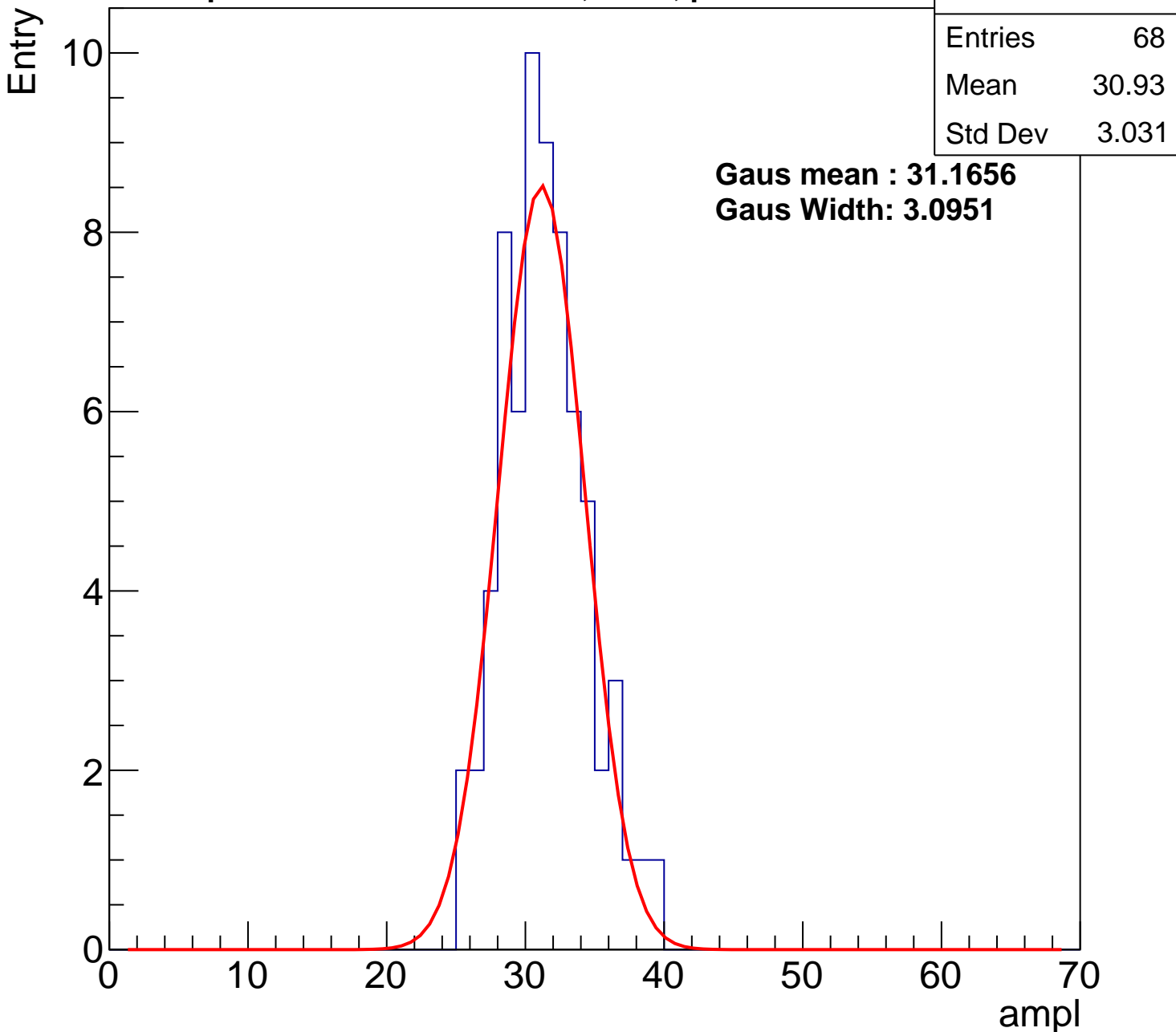
**Gaus Width: 3.0951**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L002S, U1-ch126, adc1

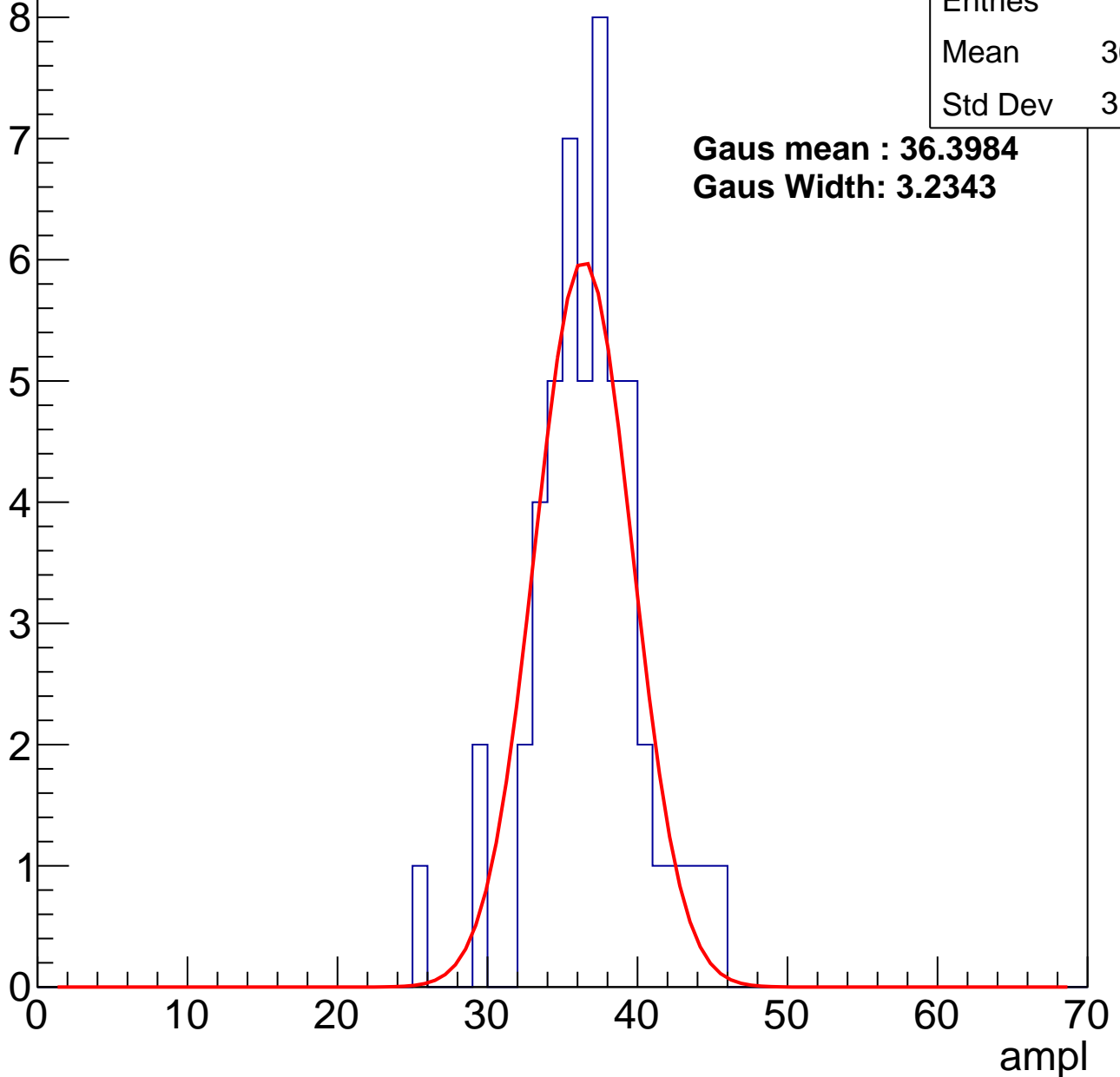
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 51    |
| Mean    | 36.27 |
| Std Dev | 3.625 |

**Gaus mean : 36.3984**

**Gaus Width: 3.2343**



# B0L002S, U1-ch126, adc2

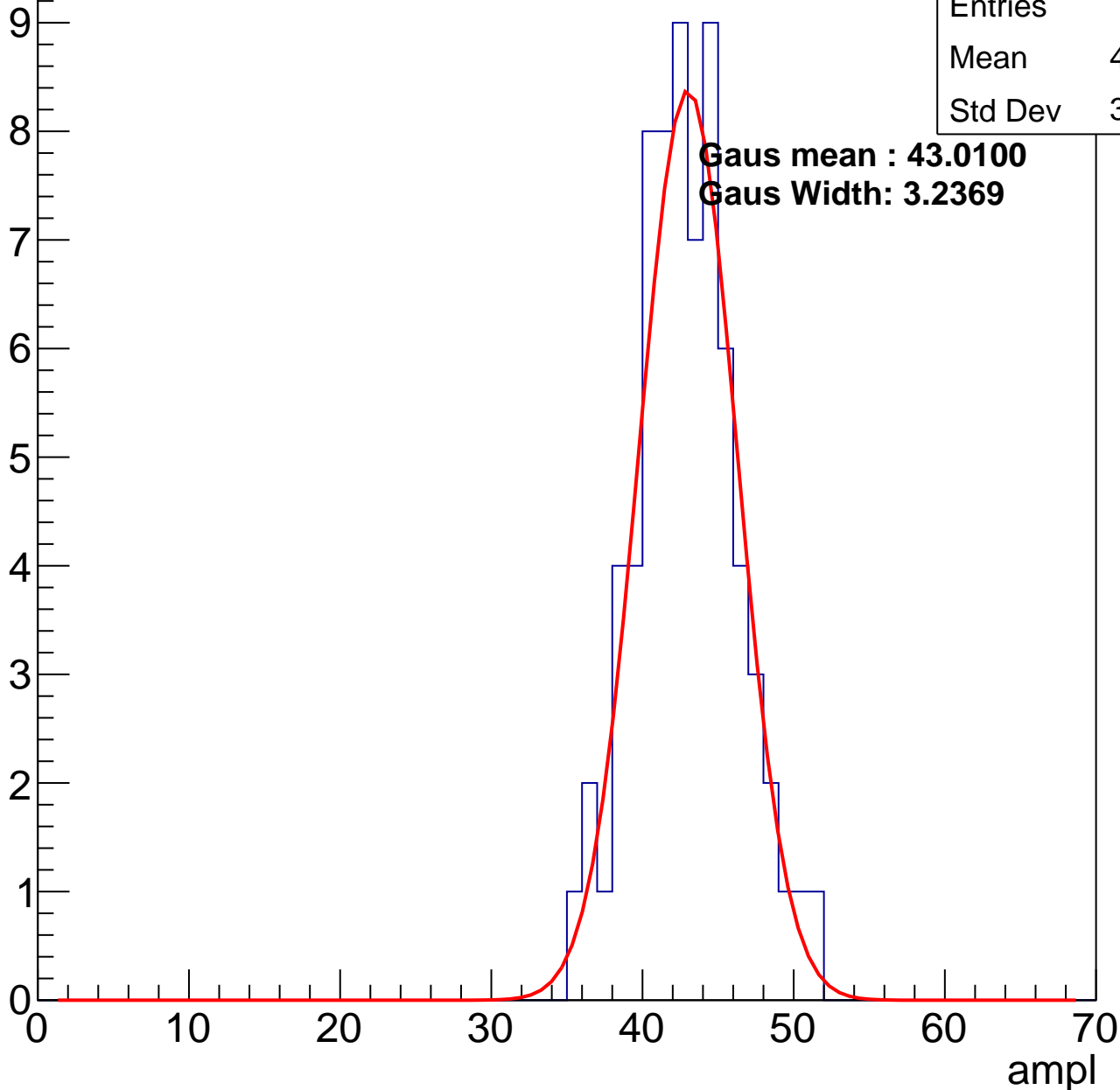
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 71    |
| Mean    | 42.48 |
| Std Dev | 3.297 |

**Gaus mean : 43.0100**

**Gaus Width: 3.2369**

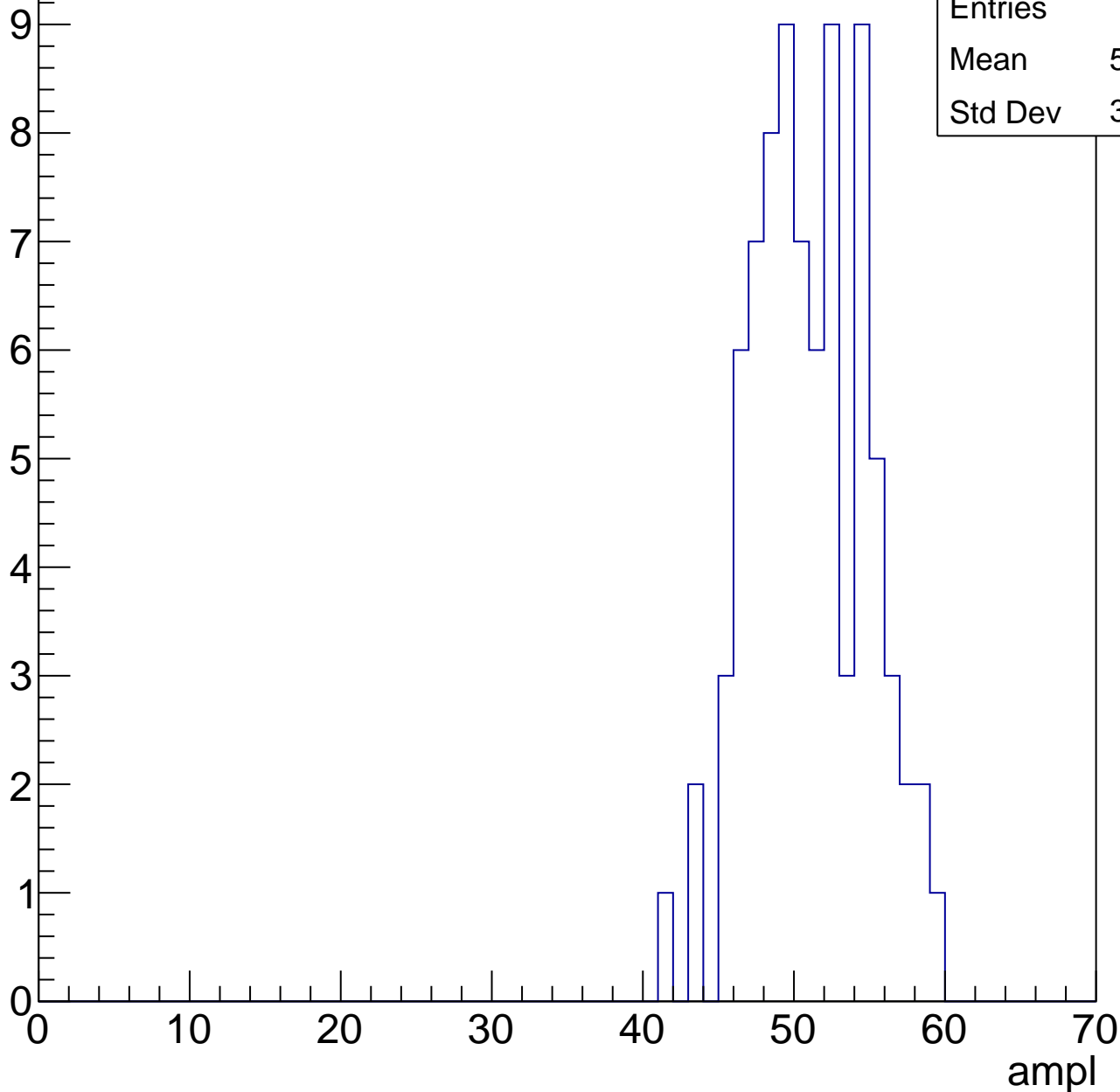


# B0L002S, U1-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 83    |
| Mean    | 50.52 |
| Std Dev | 3.778 |

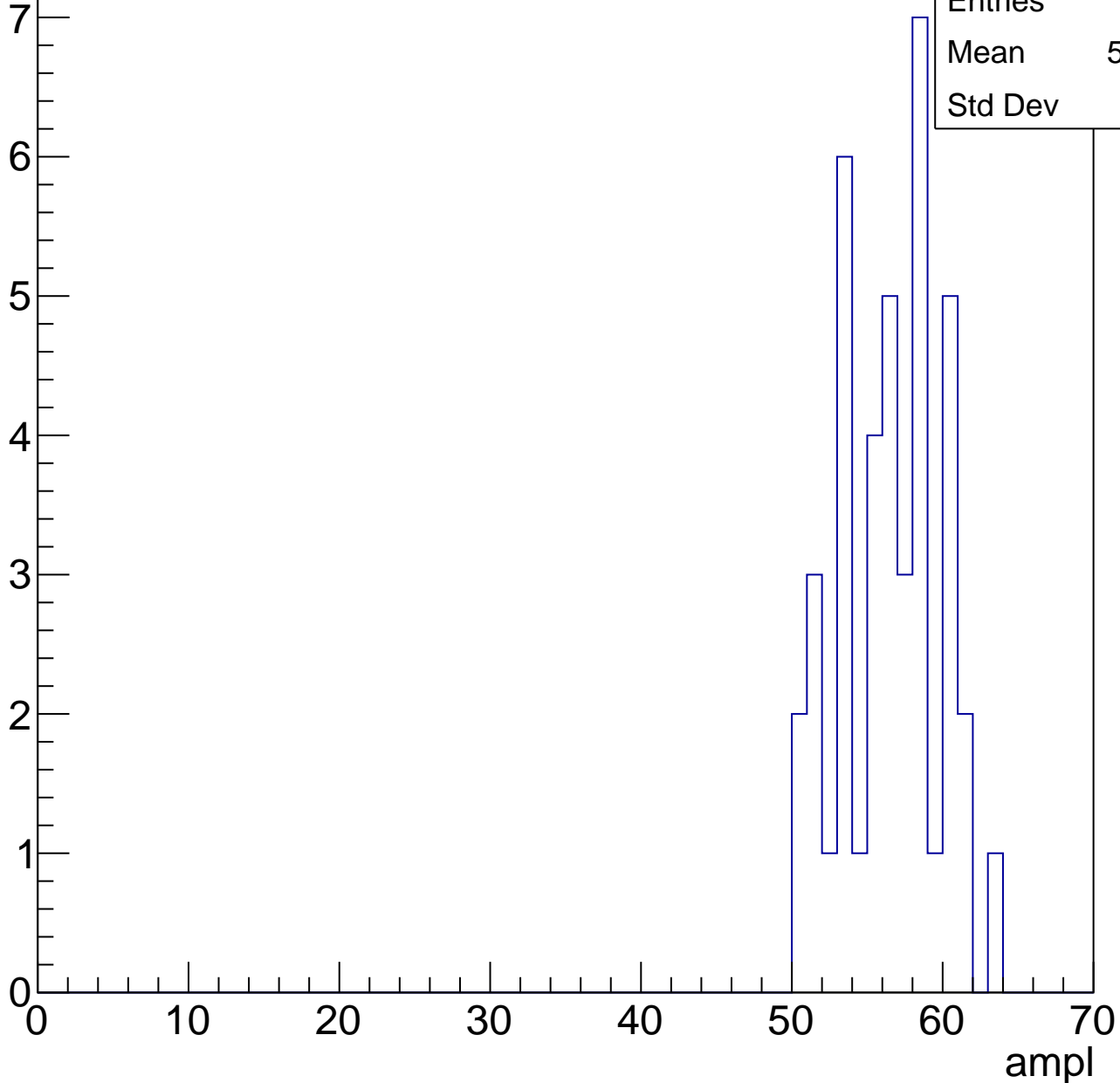


# B0L002S, U1-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

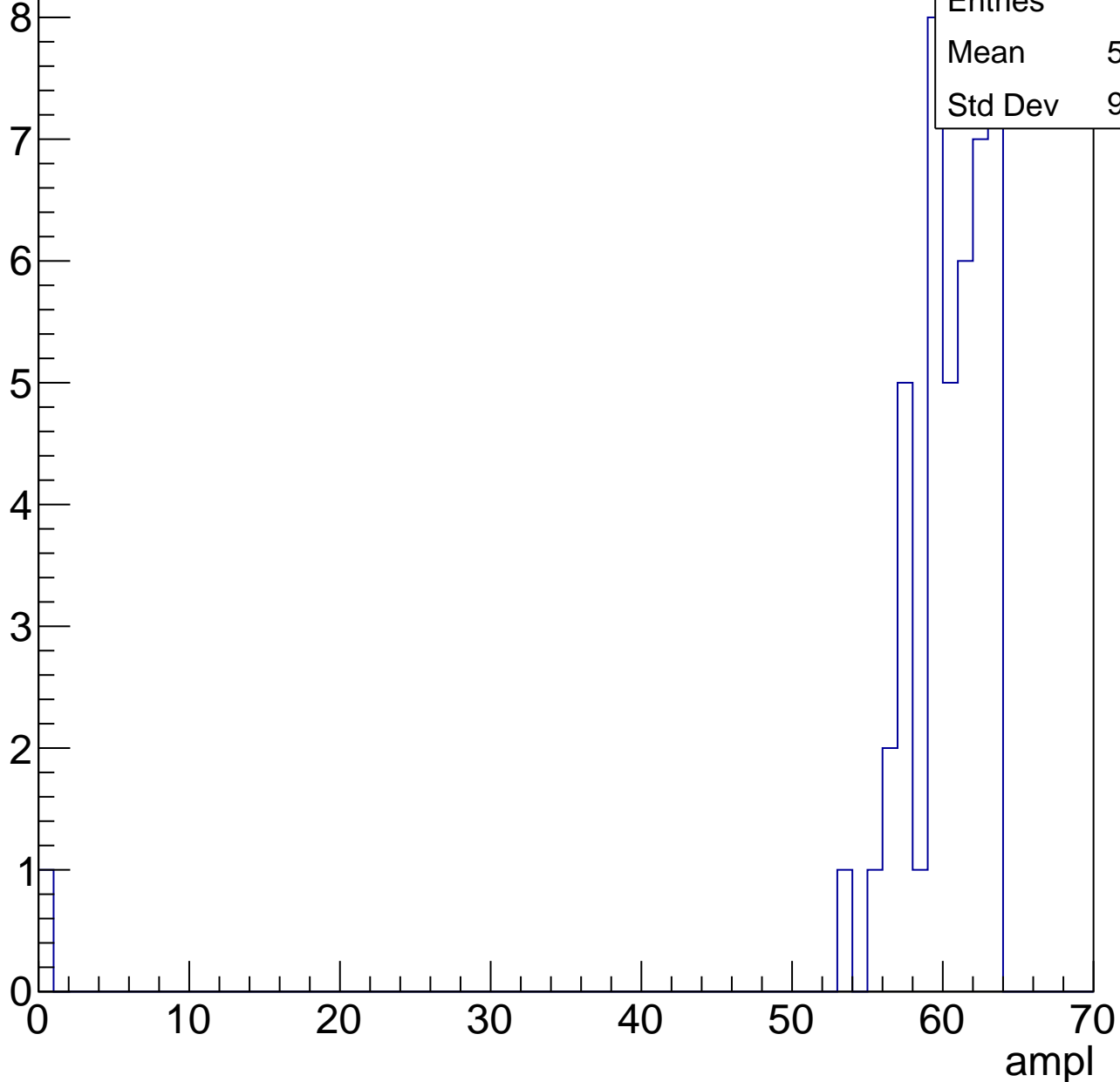
|         |       |
|---------|-------|
| Entries | 41    |
| Mean    | 56.05 |
| Std Dev | 3.29  |



# B0L002S, U1-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

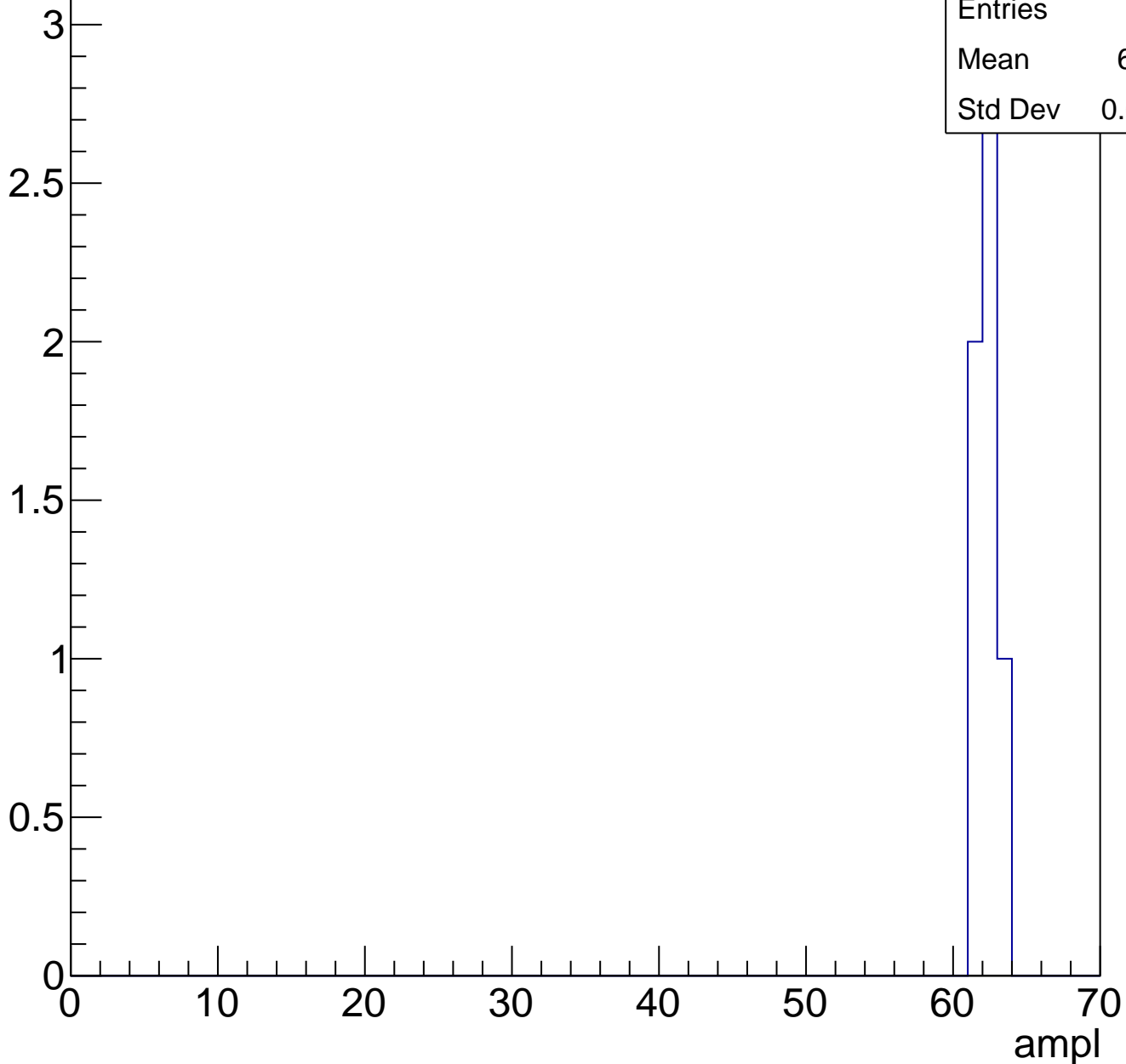
Entry



# B0L002S, U1-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry





# B0L002S, U1-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



|         |   |
|---------|---|
| Entries | 0 |
| Mean    | 0 |
| Std Dev | 0 |

# B0L002S, U1-ch127, adc0

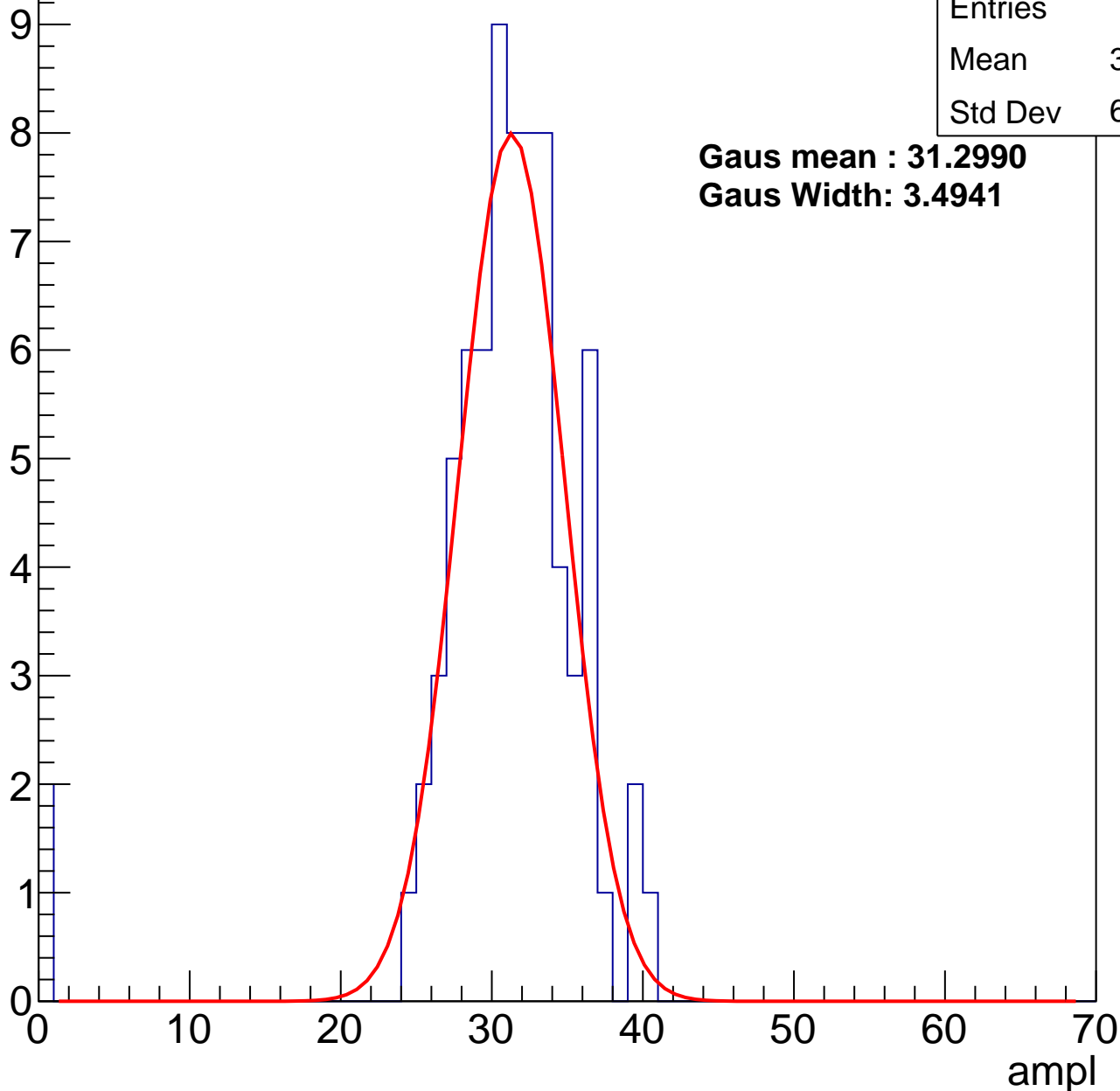
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 75    |
| Mean    | 30.39 |
| Std Dev | 6.084 |

**Gaus mean : 31.2990**

**Gaus Width: 3.4941**



# B0L002S, U1-ch127, adc1

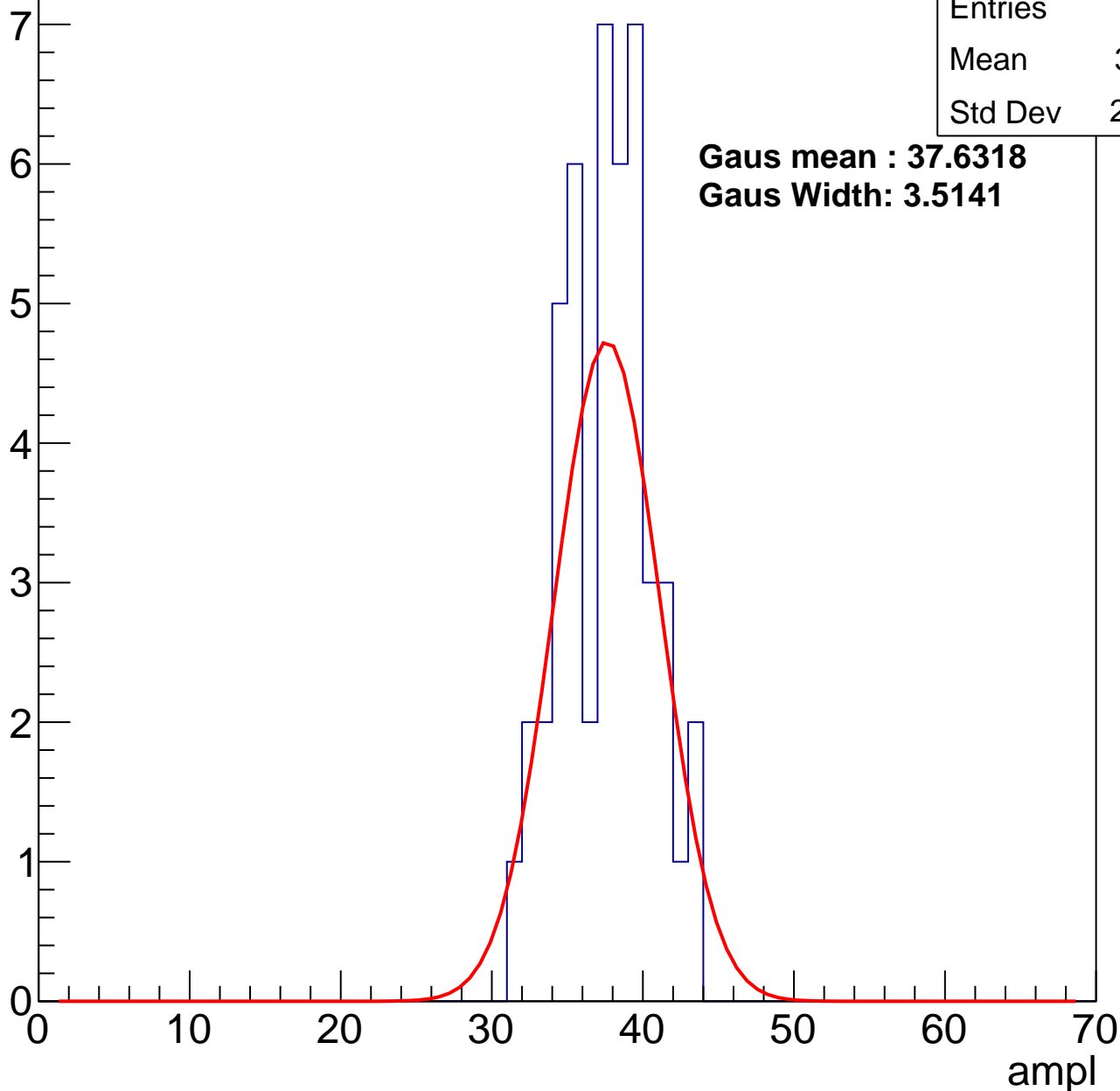
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 47    |
| Mean    | 37.11 |
| Std Dev | 2.897 |

**Gaus mean : 37.6318**

**Gaus Width: 3.5141**



# B0L002S, U1-ch127, adc2

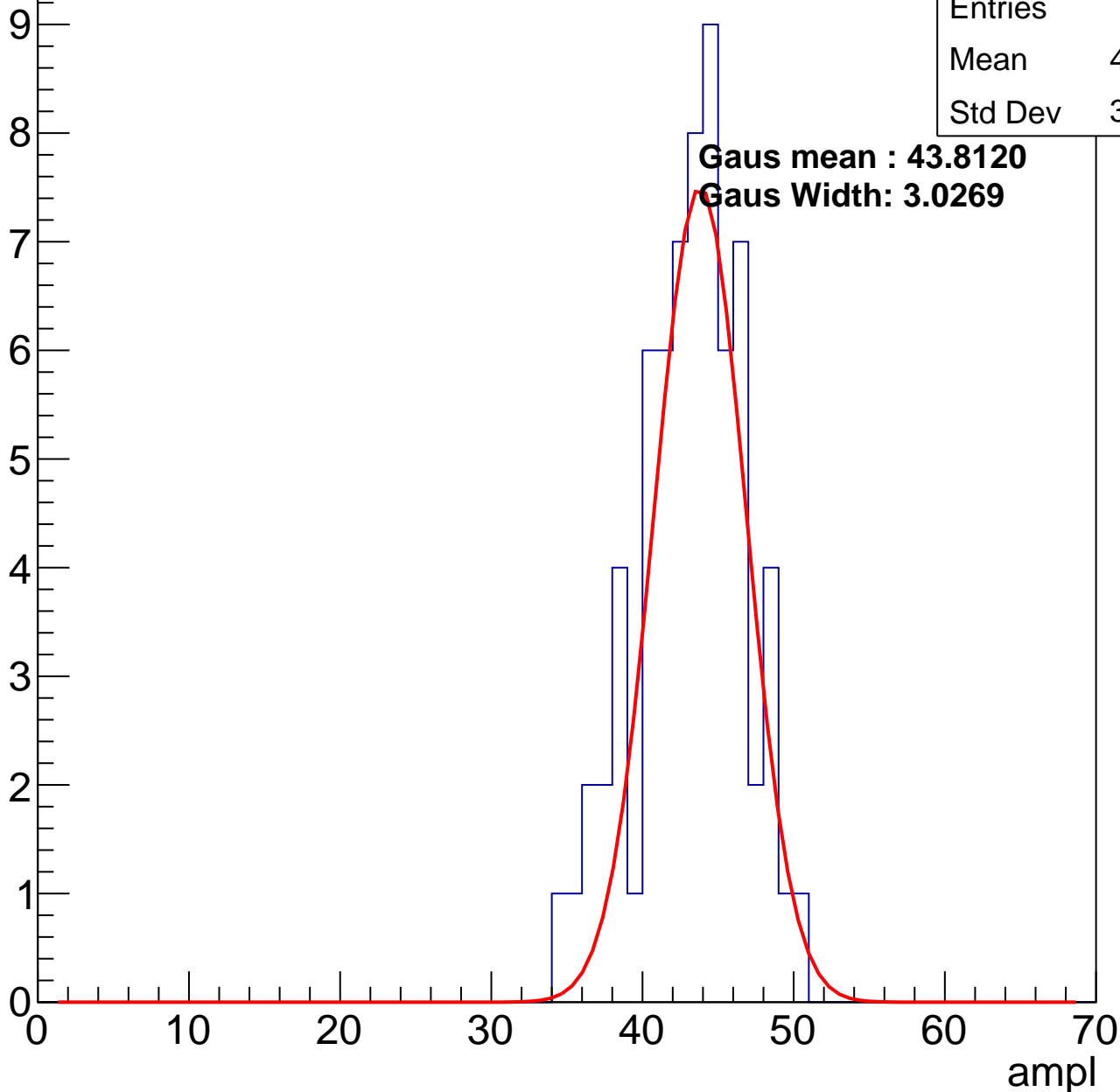
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 68    |
| Mean    | 42.69 |
| Std Dev | 3.478 |

**Gaus mean : 43.8120**

**Gaus Width: 3.0269**

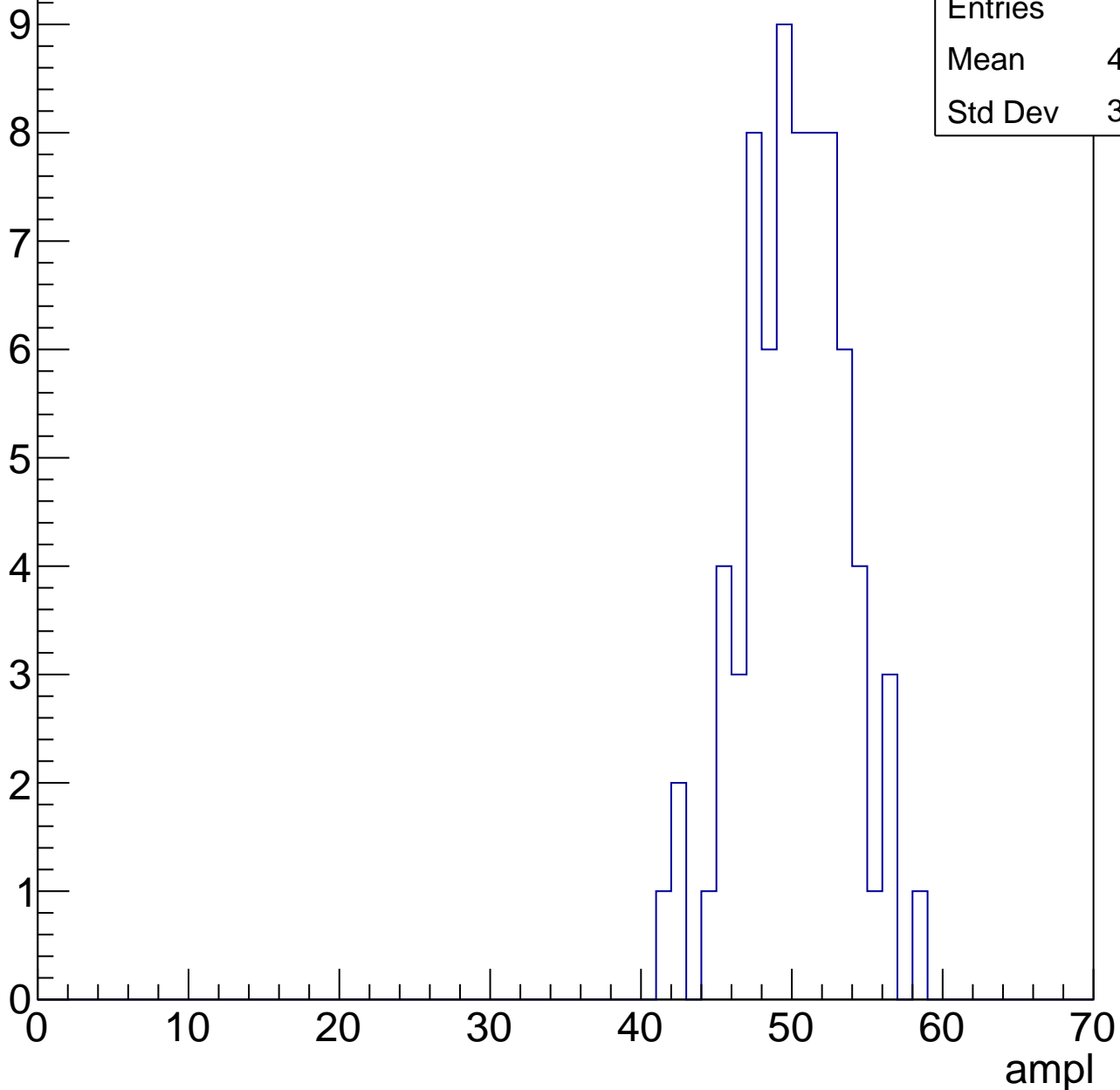


# B0L002S, U1-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 73    |
| Mean    | 49.74 |
| Std Dev | 3.408 |

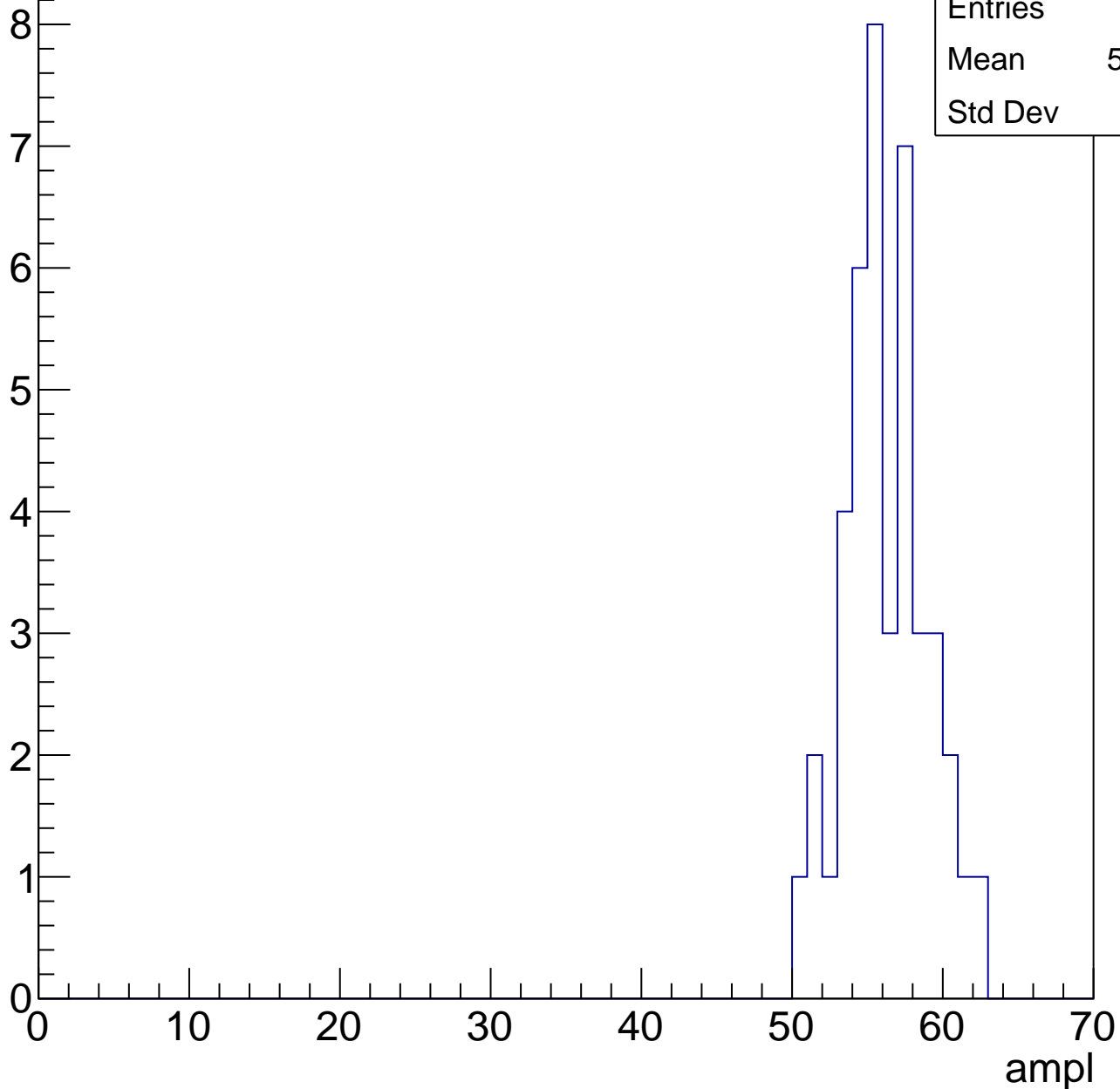


# B0L002S, U1-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 42    |
| Mean    | 55.74 |
| Std Dev | 2.7   |

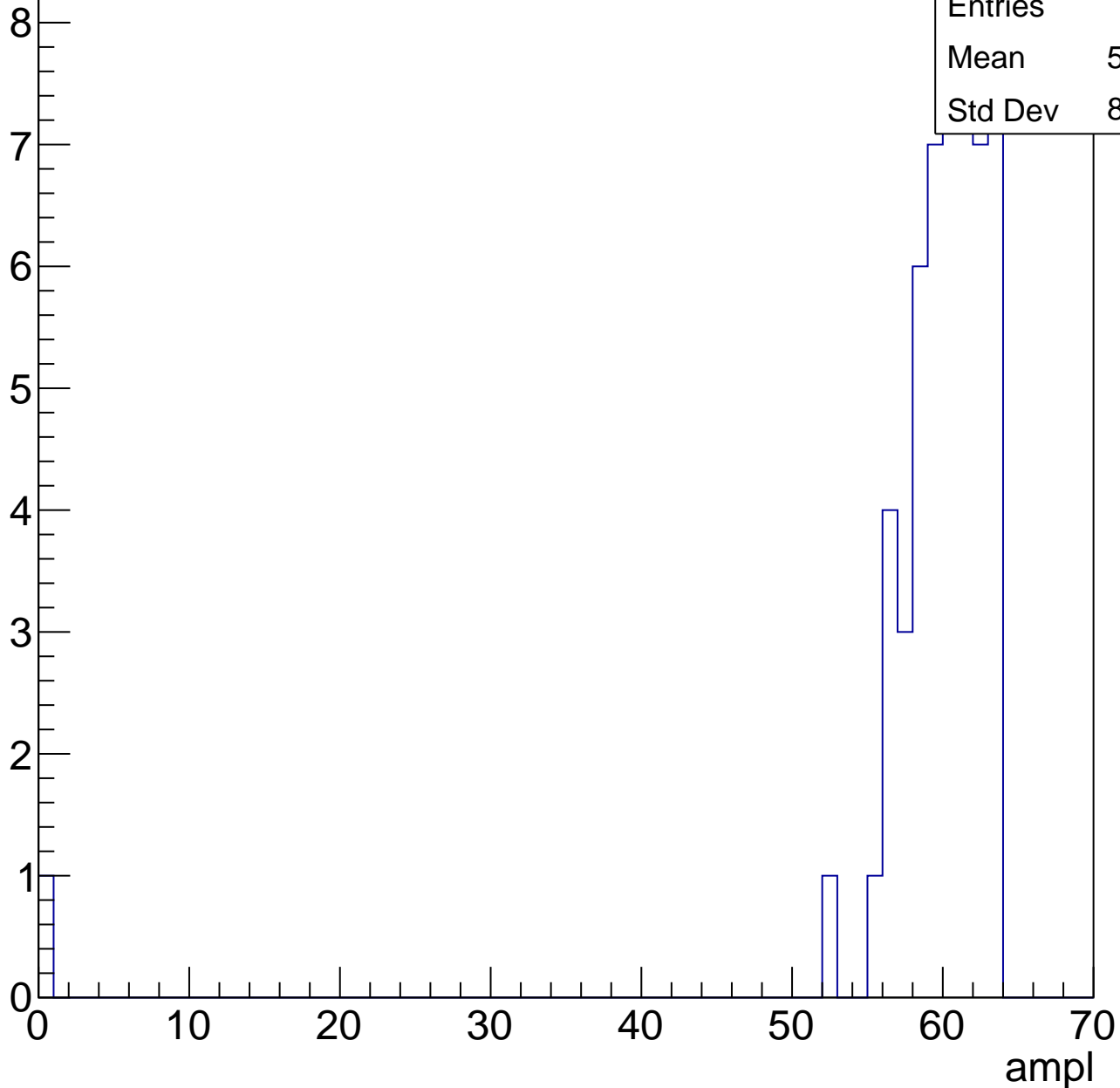


# B0L002S, U1-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

|         |       |
|---------|-------|
| Entries | 54    |
| Mean    | 58.69 |
| Std Dev | 8.417 |



# B0L002S, U1-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

|         |        |
|---------|--------|
| Entries | 5      |
| Mean    | 62.2   |
| Std Dev | 0.7483 |

ampl

0 10 20 30 40 50 60 70



# B0L002S, U1-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry



# B0L002S, U1-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entry

