



# B1L002S, U12-ch0

calib\_packv5\_042523\_0143.root, FC#10, port B3

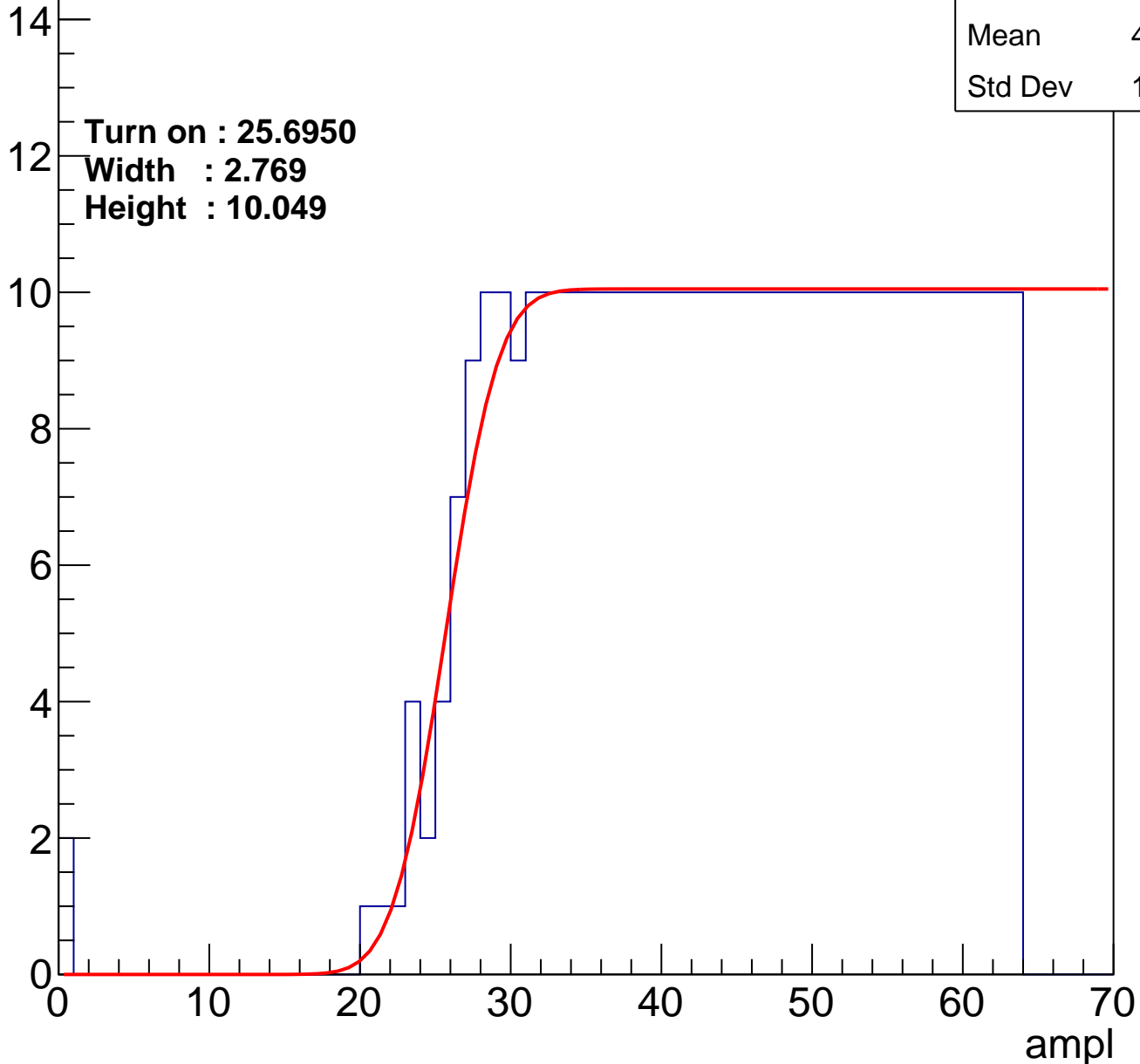
Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.6950

Width : 2.769

Height : 10.049

Entry



# B1L002S, U12-ch1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.38

Turn on : 26.5222

Width : 2.956

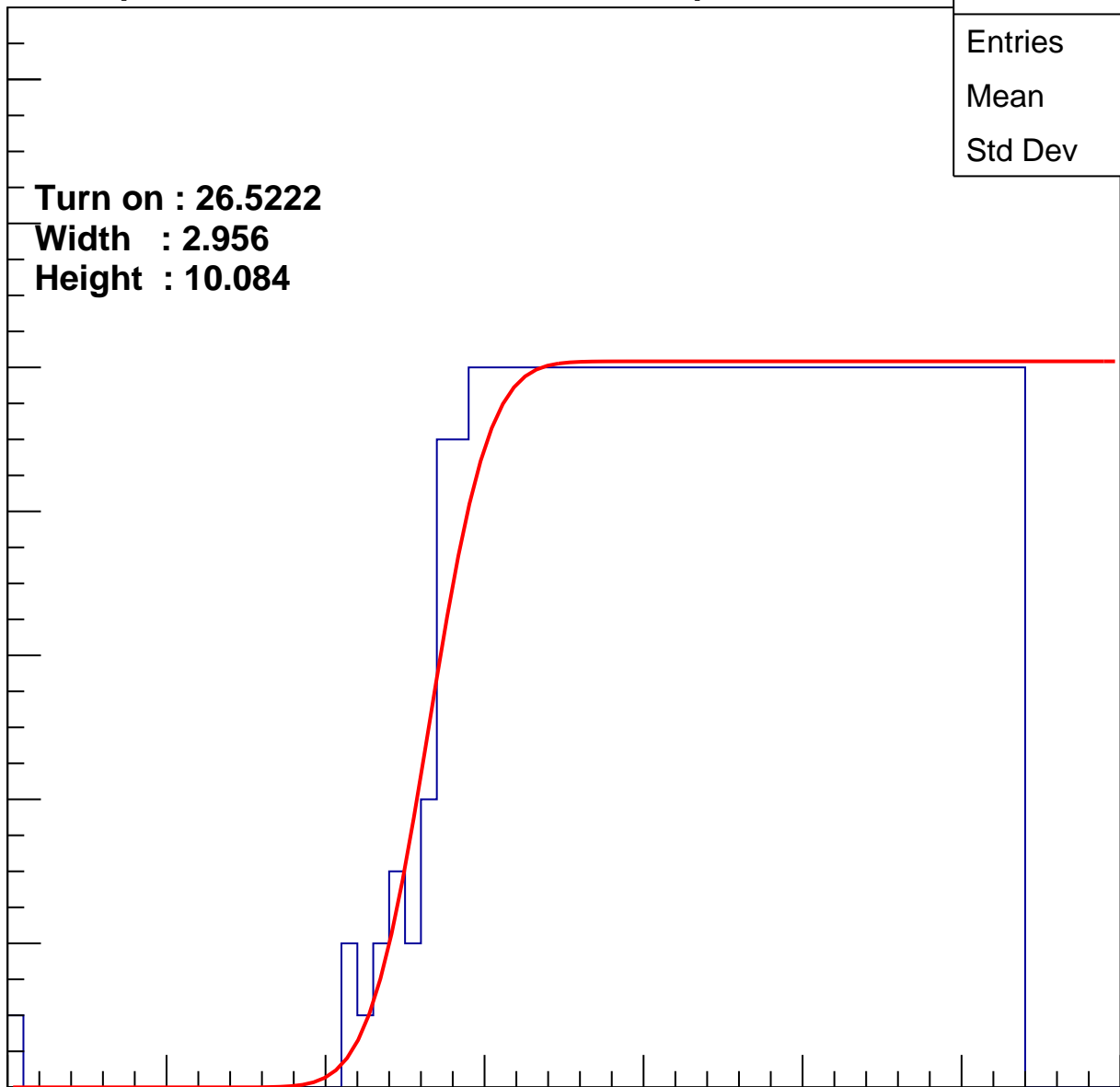
Height : 10.084

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U12-ch2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	43.98
Std Dev	11.9

Turn on : 26.1013

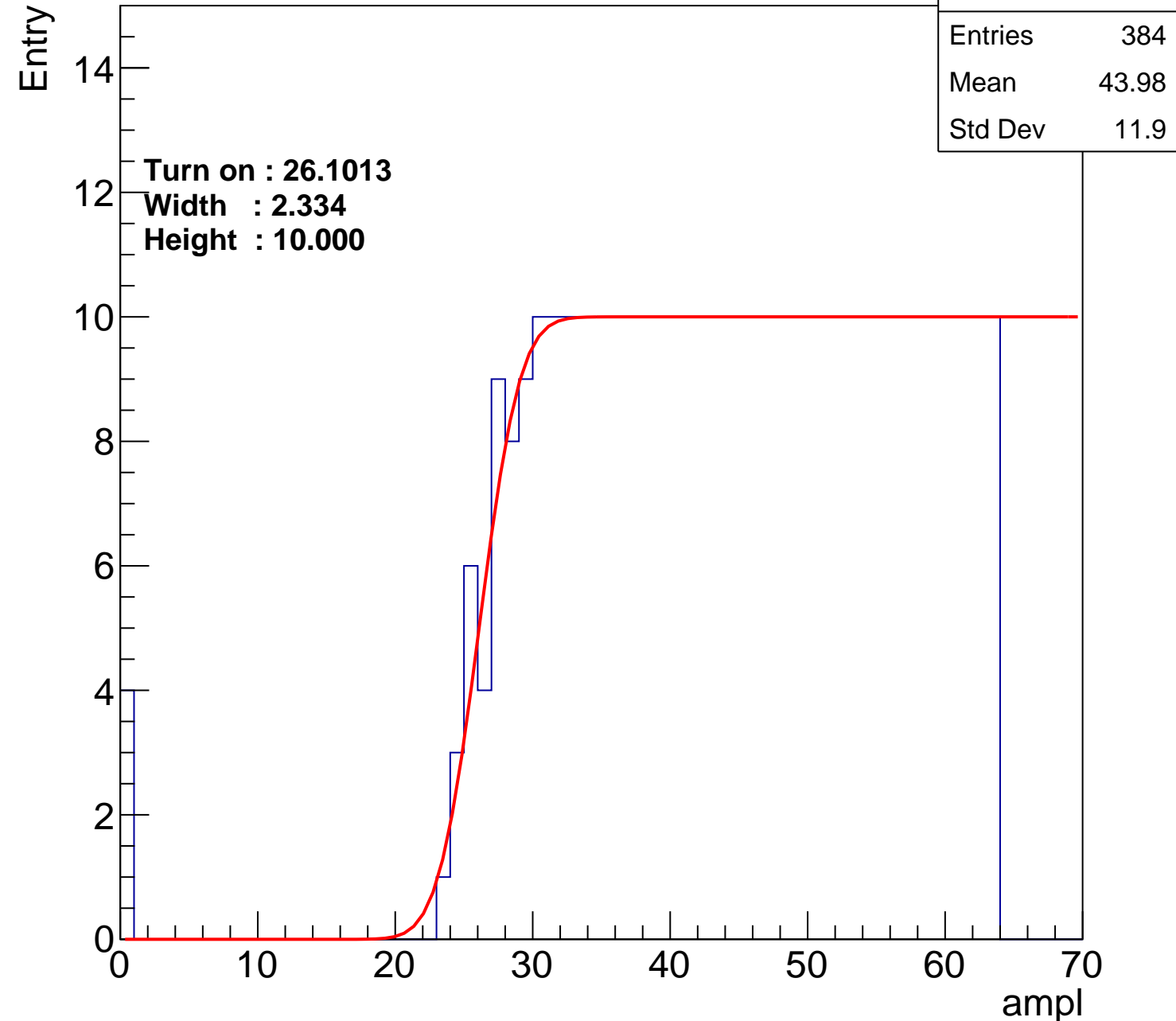
Width : 2.334

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.68
Std Dev	11.08

**Turn on : 26.8694**

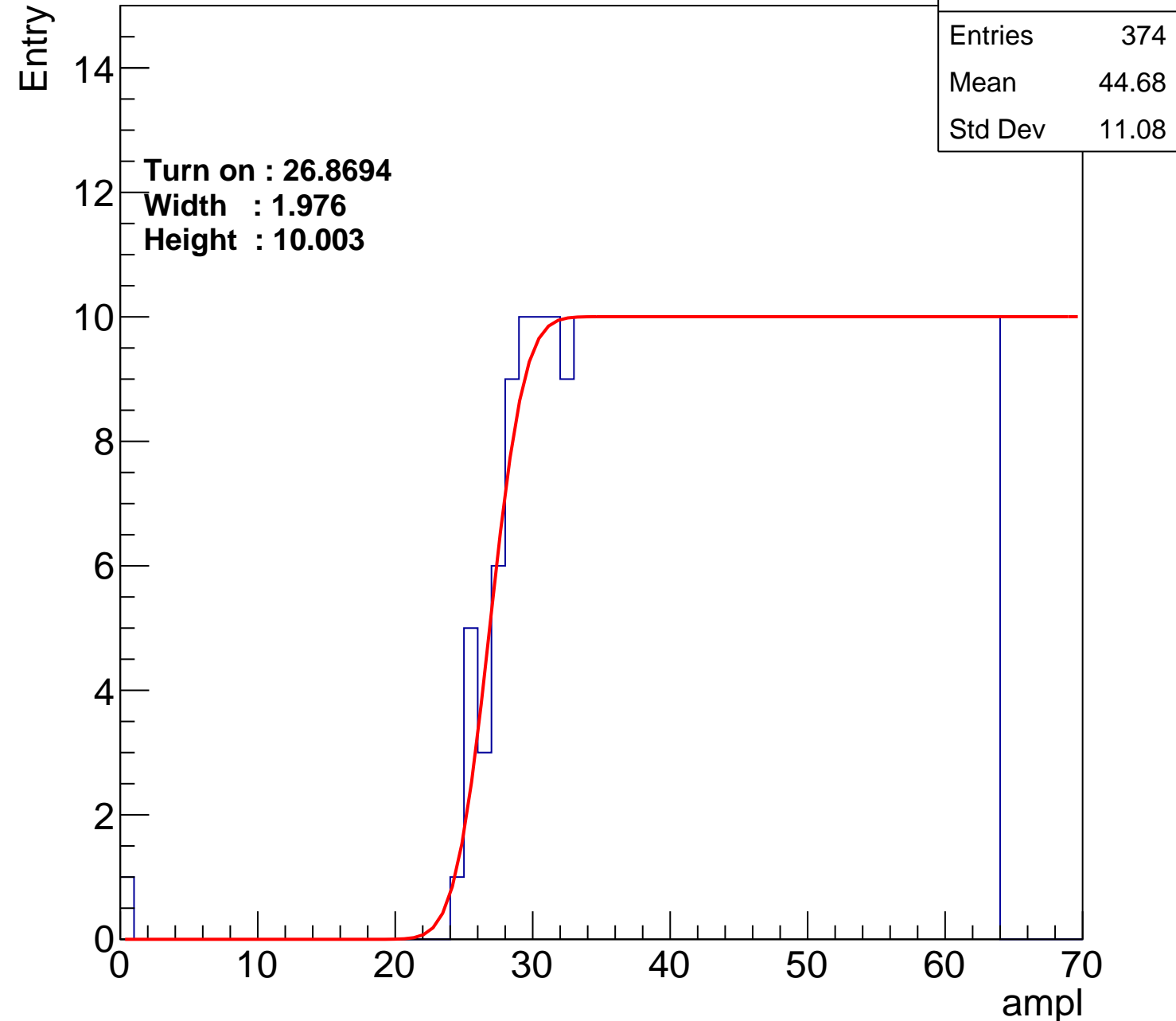
**Width : 1.976**

**Height : 10.003**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	404
Mean	42.96
Std Dev	12.43

Turn on : 24.1401

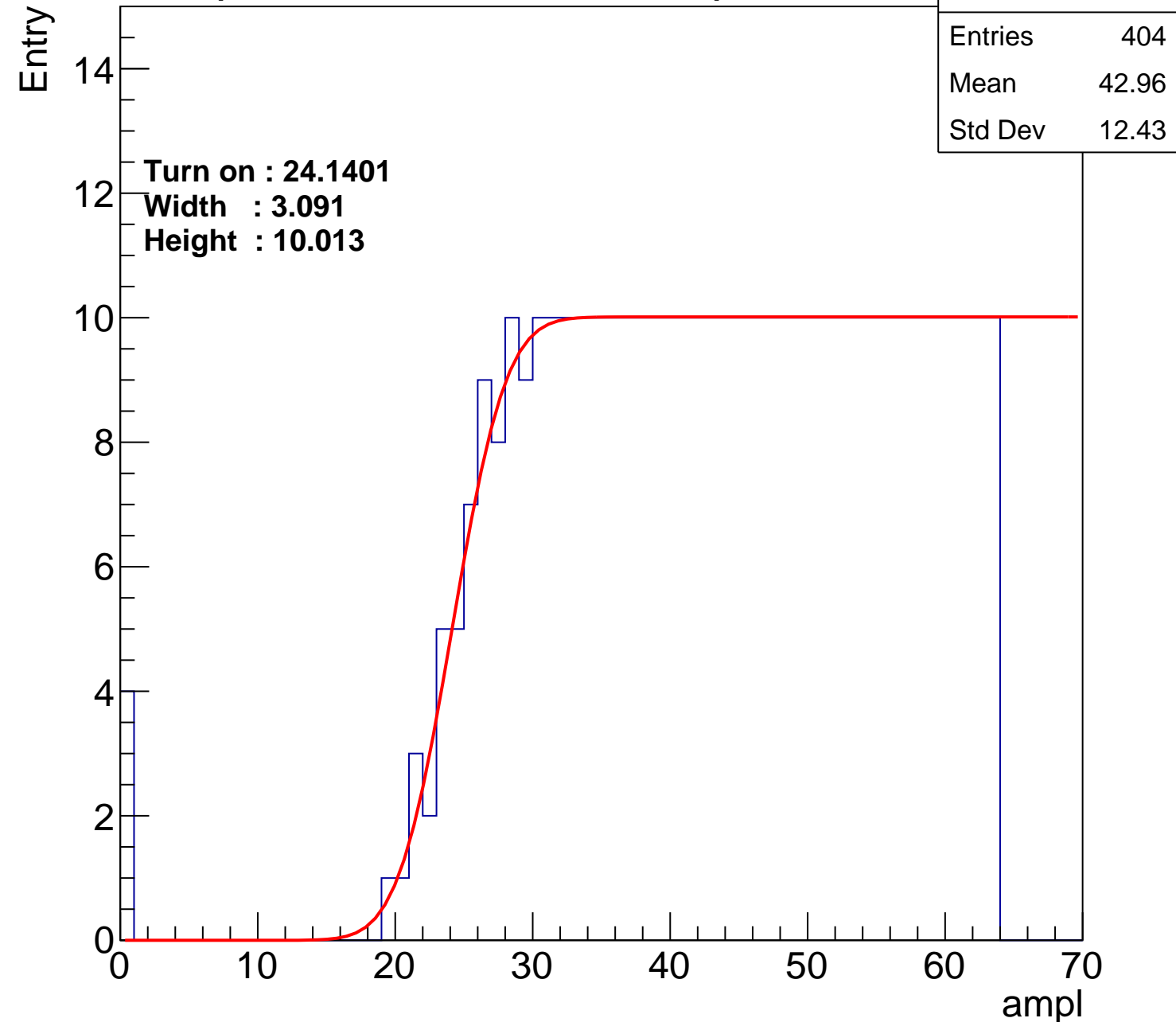
Width : 3.091

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	403
Mean	43.18
Std Dev	12.03

Turn on : 24.0785

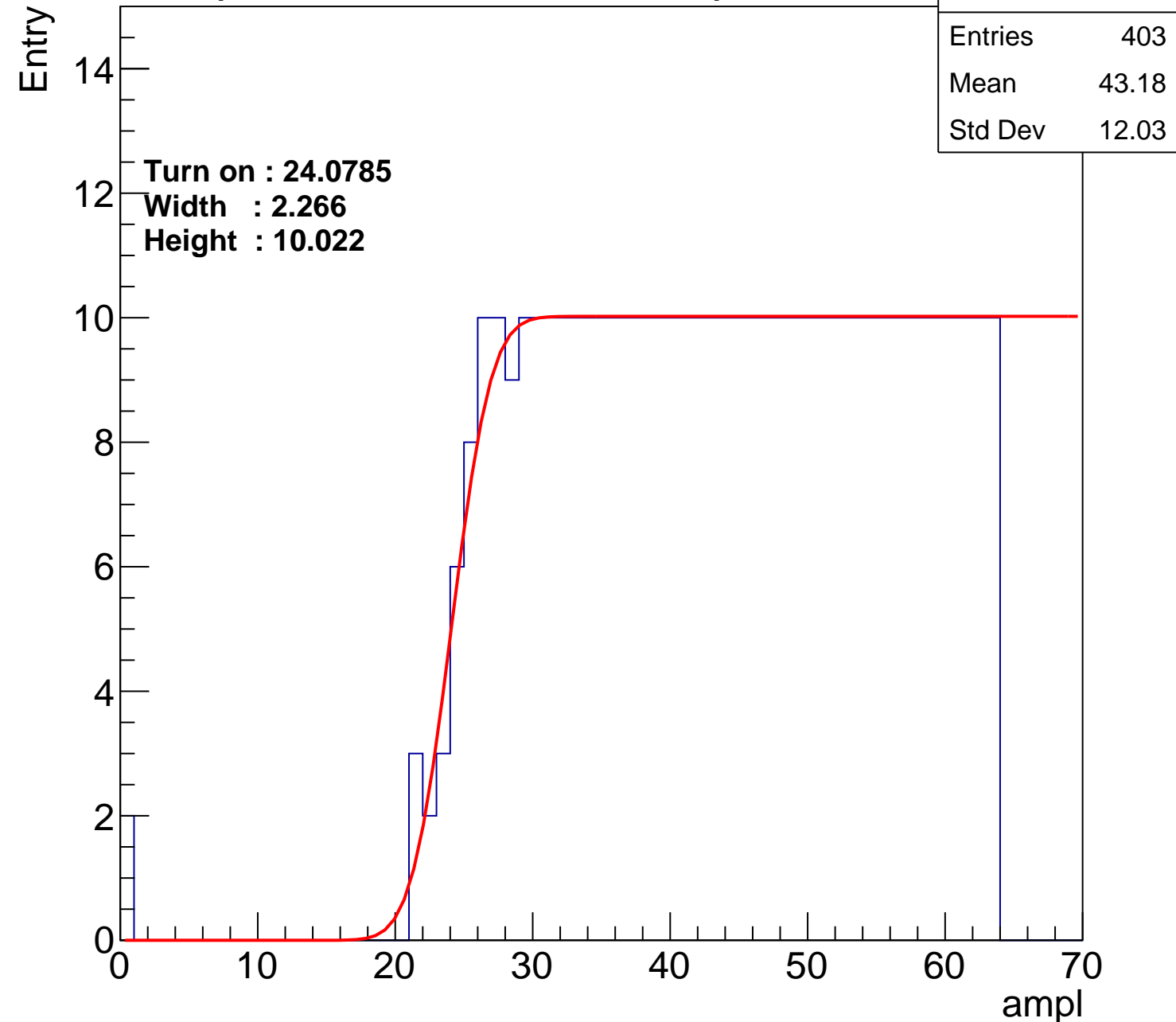
Width : 2.266

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch6

calib\_packv5\_042523\_0143.root, FC#10, port B3

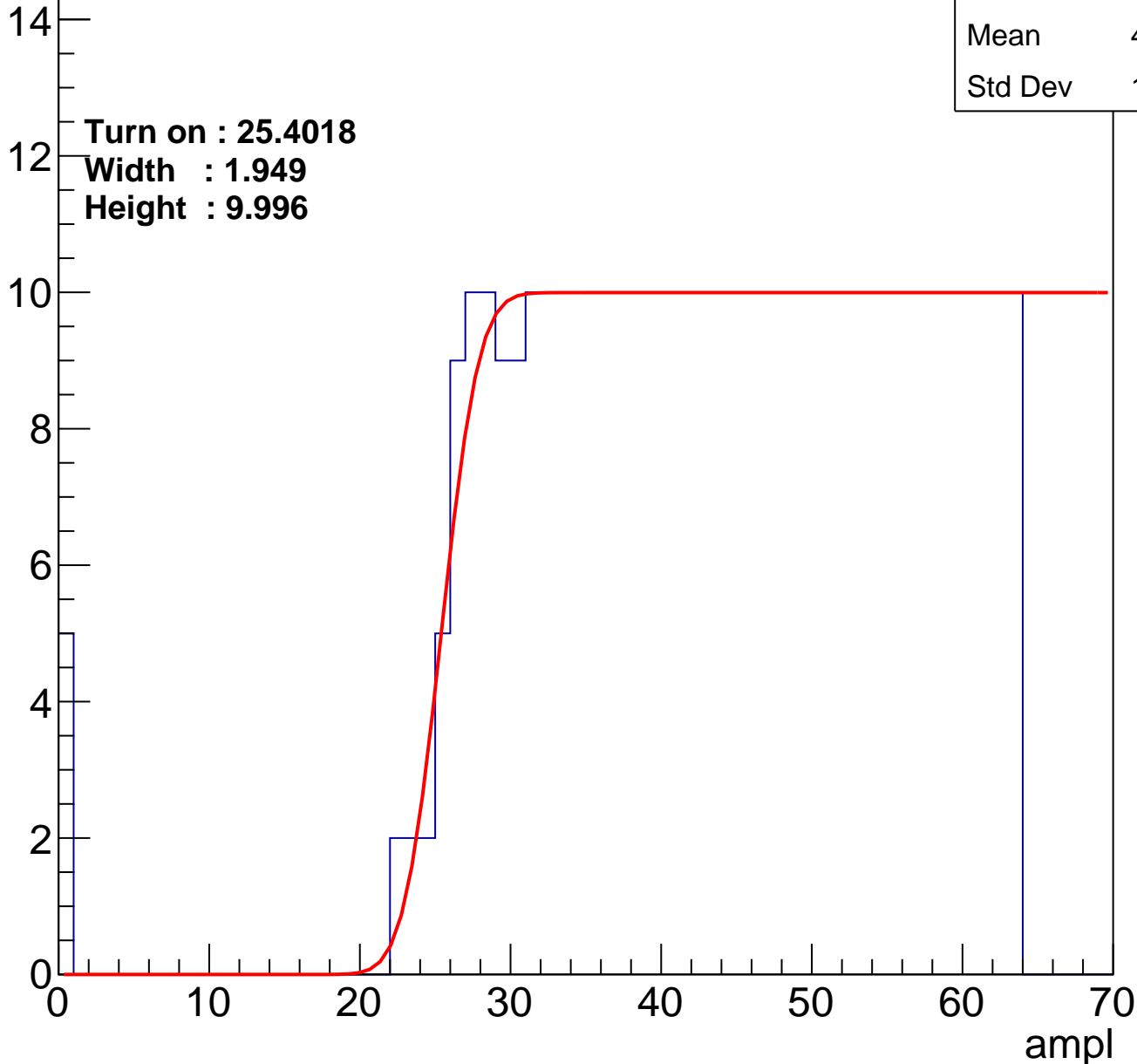
Entry

Entries	393
Mean	43.48
Std Dev	12.26

Turn on : 25.4018

Width : 1.949

Height : 9.996





# B1L002S, U12-ch7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.23
Std Dev	11.63

Turn on : 25.7995

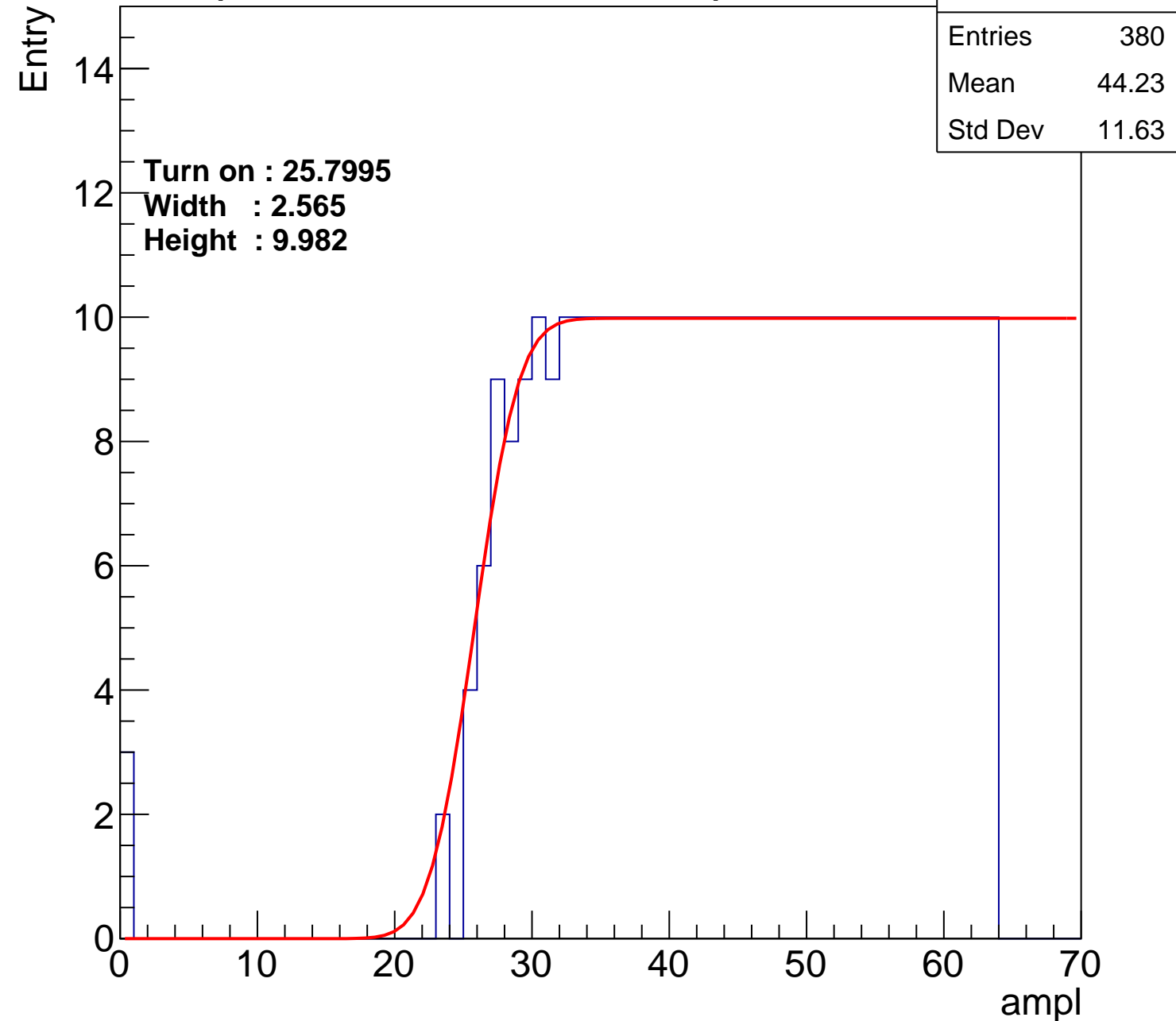
Width : 2.565

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch8

calib\_packv5\_042523\_0143.root, FC#10, port B3

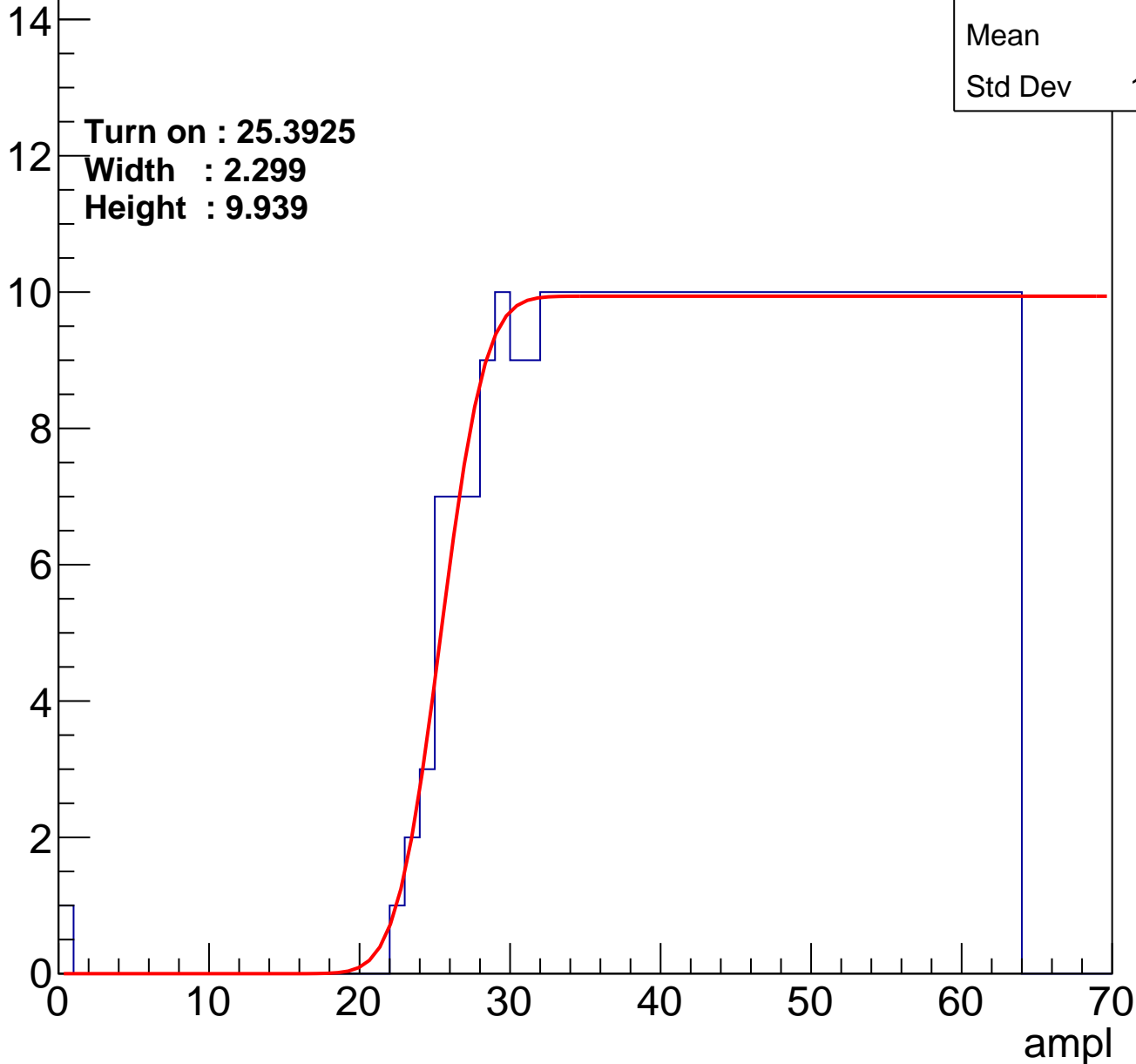
Entry

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.3925

Width : 2.299

Height : 9.939



# B1L002S, U12-ch9

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.08
Std Dev	11.42

Turn on : 25.8984

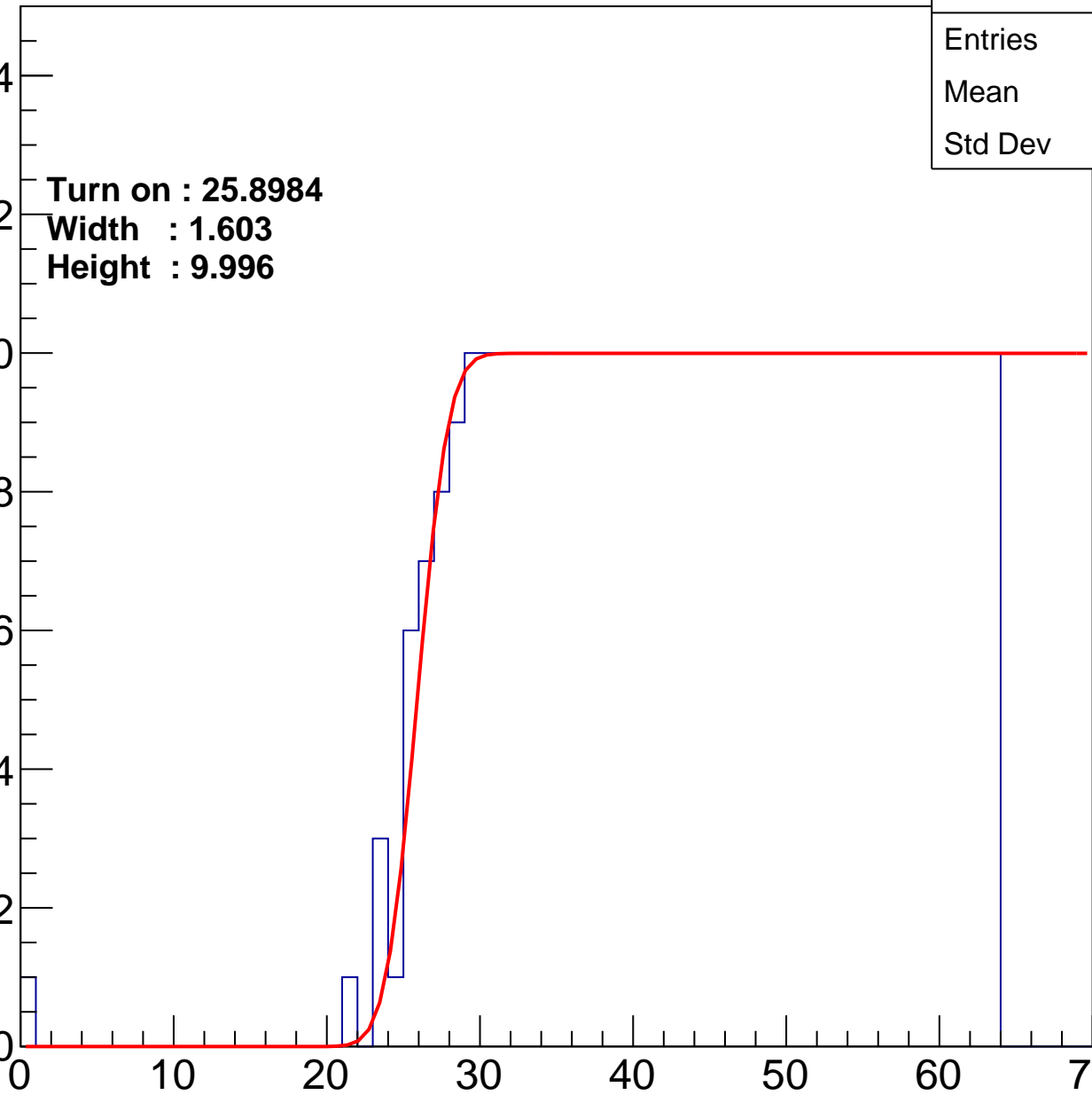
Width : 1.603

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch10

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.17
Std Dev	11.73

Turn on : 26.1451

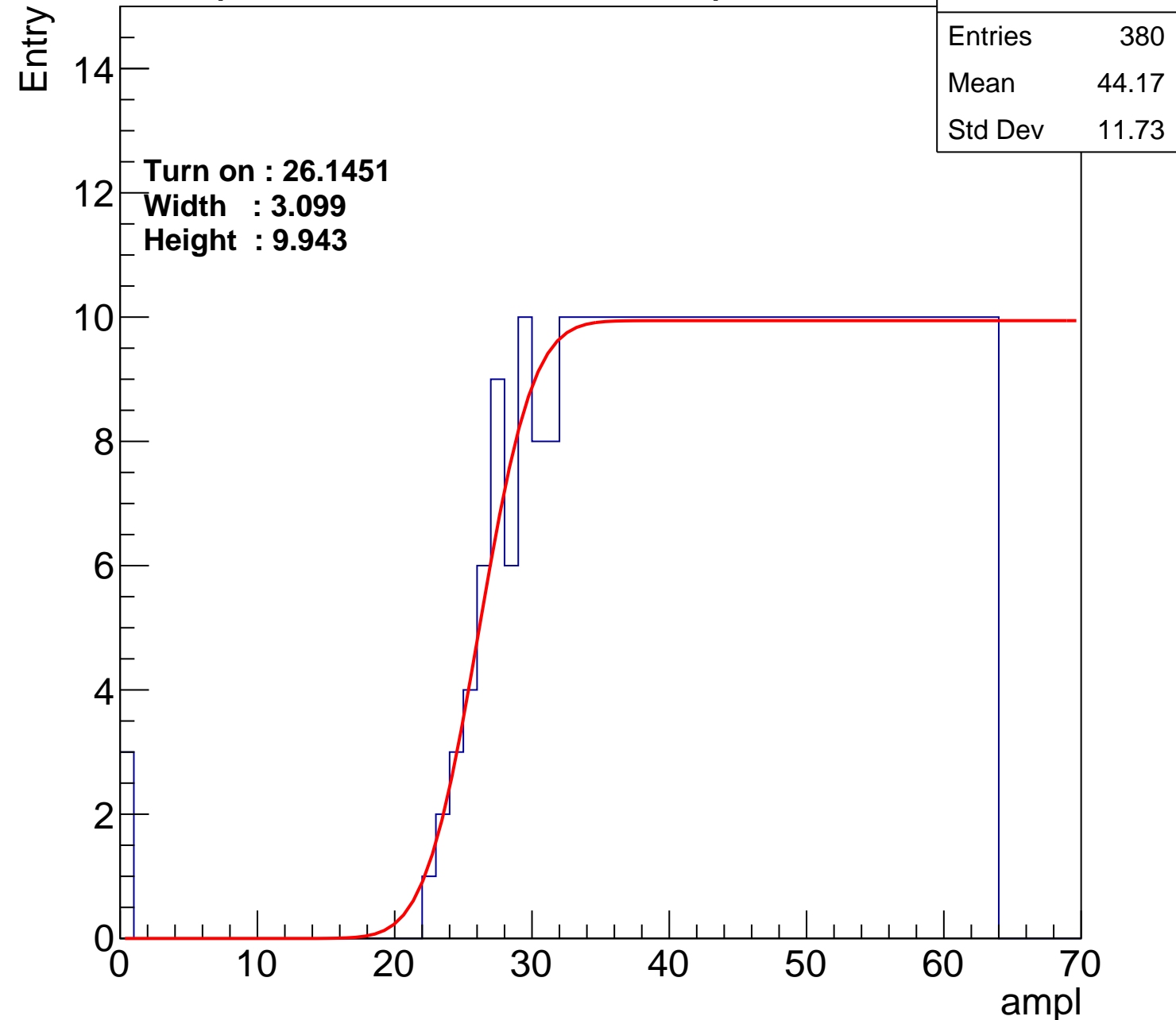
Width : 3.099

Height : 9.943

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch11

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.5
Std Dev	11.72

Turn on : 27.4887

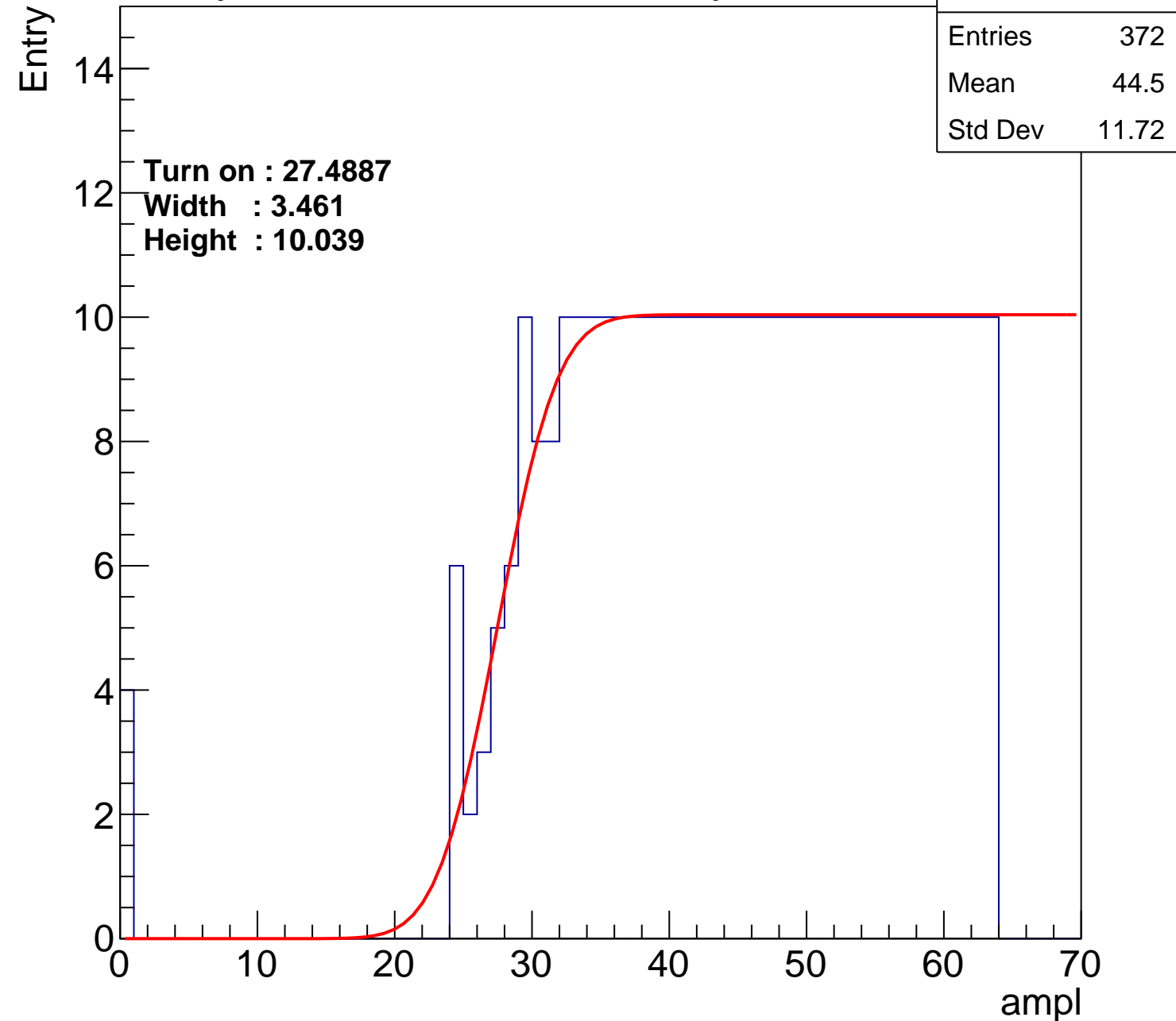
Width : 3.461

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch12

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.65
Std Dev	11.13

**Turn on : 26.6206**

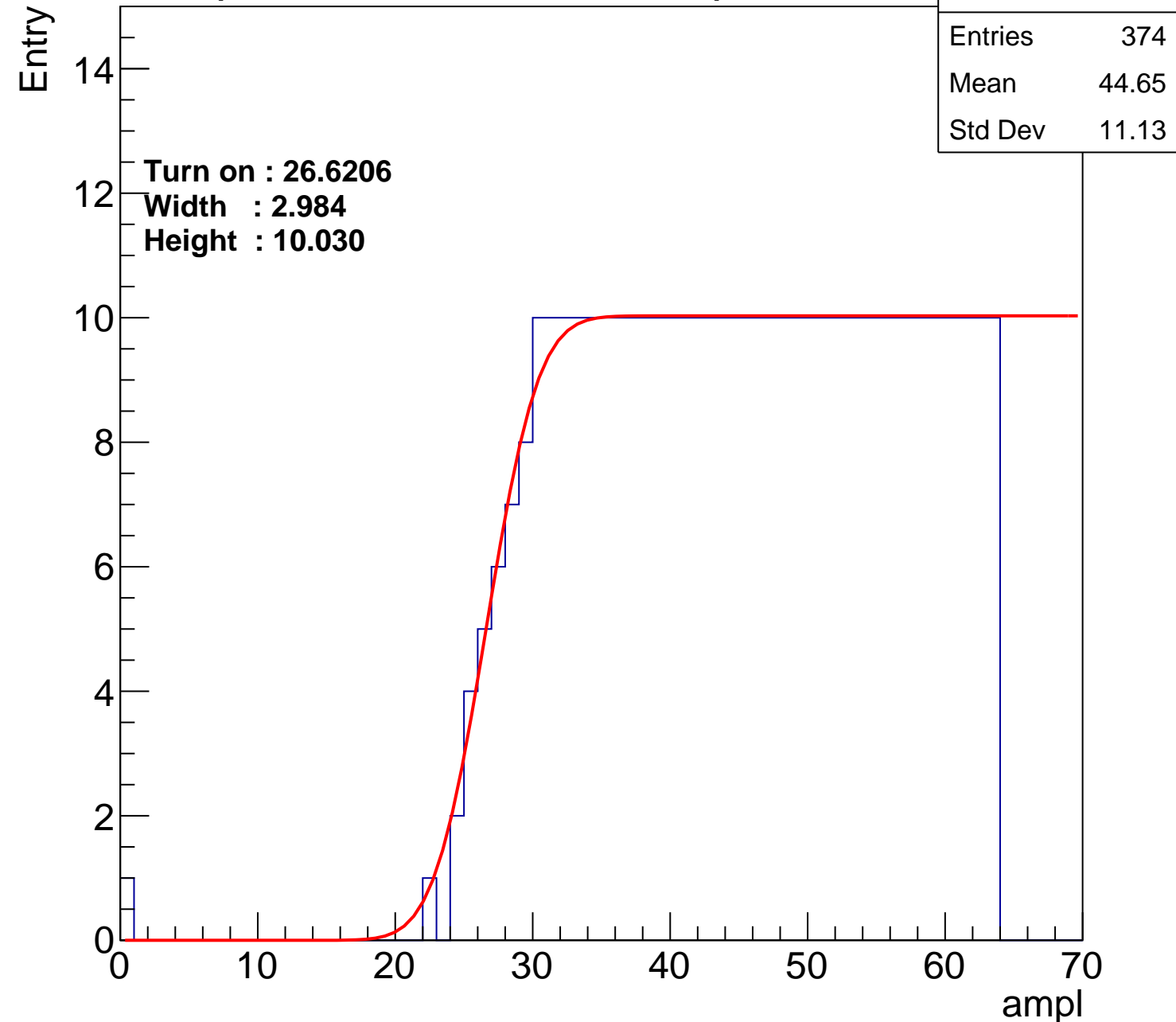
**Width : 2.984**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch13

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 26.3771

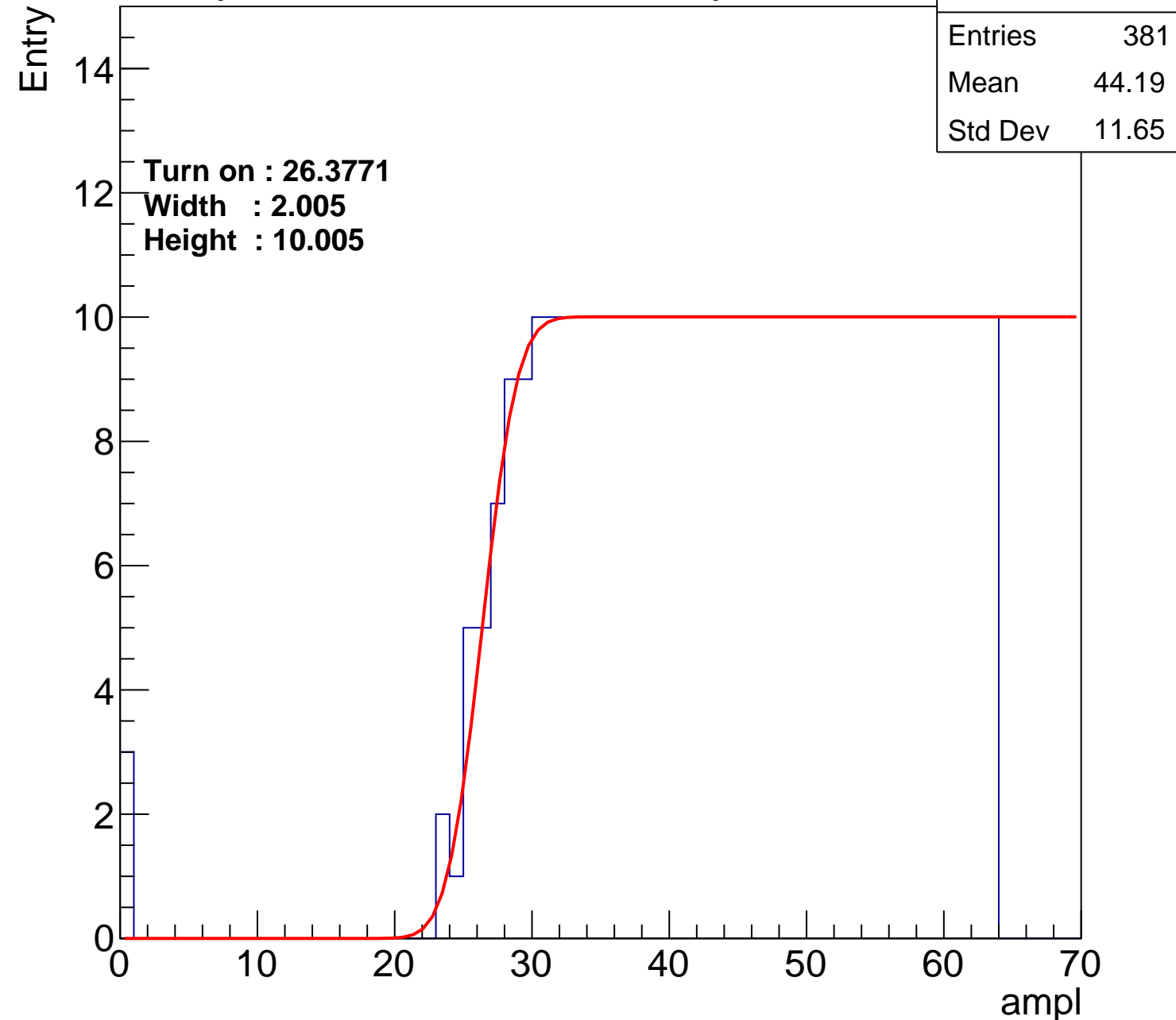
Width : 2.005

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch14

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.7623

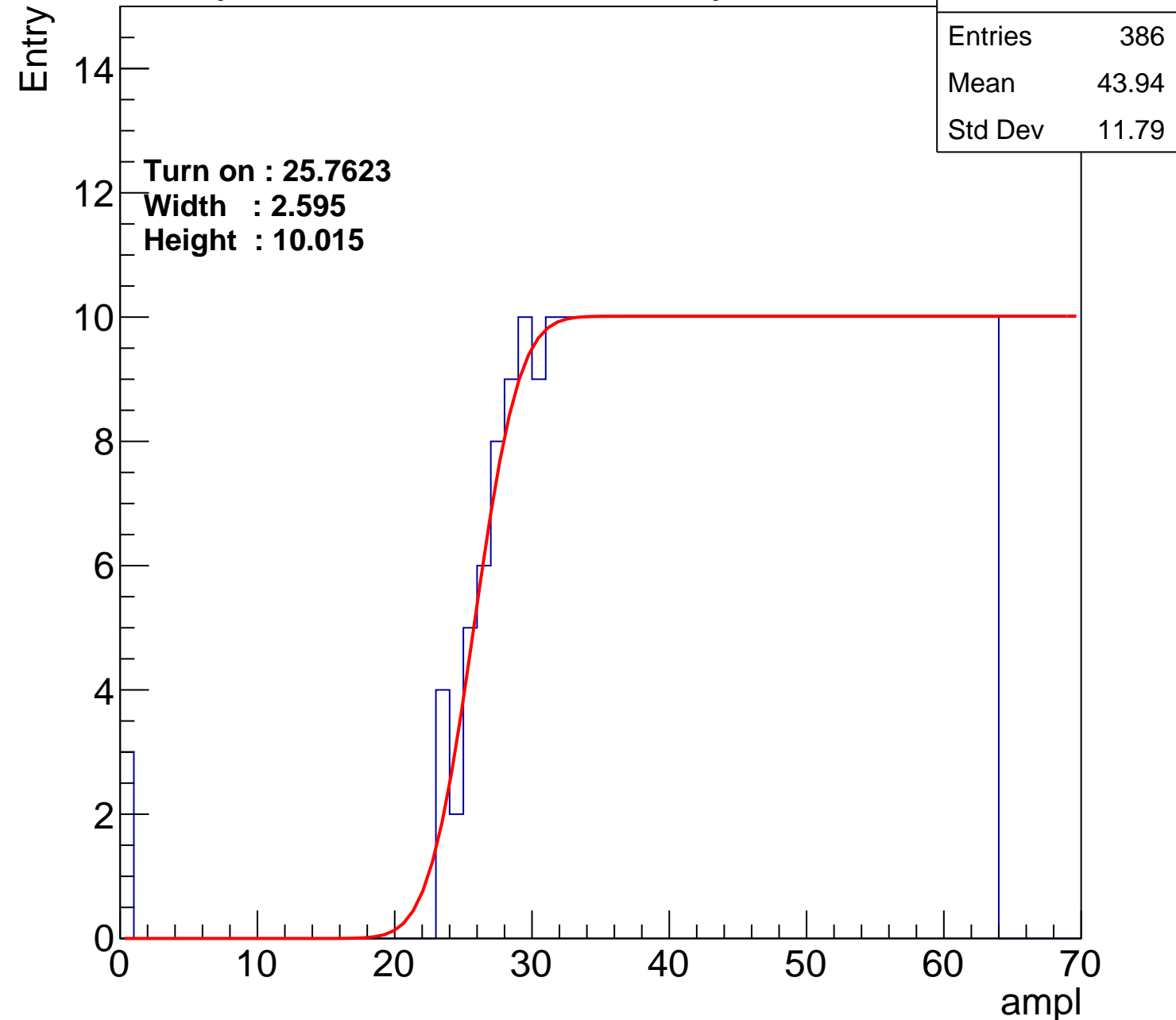
Width : 2.595

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch15

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	43.92
Std Dev	11.94

Turn on : 26.0204

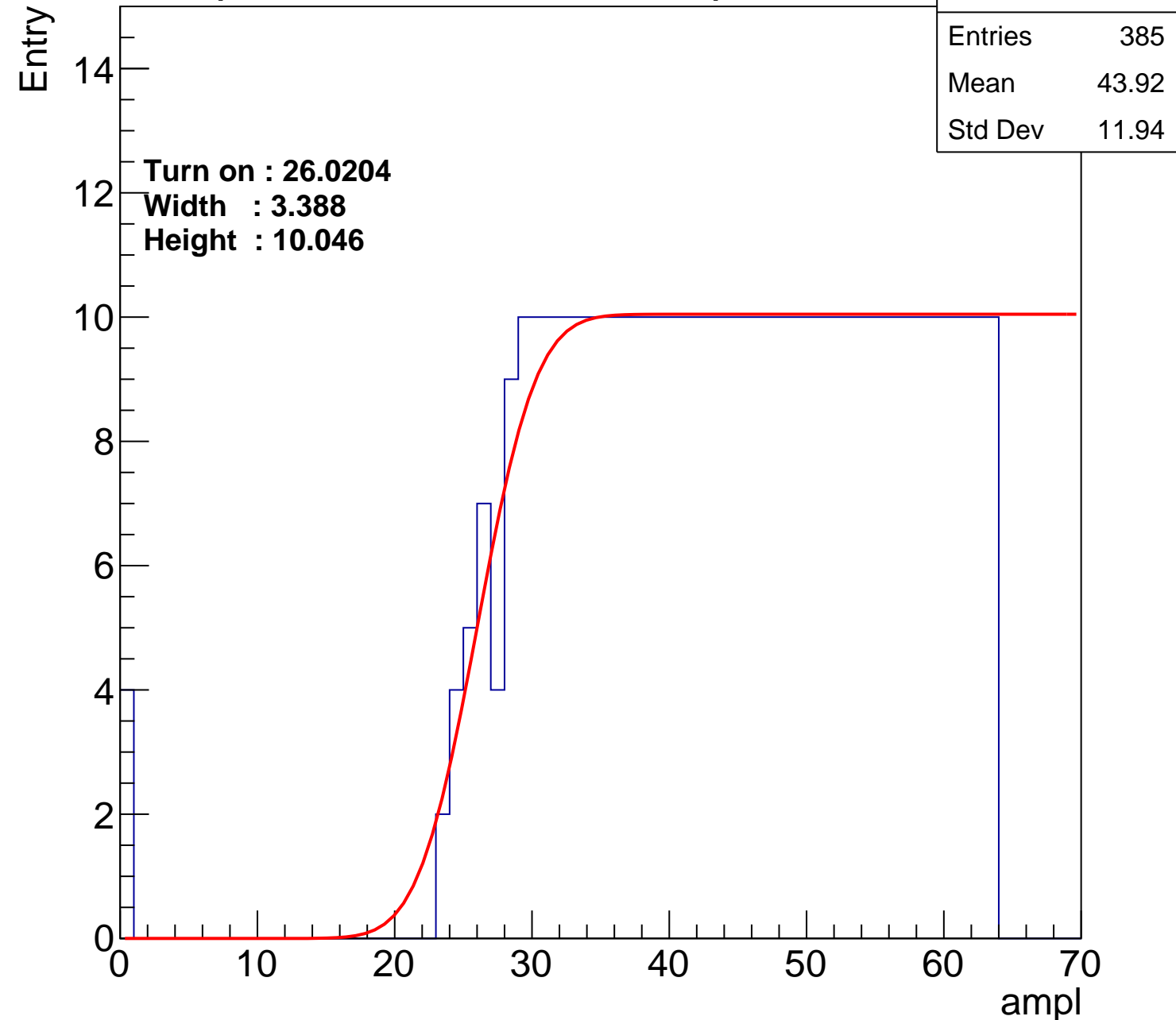
Width : 3.388

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch16

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.1841

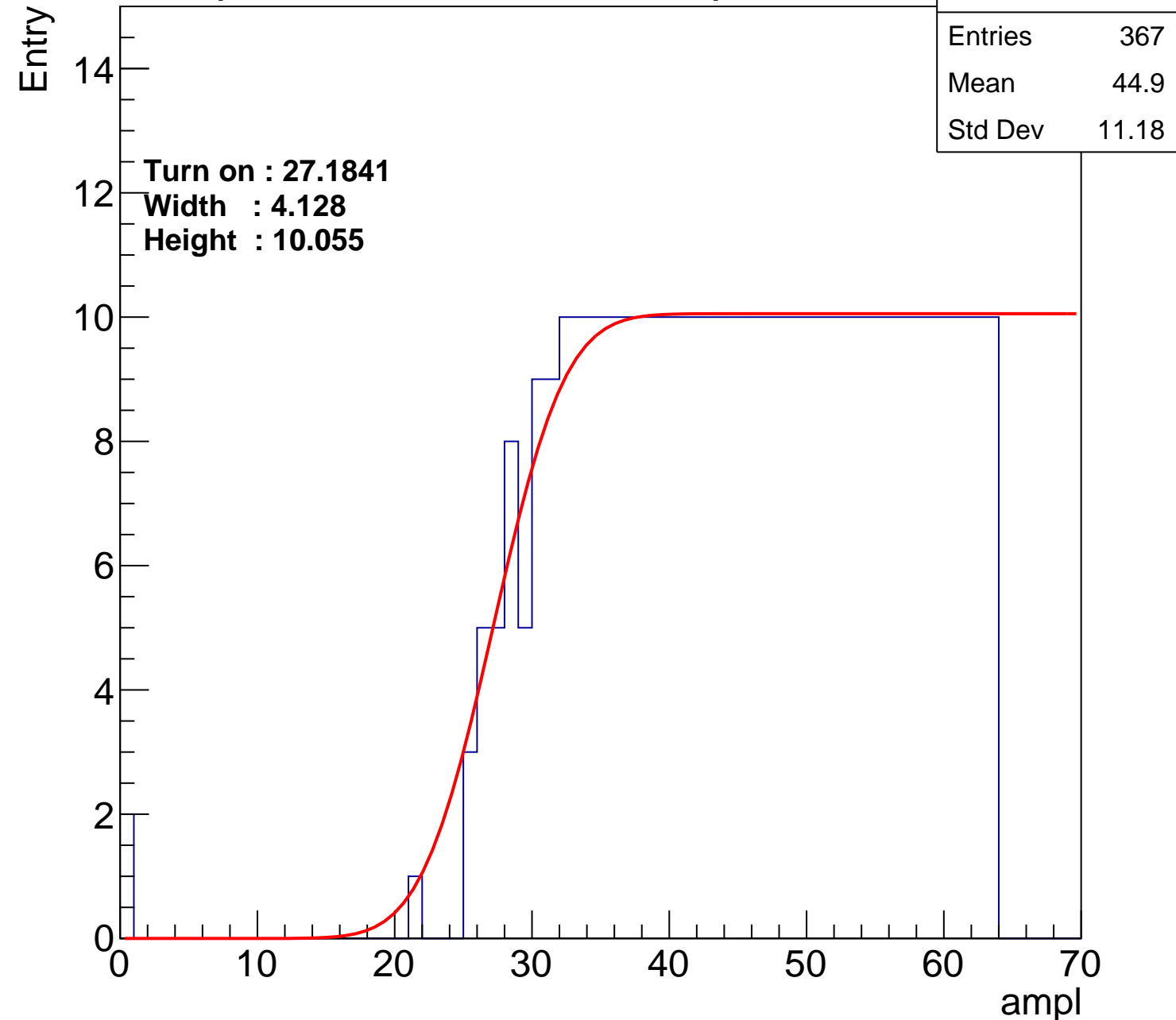
Width : 4.128

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch17

calib\_packv5\_042523\_0143.root, FC#10, port B3

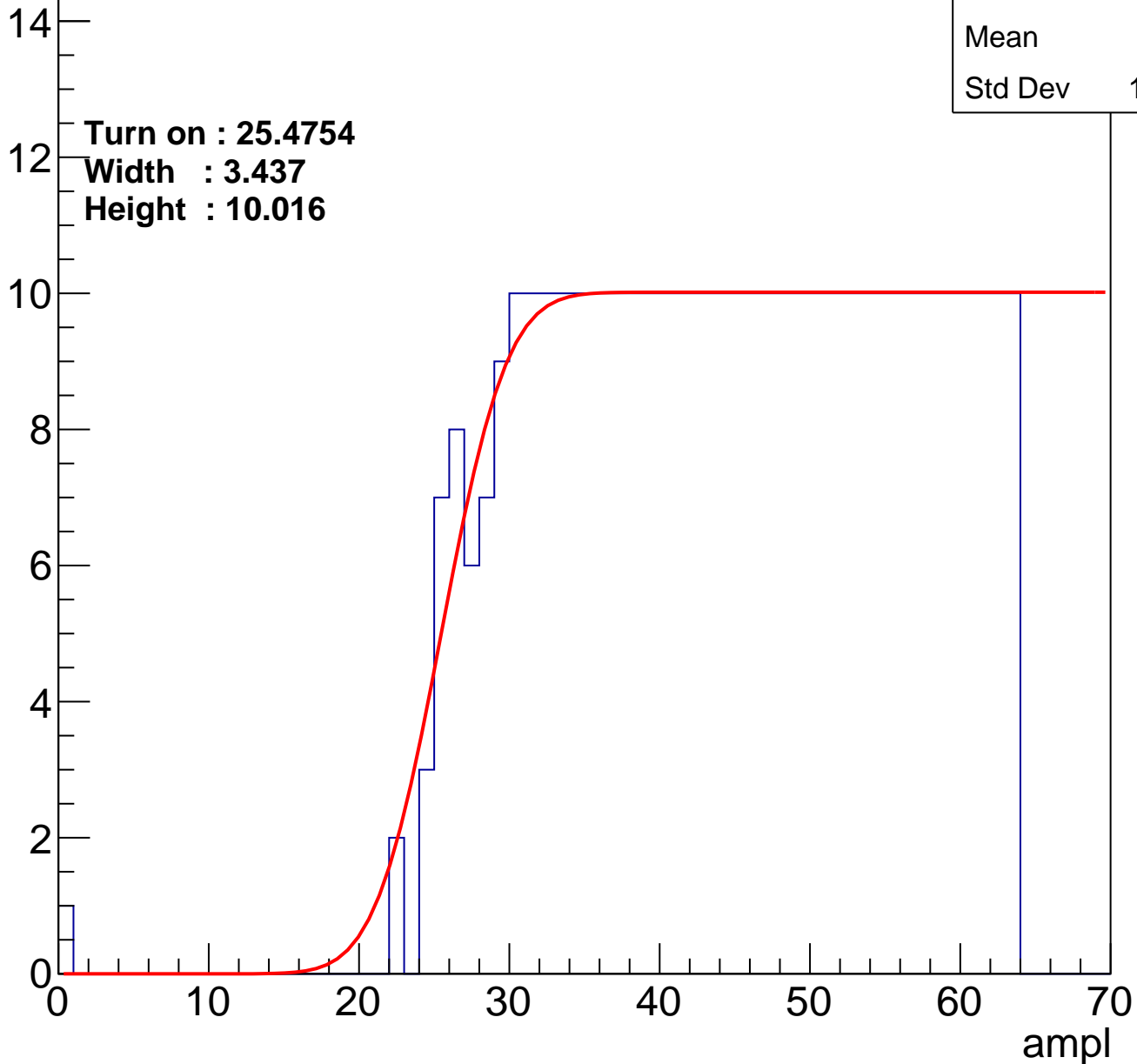
Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.4754

Width : 3.437

Height : 10.016

Entry



# B1L002S, U12-ch18

calib\_packv5\_042523\_0143.root, FC#10, port B3

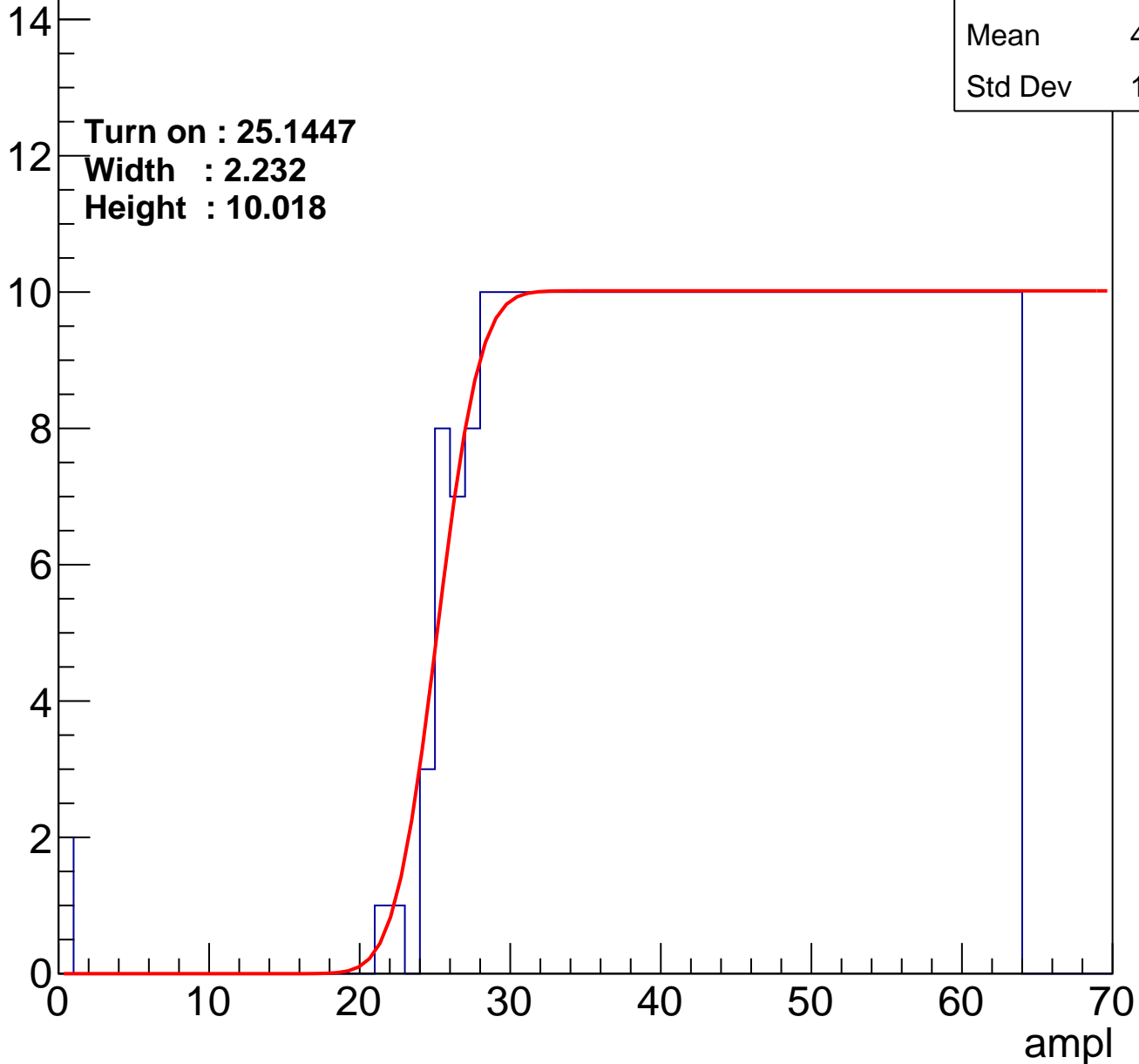
Entries	390
Mean	43.83
Std Dev	11.68

Turn on : 25.1447

Width : 2.232

Height : 10.018

Entry



# B1L002S, U12-ch19

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 24.6561

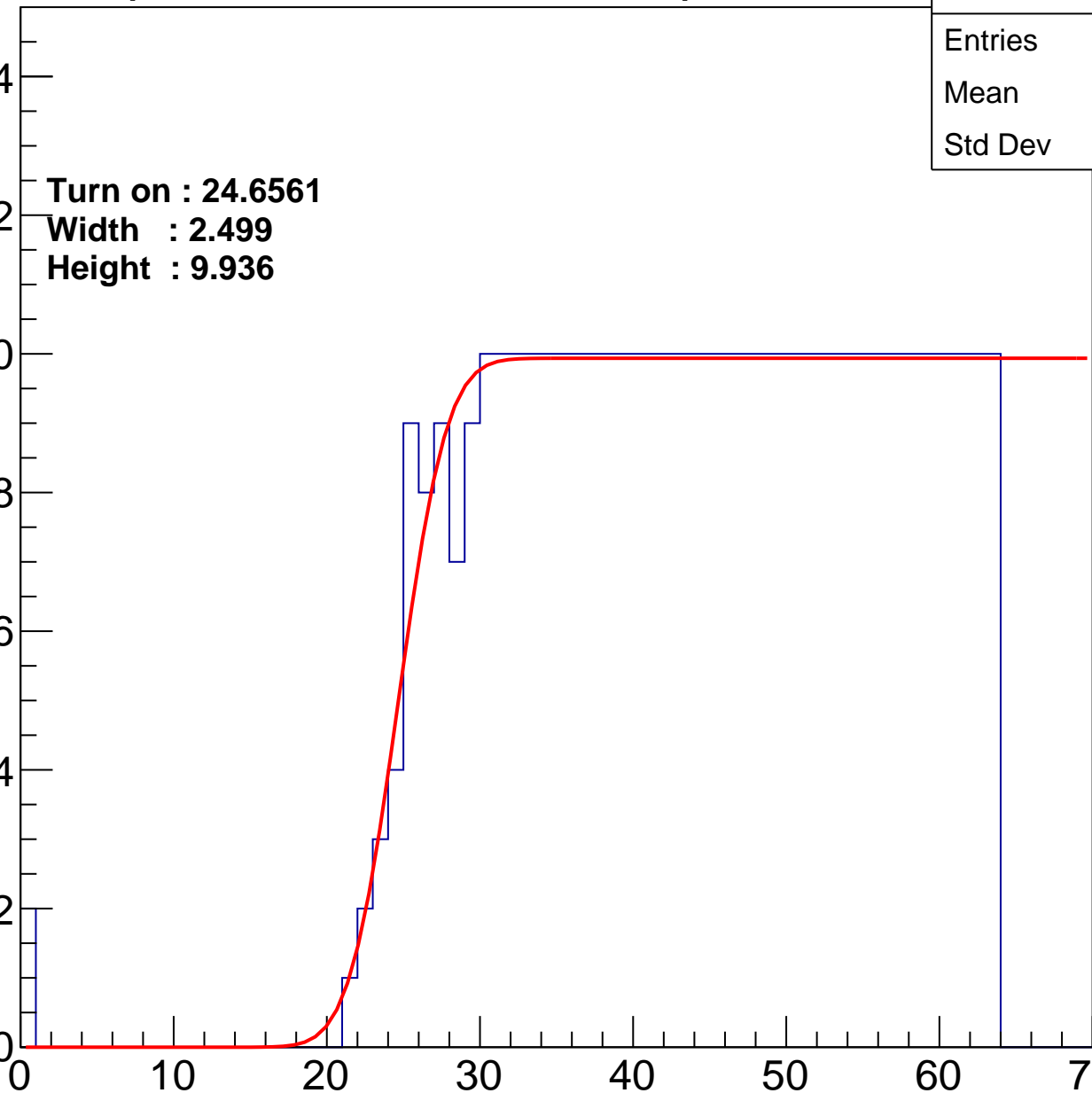
Width : 2.499

Height : 9.936

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch20

calib\_packv5\_042523\_0143.root, FC#10, port B3

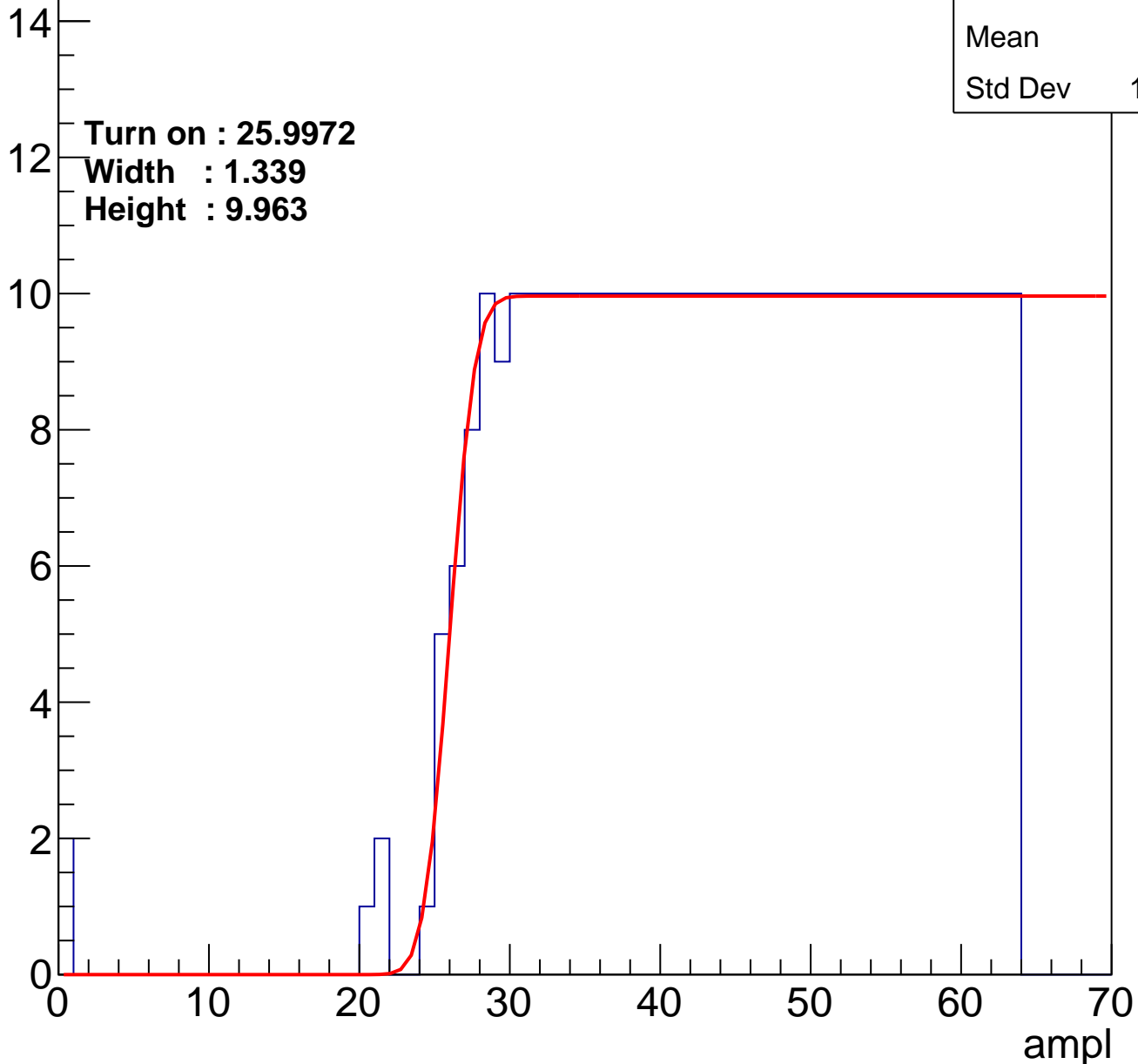
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.9972

Width : 1.339

Height : 9.963

Entry



# B1L002S, U12-ch21

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 25.5682

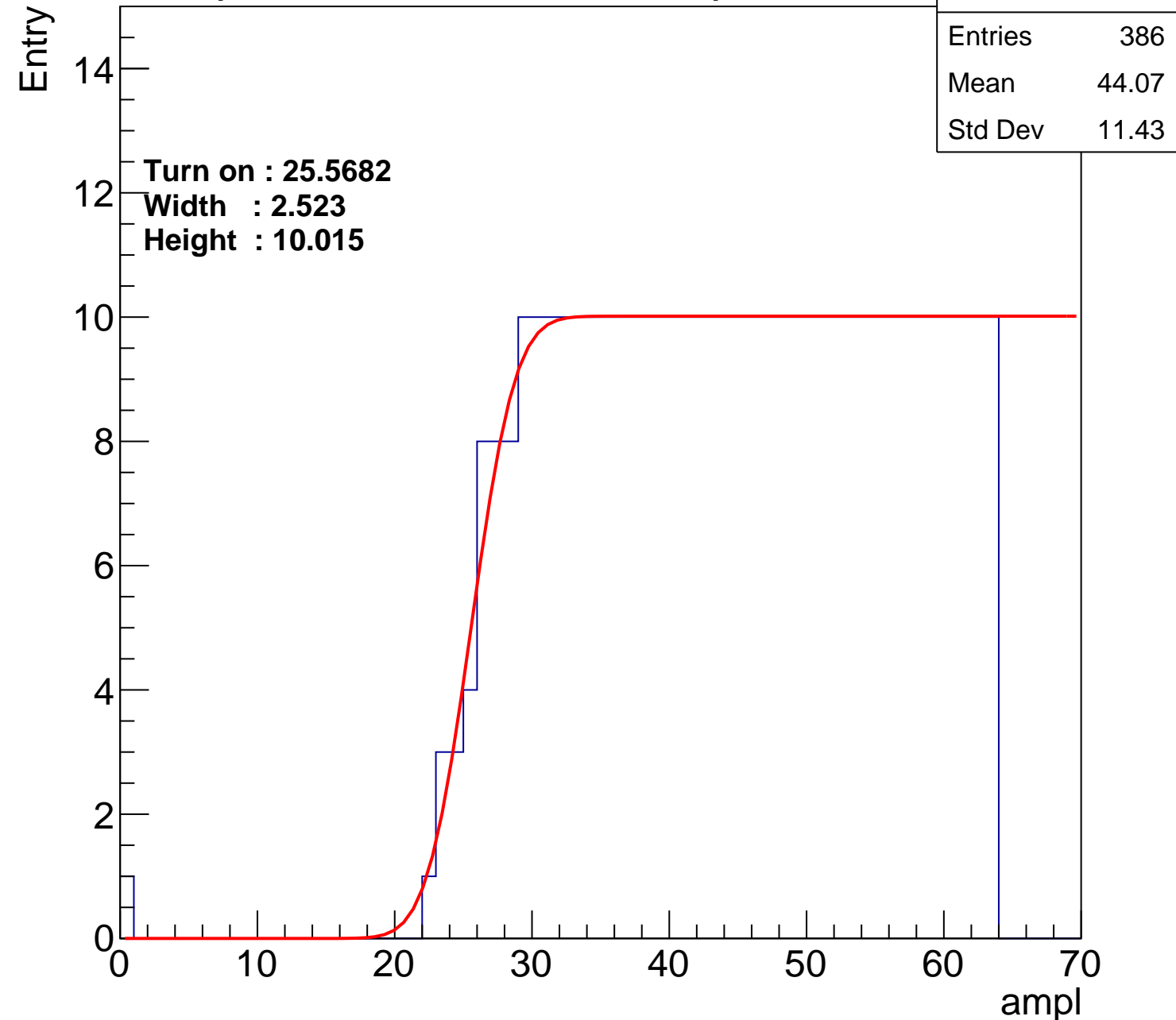
Width : 2.523

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch22

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.67

Turn on : 25.8212

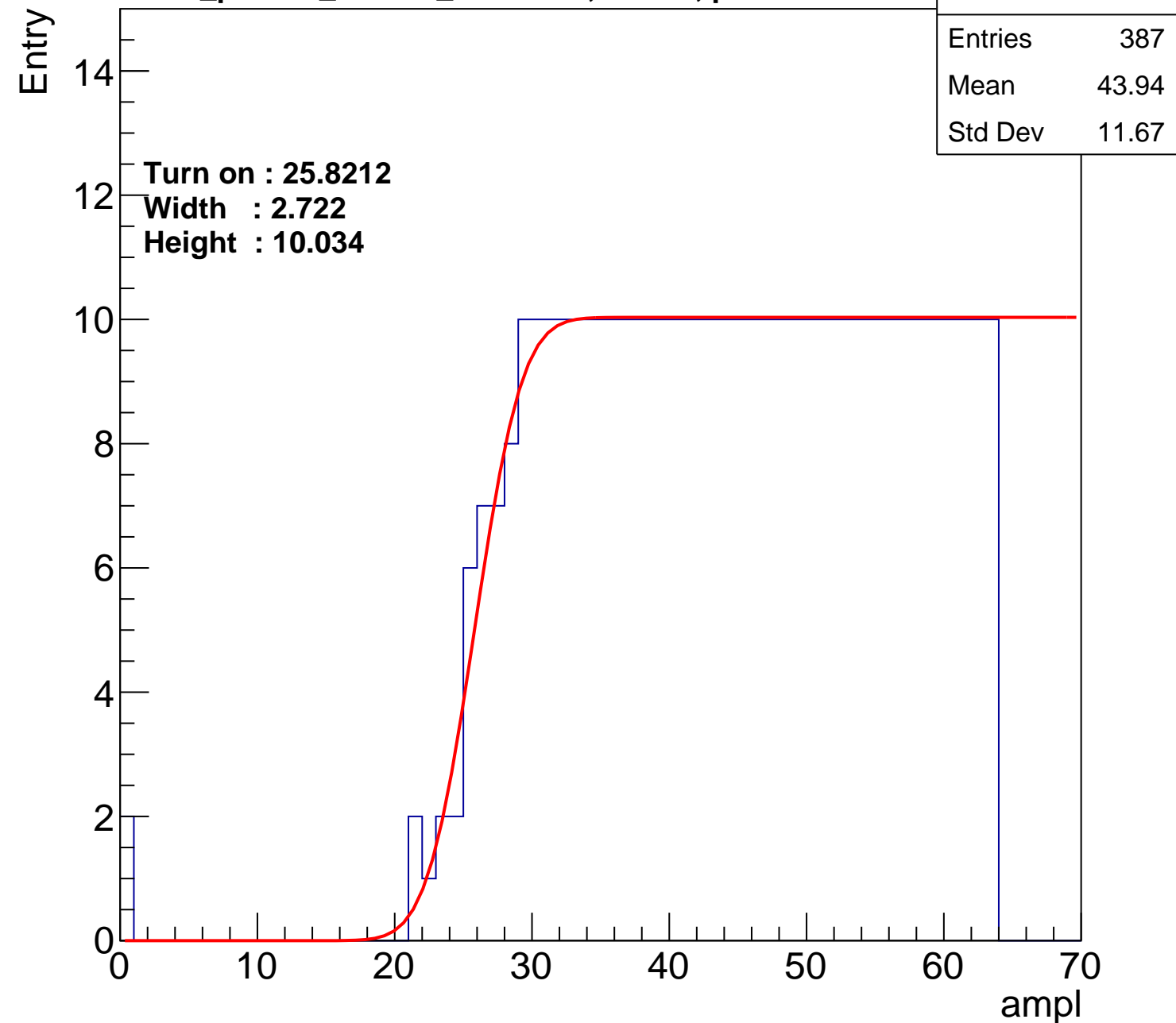
Width : 2.722

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch23

calib\_packv5\_042523\_0143.root, FC#10, port B3

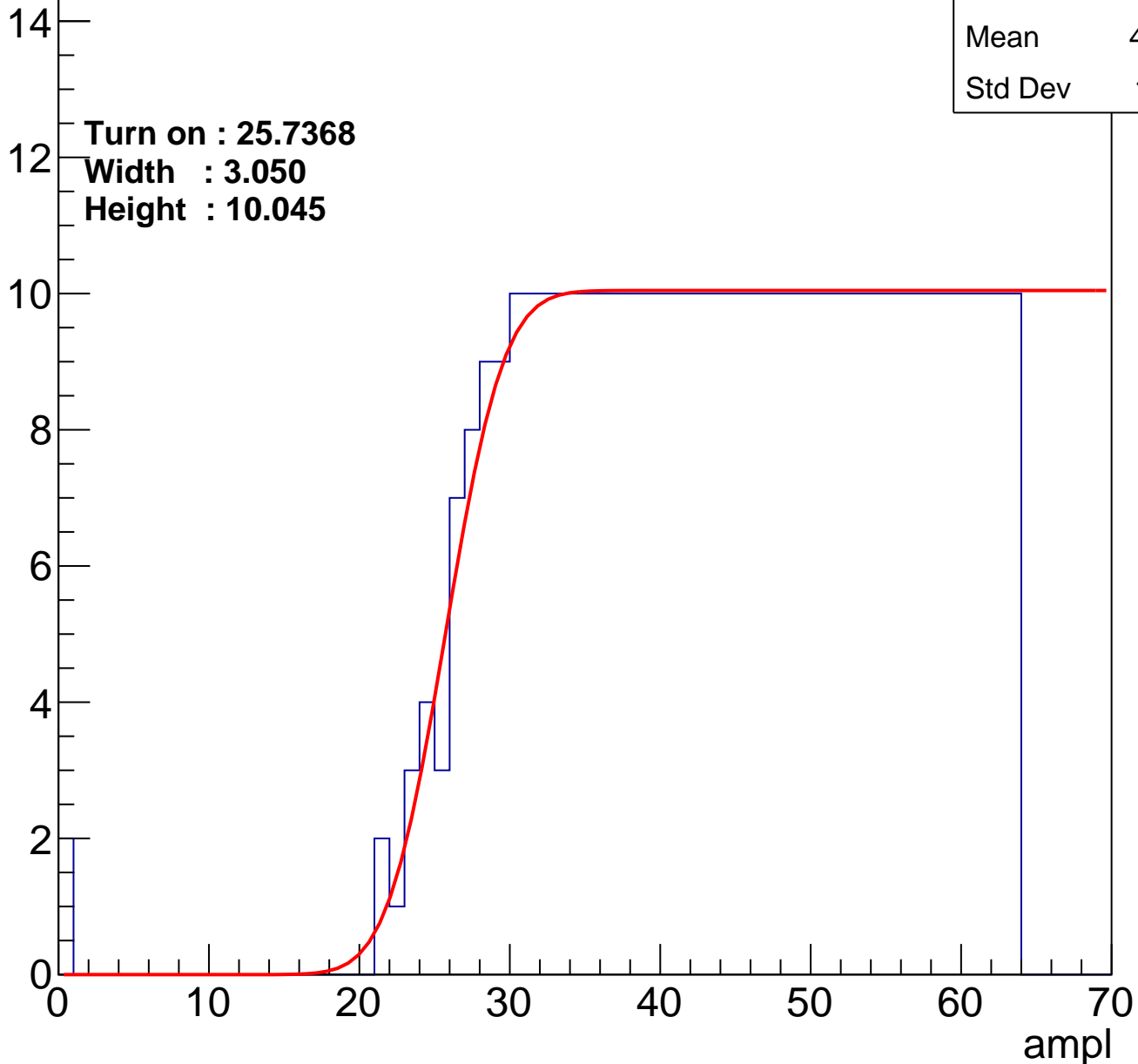
Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.7368

Width : 3.050

Height : 10.045

Entry



# B1L002S, U12-ch24

calib\_packv5\_042523\_0143.root, FC#10, port B3

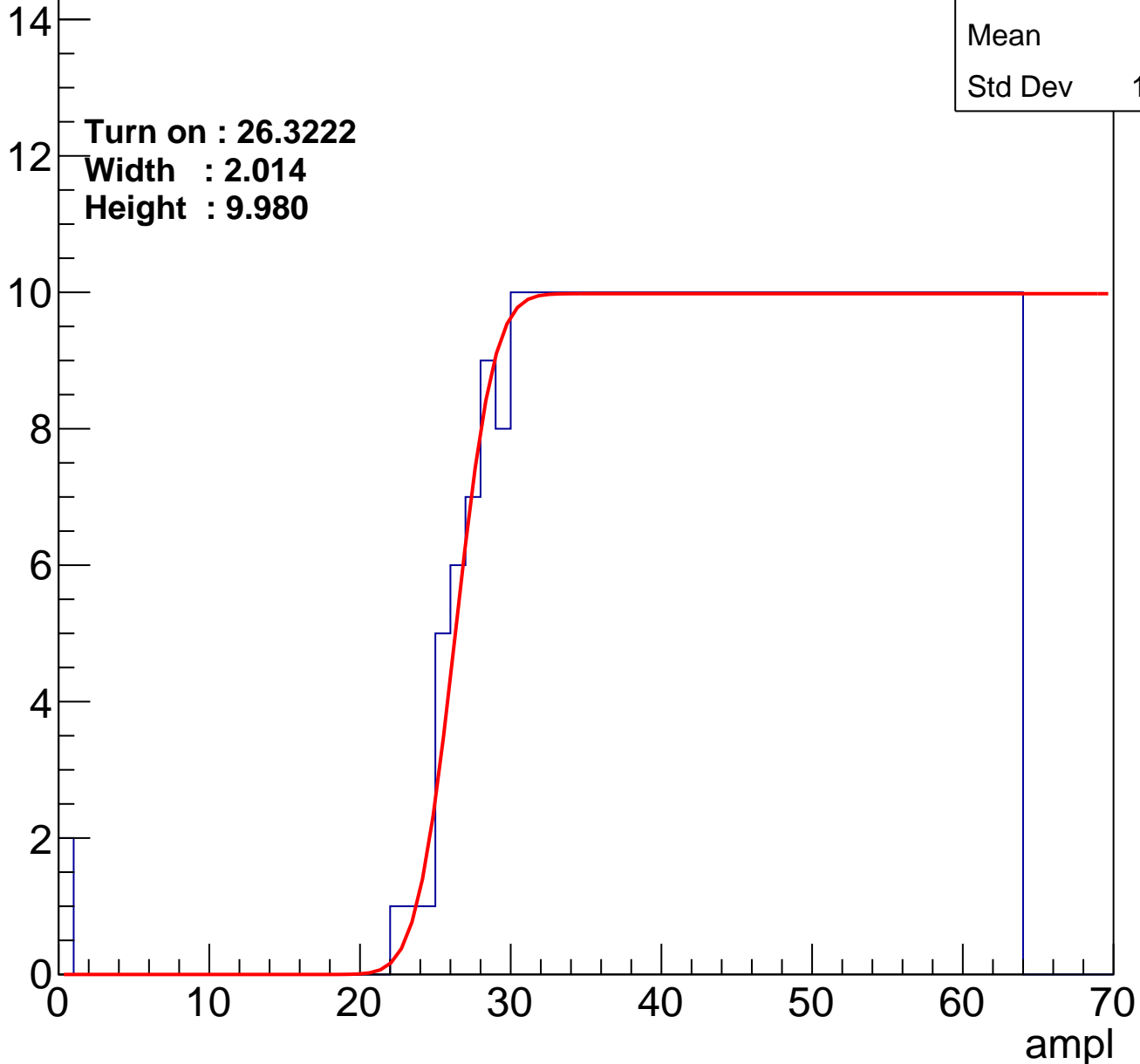
Entries	380
Mean	44.3
Std Dev	11.46

Turn on : 26.3222

Width : 2.014

Height : 9.980

Entry



# B1L002S, U12-ch25

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.44
Std Dev	11.27

Turn on : 26.5267

Width : 3.429

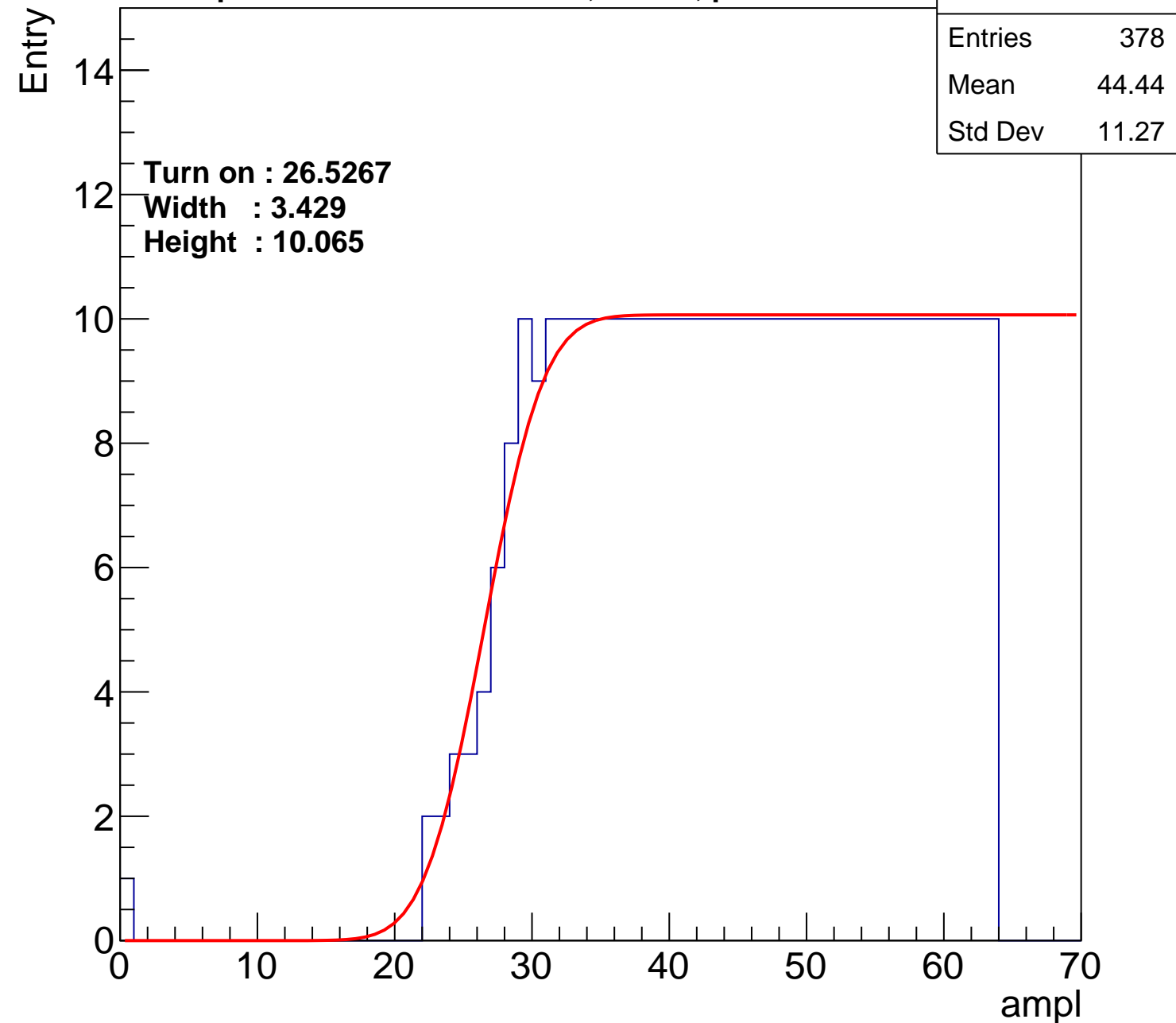
Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U12-ch26

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.88
Std Dev	10.97

Turn on : 26.8953

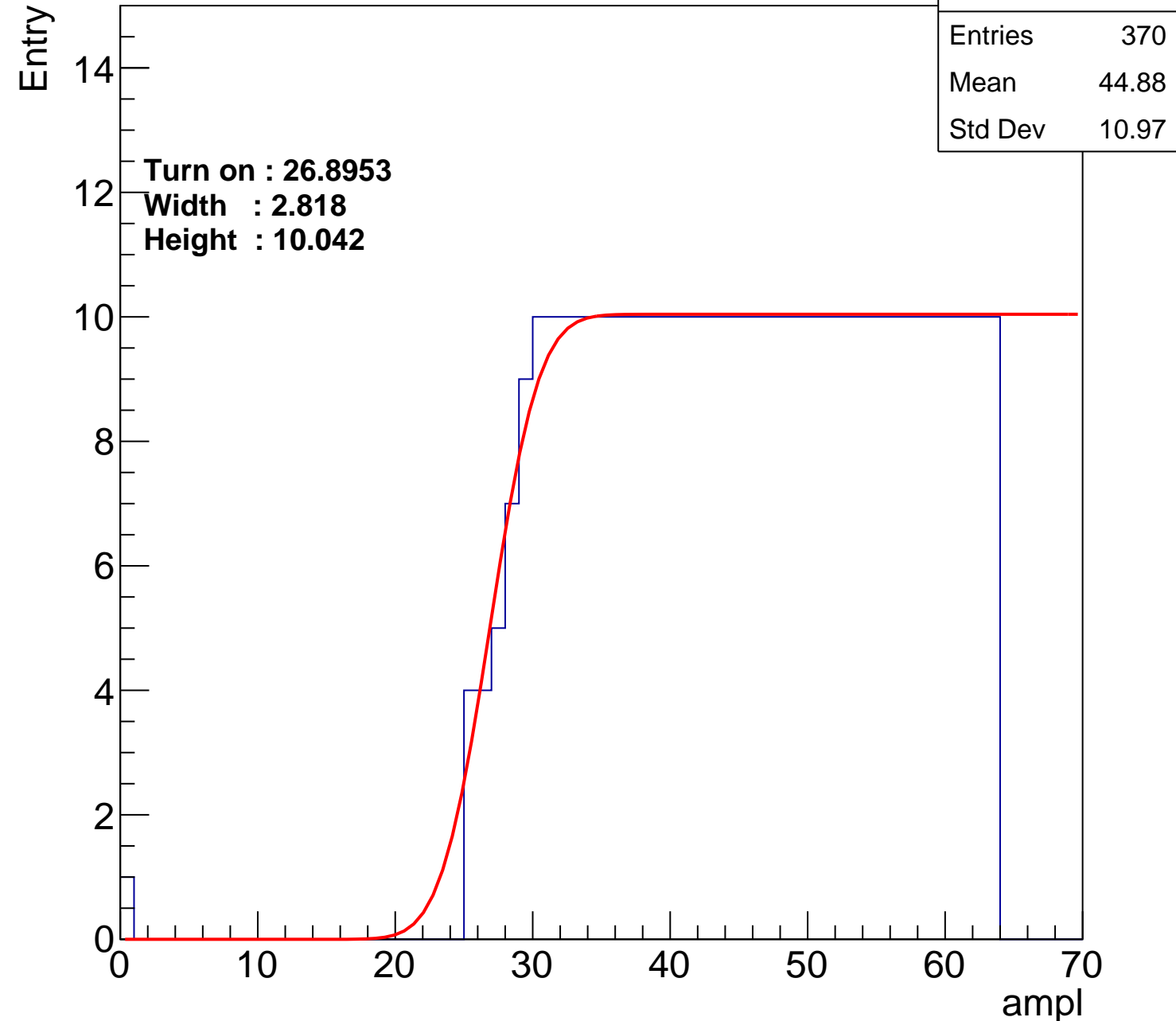
Width : 2.818

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch27

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.72

Turn on : 24.5064

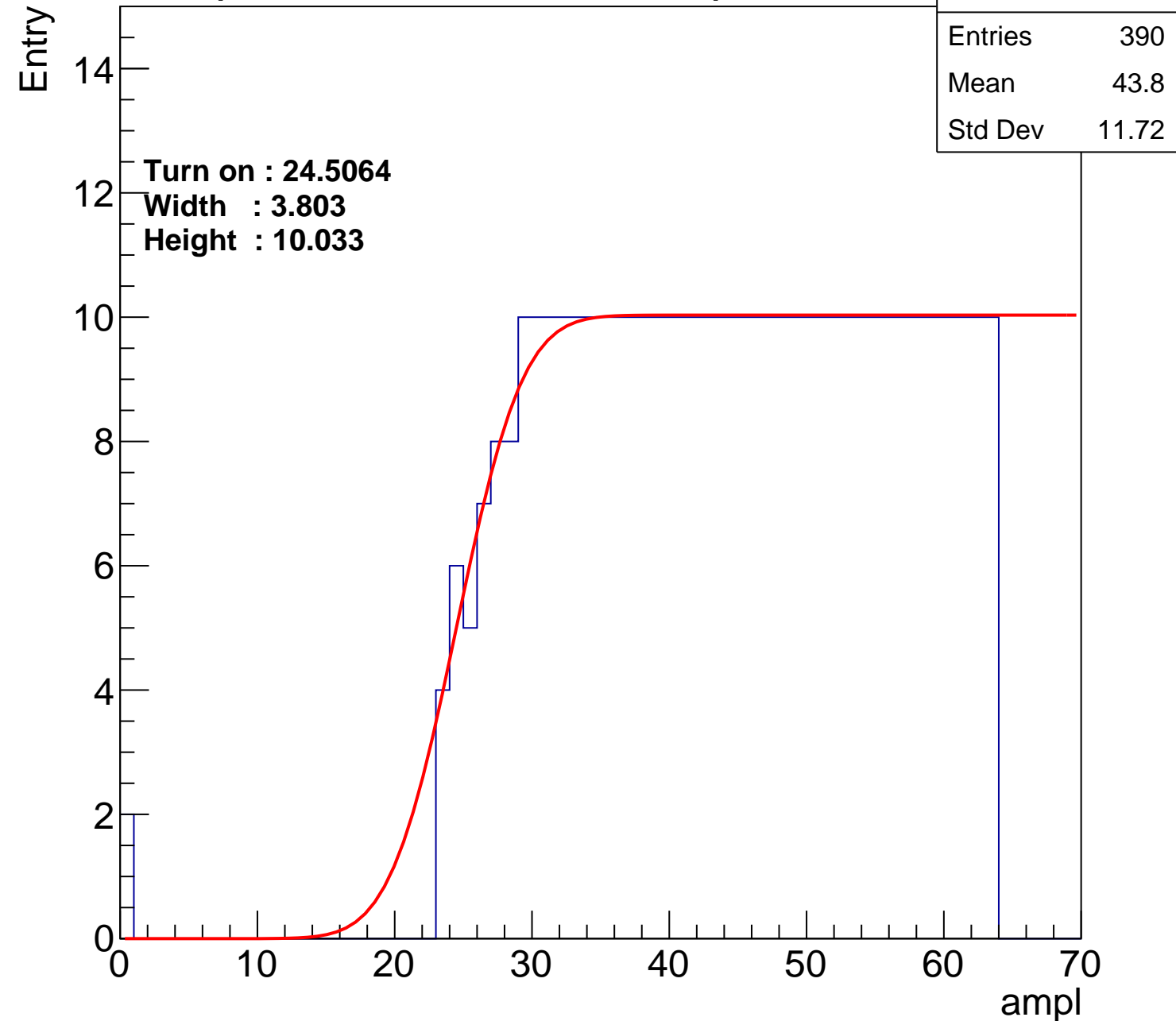
Width : 3.803

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch28

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.54
Std Dev	12.12

Turn on : 25.3767

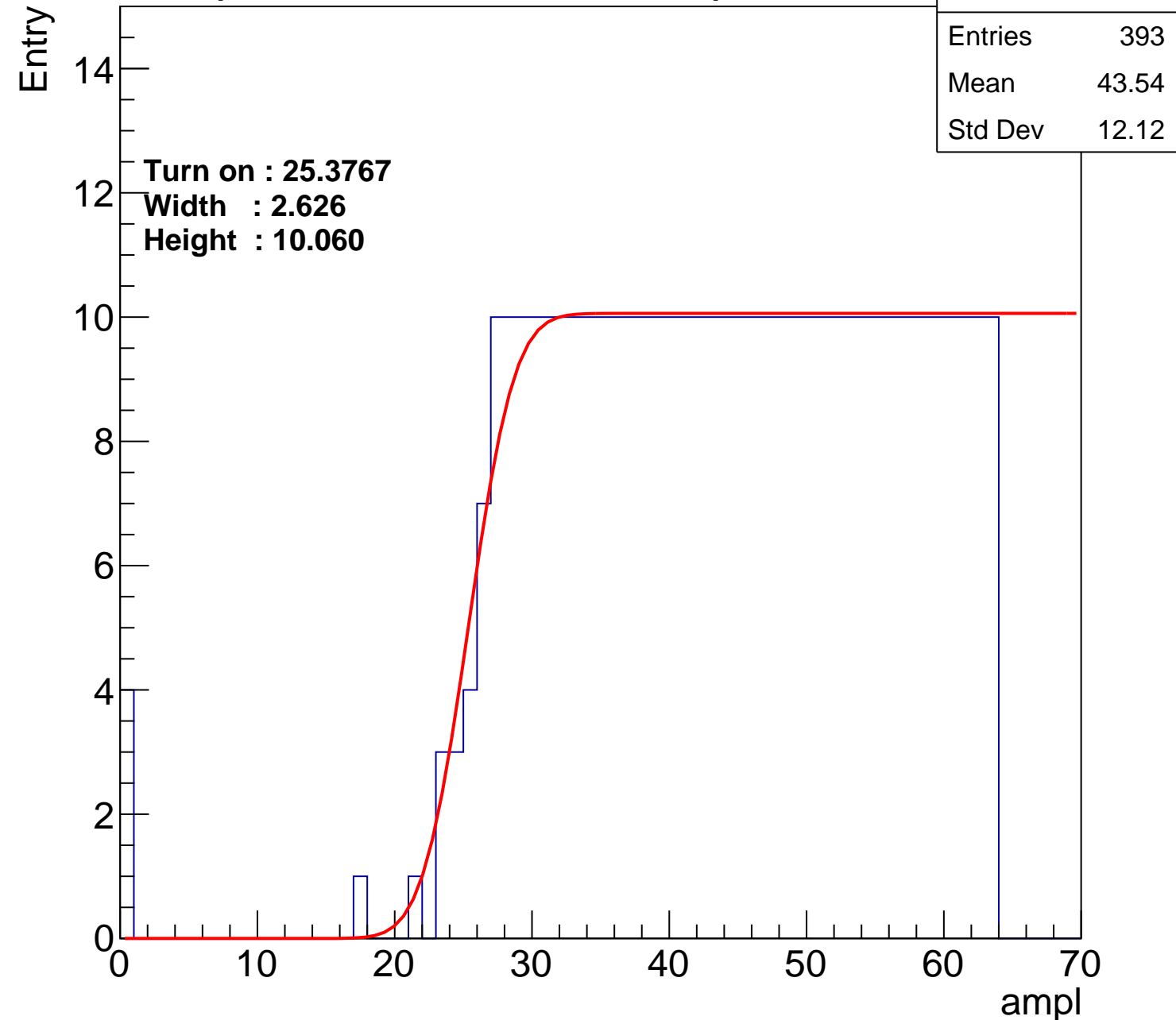
Width : 2.626

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch29

calib\_packv5\_042523\_0143.root, FC#10, port B3

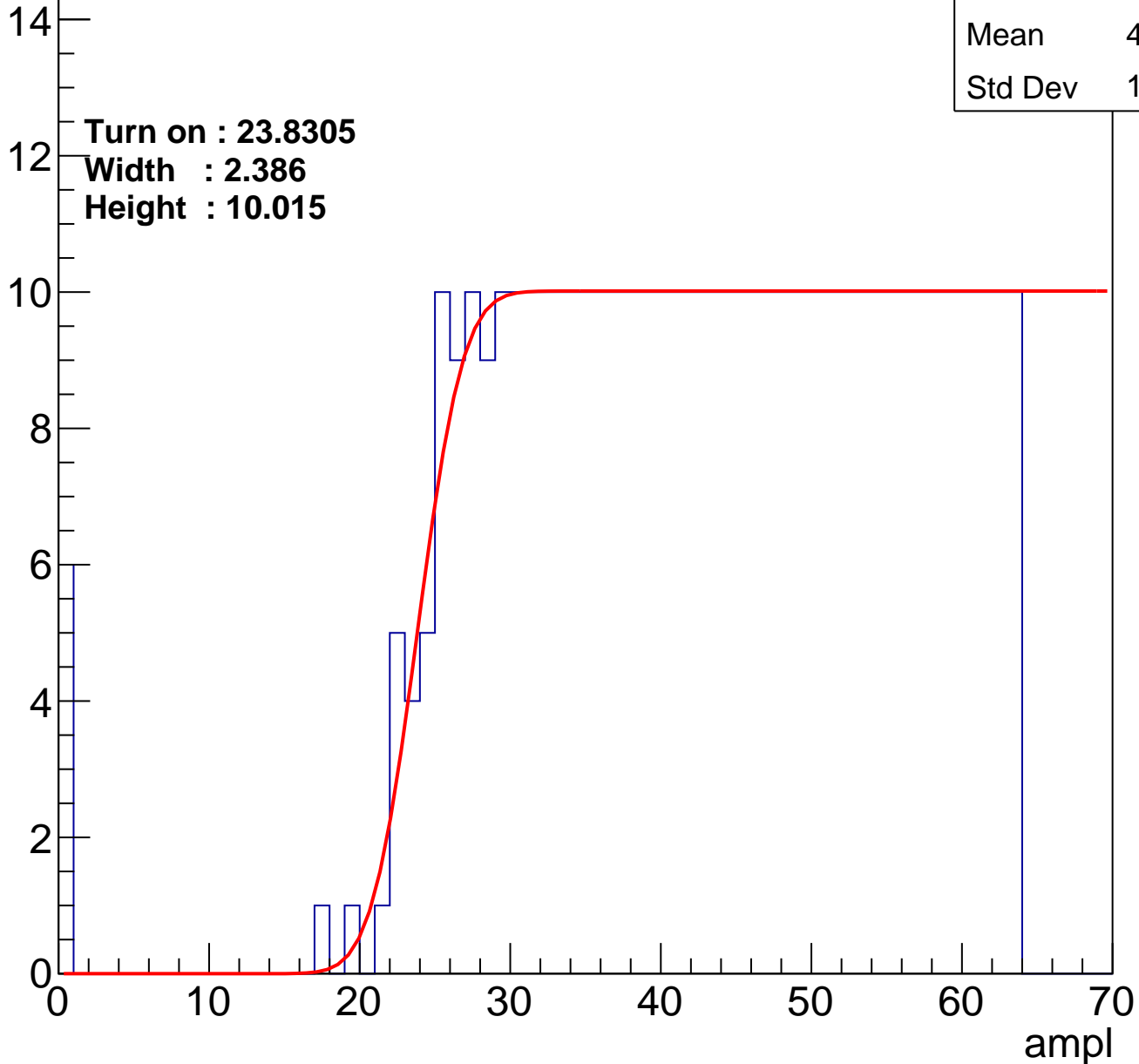
Entries	411
Mean	42.54
Std Dev	12.83

**Turn on : 23.8305**

**Width : 2.386**

**Height : 10.015**

Entry



# B1L002S, U12-ch30

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	399
Mean	43.27
Std Dev	12.15

Turn on : 24.6937

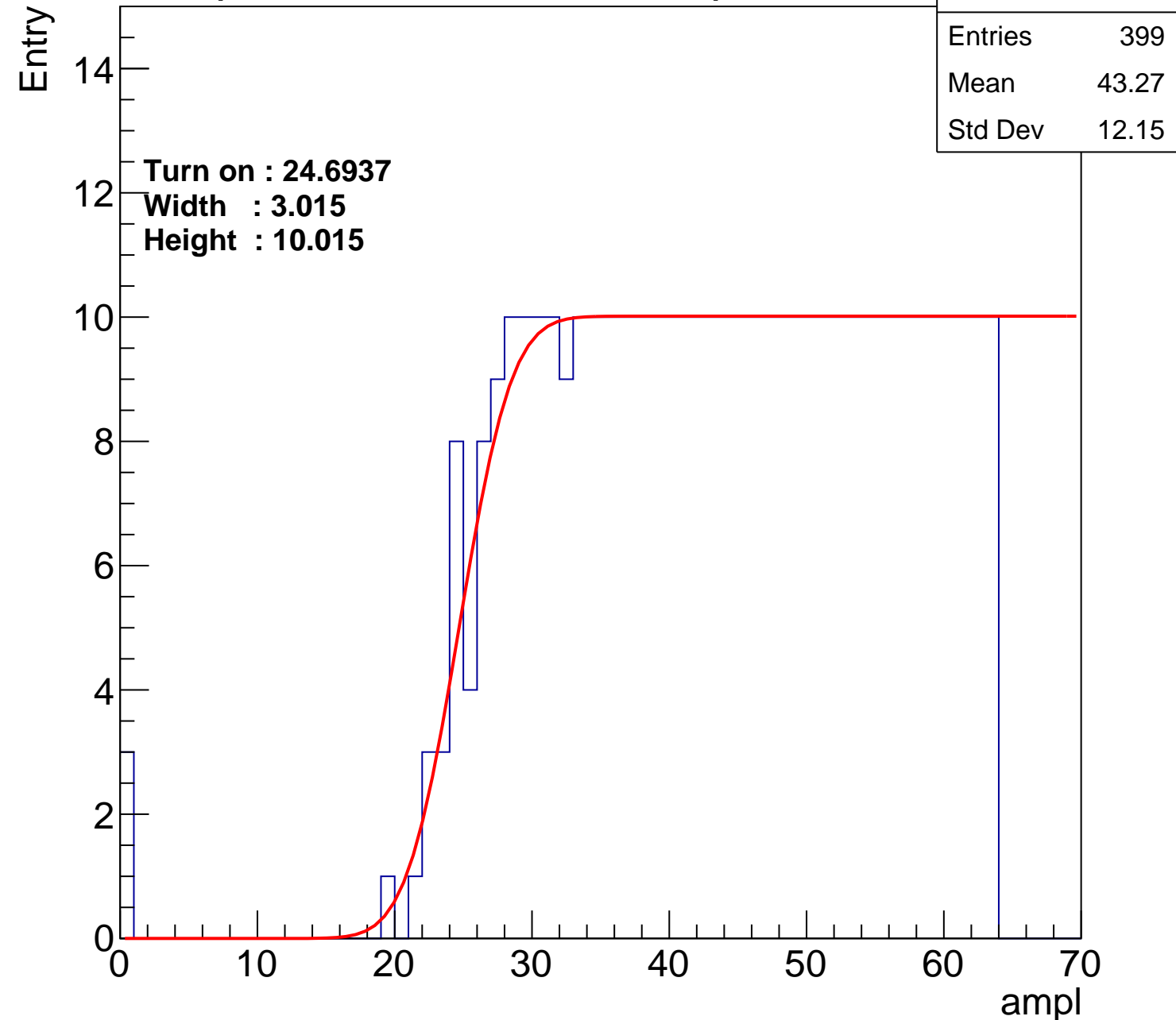
Width : 3.015

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch31

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.0243

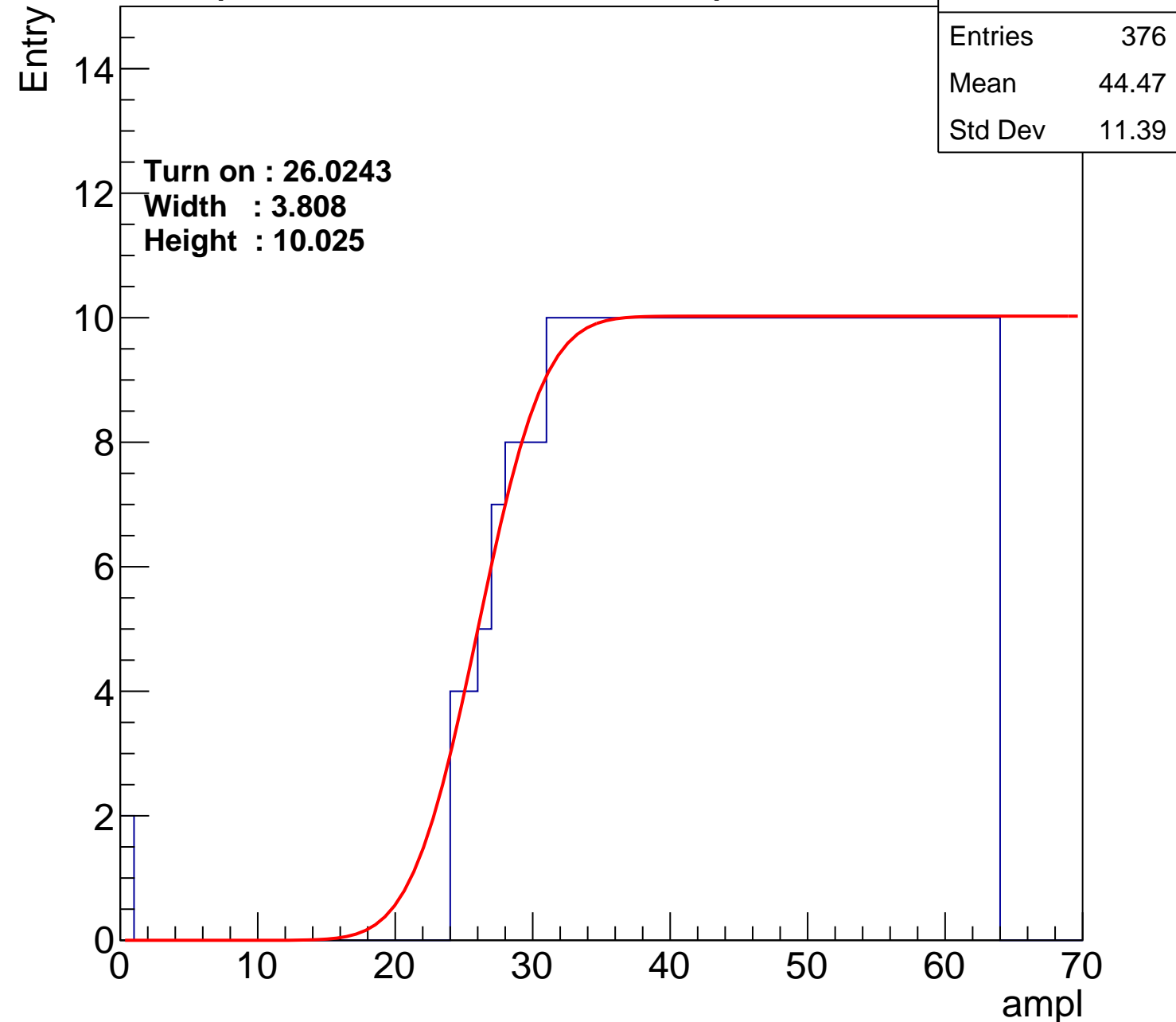
Width : 3.808

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch32

calib\_packv5\_042523\_0143.root, FC#10, port B3

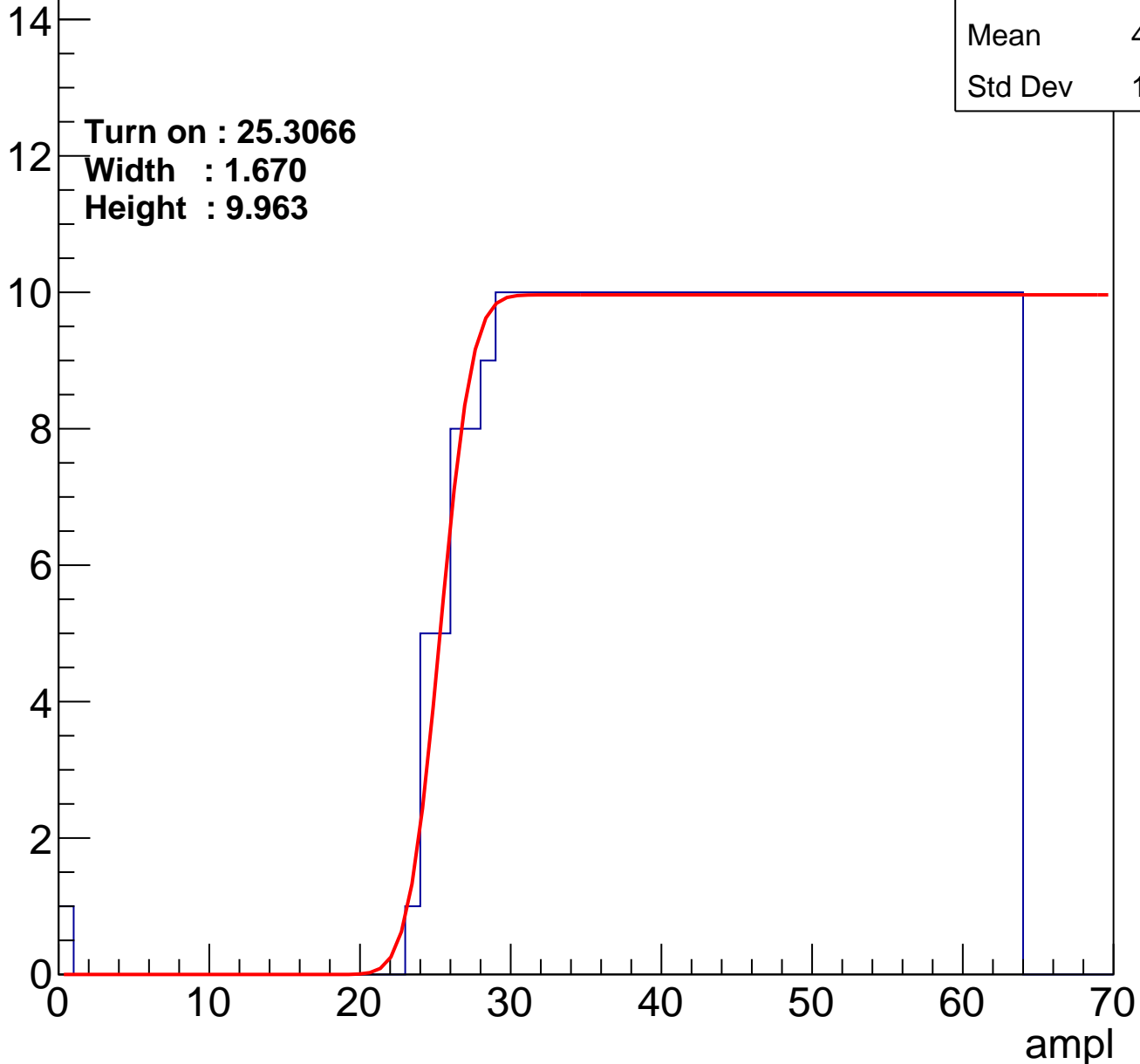
Entries	387
Mean	44.04
Std Dev	11.42

Turn on : 25.3066

Width : 1.670

Height : 9.963

Entry



# B1L002S, U12-ch33

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	406
Mean	42.88
Std Dev	12.45

Turn on : 24.0192

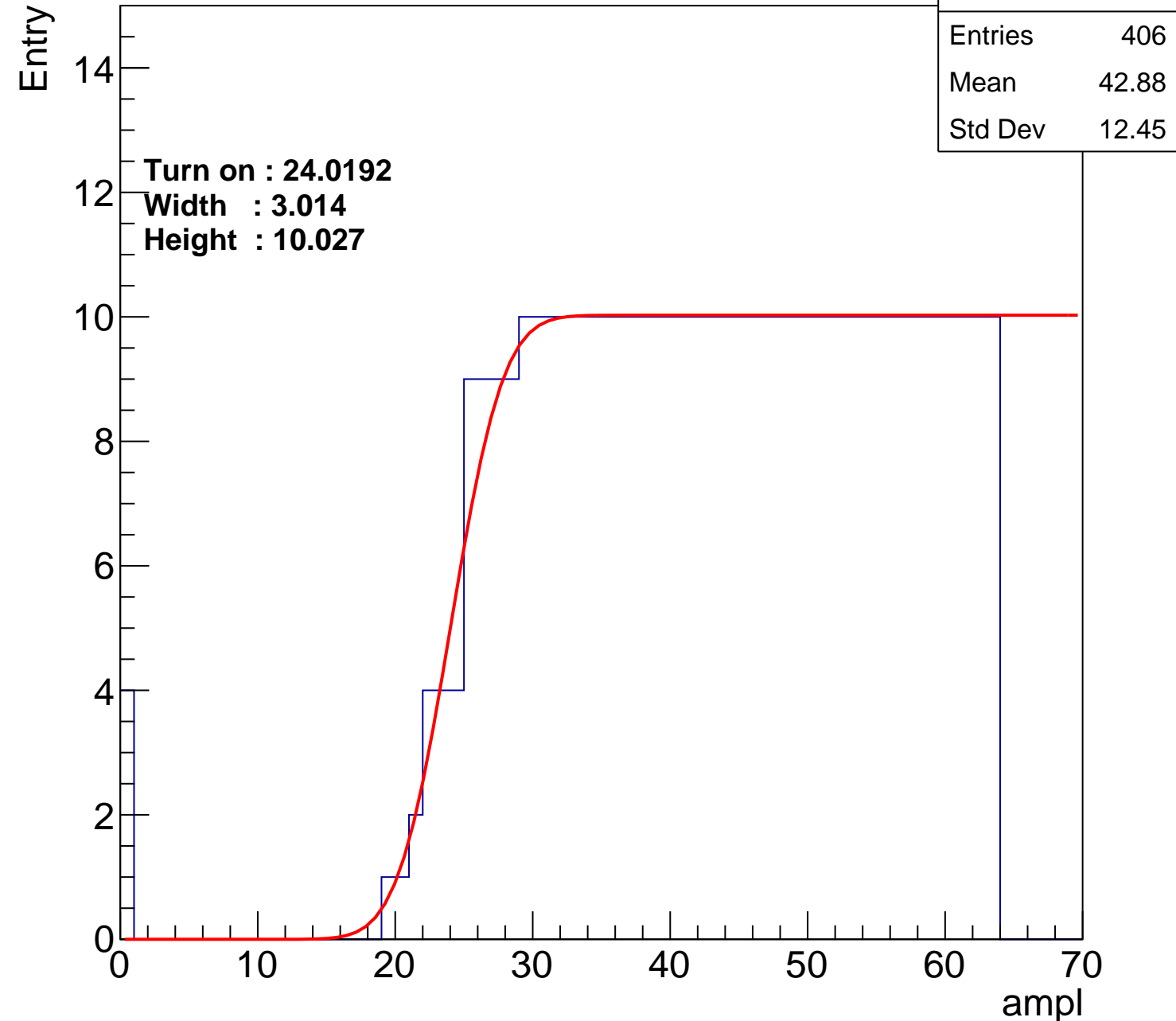
Width : 3.014

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch34

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.94
Std Dev	11.53

Turn on : 25.1264

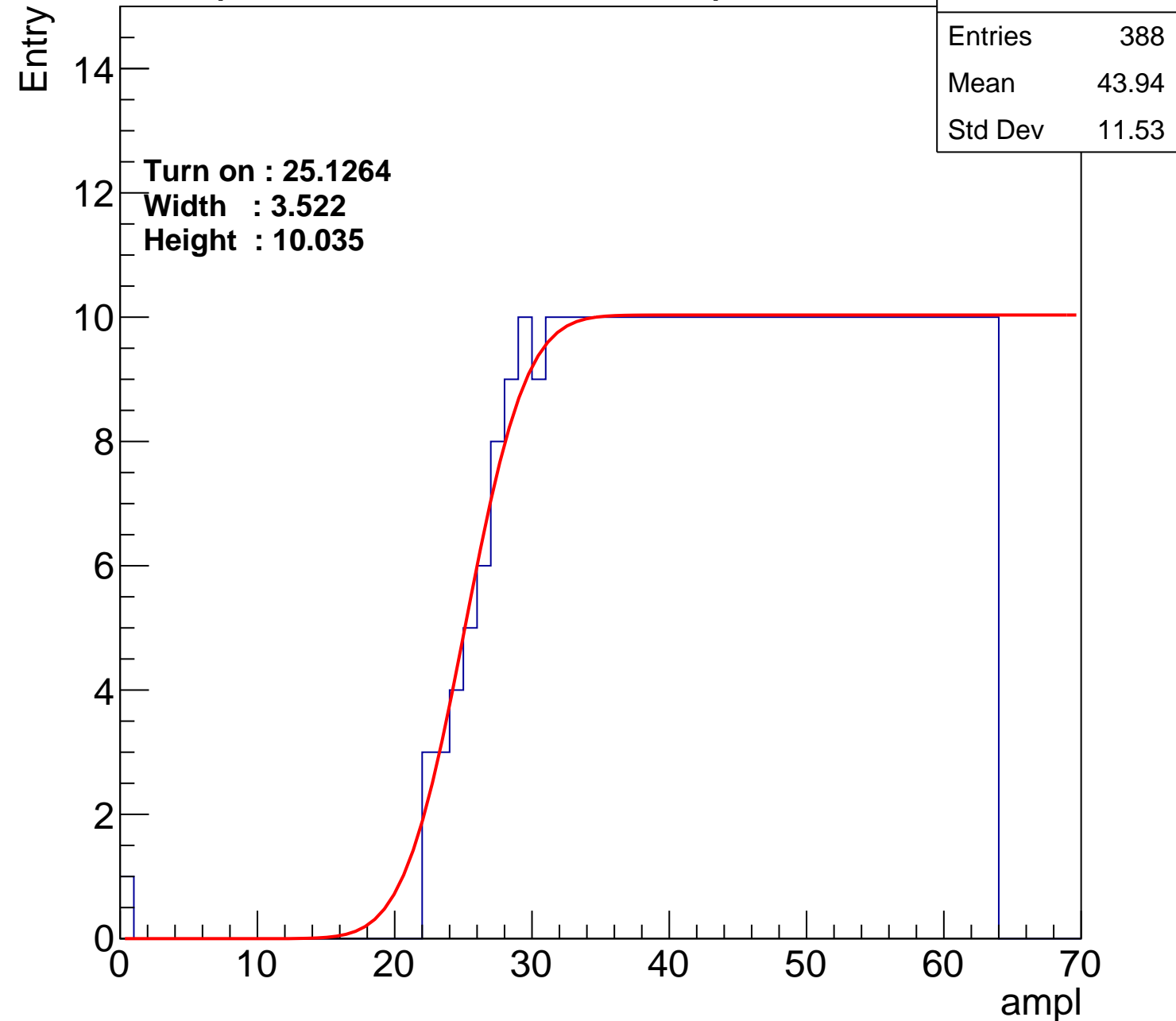
Width : 3.522

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch35

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.34
Std Dev	11.75

Turn on : 26.9402

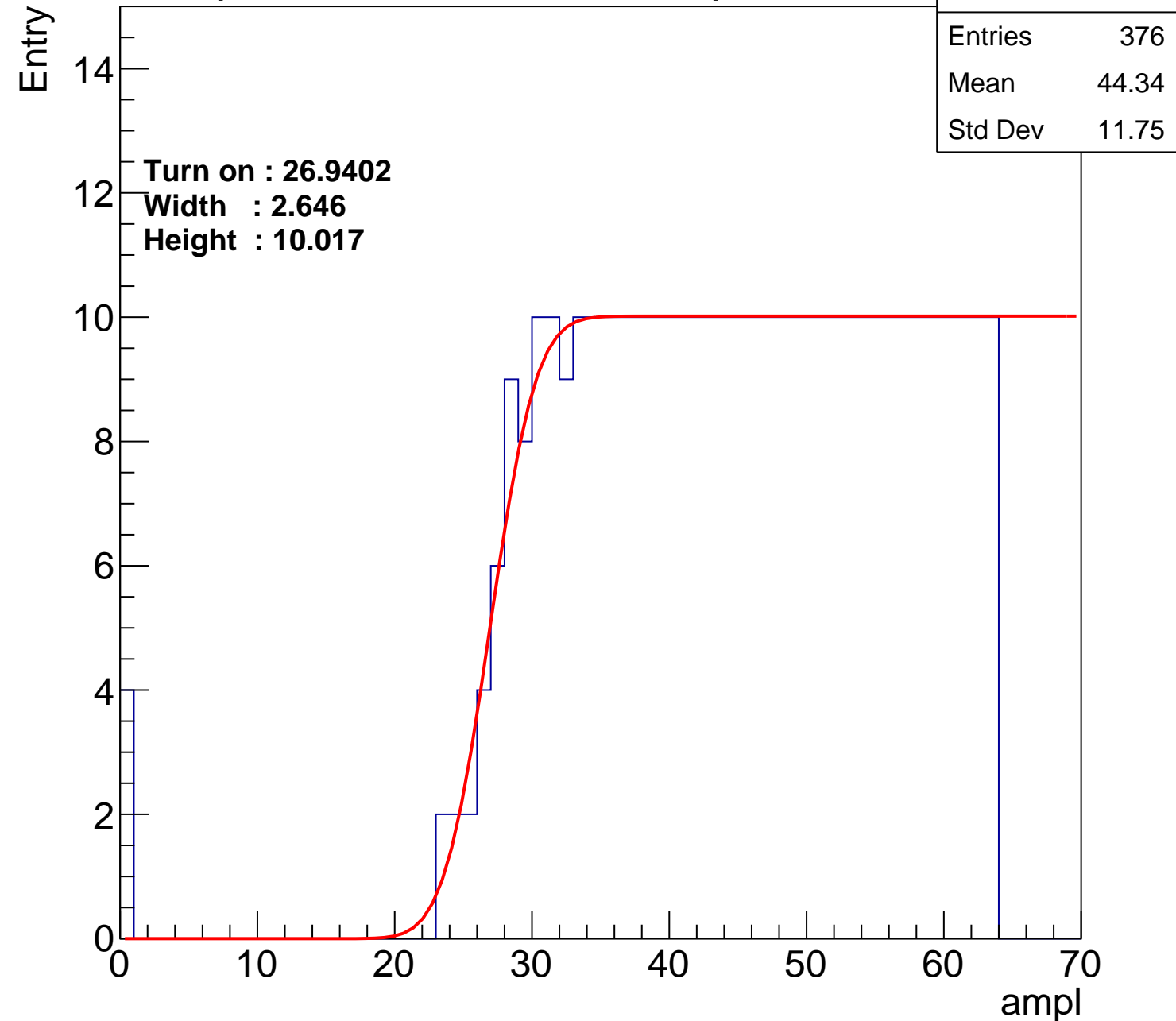
Width : 2.646

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch36

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.42
Std Dev	11.9

Turn on : 24.4310

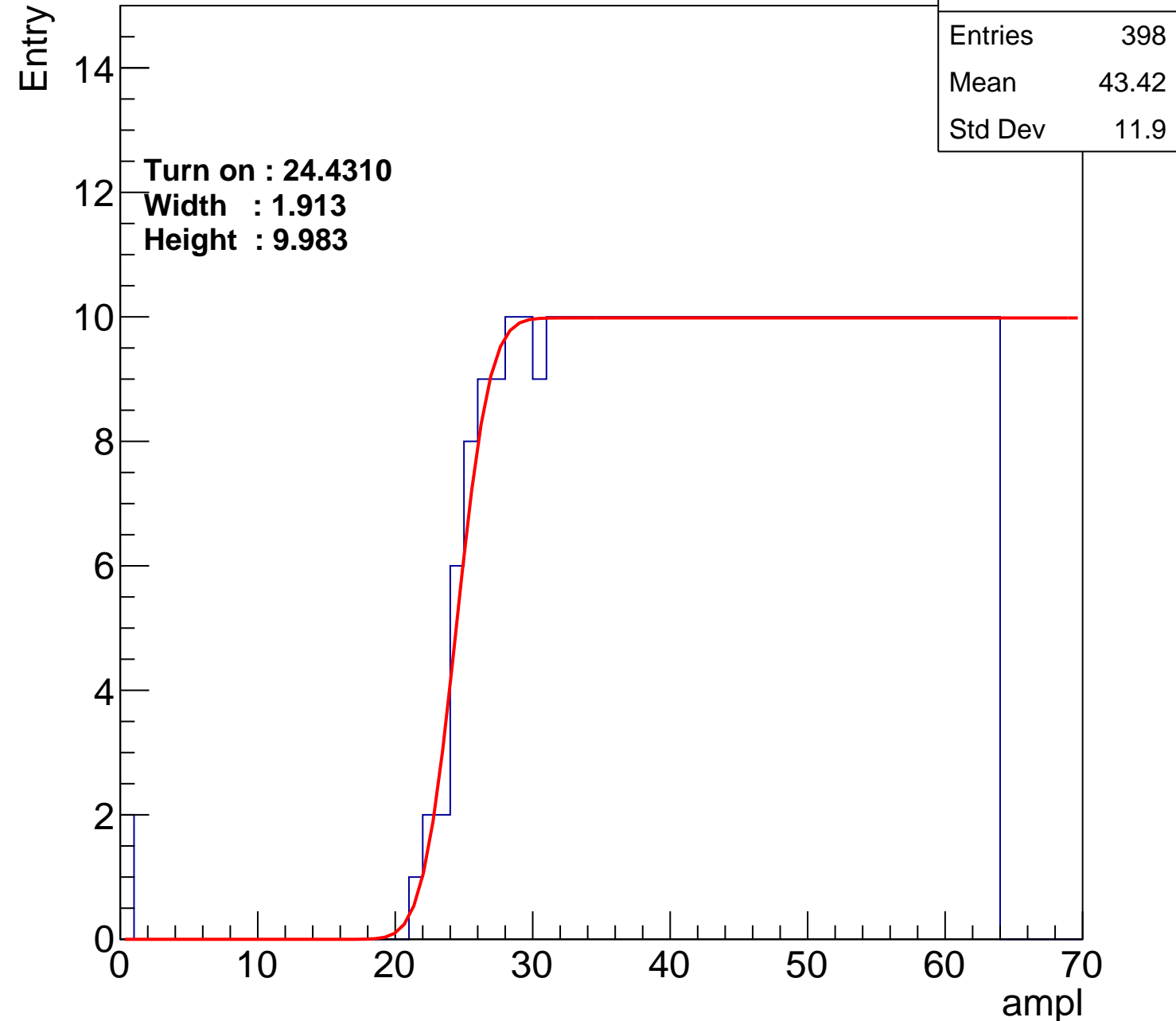
Width : 1.913

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch37

calib\_packv5\_042523\_0143.root, FC#10, port B3

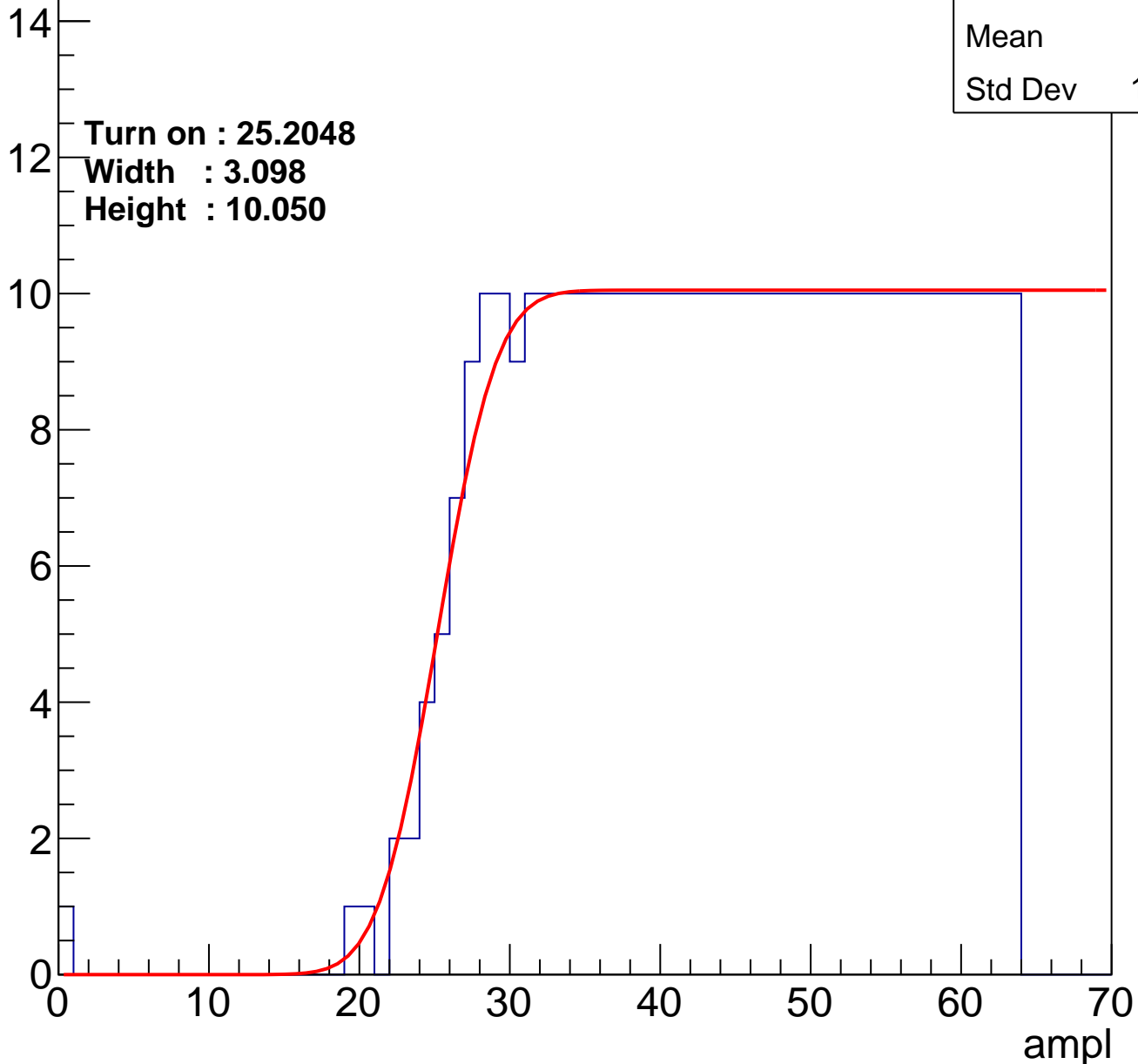
Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.2048

Width : 3.098

Height : 10.050

Entry



# B1L002S, U12-ch38

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.68

Turn on : 24.8672

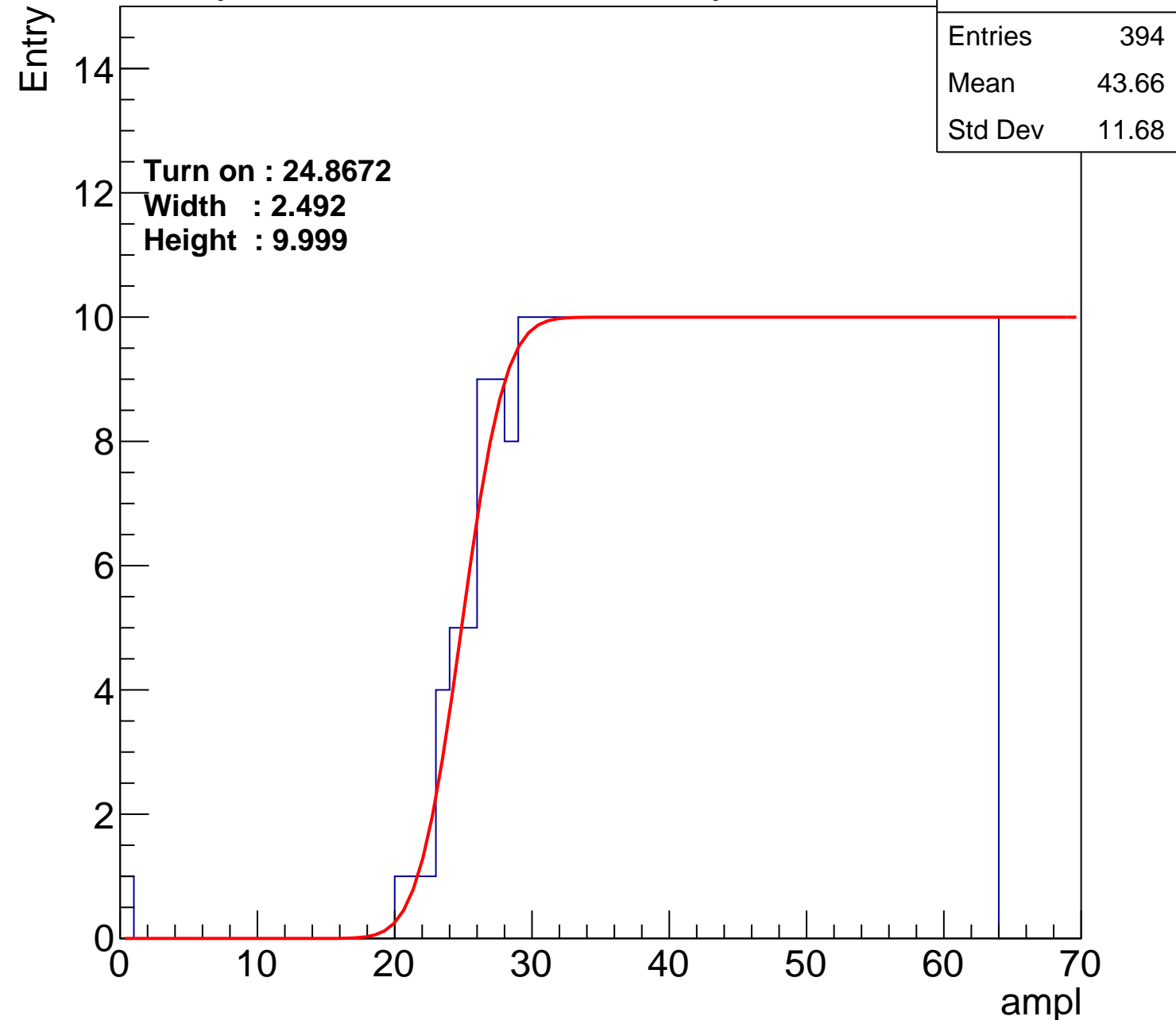
Width : 2.492

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch39

calib\_packv5\_042523\_0143.root, FC#10, port B3

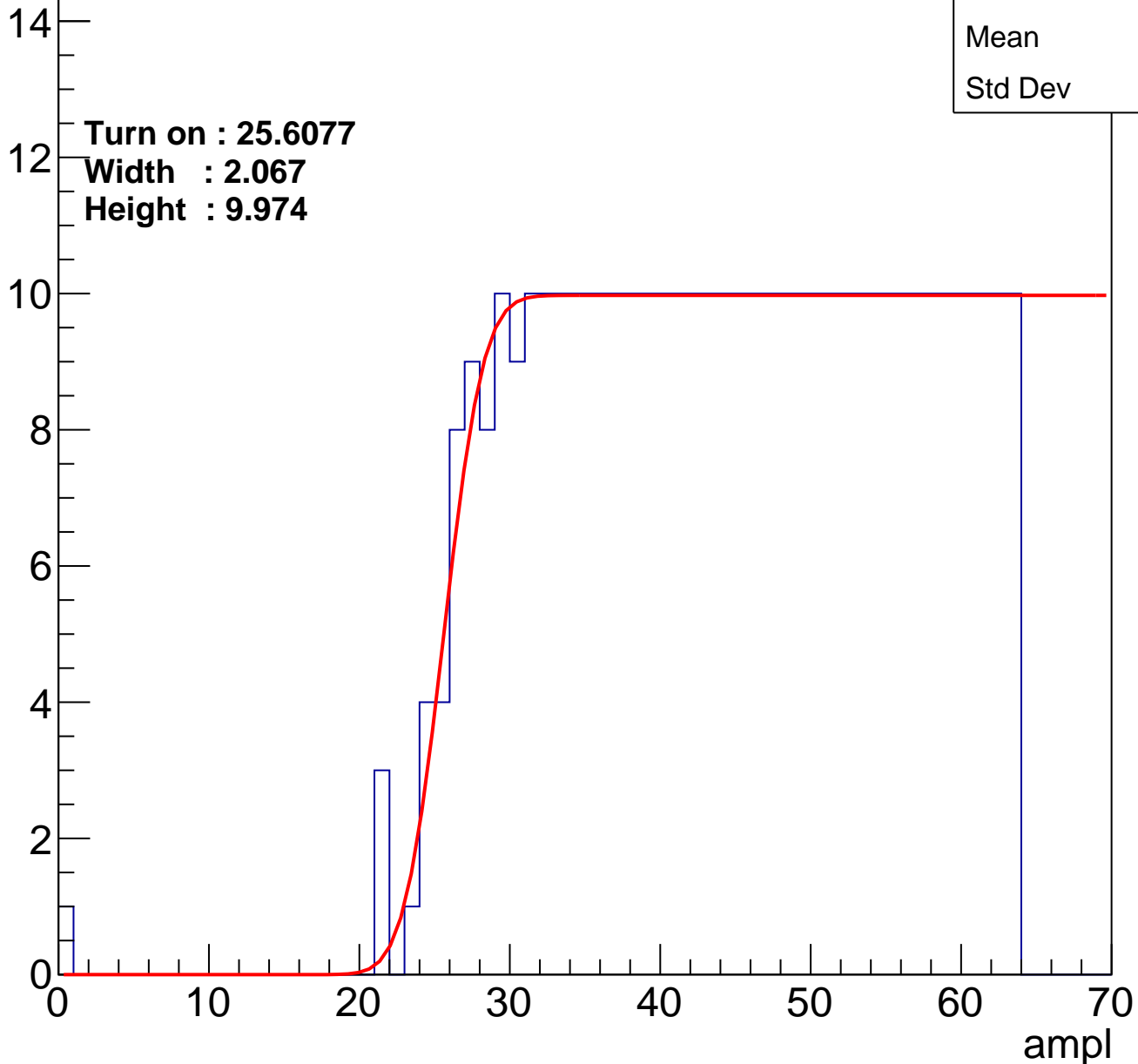
Entries	387
Mean	44
Std Dev	11.5

Turn on : 25.6077

Width : 2.067

Height : 9.974

Entry



# B1L002S, U12-ch40

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.2759

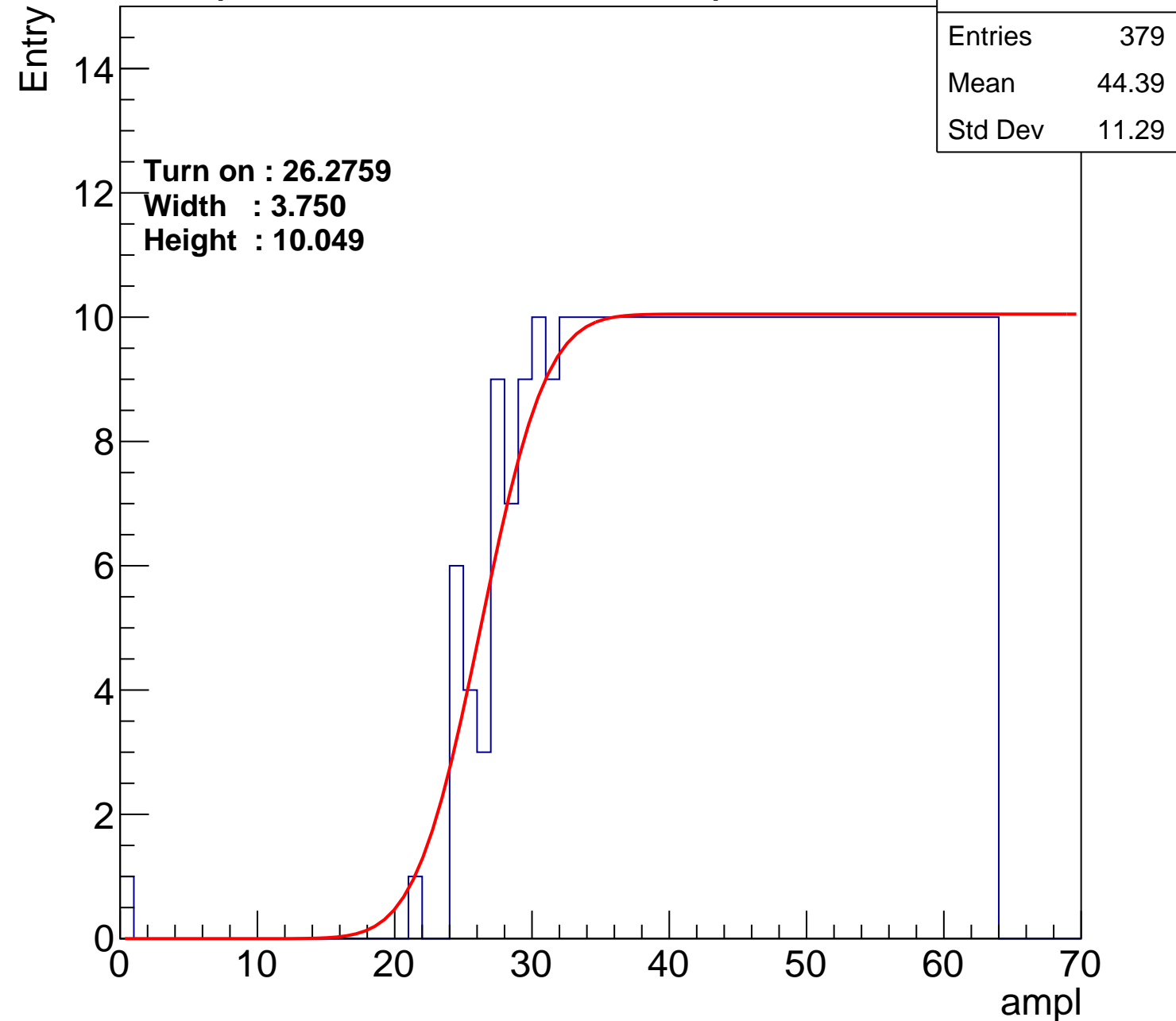
Width : 3.750

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch41

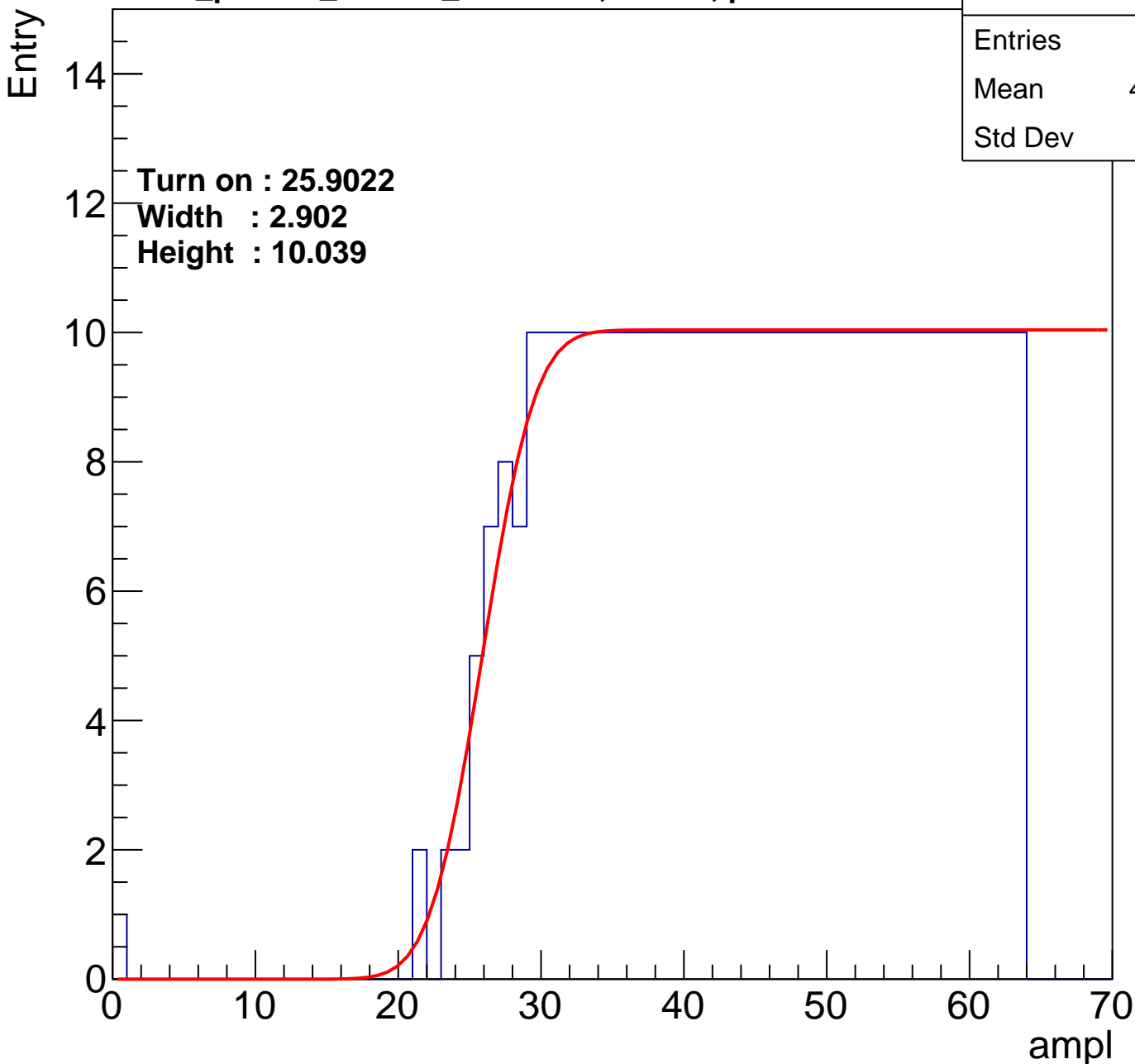
**calib\_packv5\_042523\_0143.root, FC#10, port B3**

**Turn on : 25.9022**

**Width : 2.902**

**Height : 10.039**

Entries	384
Mean	44.15
Std Dev	11.4



# B1L002S, U12-ch42

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	367
Mean	44.89
Std Dev	11.2

Turn on : 27.6387

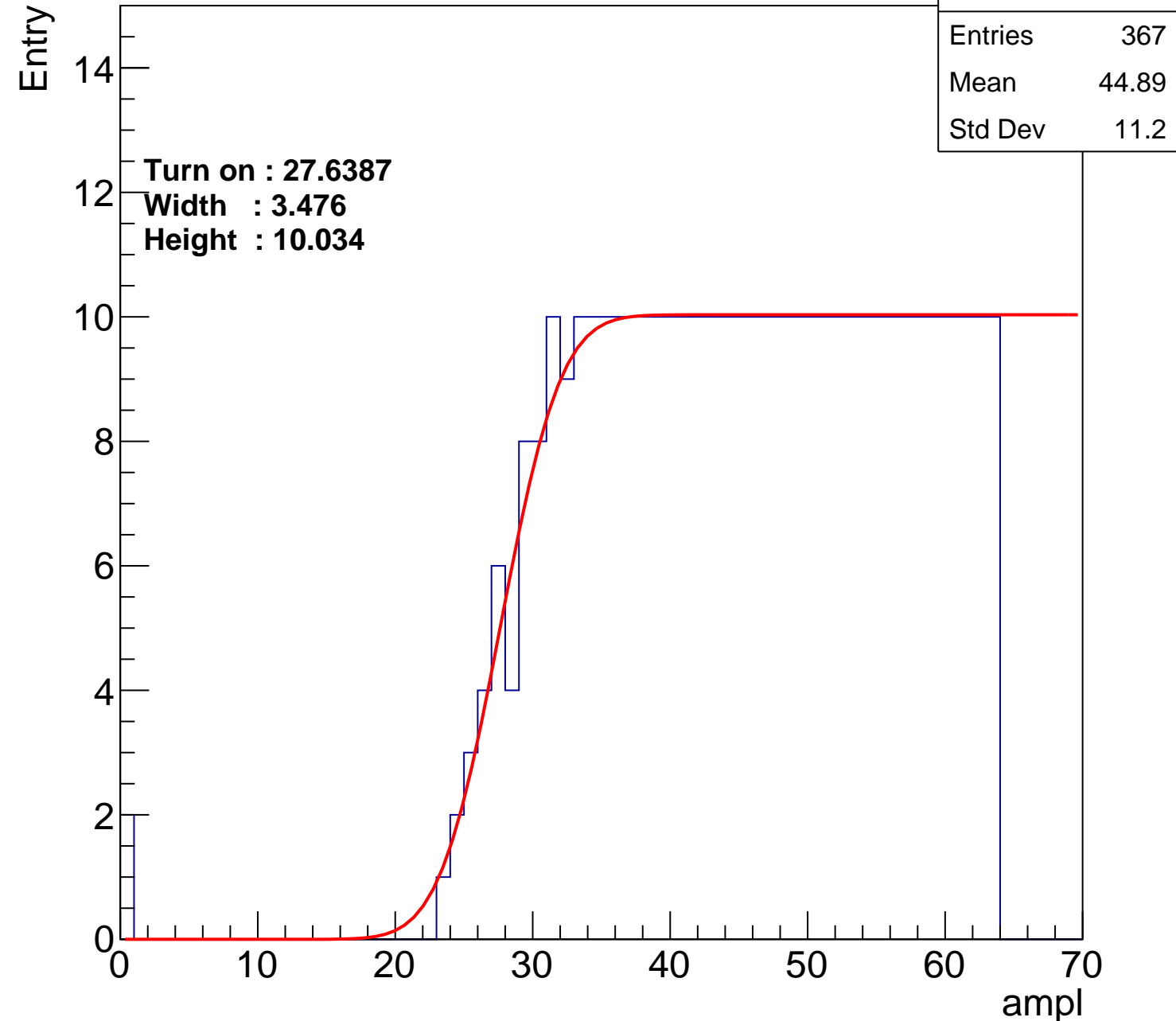
Width : 3.476

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch43

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.1862

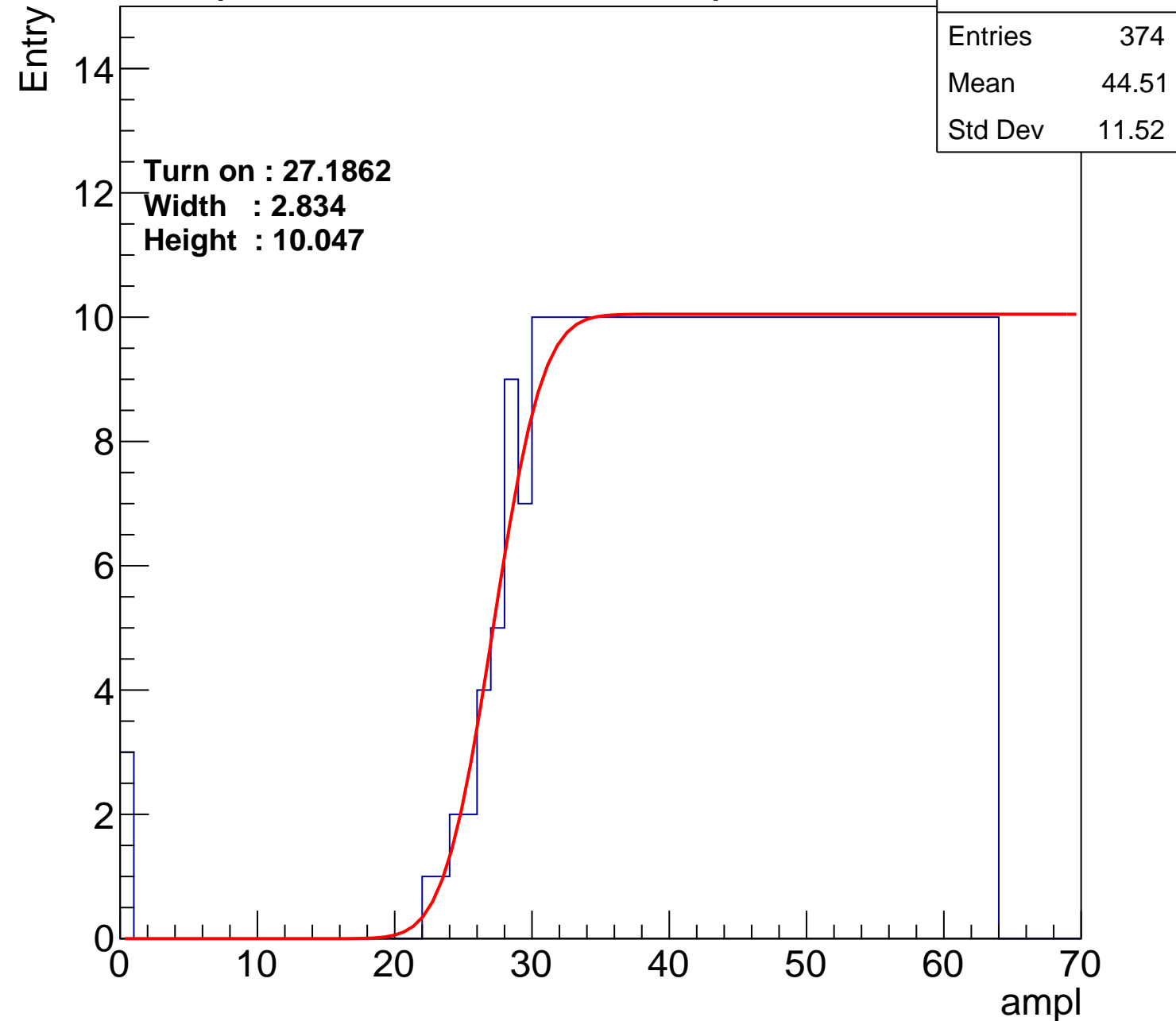
Width : 2.834

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch44

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.68
Std Dev	11.8

Turn on : 24.9456

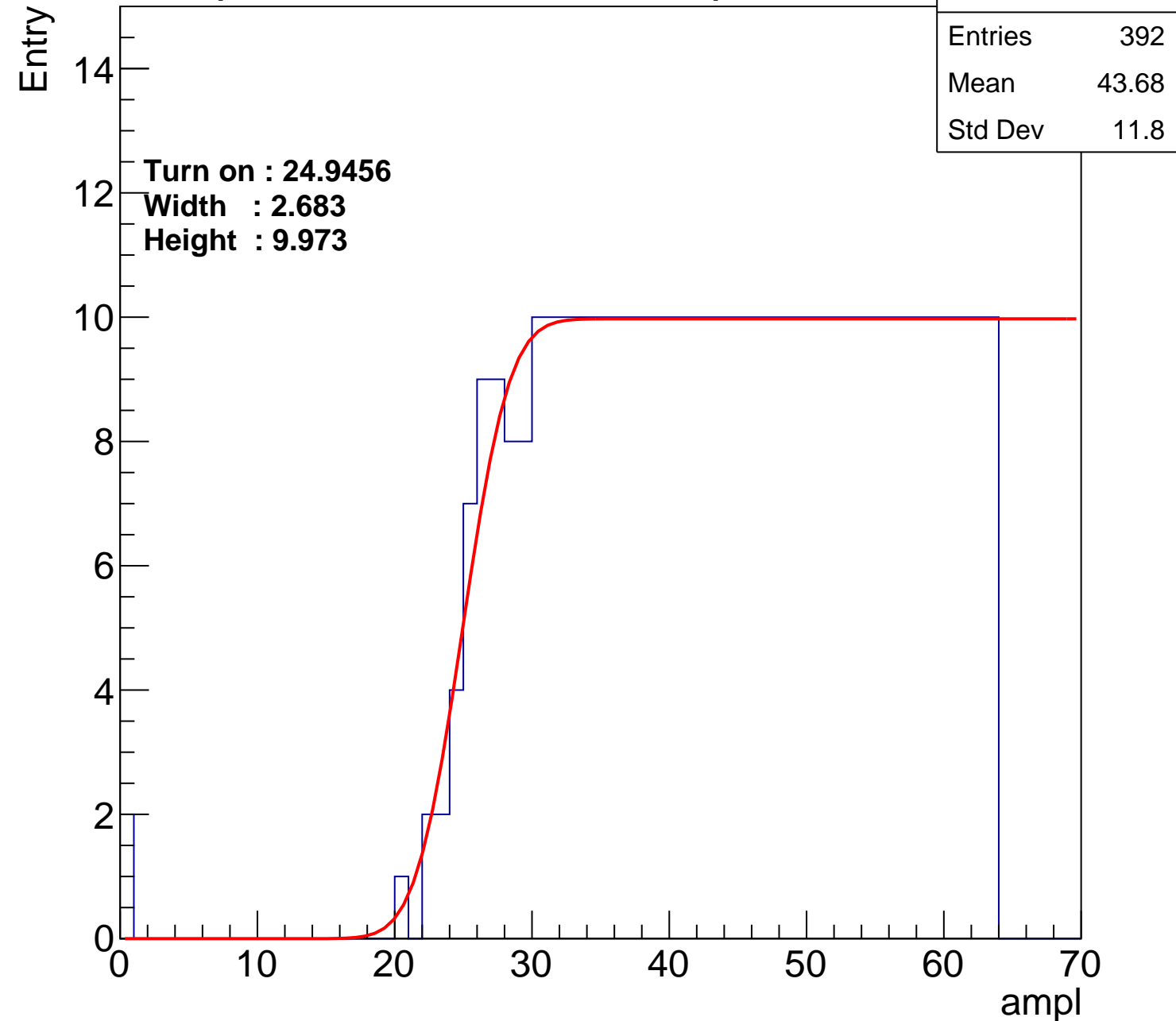
Width : 2.683

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch45

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.3
Std Dev	11.51

Turn on : 26.4277

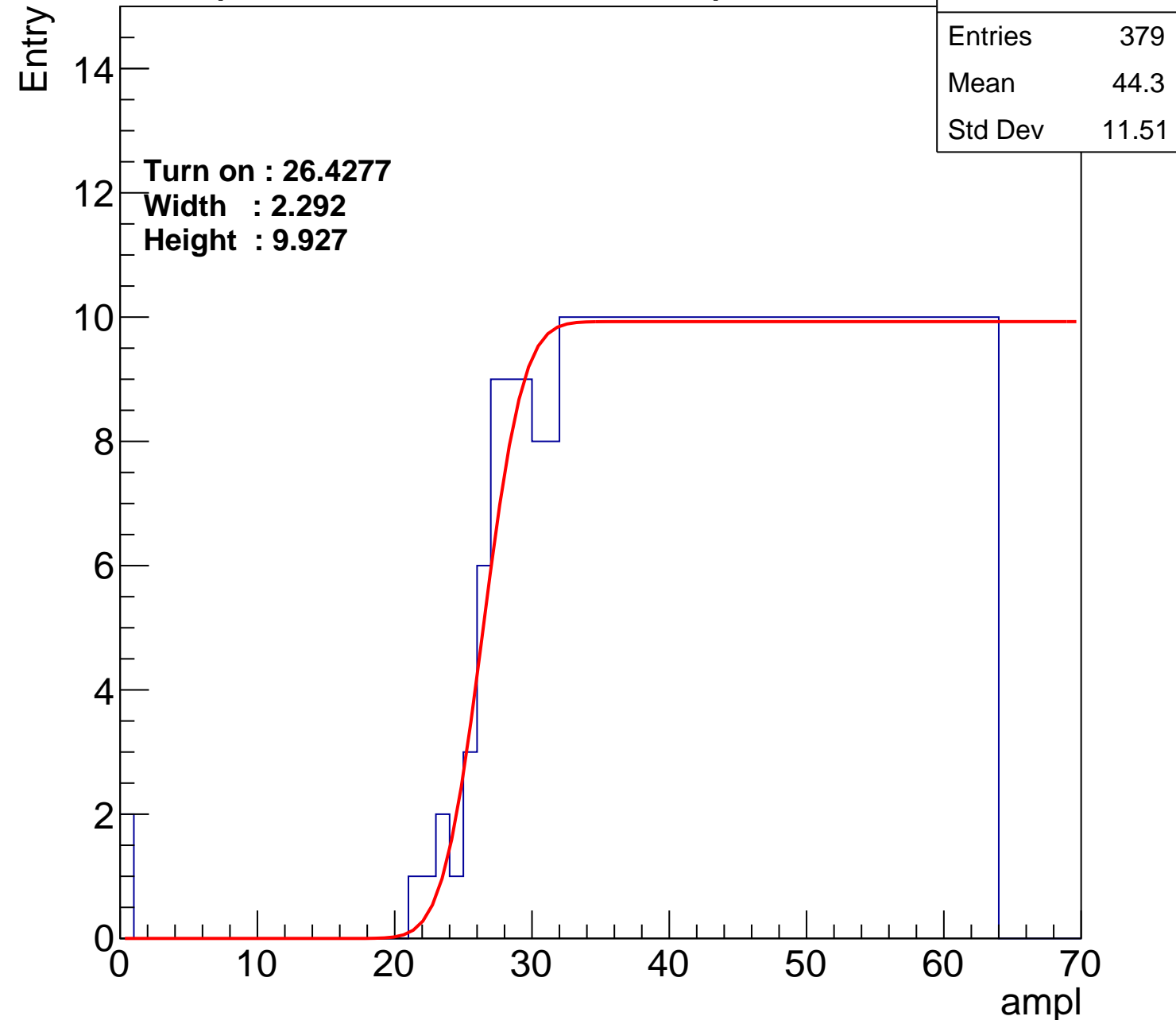
Width : 2.292

Height : 9.927

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch46

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.26
Std Dev	11.66

**Turn on : 26.8057**

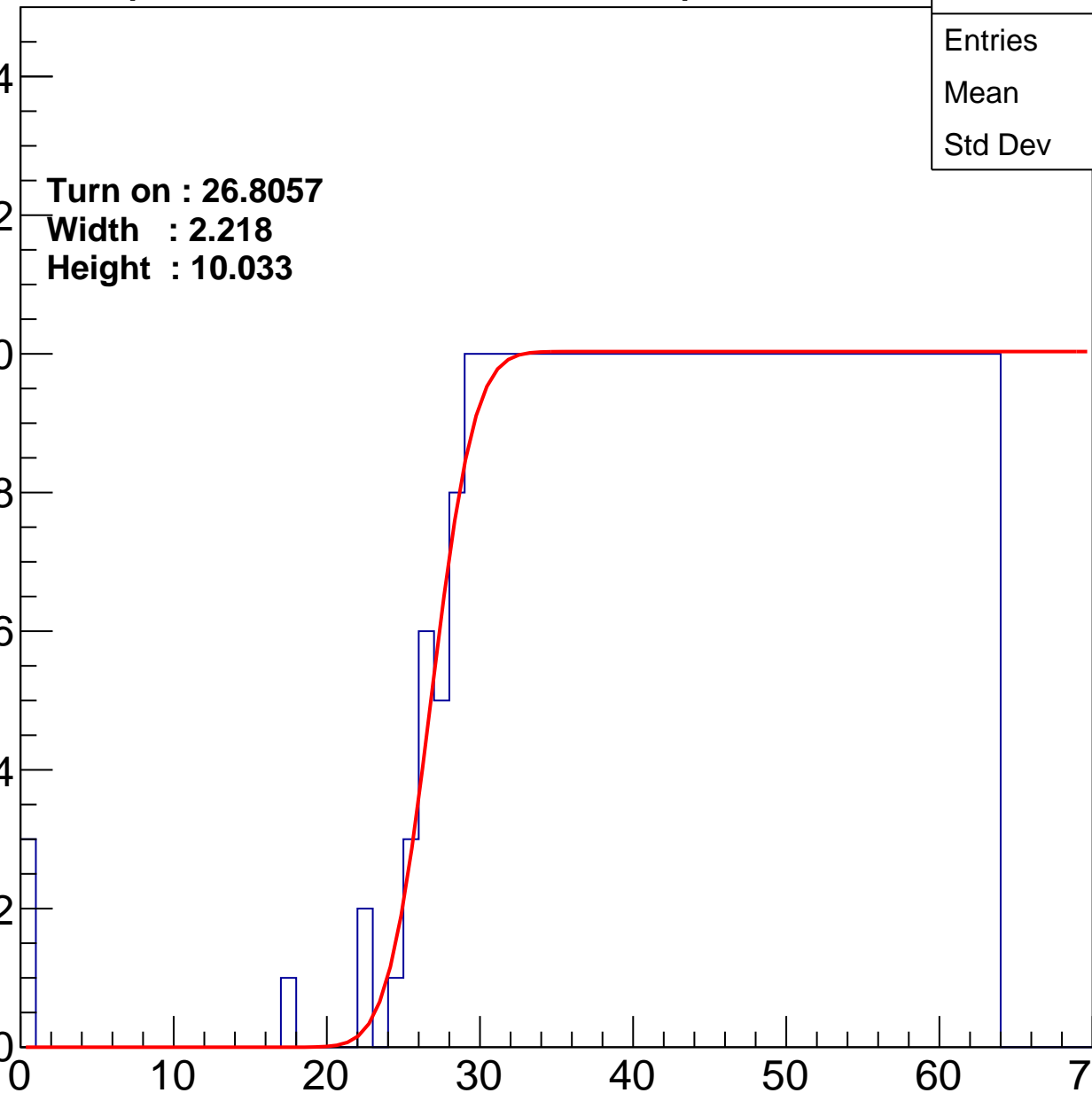
**Width : 2.218**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch47

calib\_packv5\_042523\_0143.root, FC#10, port B3

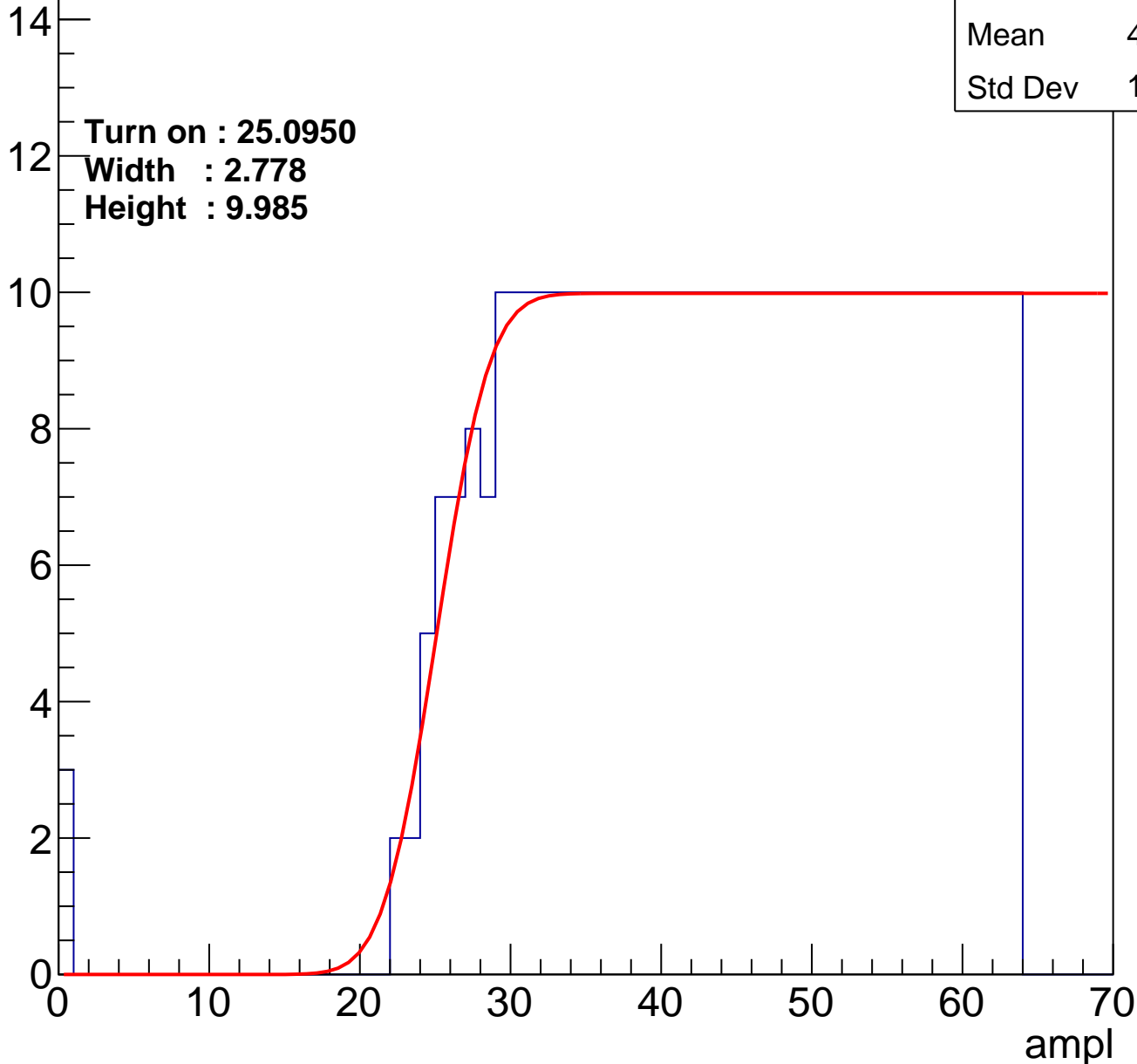
Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.0950

Width : 2.778

Height : 9.985

Entry



# B1L002S, U12-ch48

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.4276

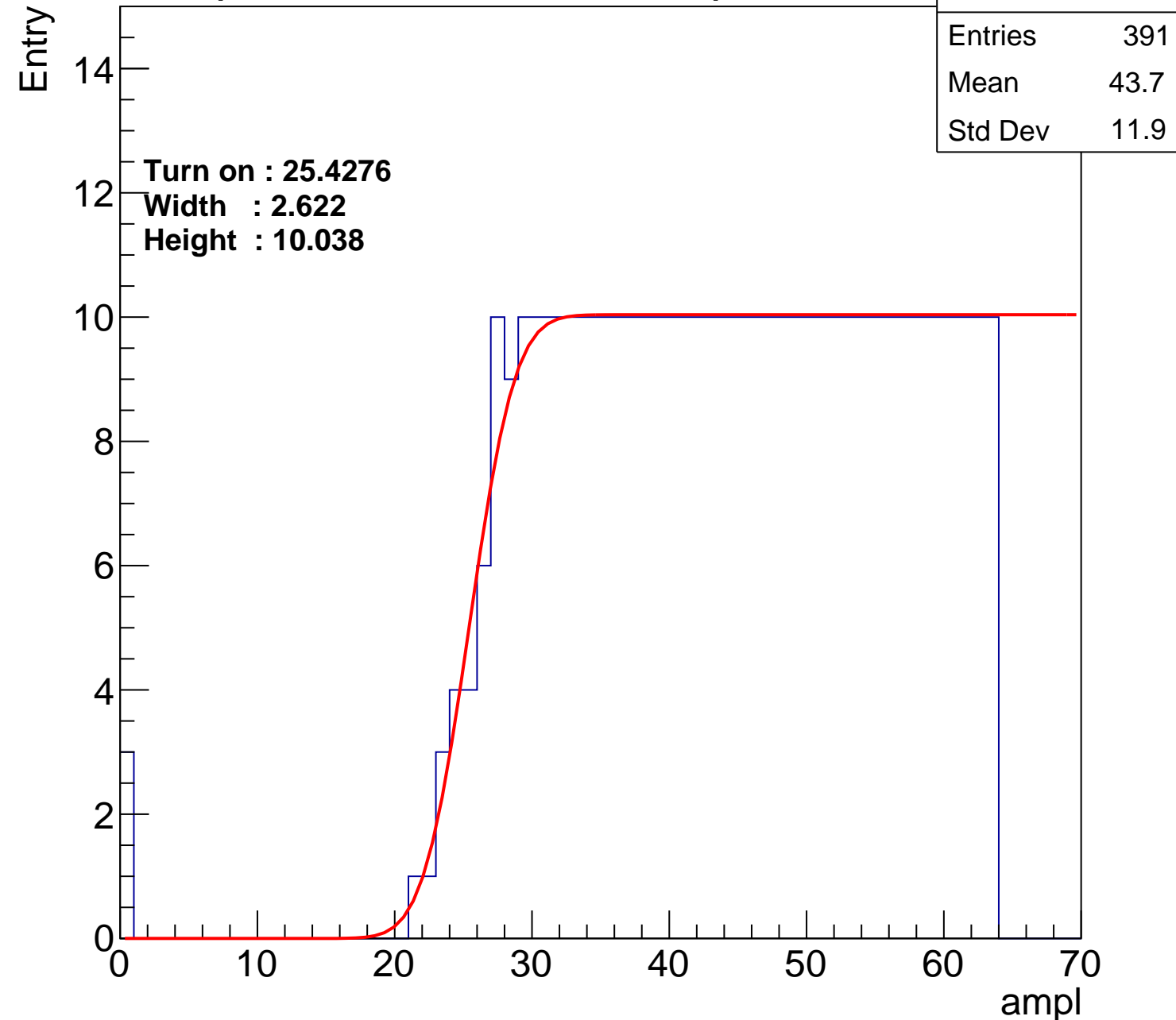
Width : 2.622

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch49

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.44
Std Dev	11.76

Turn on : 27.1628

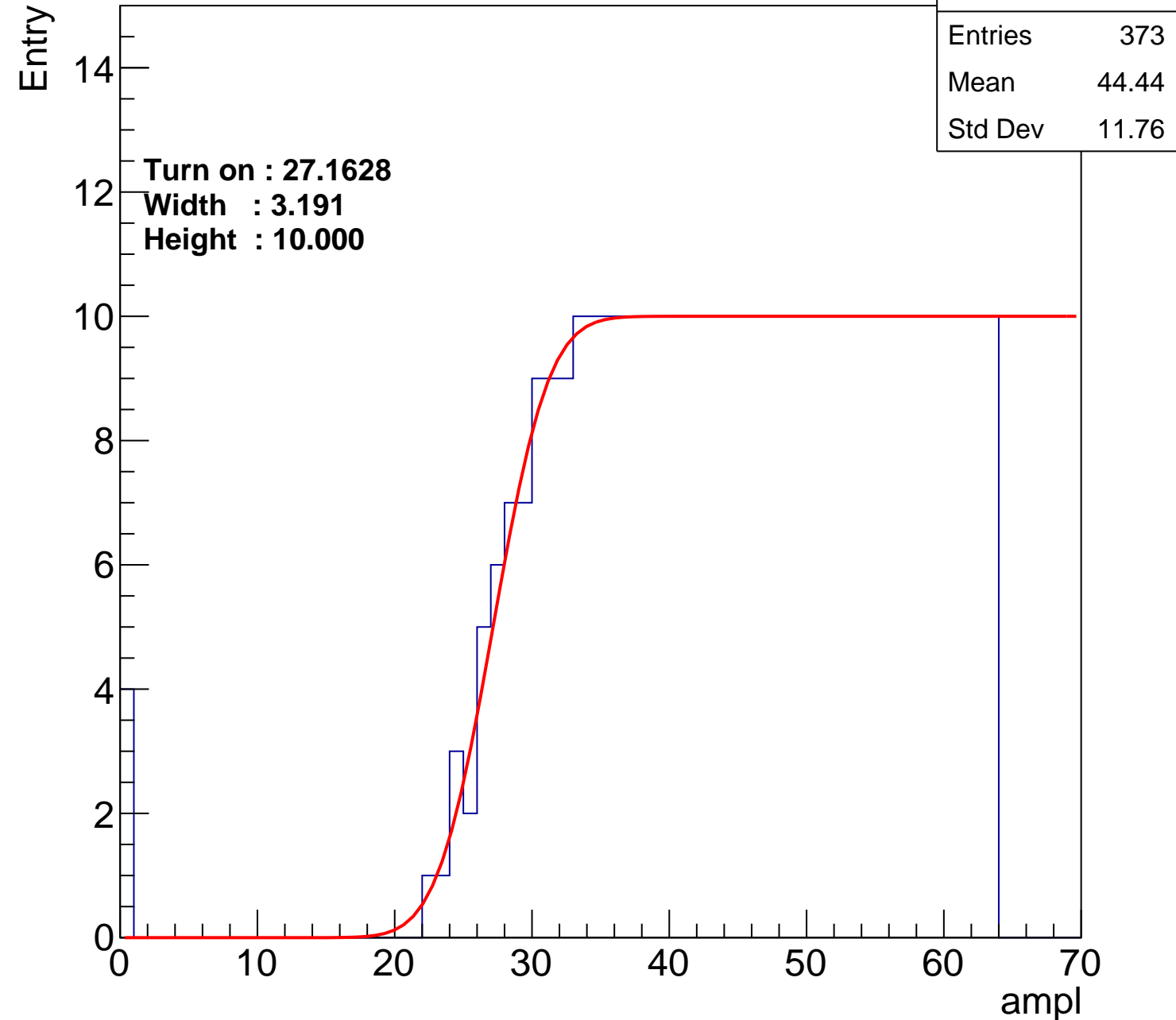
Width : 3.191

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch50

calib\_packv5\_042523\_0143.root, FC#10, port B3

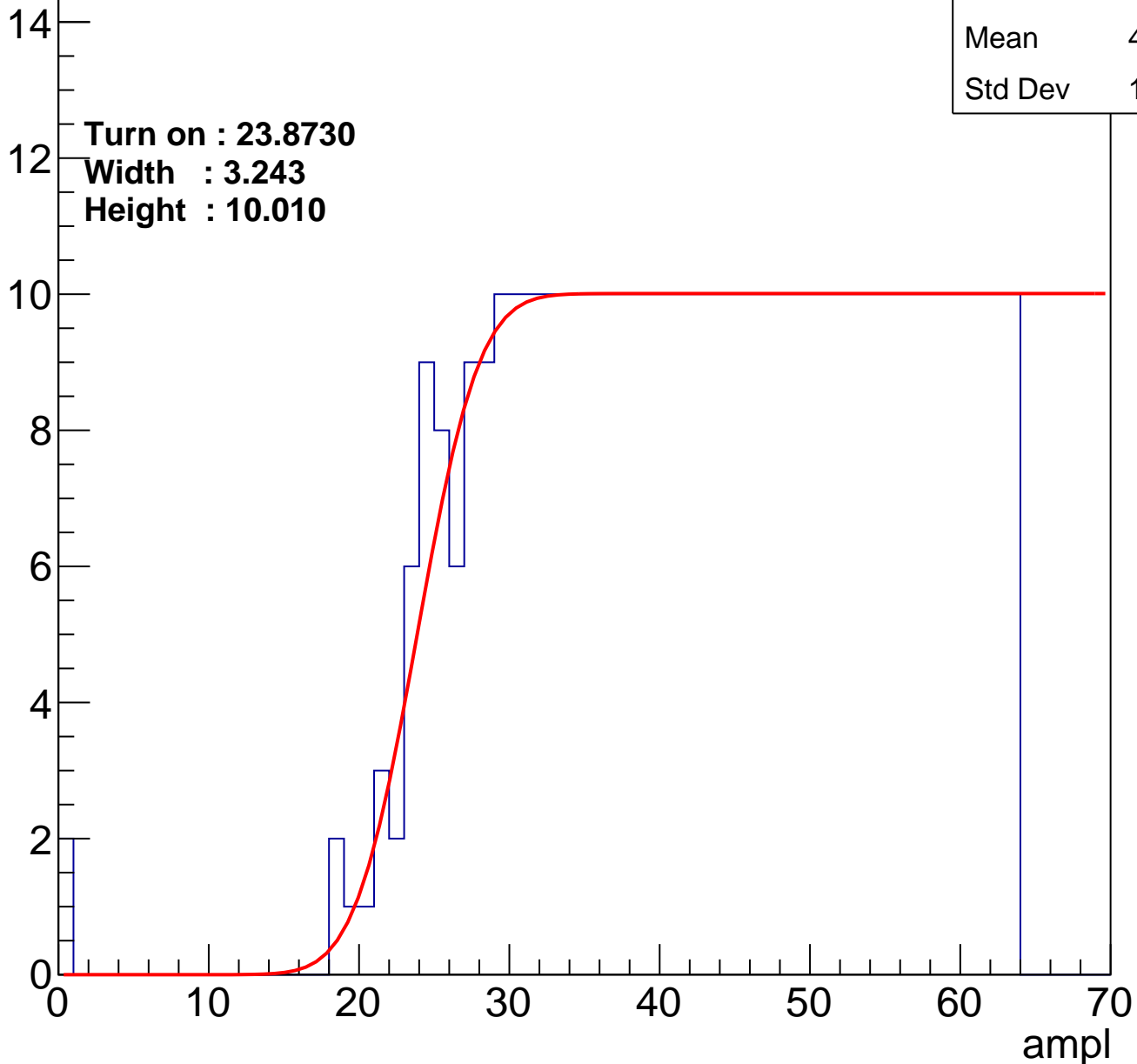
Entries	408
Mean	42.86
Std Dev	12.28

Turn on : 23.8730

Width : 3.243

Height : 10.010

Entry



# B1L002S, U12-ch51

calib\_packv5\_042523\_0143.root, FC#10, port B3

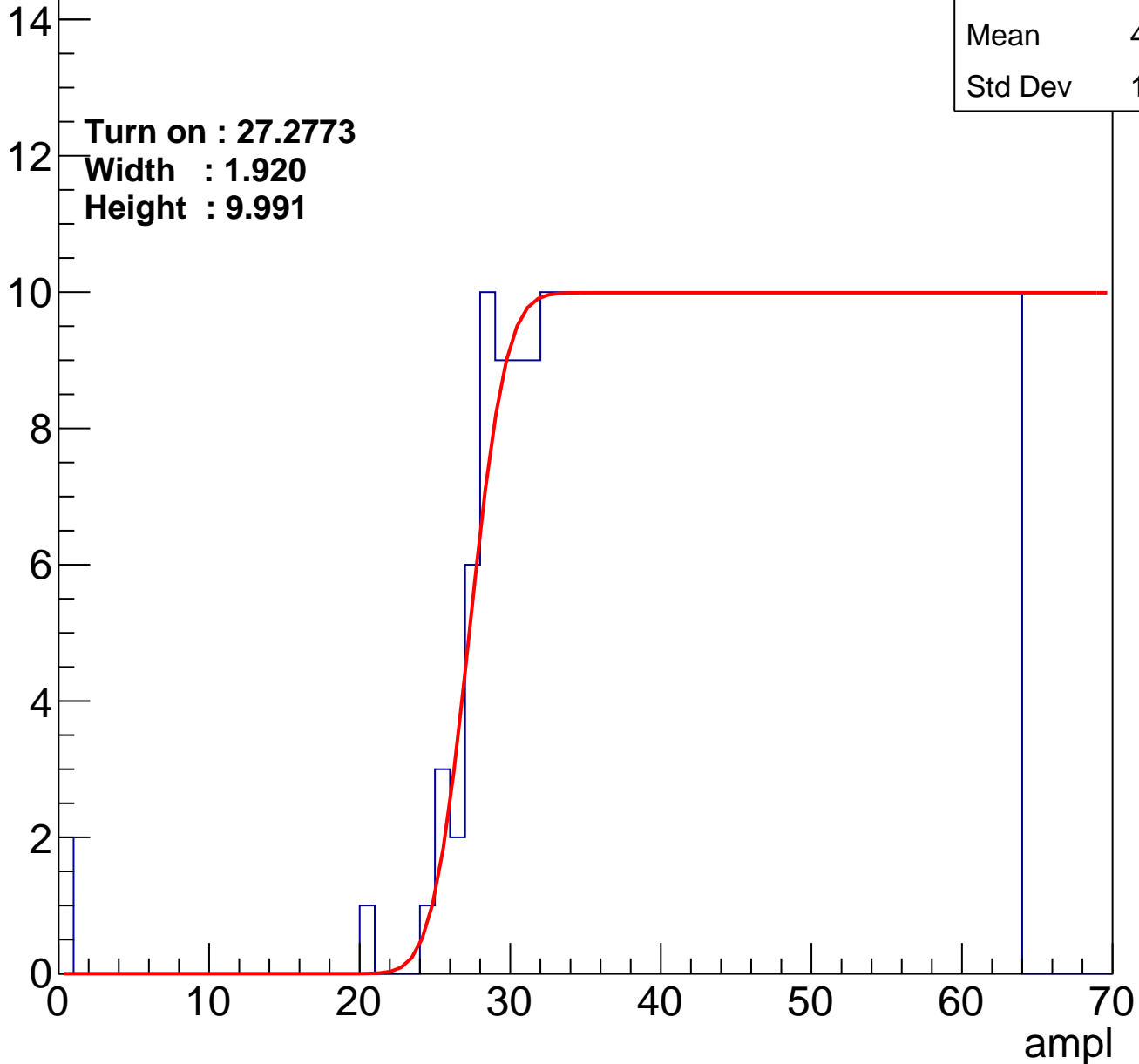
Entries	372
Mean	44.69
Std Dev	11.27

Turn on : 27.2773

Width : 1.920

Height : 9.991

Entry



# B1L002S, U12-ch52

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.42
Std Dev	11.9

Turn on : 24.1897

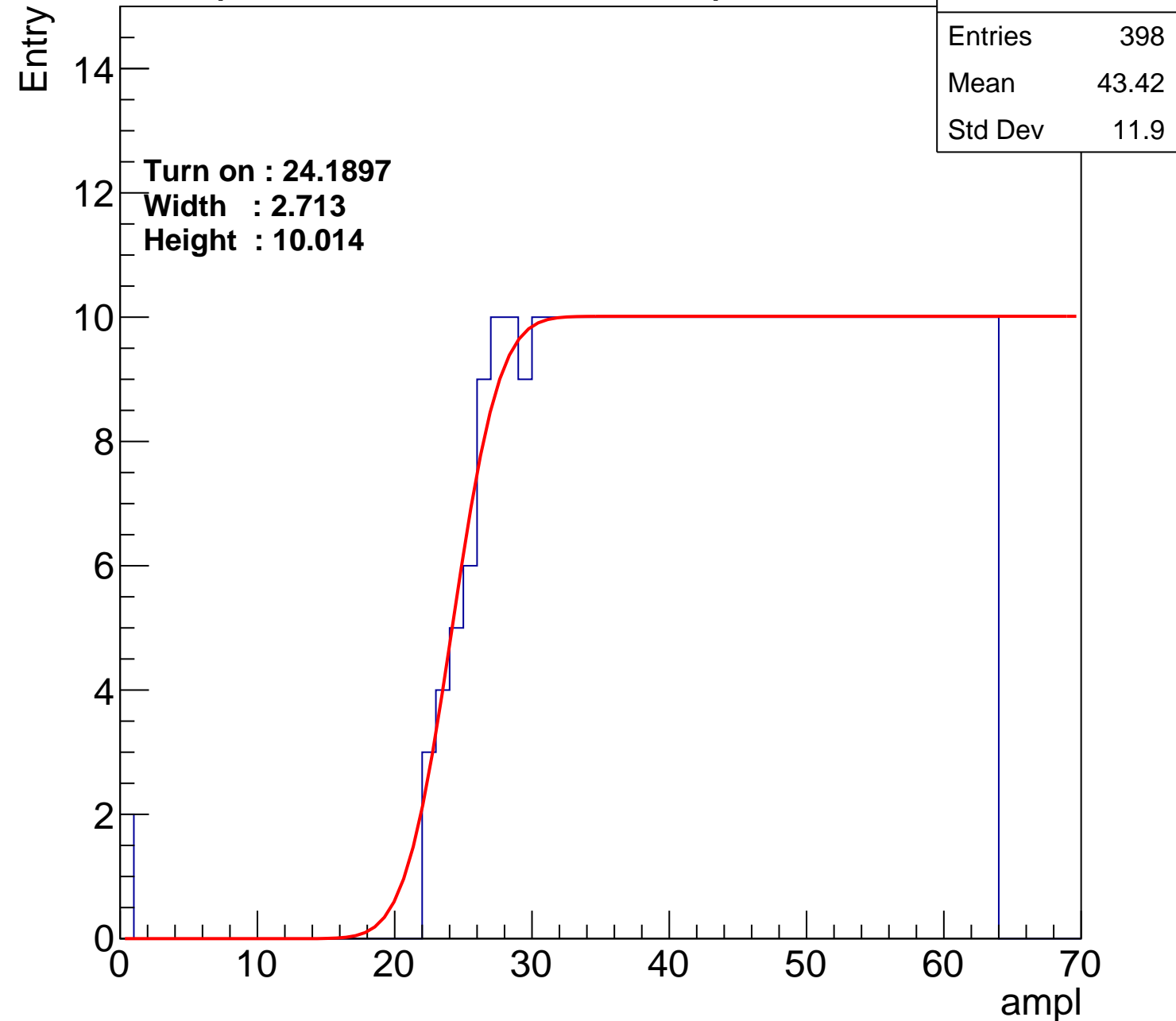
Width : 2.713

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch53

calib\_packv5\_042523\_0143.root, FC#10, port B3

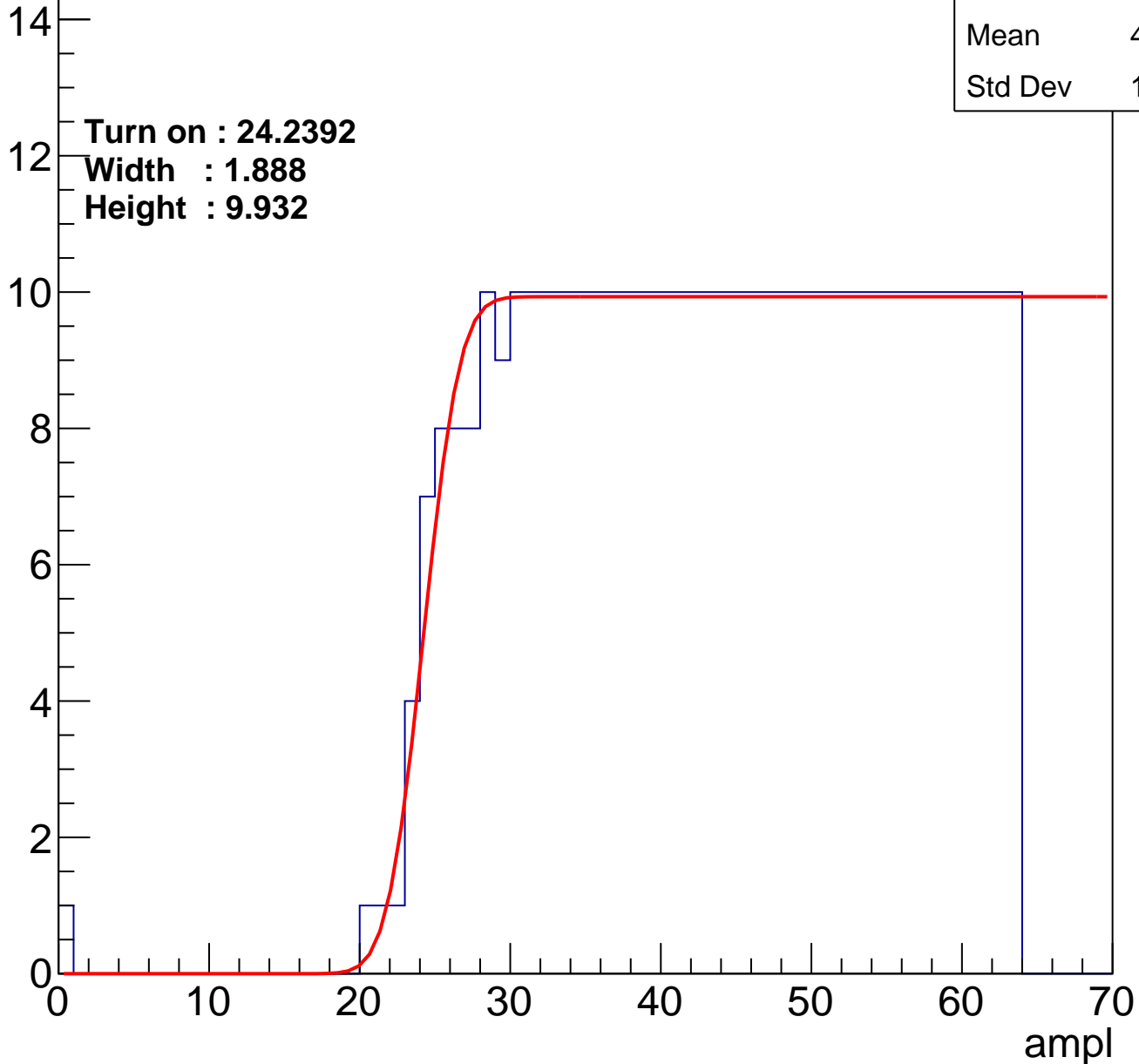
Entries	398
Mean	43.46
Std Dev	11.78

Turn on : 24.2392

Width : 1.888

Height : 9.932

Entry



# B1L002S, U12-ch54

calib\_packv5\_042523\_0143.root, FC#10, port B3

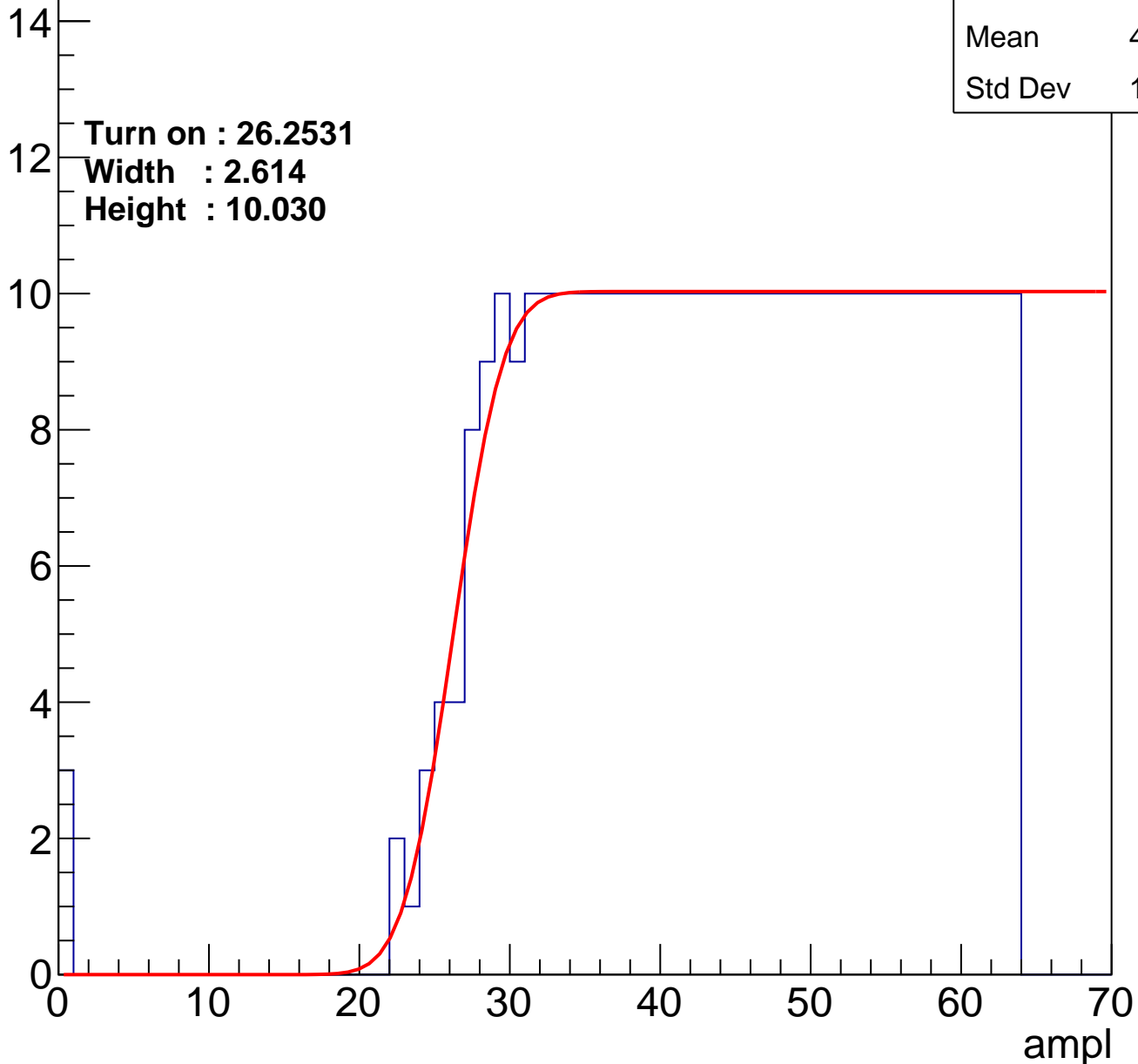
Entries	383
Mean	44.08
Std Dev	11.73

Turn on : 26.2531

Width : 2.614

Height : 10.030

Entry





# B1L002S, U12-ch55

calib\_packv5\_042523\_0143.root, FC#10, port B3

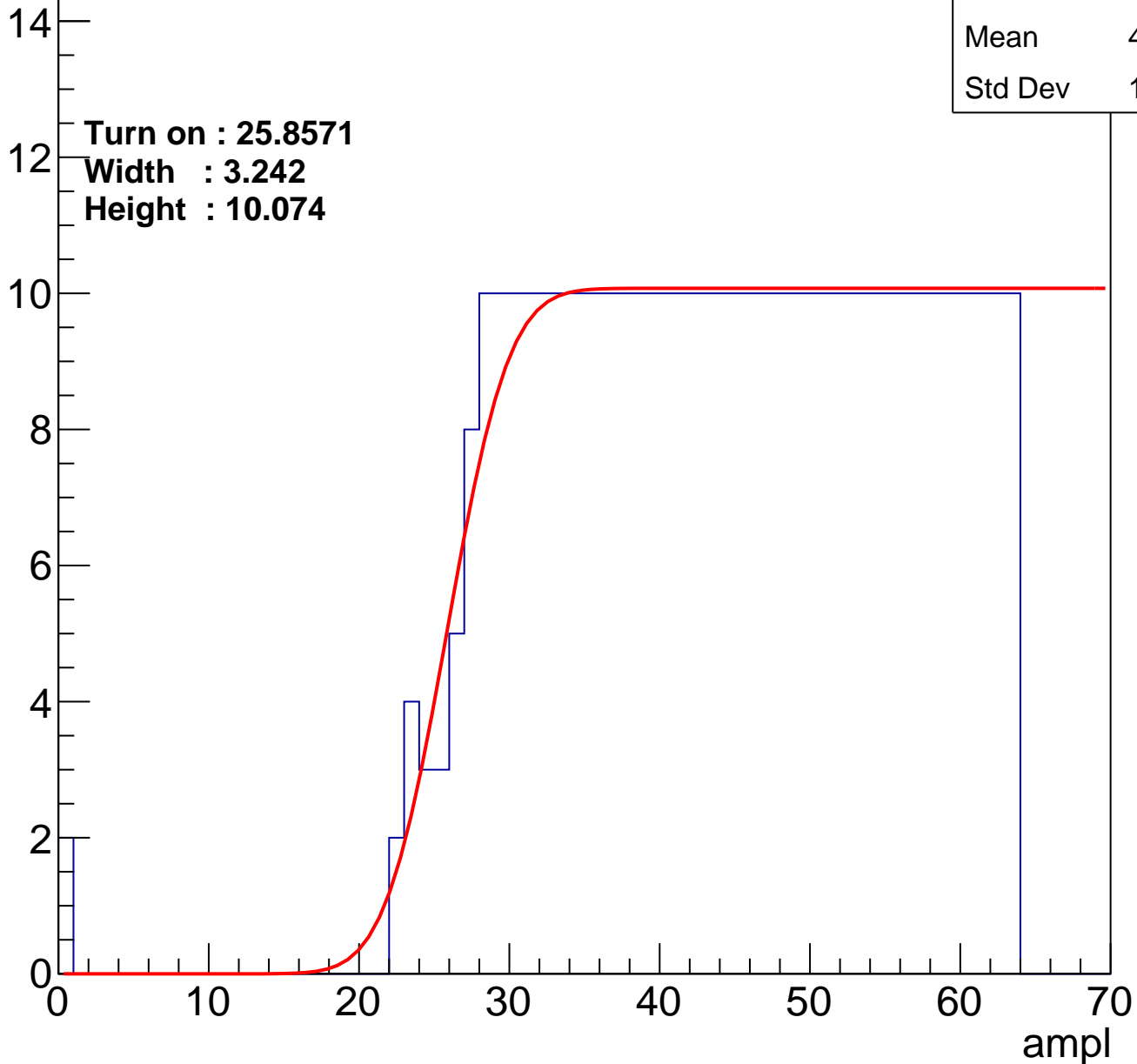
Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.8571

Width : 3.242

Height : 10.074

Entry



# B1L002S, U12-ch56

**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Entries	405
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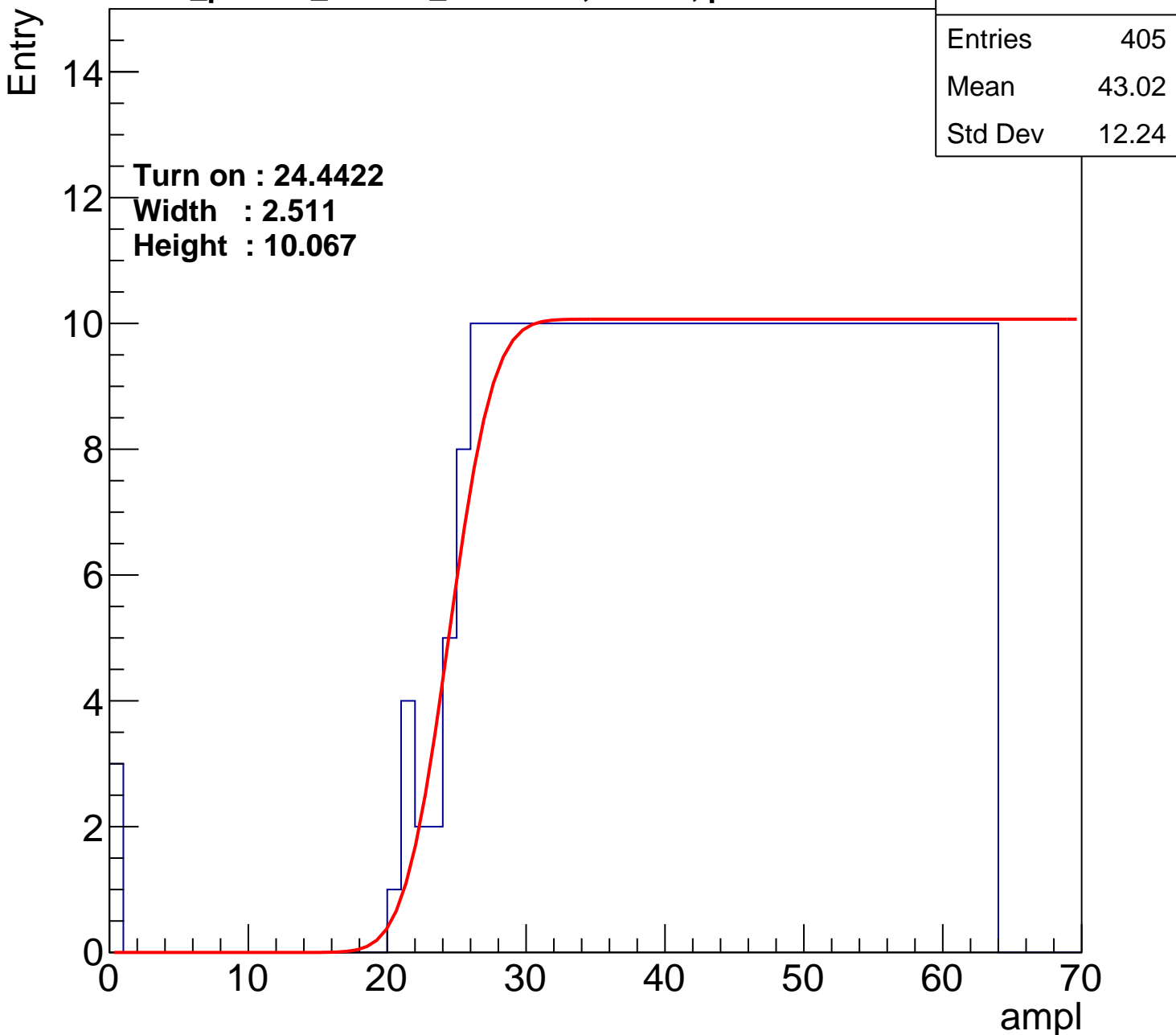
Mean	43.02
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Std Dev	12.24
---------	-------

**Turn on : 24.4422**

**Width : 2.511**

**Height : 10.067**



# B1L002S, U12-ch57

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.23
Std Dev	11.82

Turn on : 26.9784

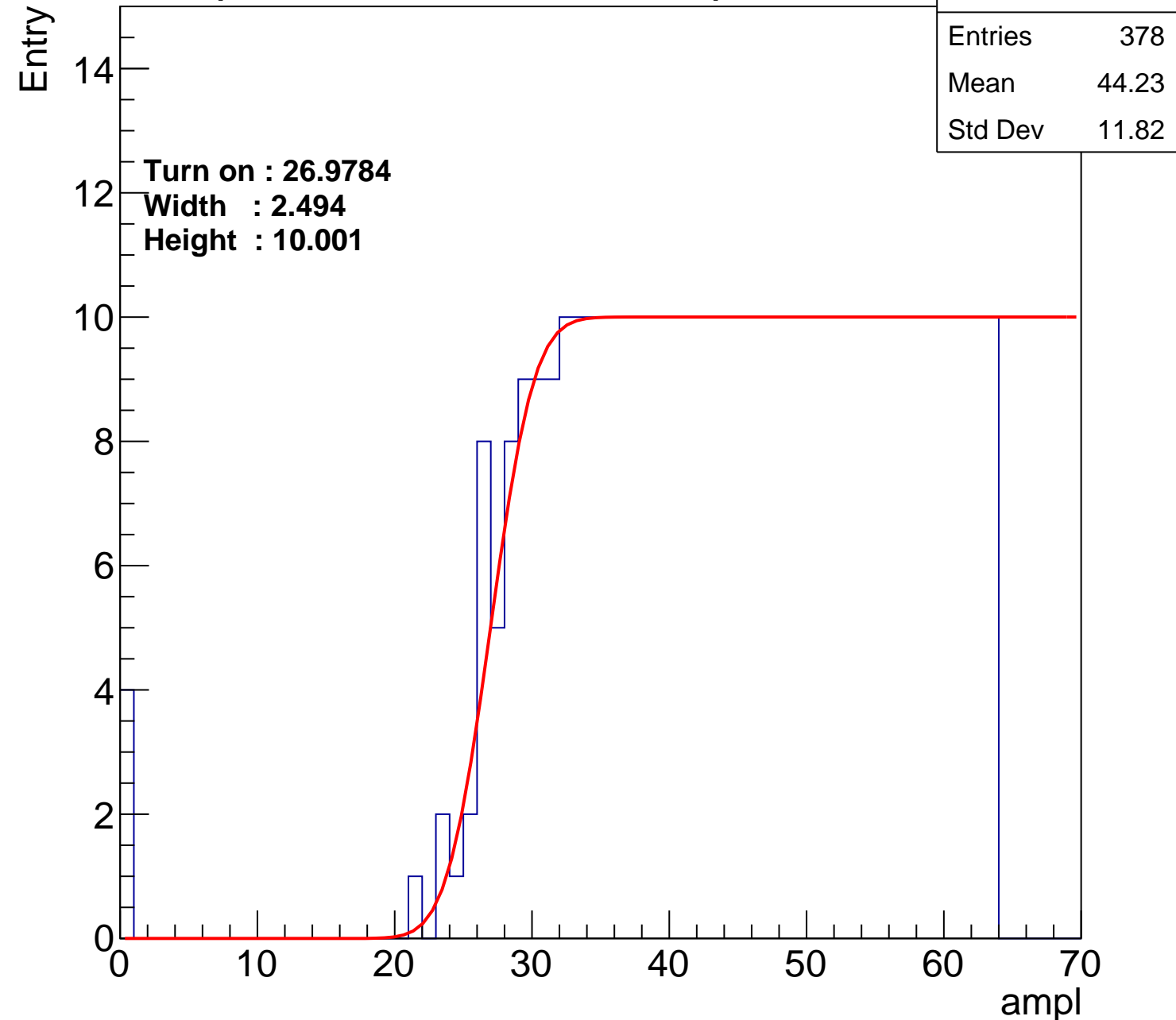
Width : 2.494

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch58

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	361
Mean	45.23
Std Dev	10.98

Turn on : 28.1682

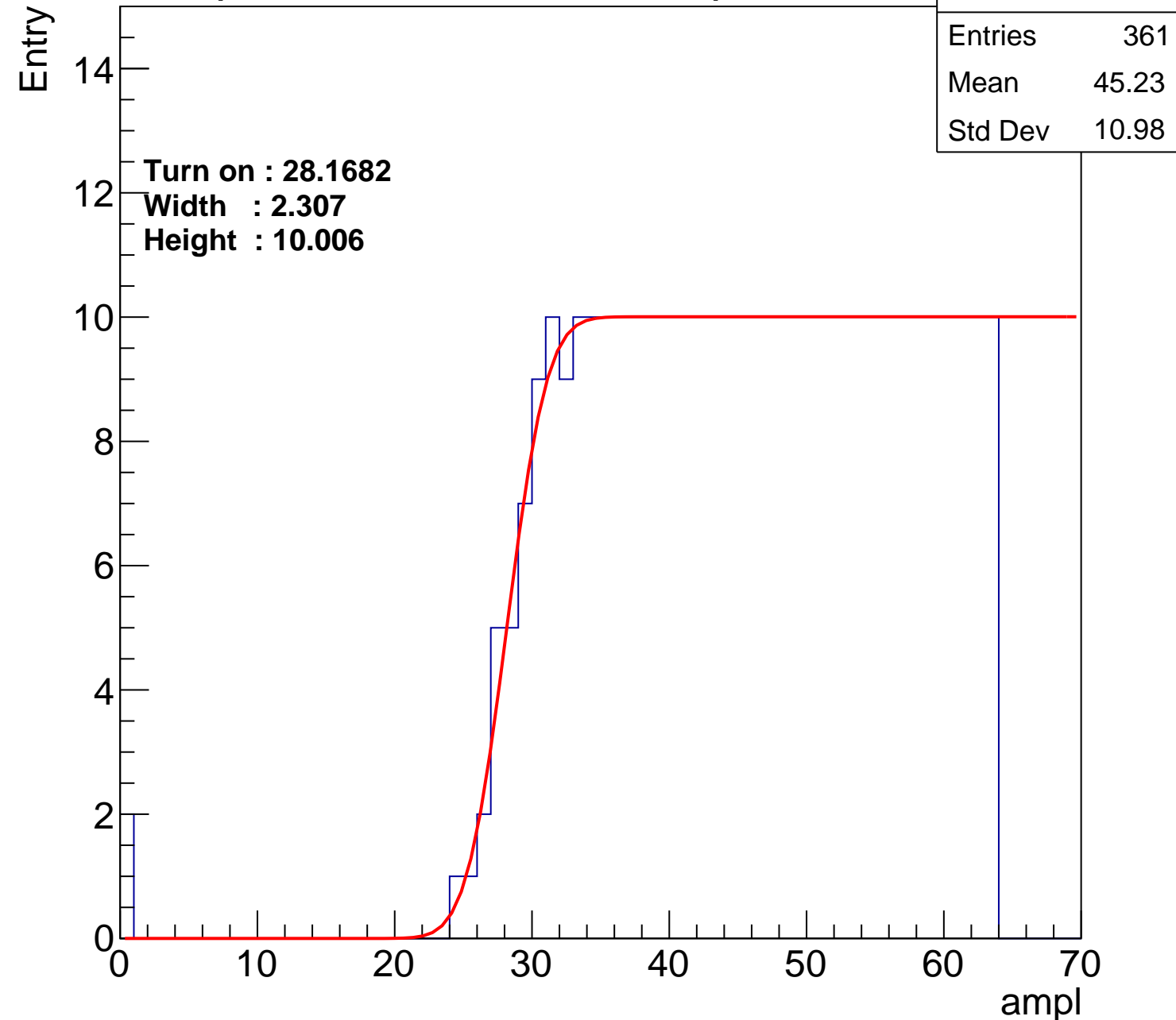
Width : 2.307

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch59

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 26.1903

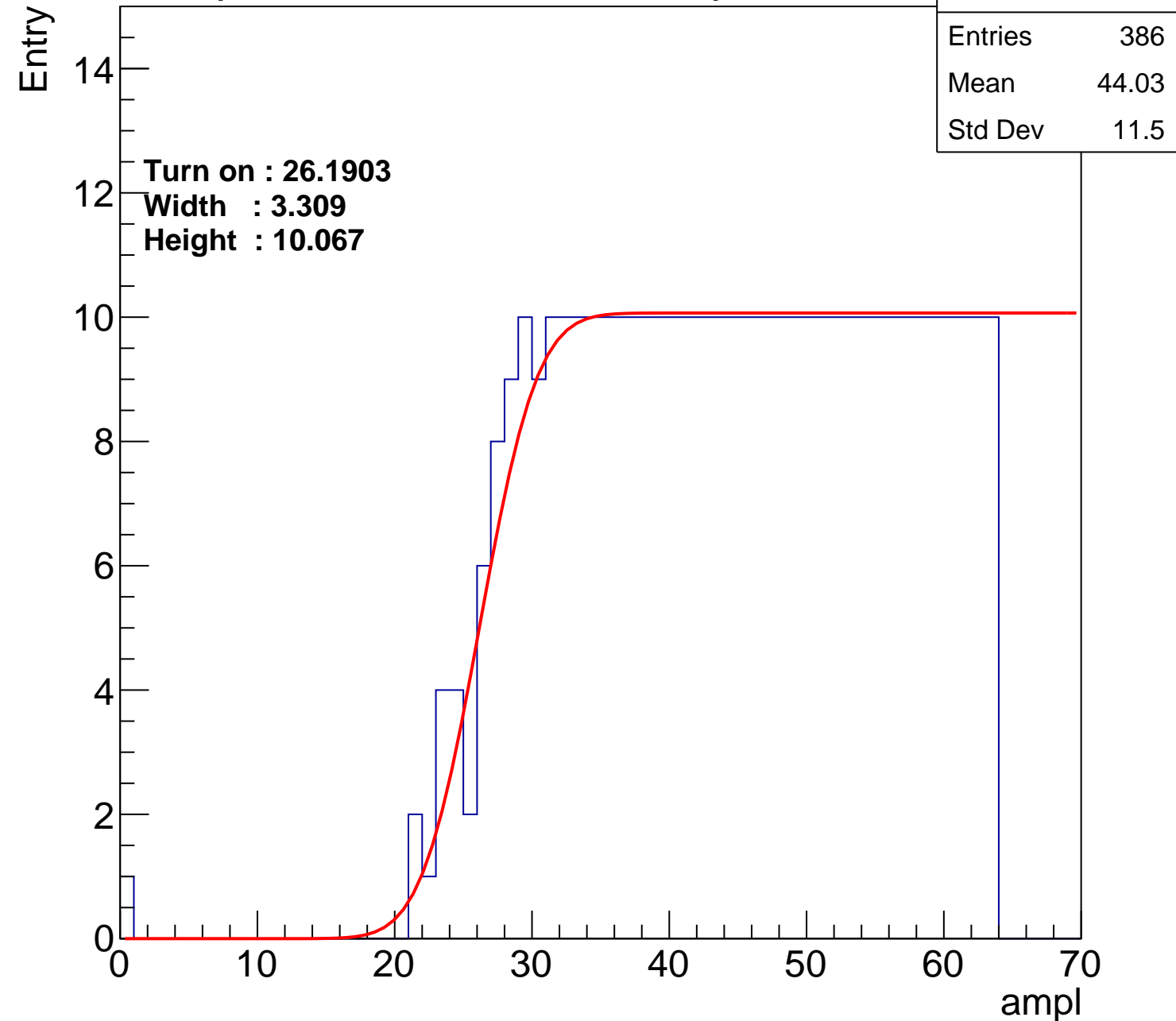
Width : 3.309

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch60

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.7751

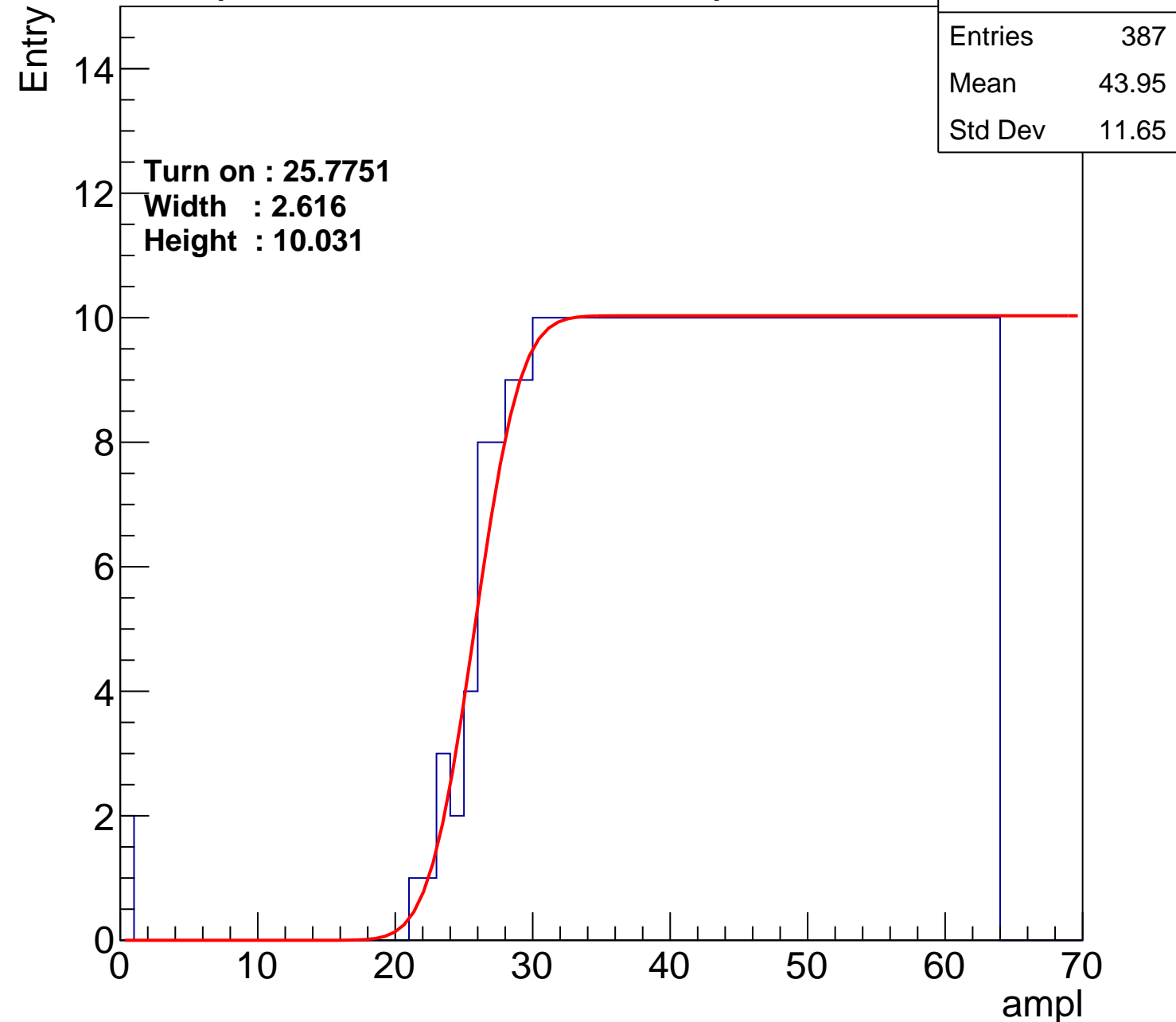
Width : 2.616

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch61

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 24.8101

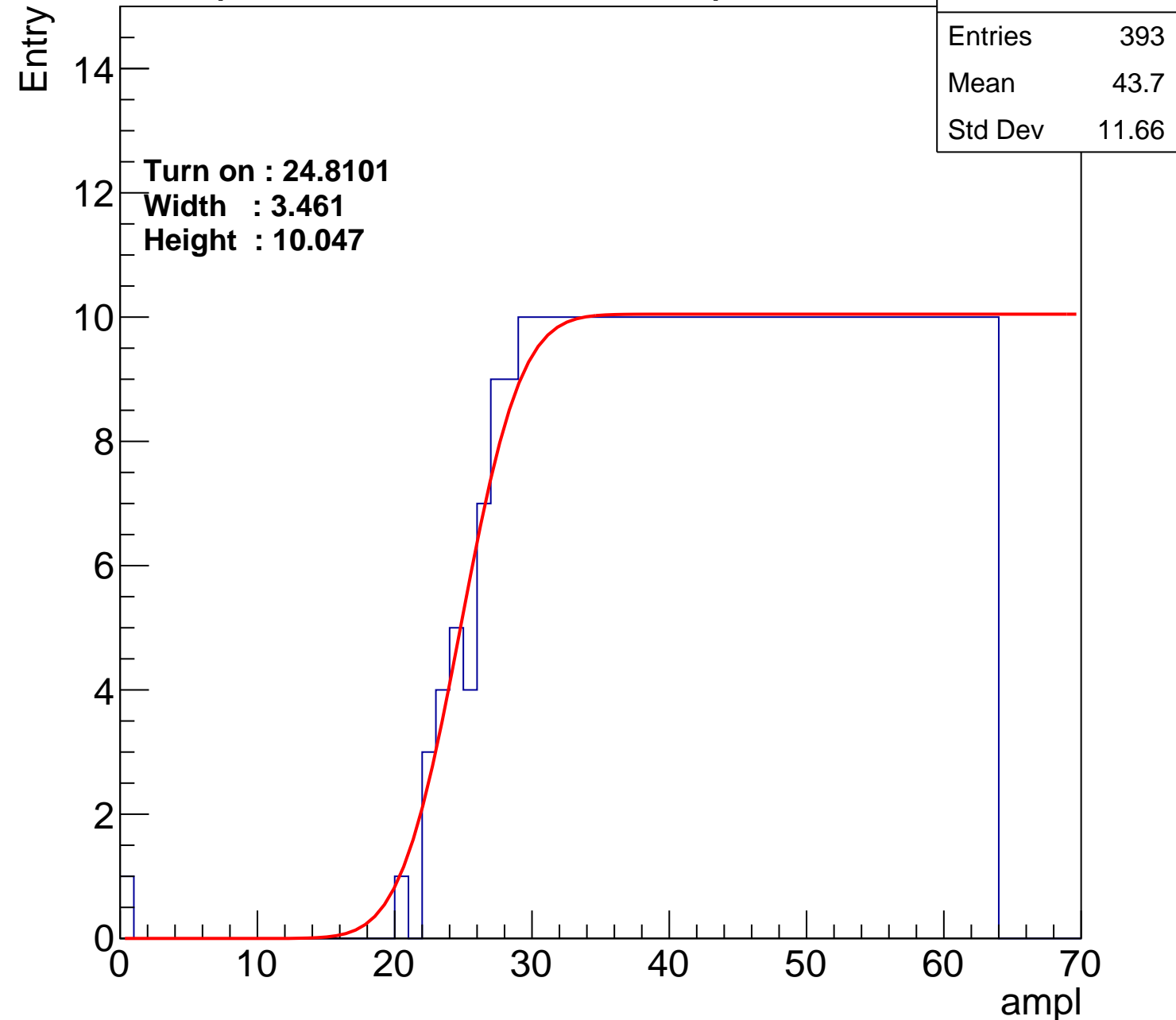
Width : 3.461

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch62

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.0927

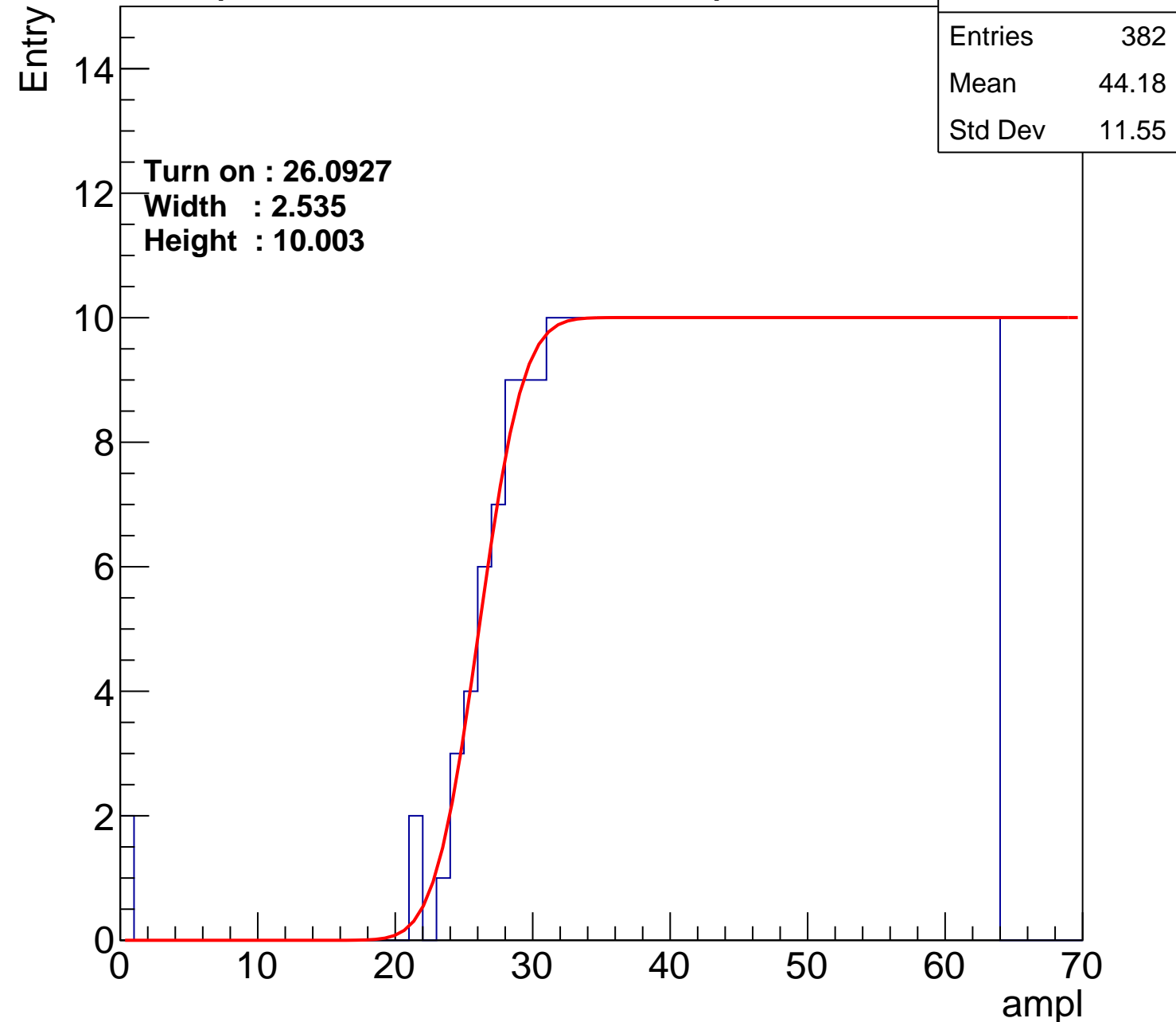
Width : 2.535

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch63

calib\_packv5\_042523\_0143.root, FC#10, port B3

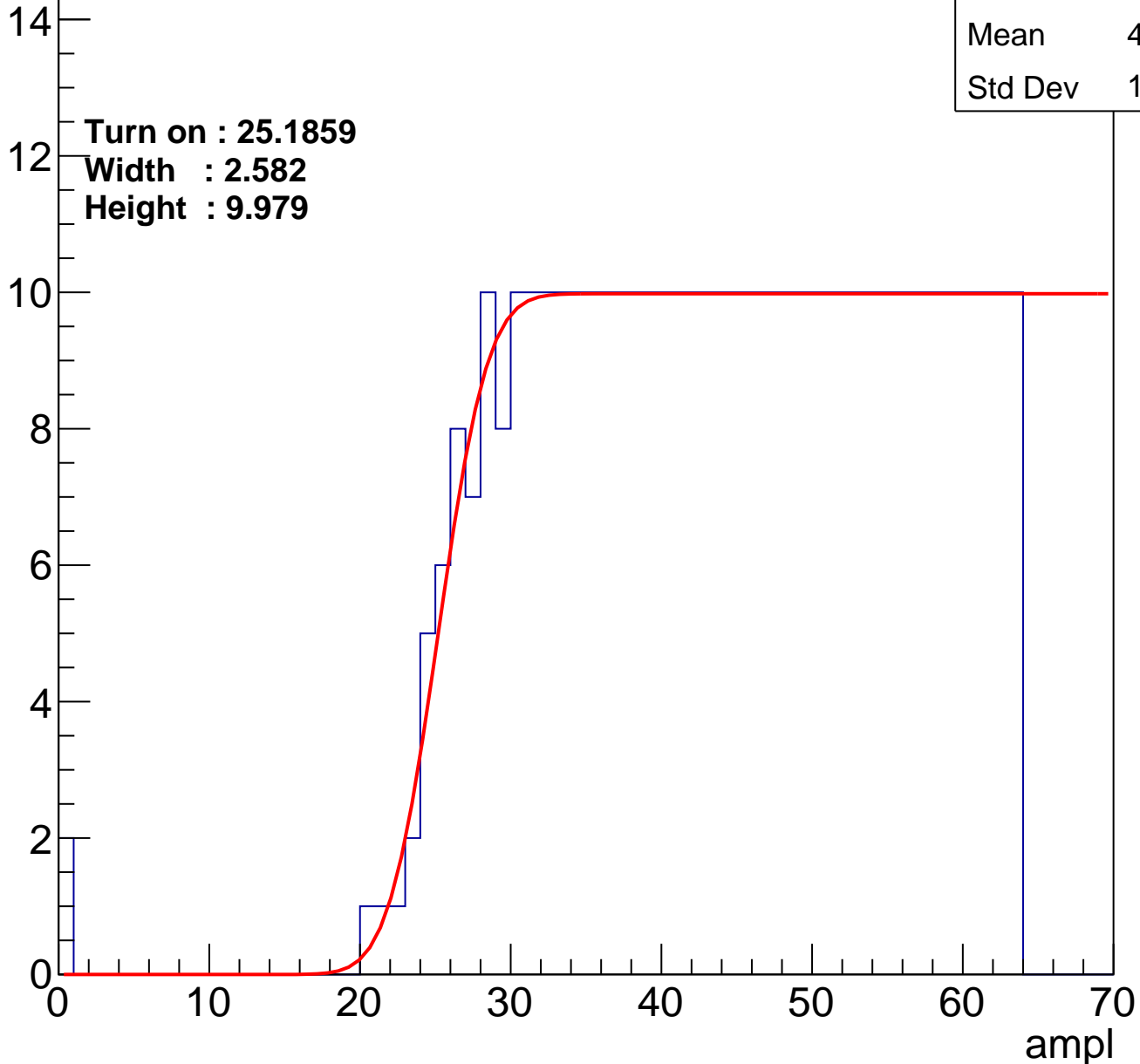
Entries	391
Mean	43.73
Std Dev	11.79

Turn on : 25.1859

Width : 2.582

Height : 9.979

Entry



# B1L002S, U12-ch64

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	399
Mean	43.25
Std Dev	12.25

Turn on : 24.6204

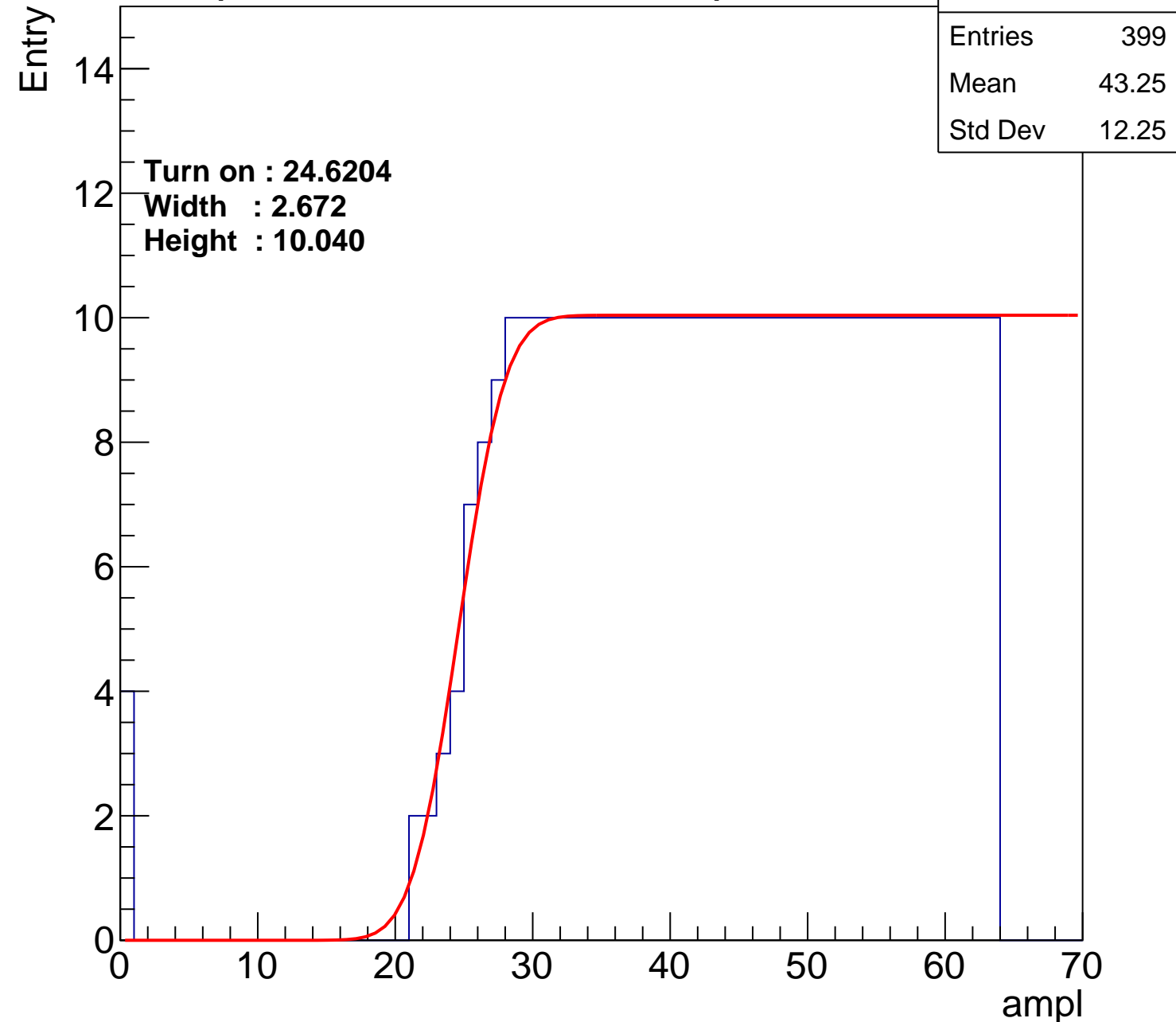
Width : 2.672

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch65

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.58
Std Dev	11.75

Turn on : 24.9692

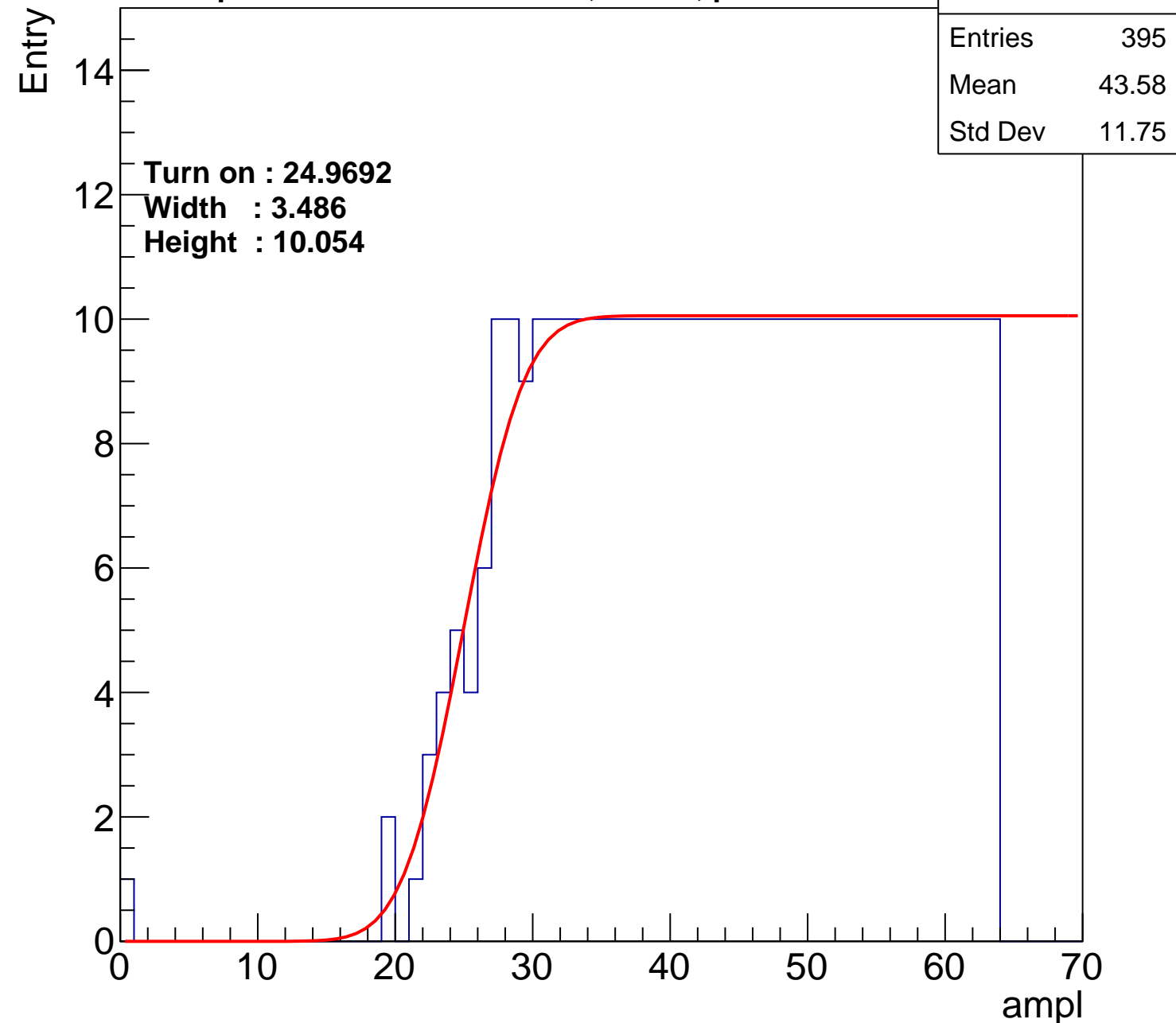
Width : 3.486

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch66

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.33
Std Dev	11.64

**Turn on : 26.2759**

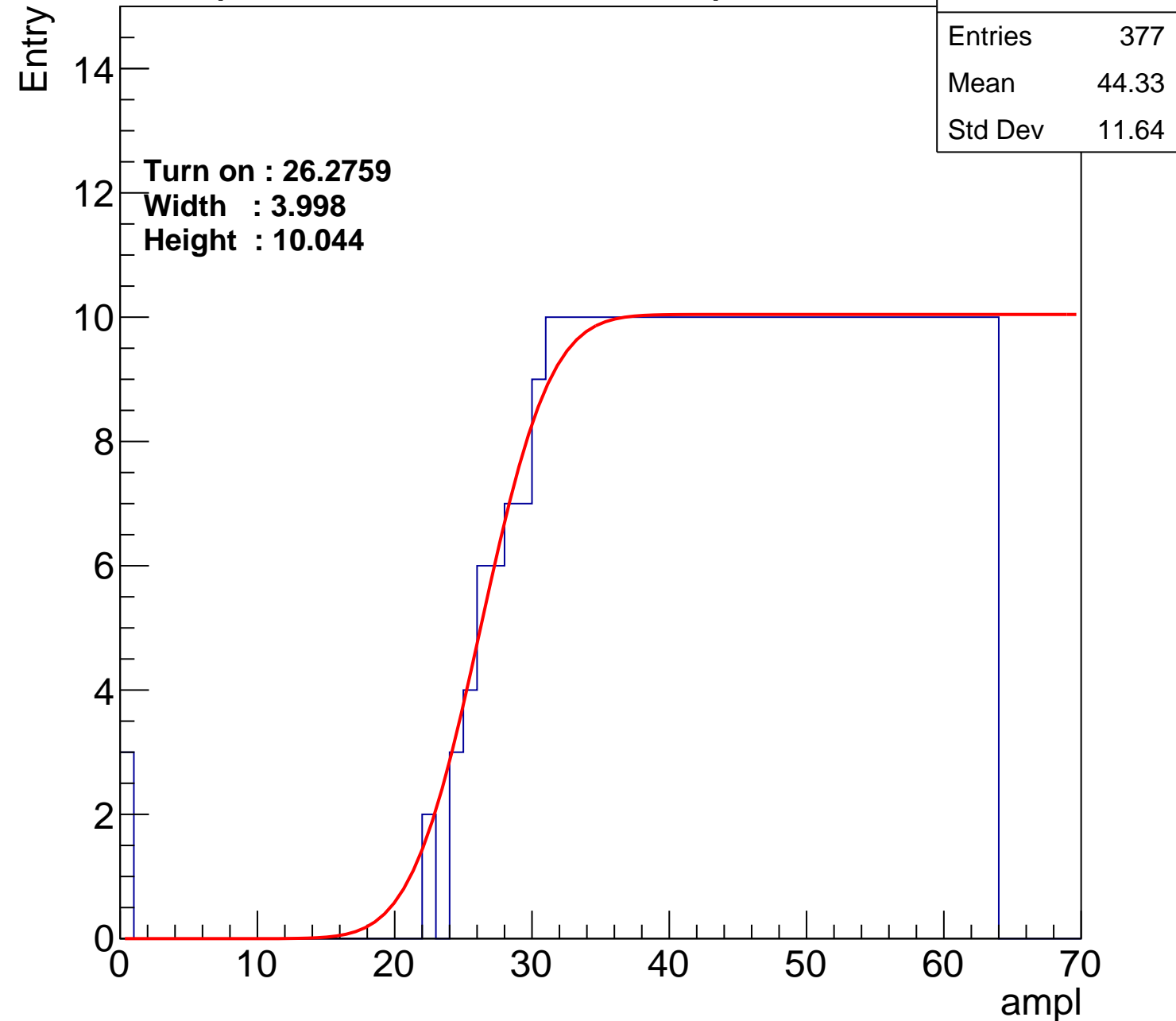
**Width : 3.998**

**Height : 10.044**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch67

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.8605

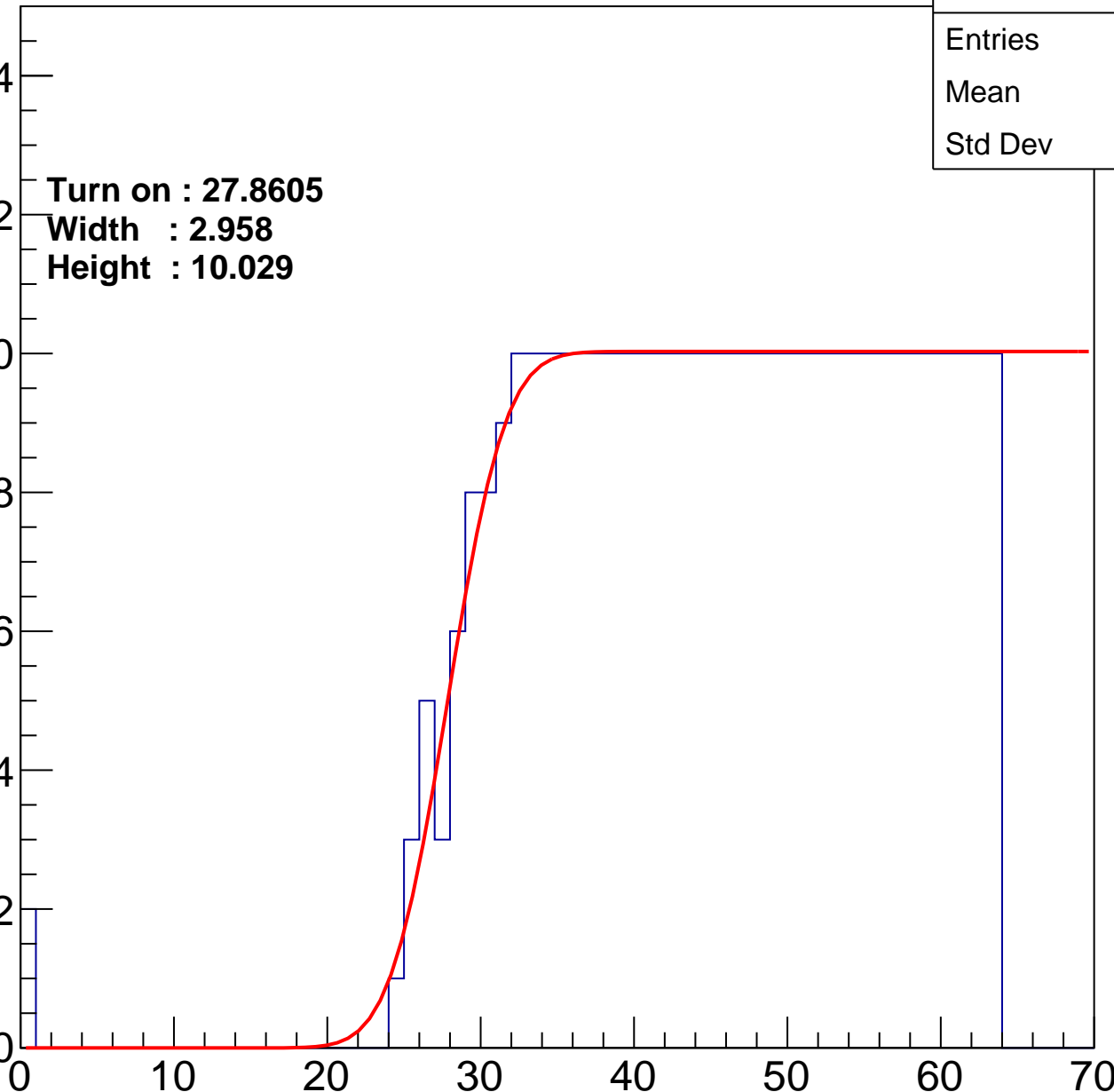
Width : 2.958

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch68

calib\_packv5\_042523\_0143.root, FC#10, port B3

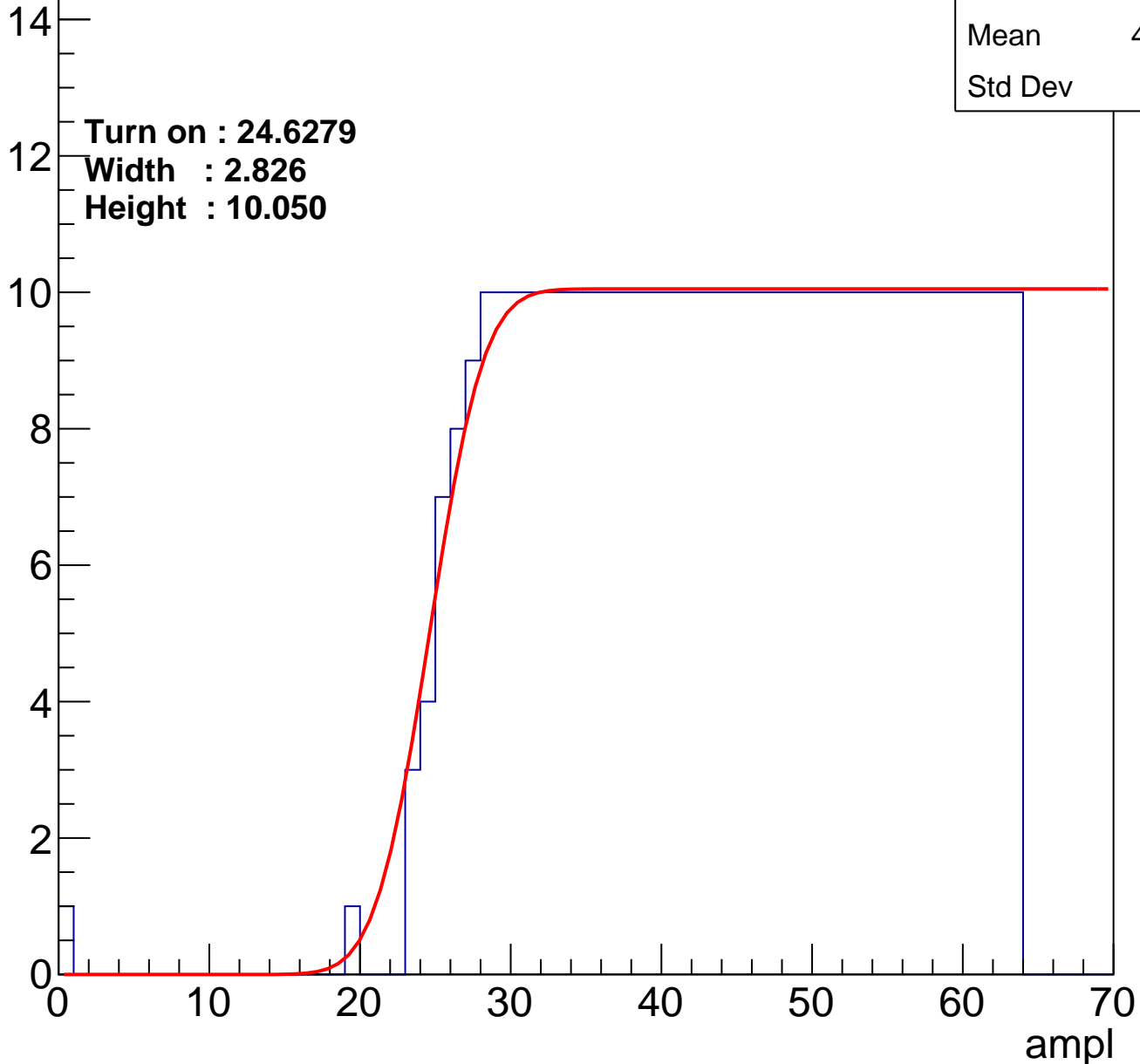
Entries	393
Mean	43.74
Std Dev	11.6

Turn on : 24.6279

Width : 2.826

Height : 10.050

Entry



# B1L002S, U12-ch69

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 24.9502

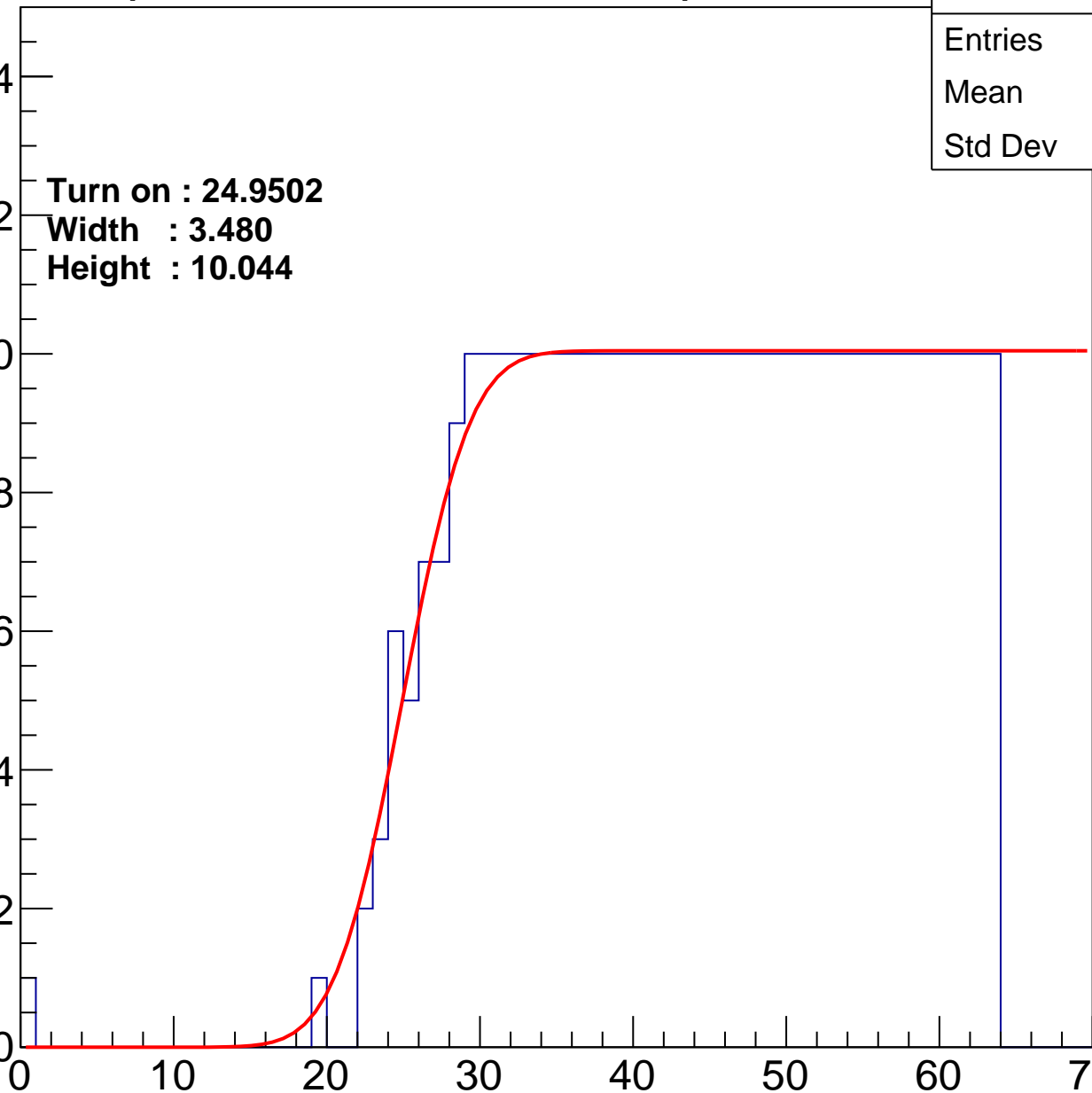
Width : 3.480

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch70

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 25.8671

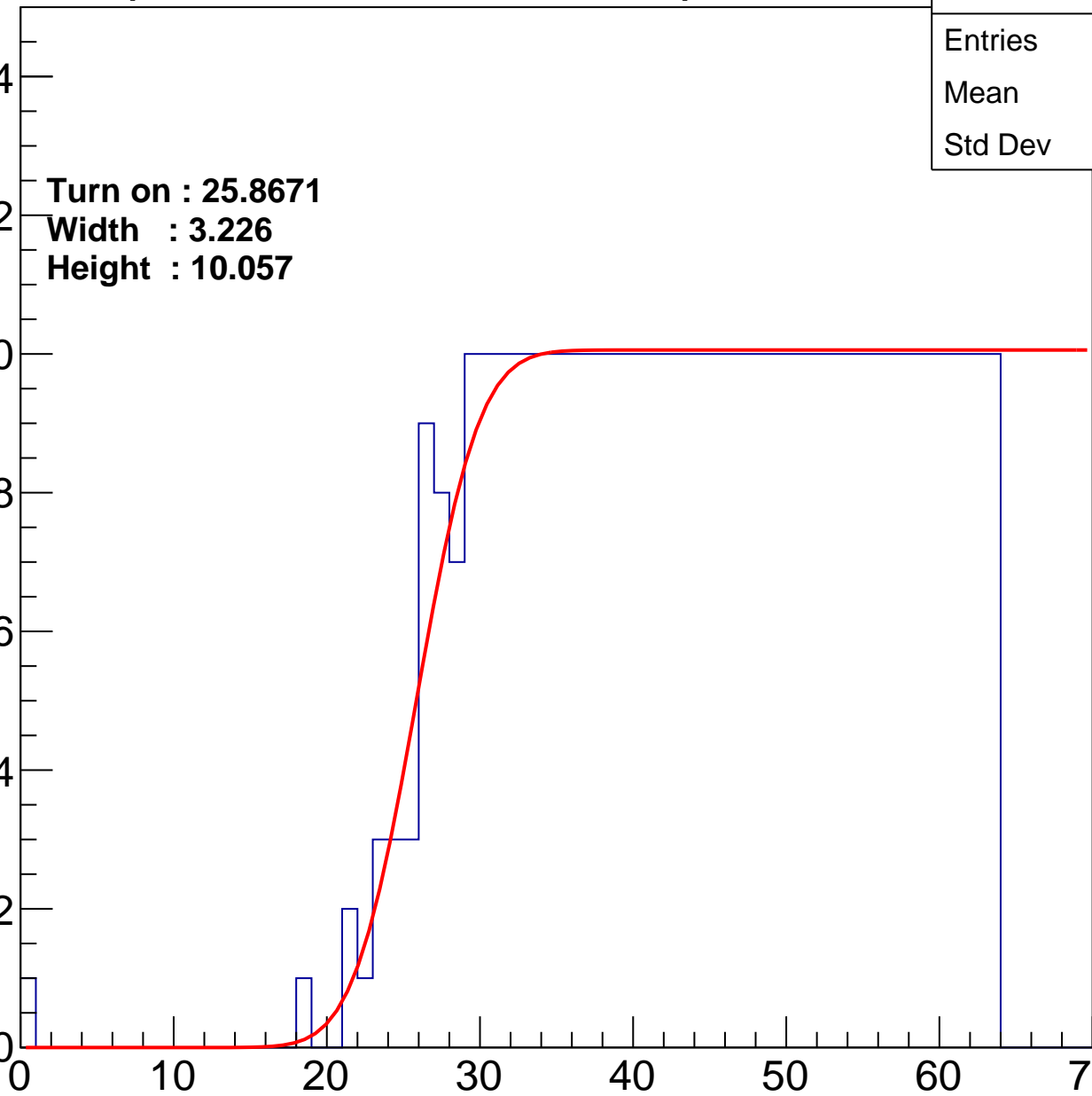
Width : 3.226

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch71

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.7505

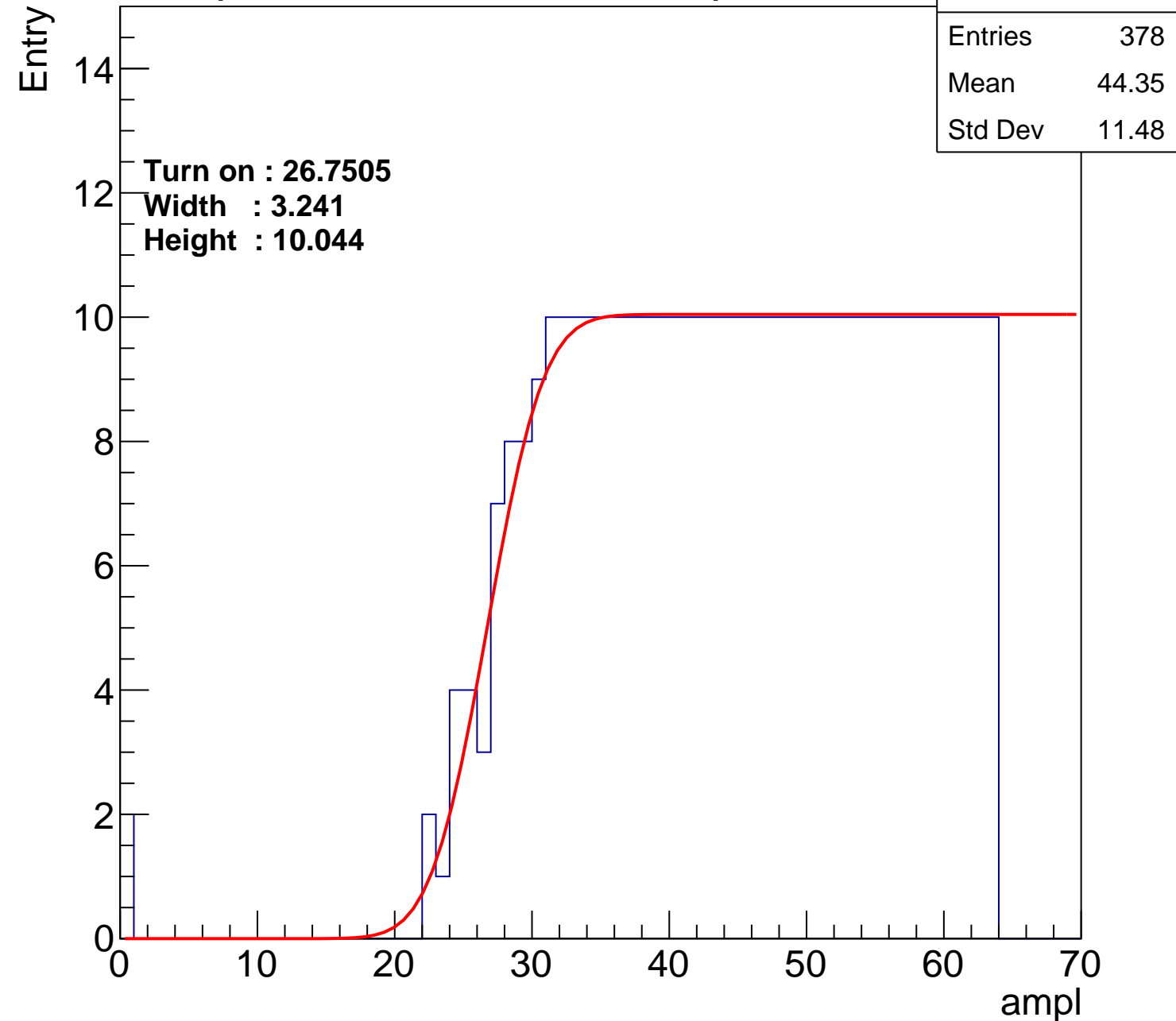
Width : 3.241

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch72

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	362
Mean	45.15
Std Dev	11.06

Turn on : 28.5850

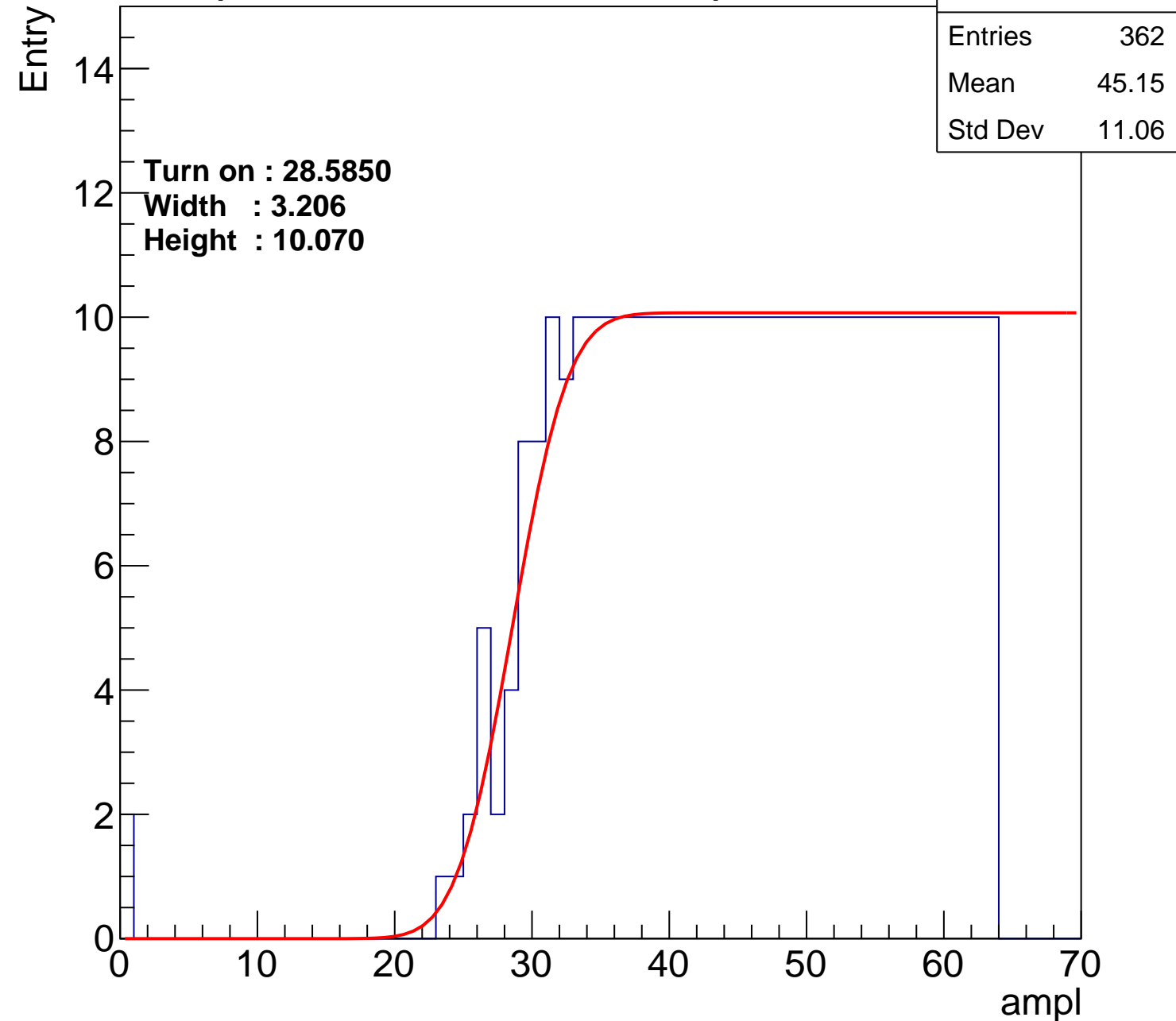
Width : 3.206

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch73

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	399
Mean	43.33
Std Dev	12

Turn on : 24.0841

Width : 3.918

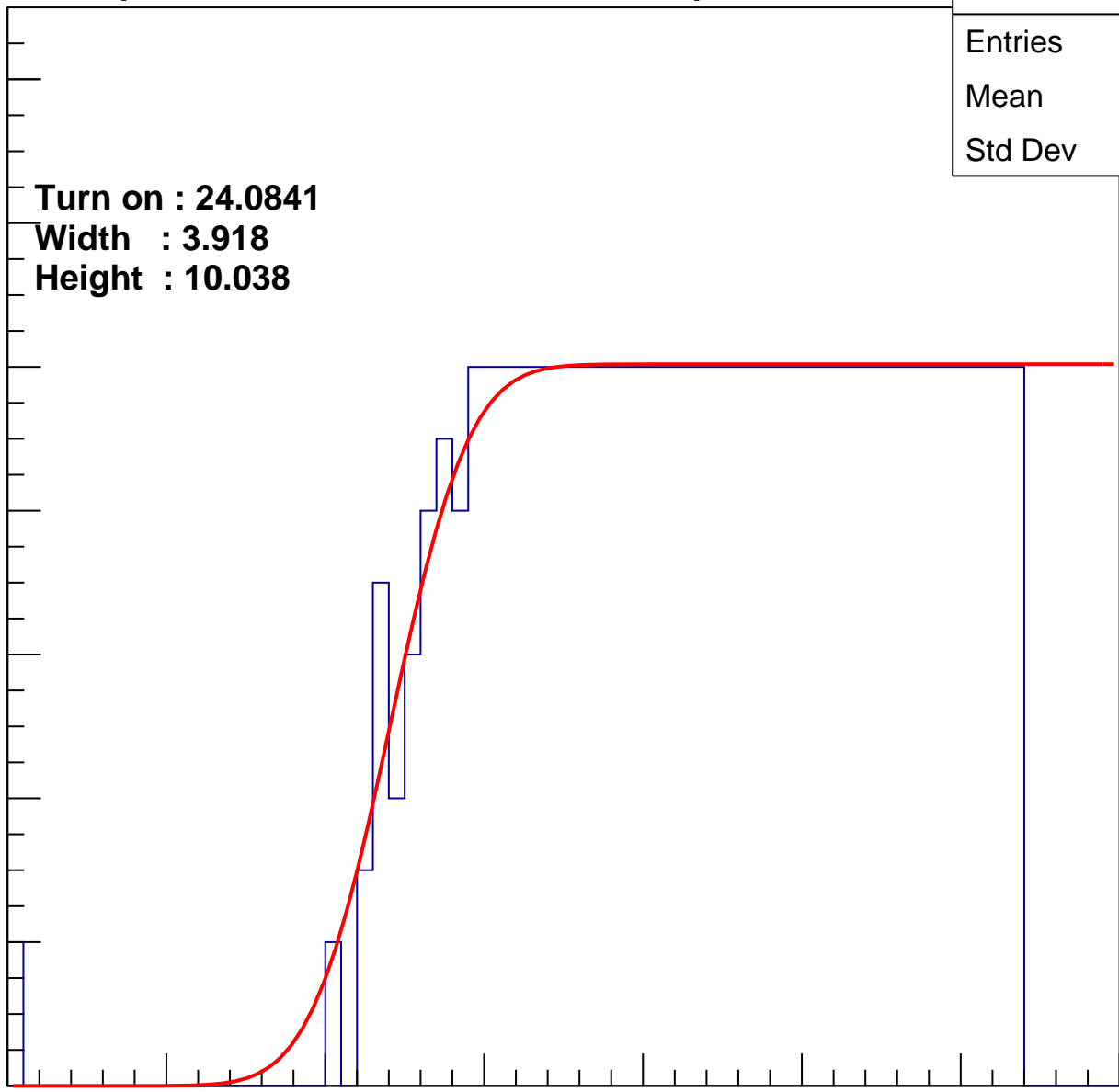
Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U12-ch74

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.62
Std Dev	11.69

Turn on : 24.9259

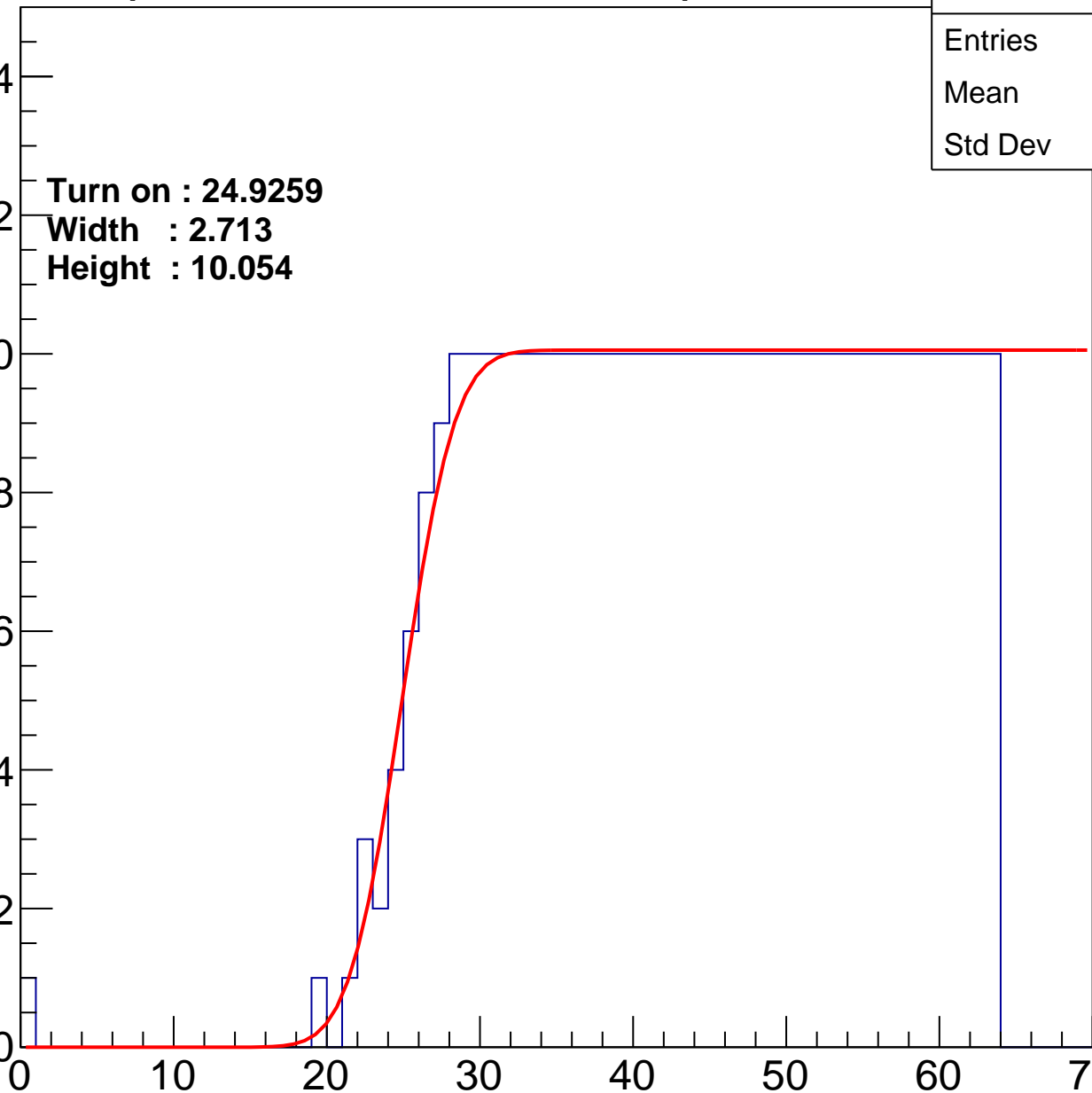
Width : 2.713

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch75

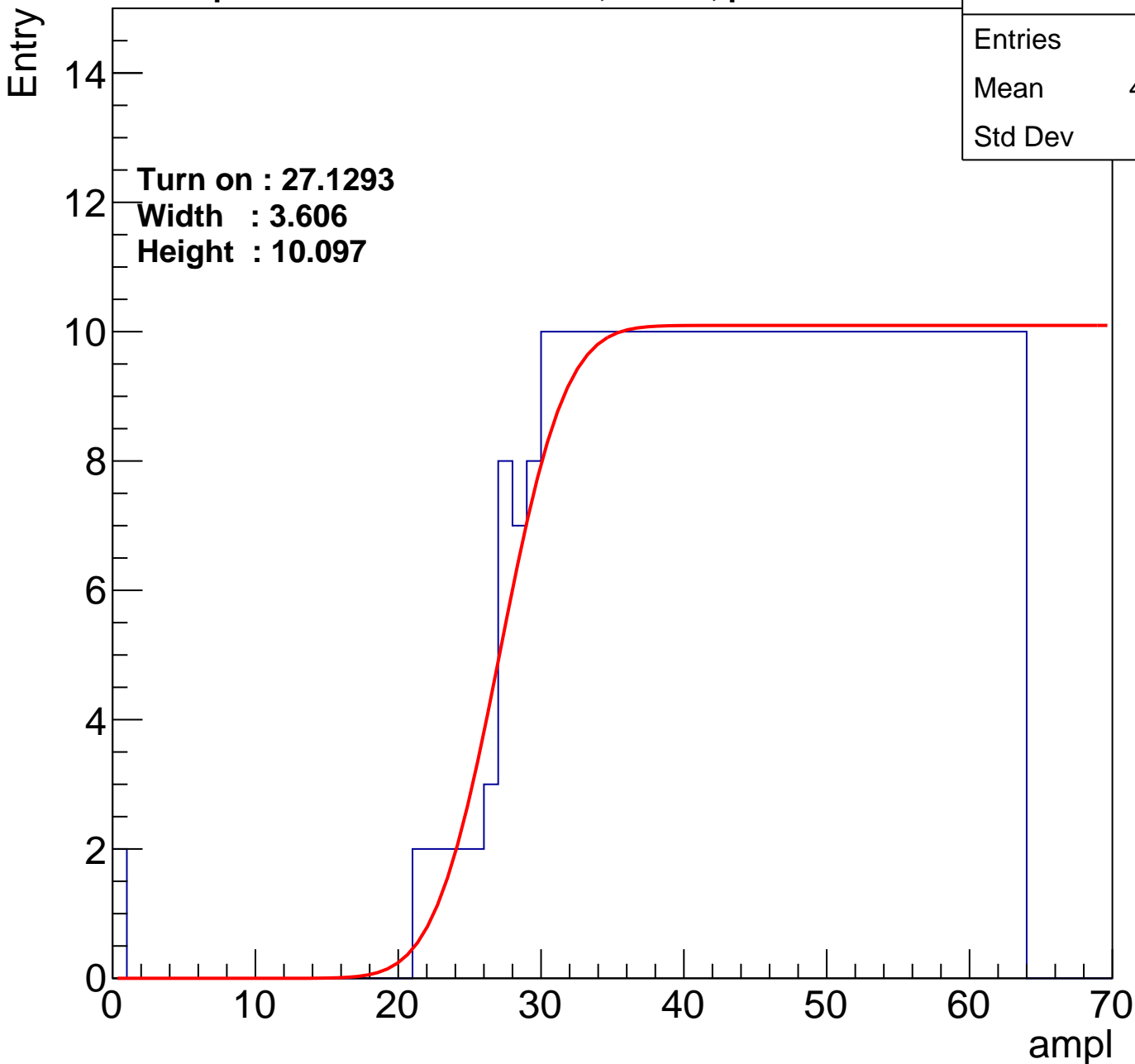
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 27.1293

Width : 3.606

Height : 10.097



# B1L002S, U12-ch76

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	415
Mean	42.54
Std Dev	12.42

Turn on : 22.9803

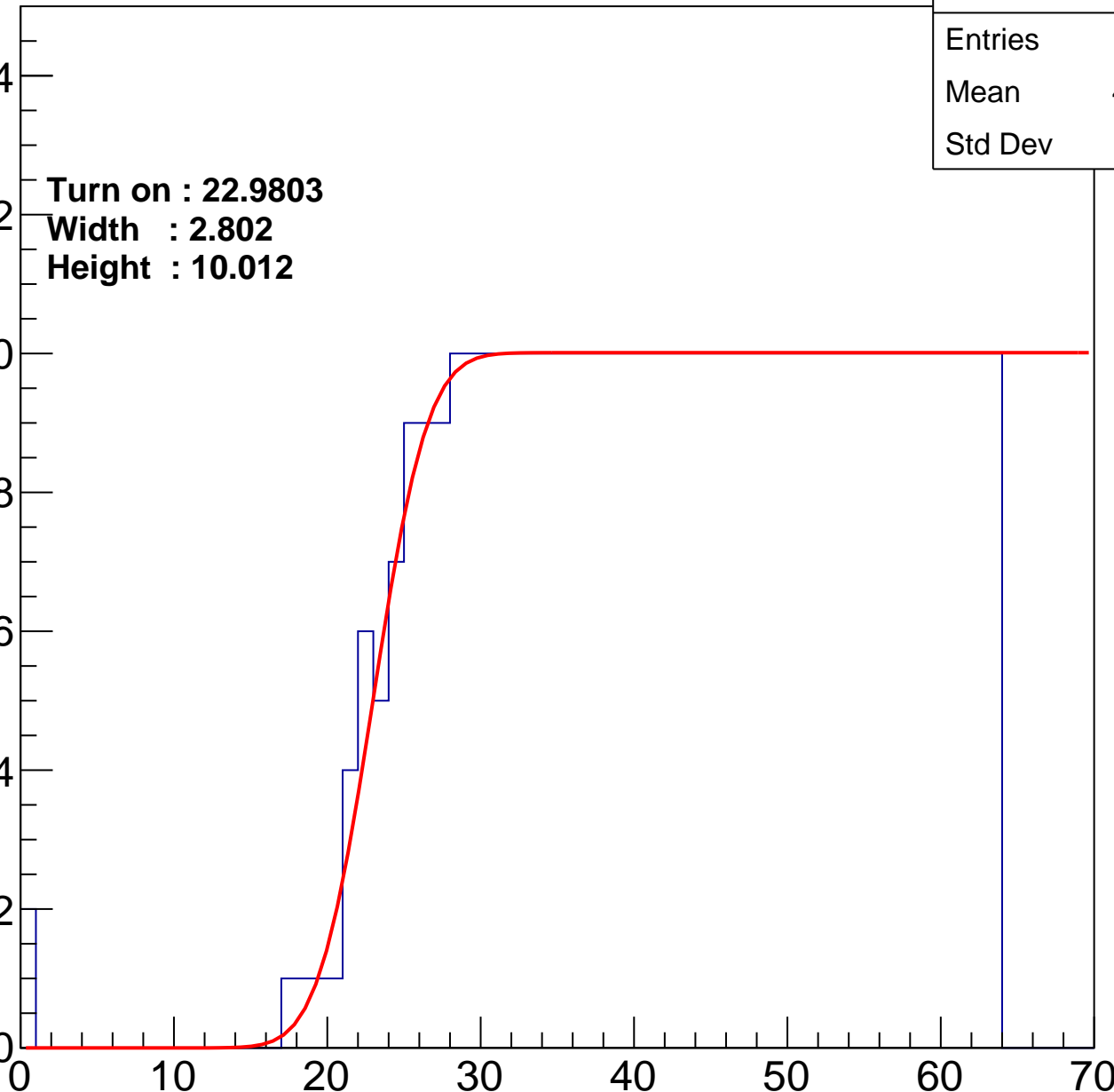
Width : 2.802

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch77

calib\_packv5\_042523\_0143.root, FC#10, port B3

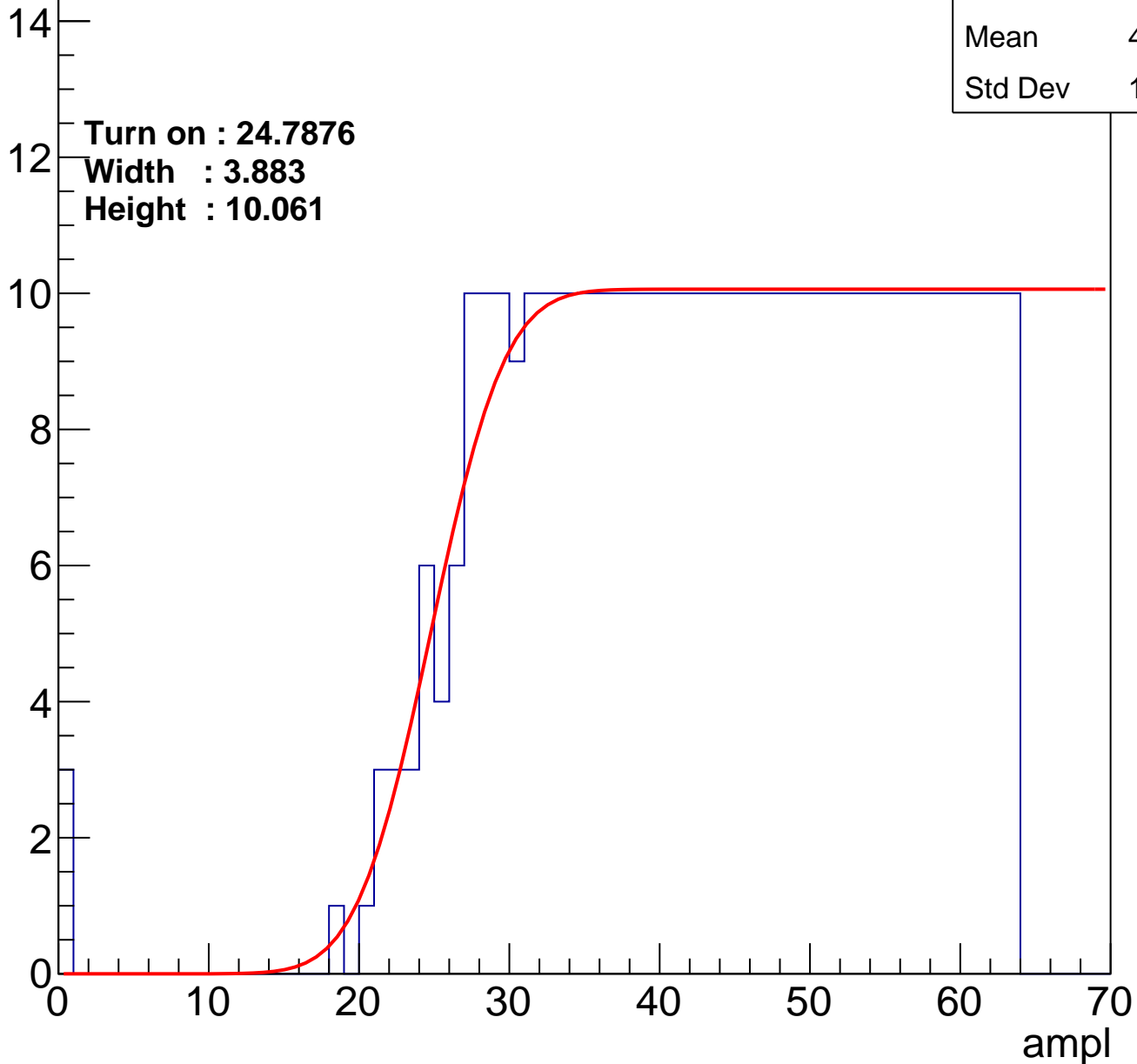
Entries	399
Mean	43.25
Std Dev	12.19

Turn on : 24.7876

Width : 3.883

Height : 10.061

Entry



# B1L002S, U12-ch78

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	414
Mean	42.57
Std Dev	12.48

Turn on : 22.8121

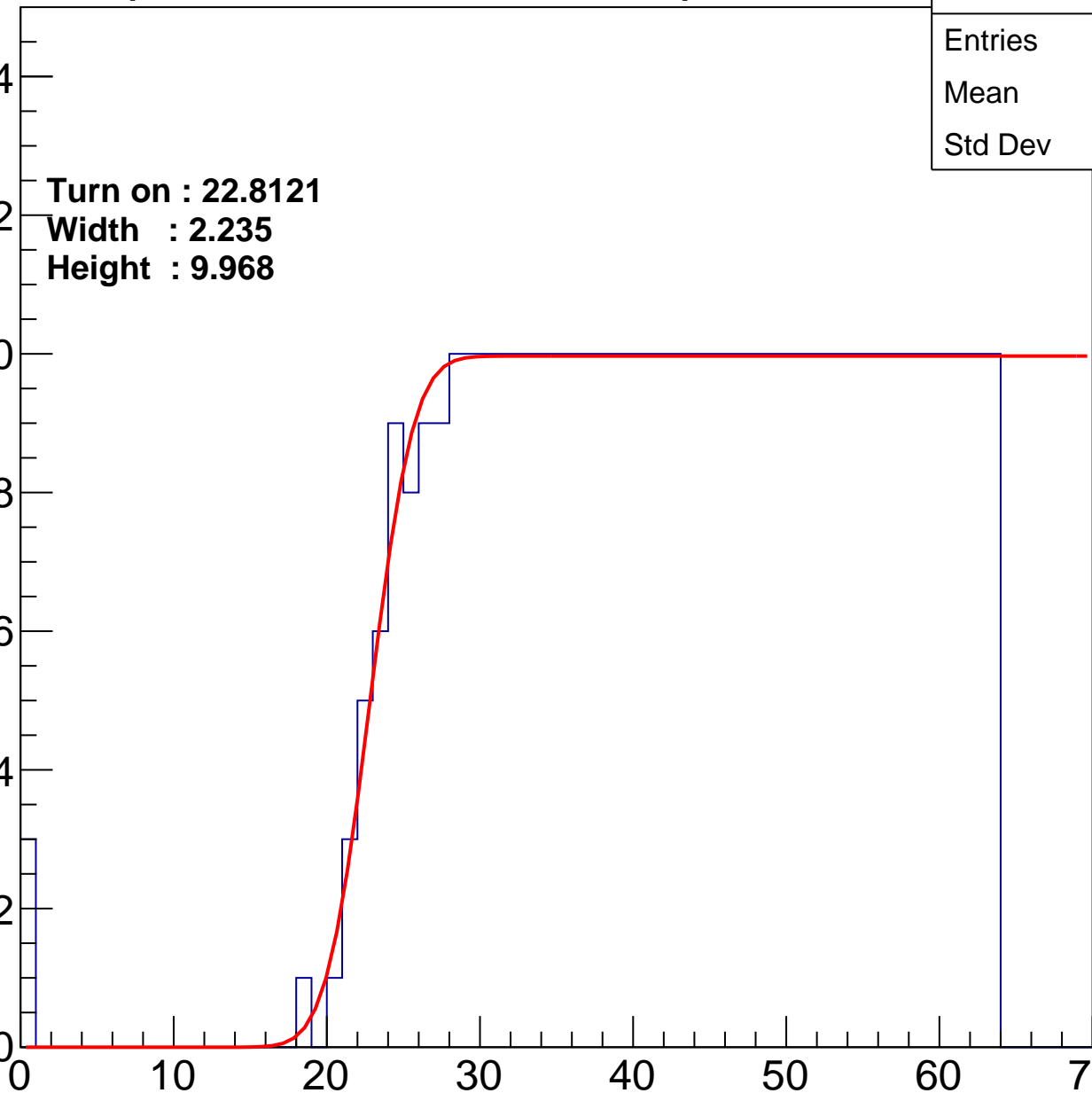
Width : 2.235

Height : 9.968

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch79

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	43.04
Std Dev	12.16

Turn on : 24.1470

Width : 2.609

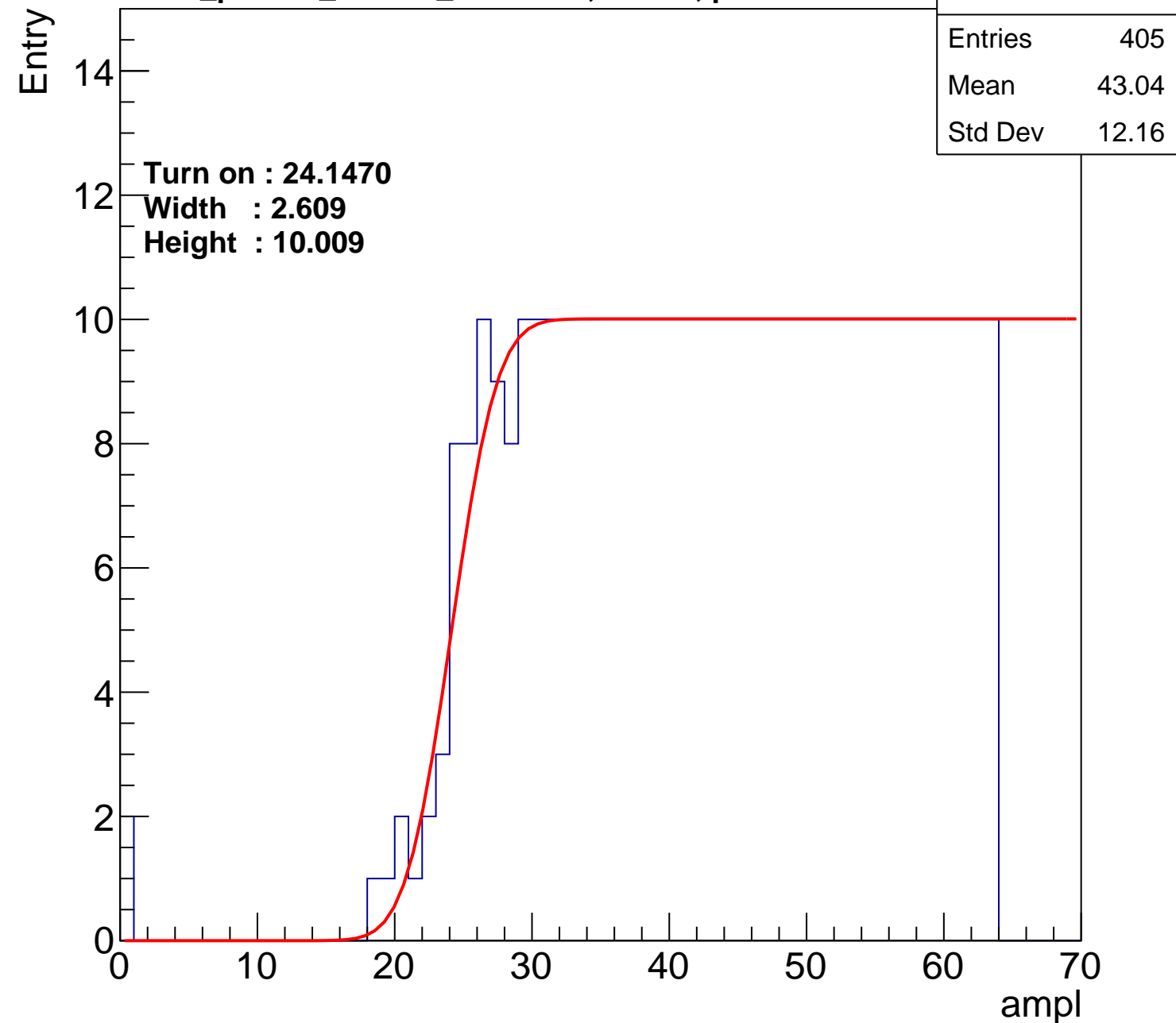
Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U12-ch80

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.84
Std Dev	11.95

Turn on : 25.4896

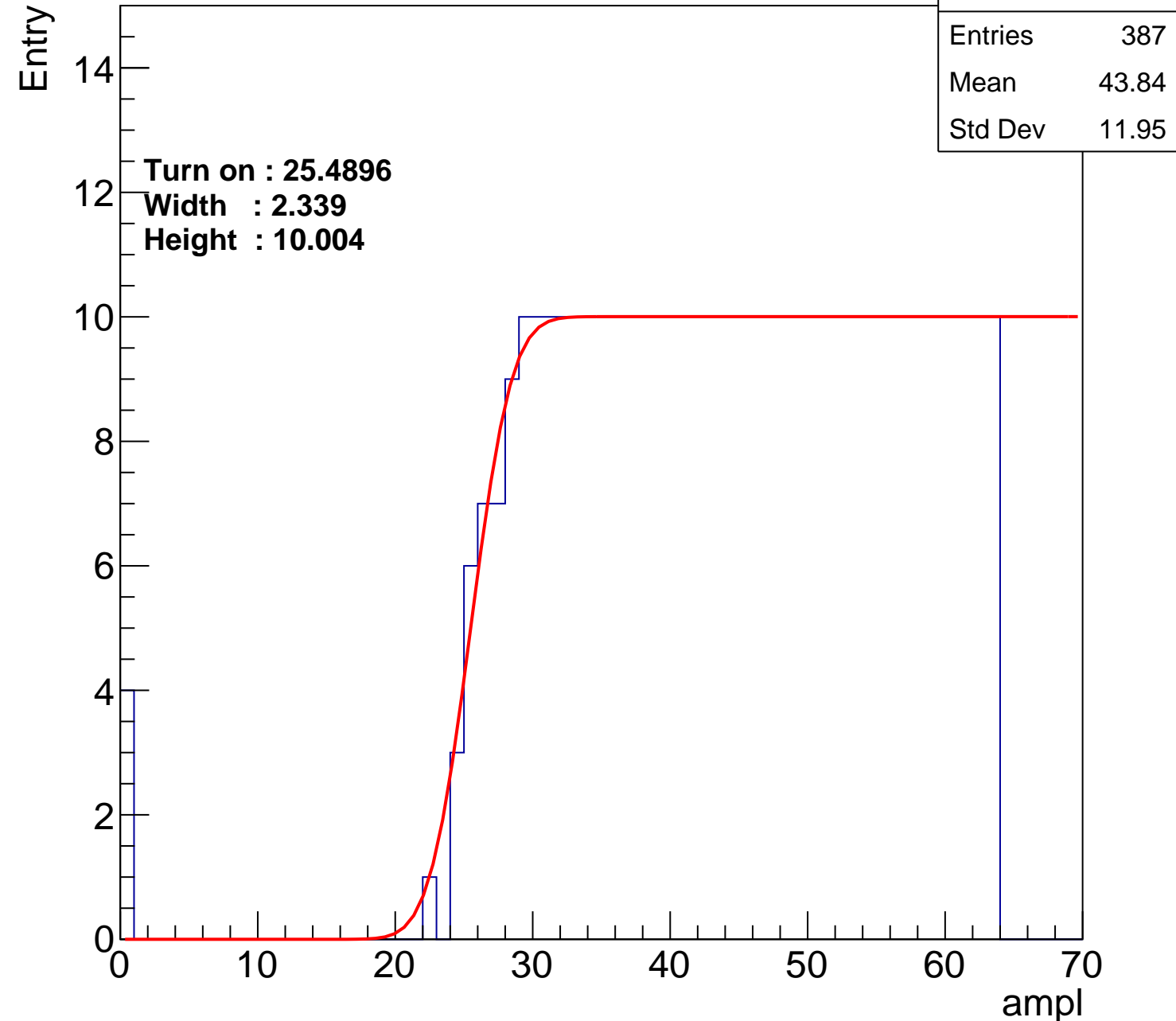
Width : 2.339

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch81

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.51
Std Dev	11.98

Turn on : 24.7856

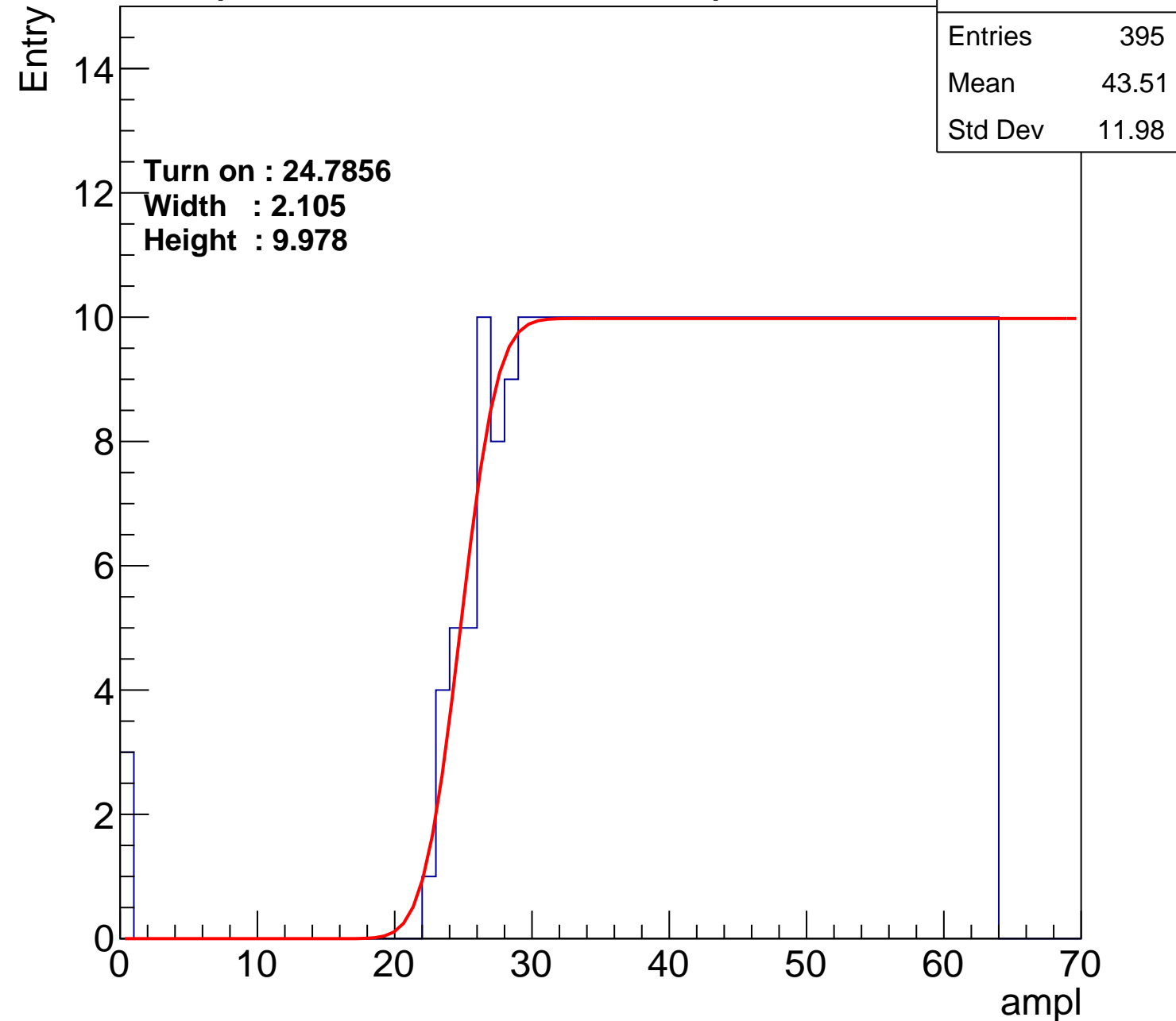
Width : 2.105

Height : 9.978

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch82

calib\_packv5\_042523\_0143.root, FC#10, port B3

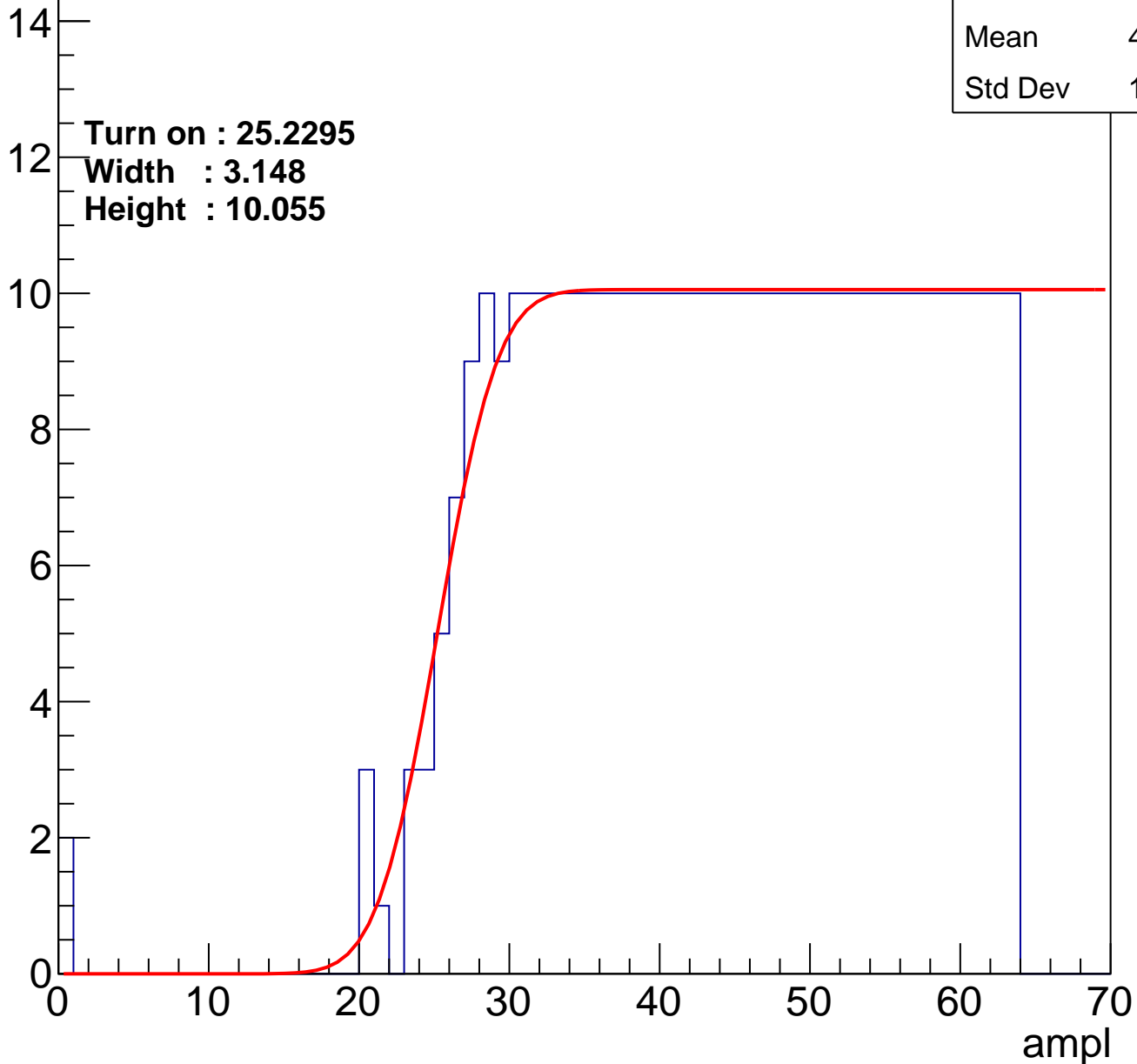
Entries	392
Mean	43.68
Std Dev	11.82

Turn on : 25.2295

Width : 3.148

Height : 10.055

Entry



# B1L002S, U12-ch83

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	43.83
Std Dev	12.14

Turn on : 26.2896

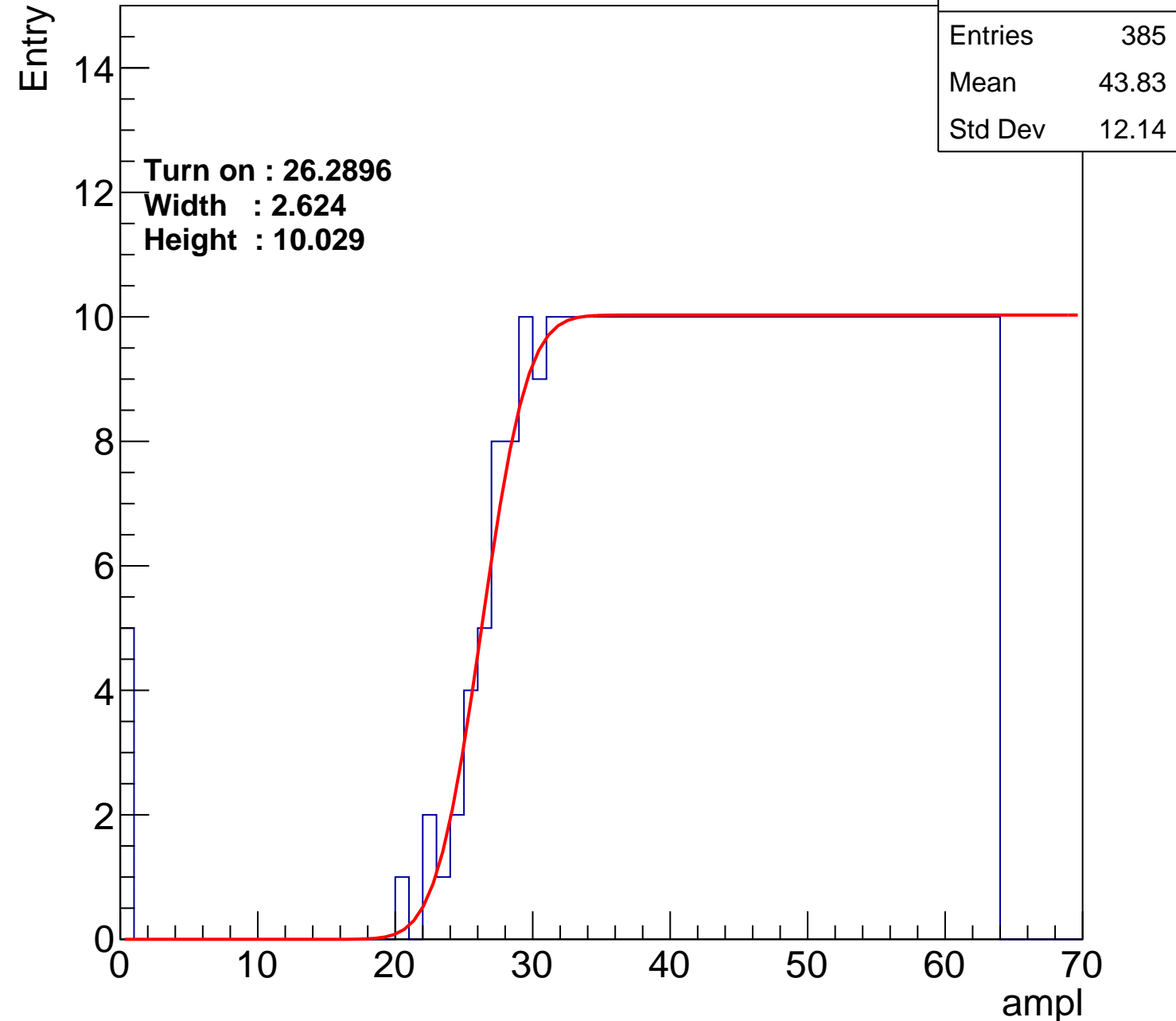
Width : 2.624

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch84

calib\_packv5\_042523\_0143.root, FC#10, port B3

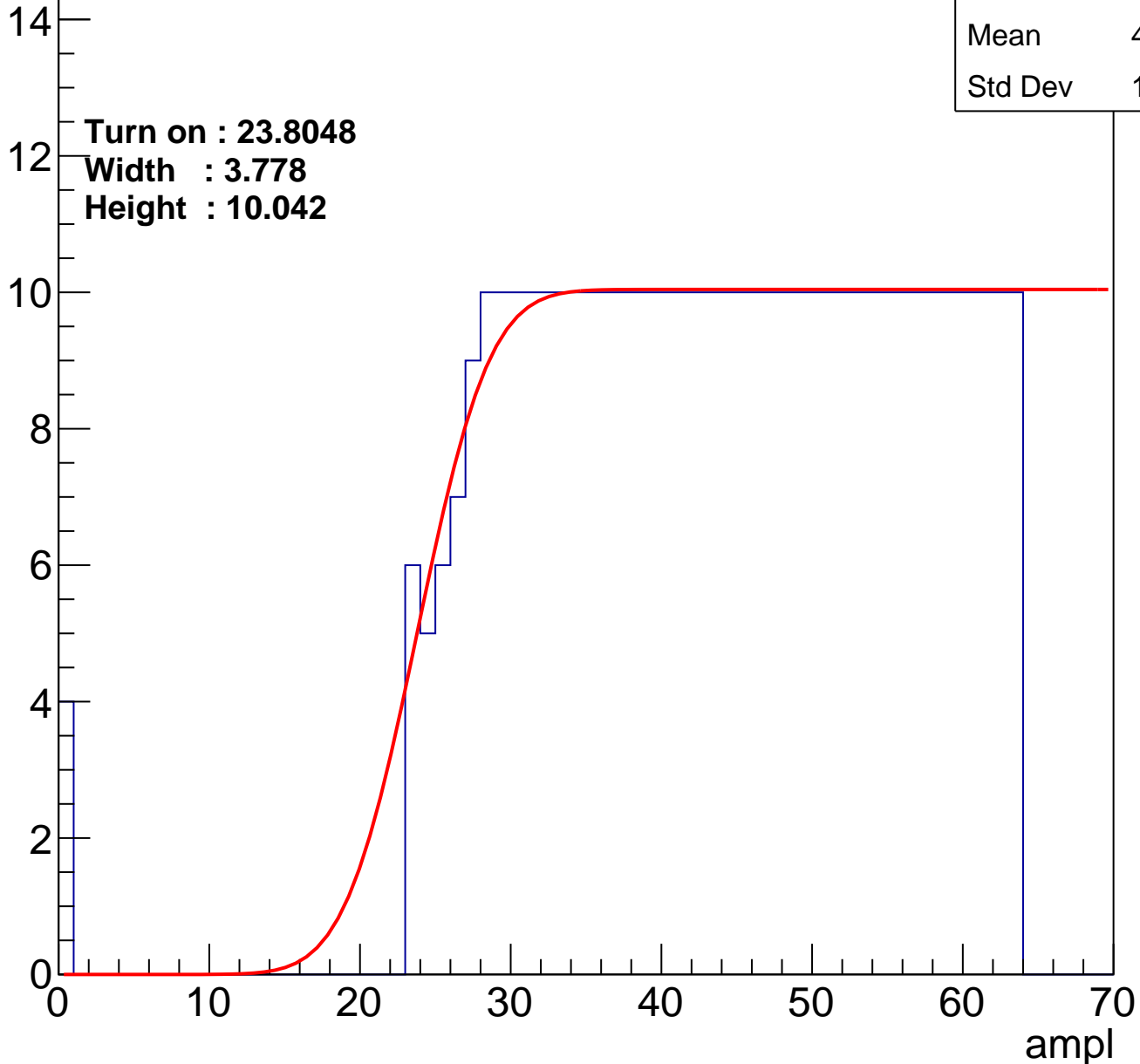
Entries	397
Mean	43.36
Std Dev	12.18

Turn on : 23.8048

Width : 3.778

Height : 10.042

Entry



# B1L002S, U12-ch85

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	411
Mean	42.73
Std Dev	12.39

Turn on : 23.6659

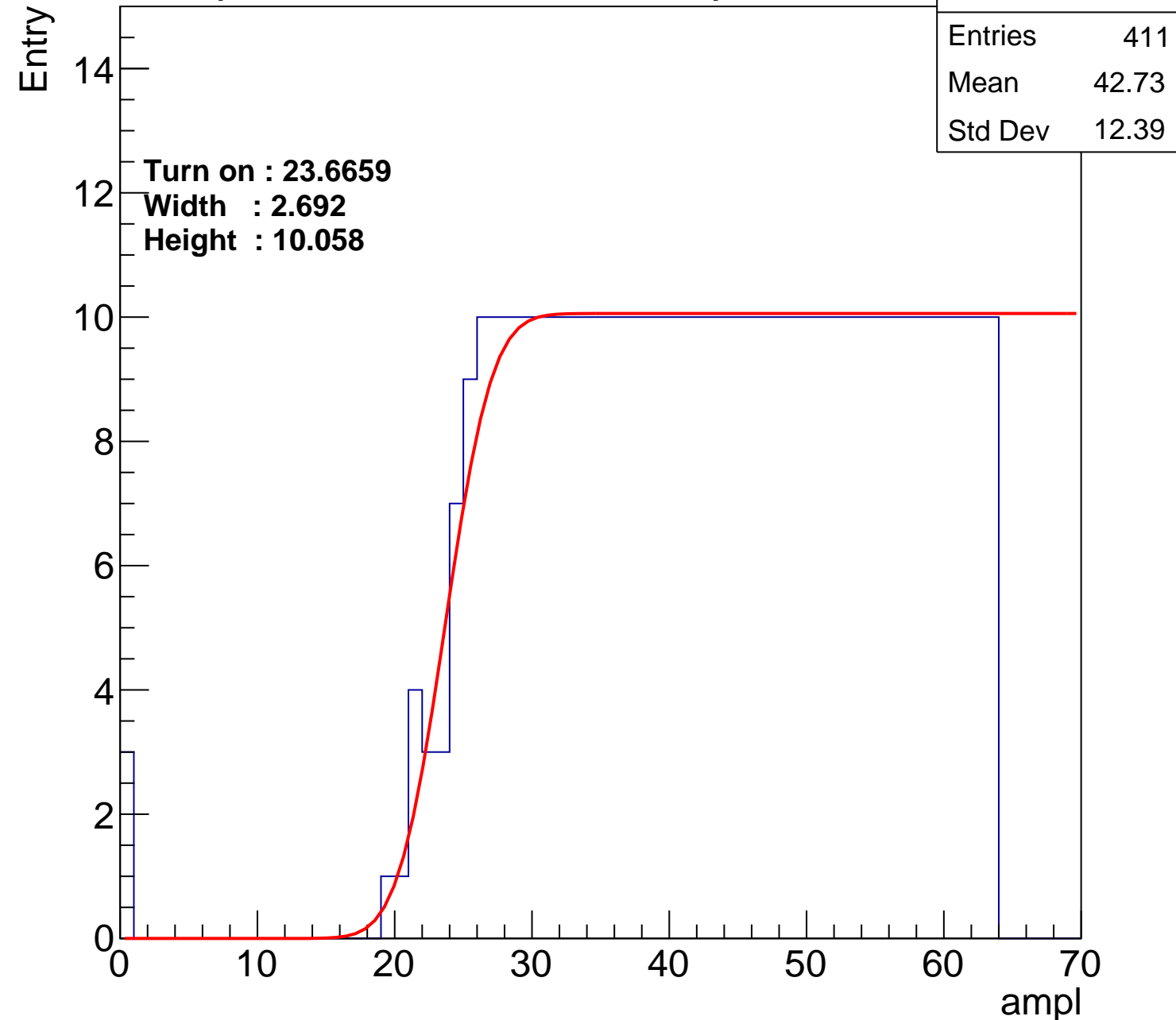
Width : 2.692

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch86

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.2868

Width : 2.699

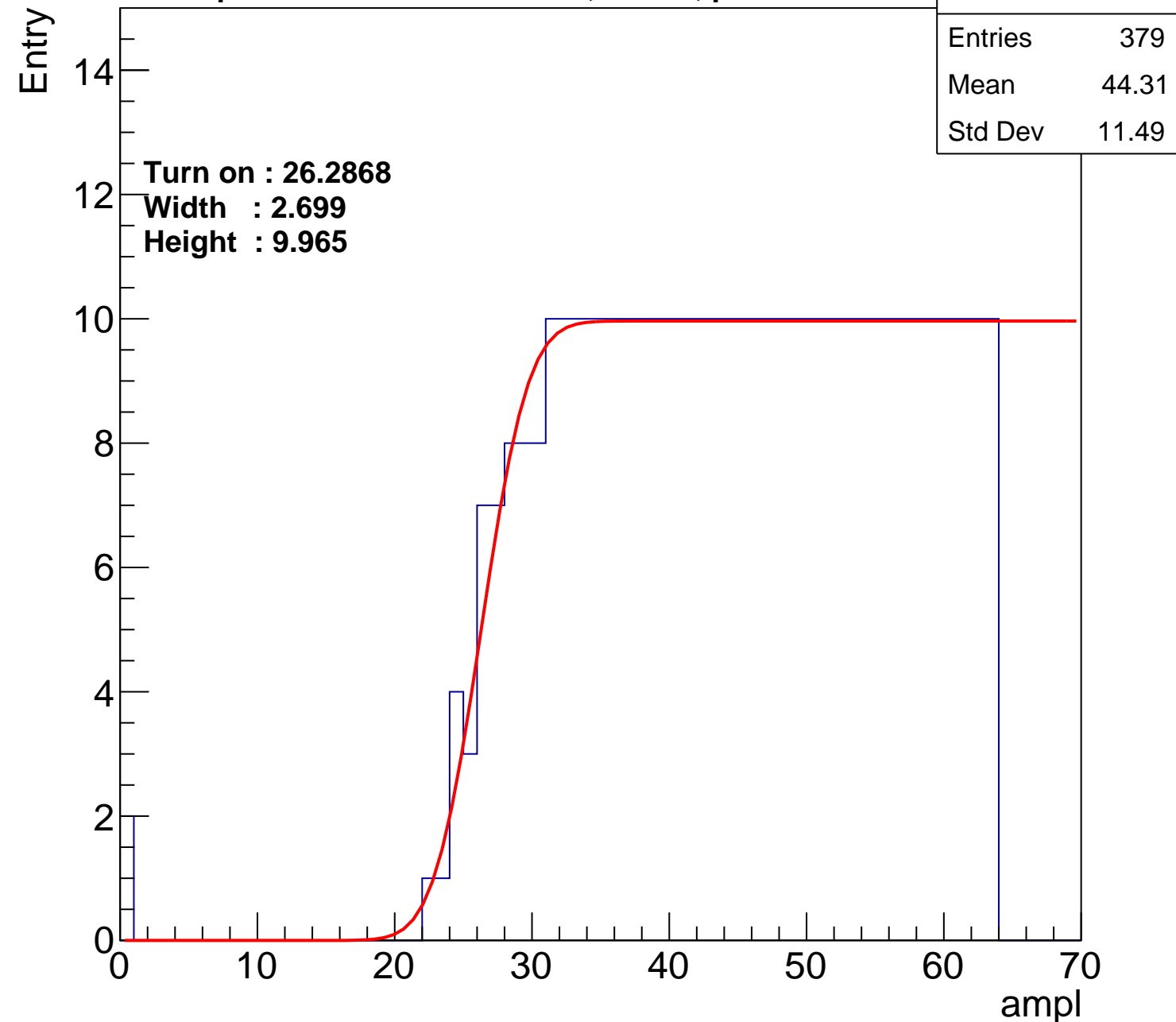
Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B1L002S, U12-ch87

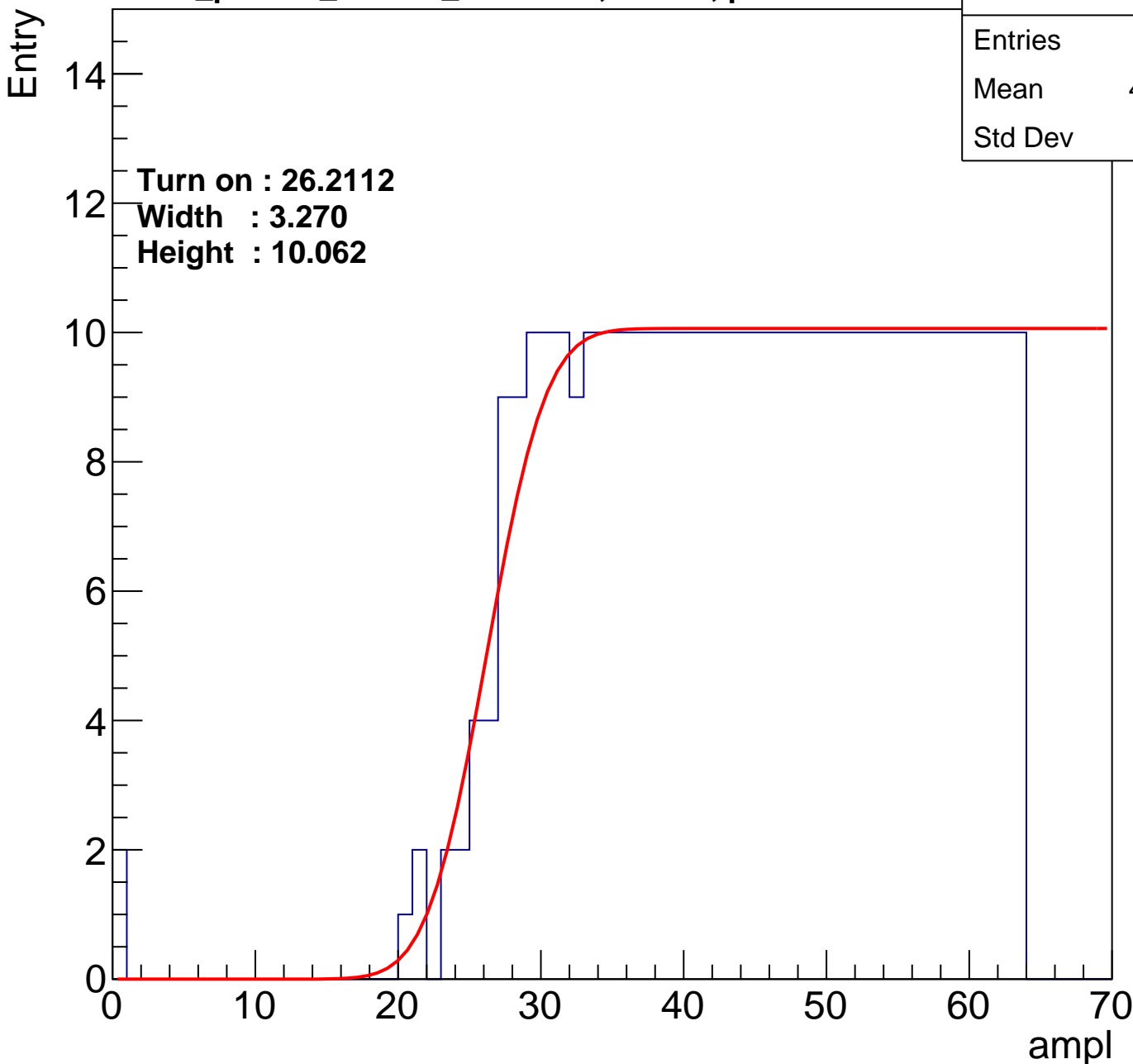
**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Entries	384
Mean	44.07
Std Dev	11.61

**Turn on : 26.2112**

**Width : 3.270**

**Height : 10.062**



# B1L002S, U12-ch88

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.75
Std Dev	11.87

Turn on : 25.4456

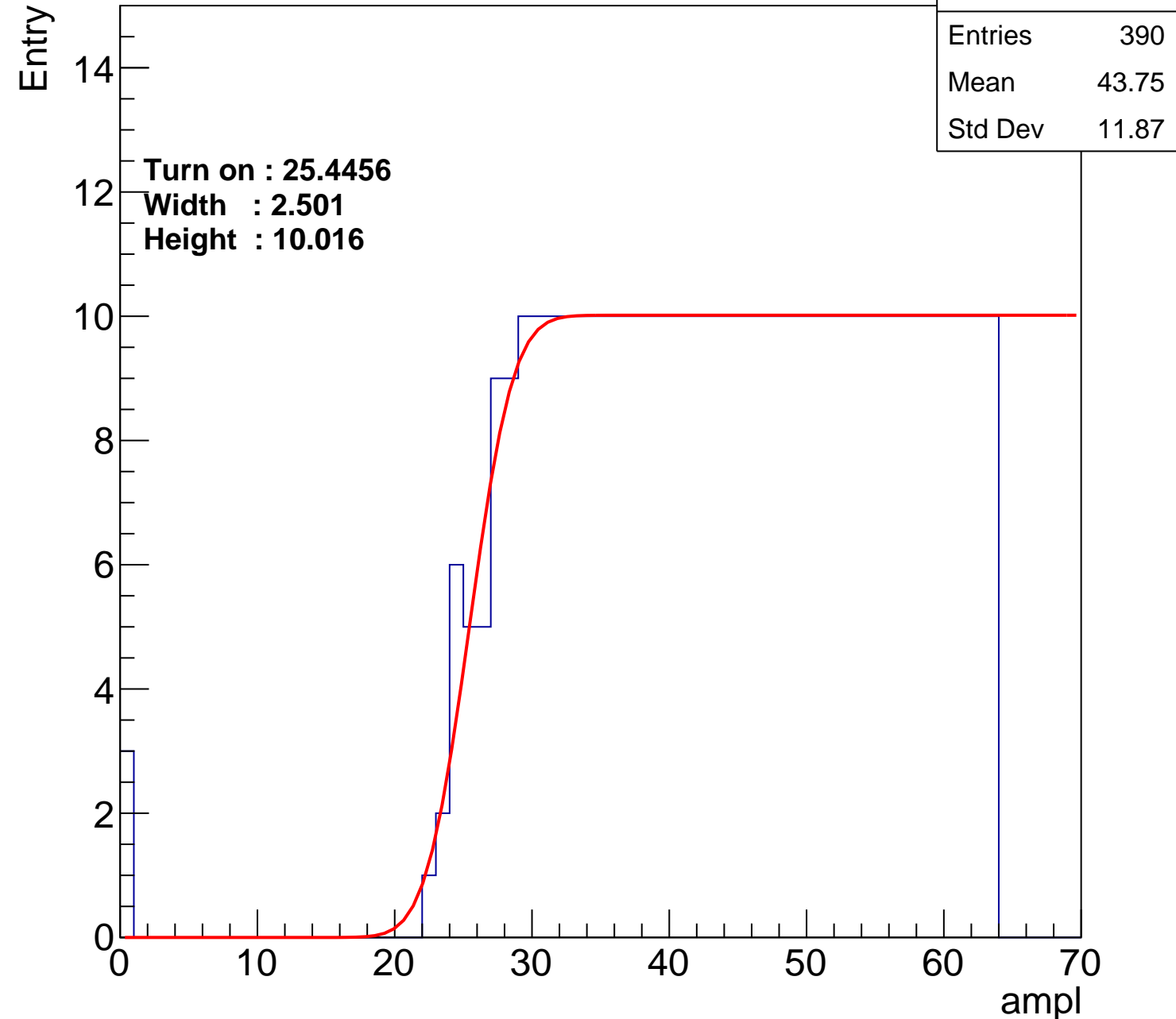
Width : 2.501

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch89

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 24.9084

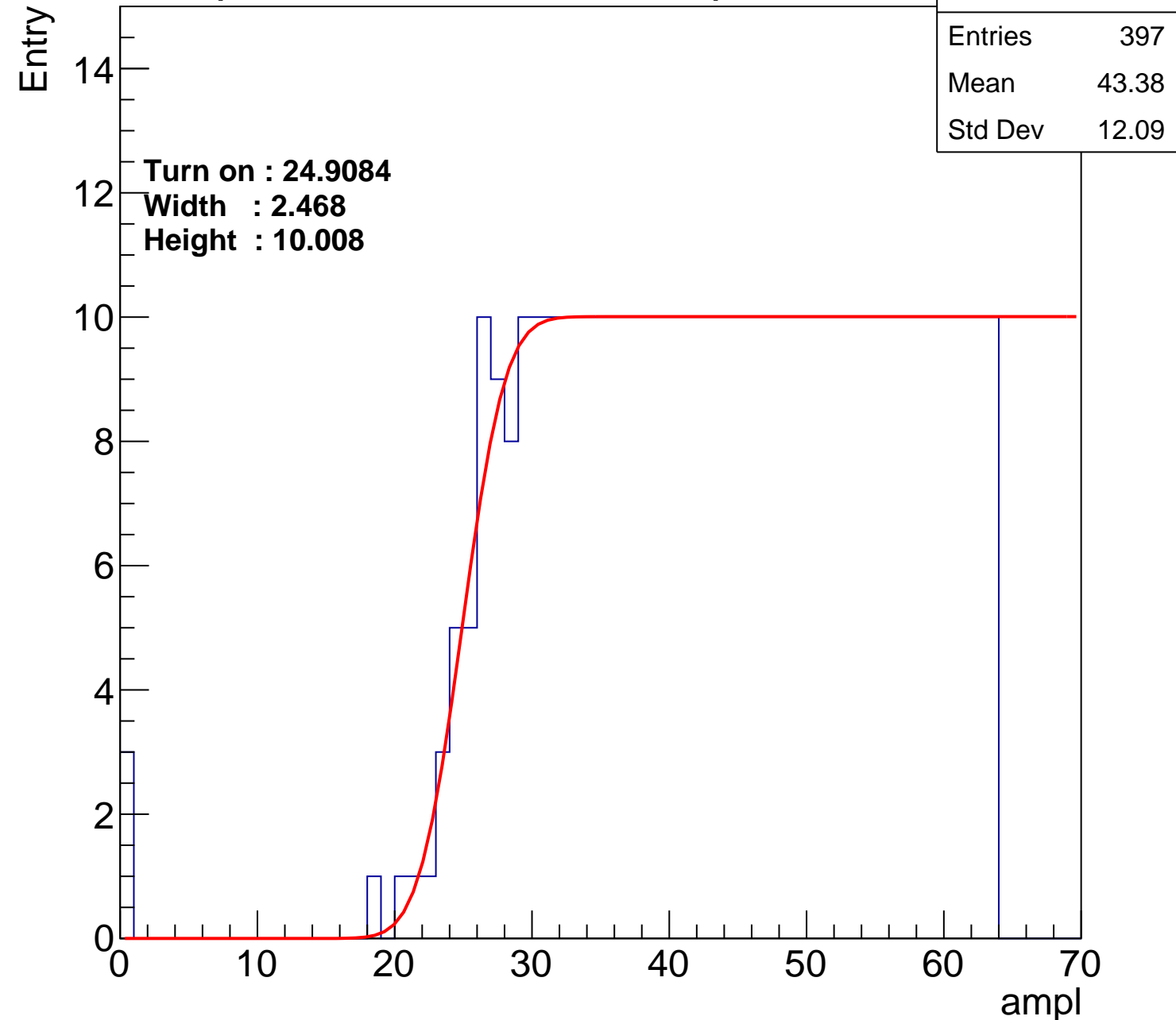
Width : 2.468

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch90

calib\_packv5\_042523\_0143.root, FC#10, port B3

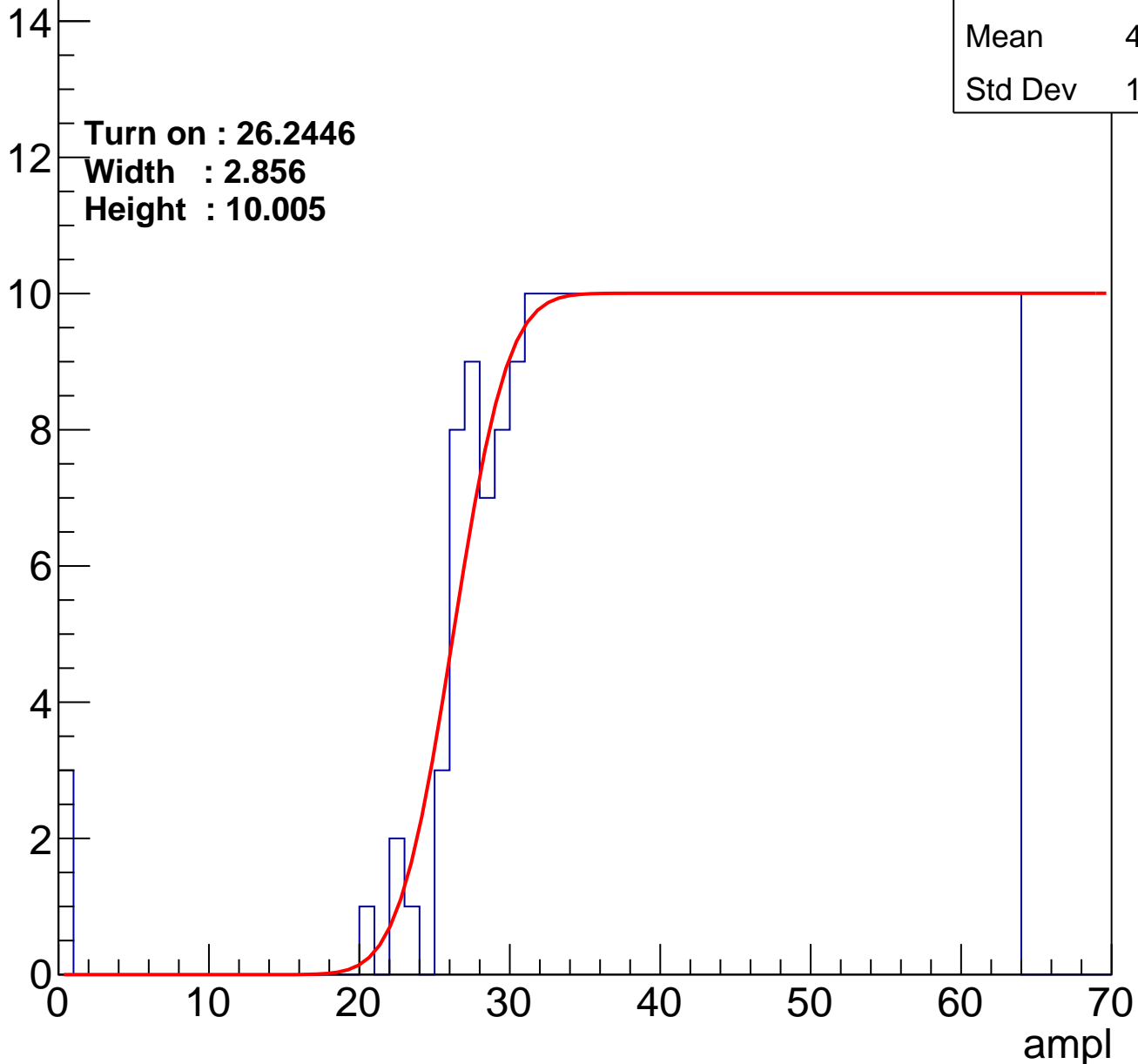
Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.2446

Width : 2.856

Height : 10.005

Entry



# B1L002S, U12-ch91

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	400
Mean	43.26
Std Dev	12.12

Turn on : 24.3003

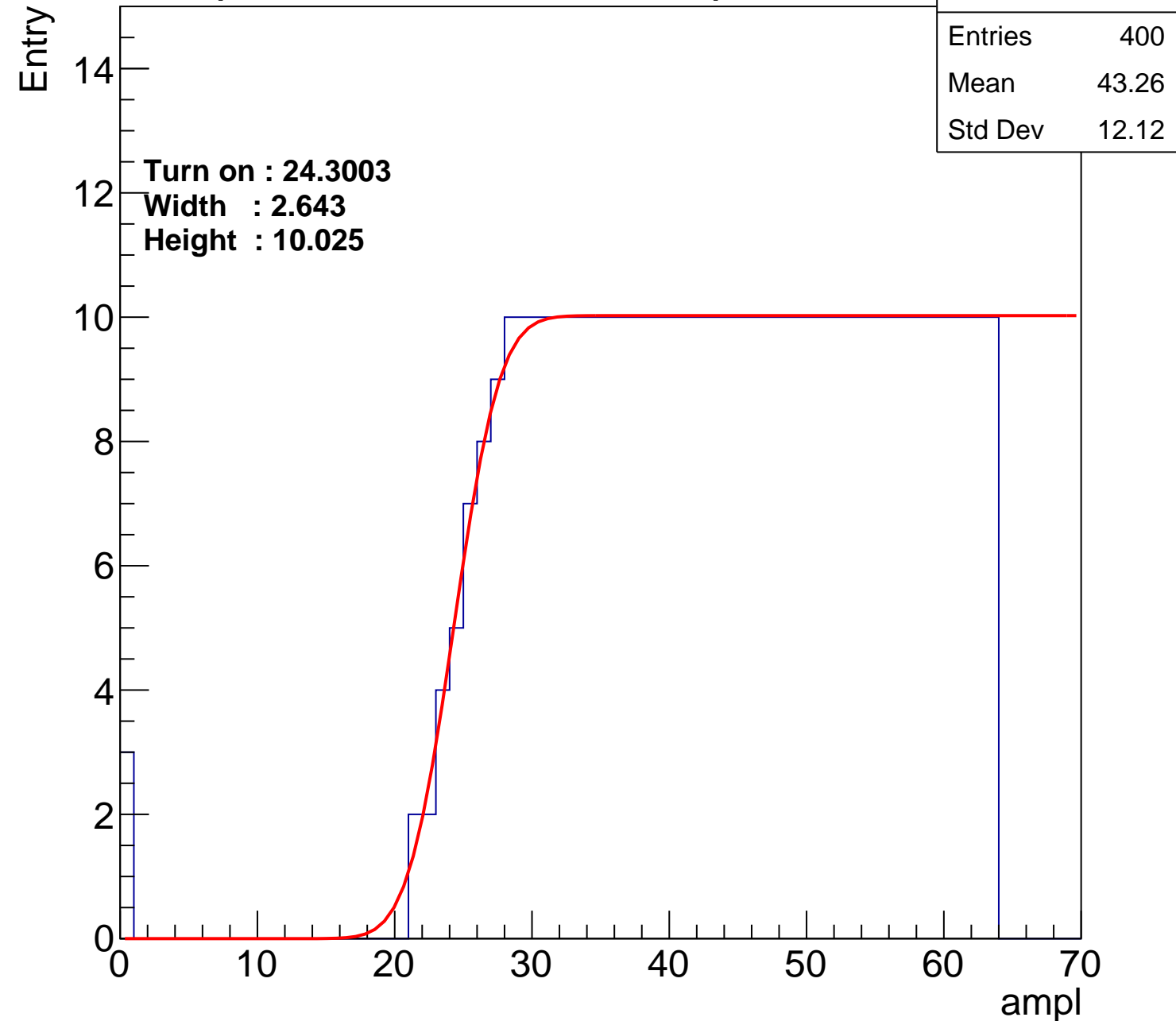
Width : 2.643

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch92

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.9166

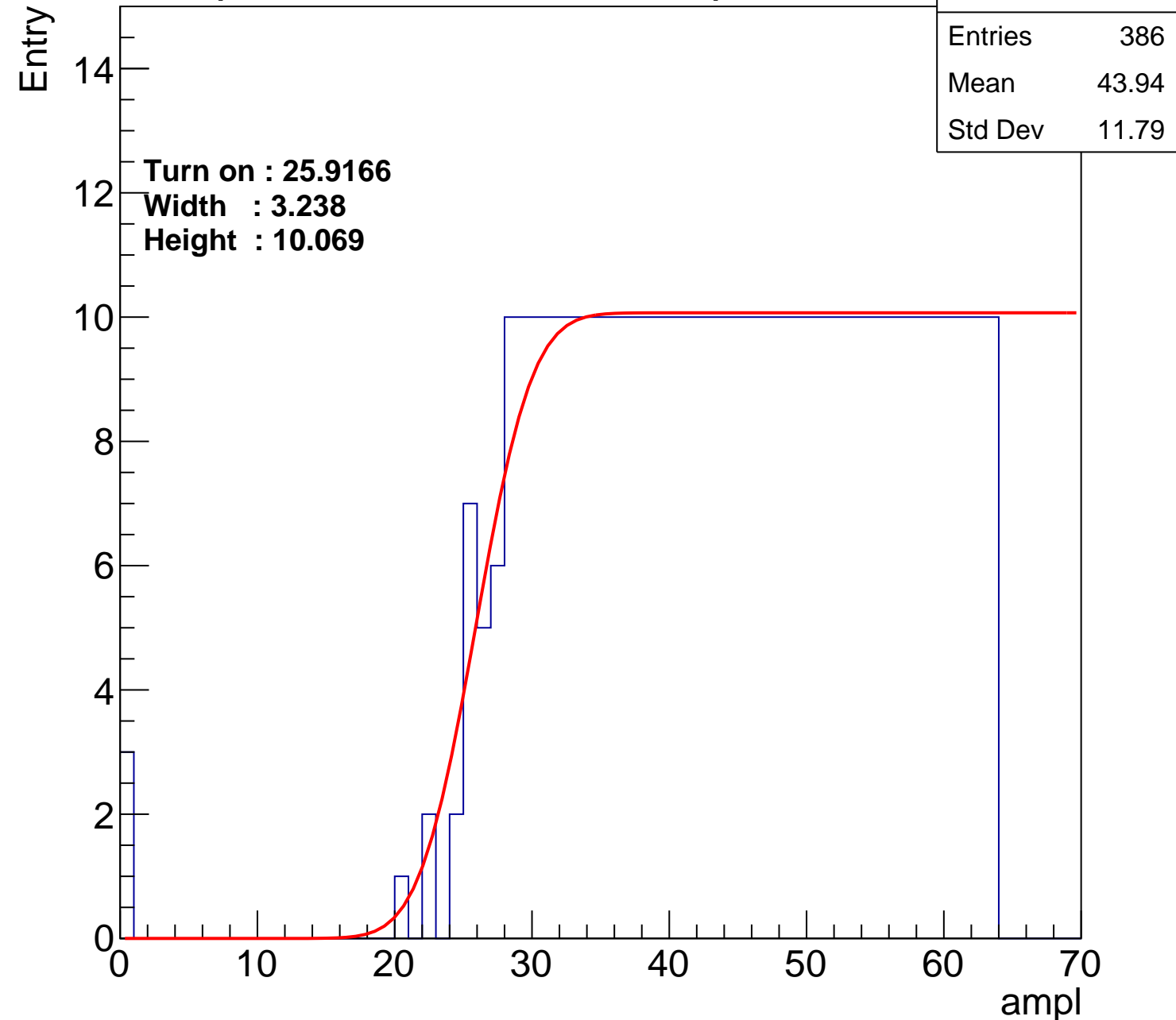
Width : 3.238

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch93

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.44
Std Dev	11.21

Turn on : 26.5278

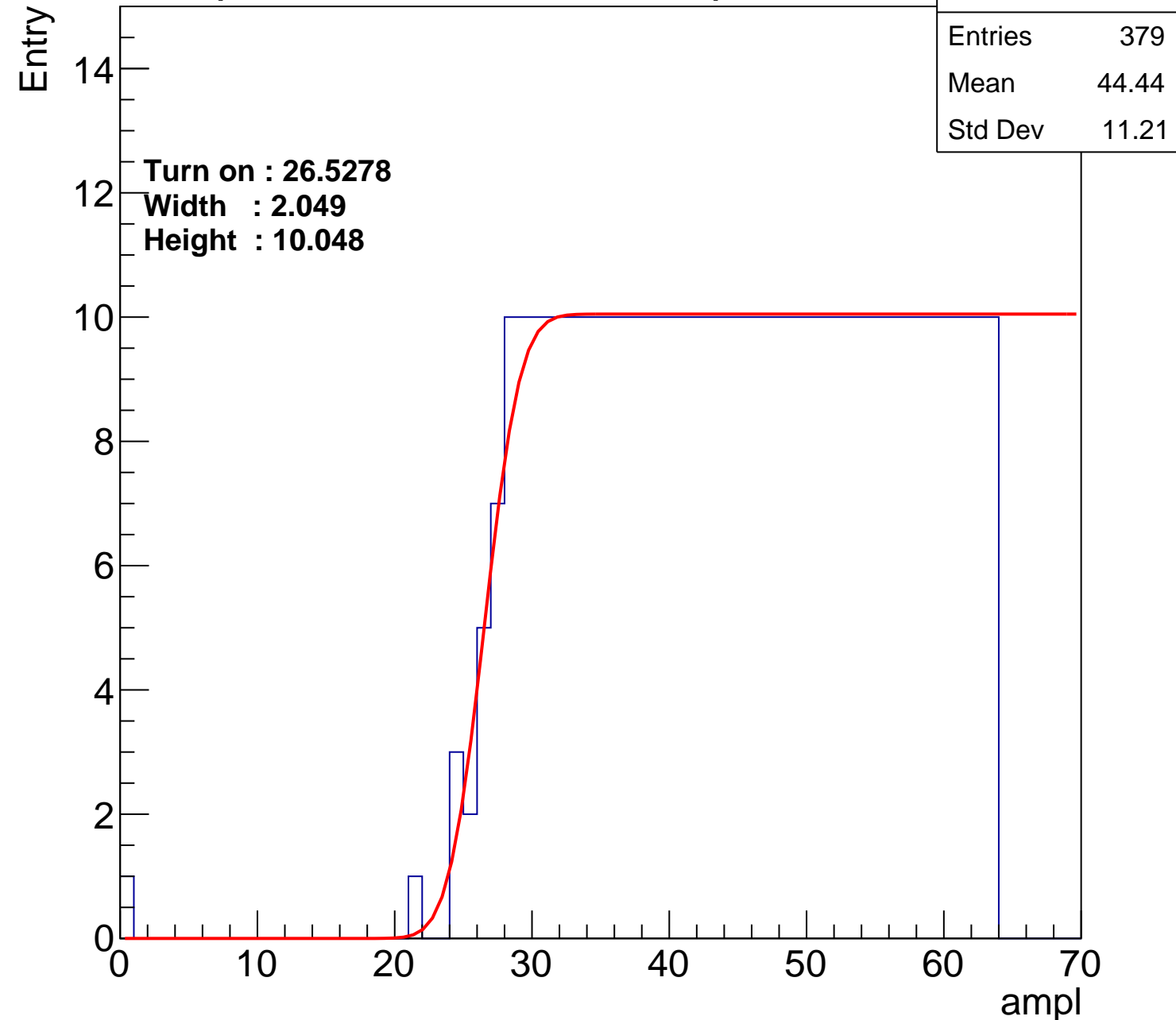
Width : 2.049

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch94

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 26.9898

Width : 3.193

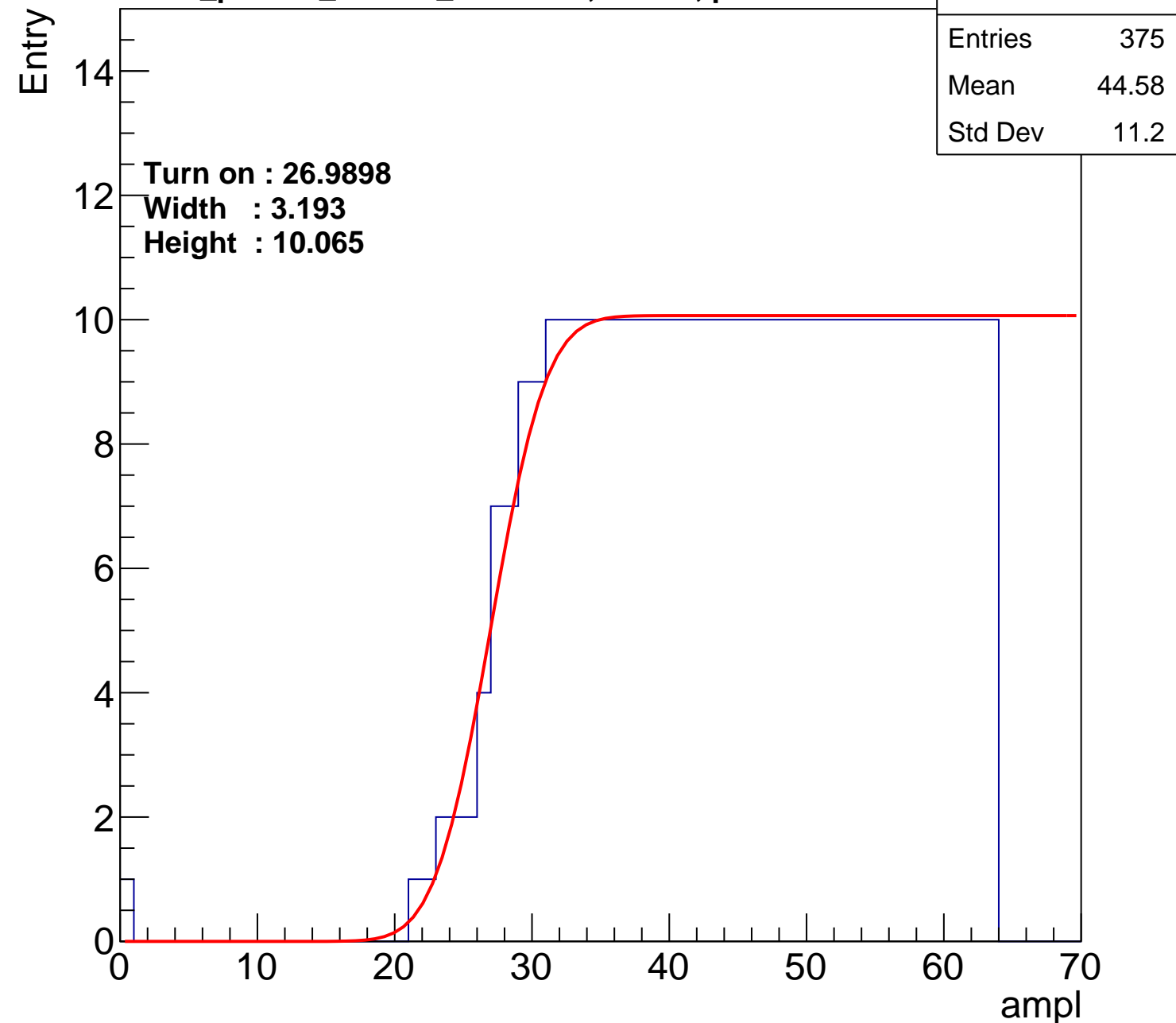
Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B1L002S, U12-ch95

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.6282

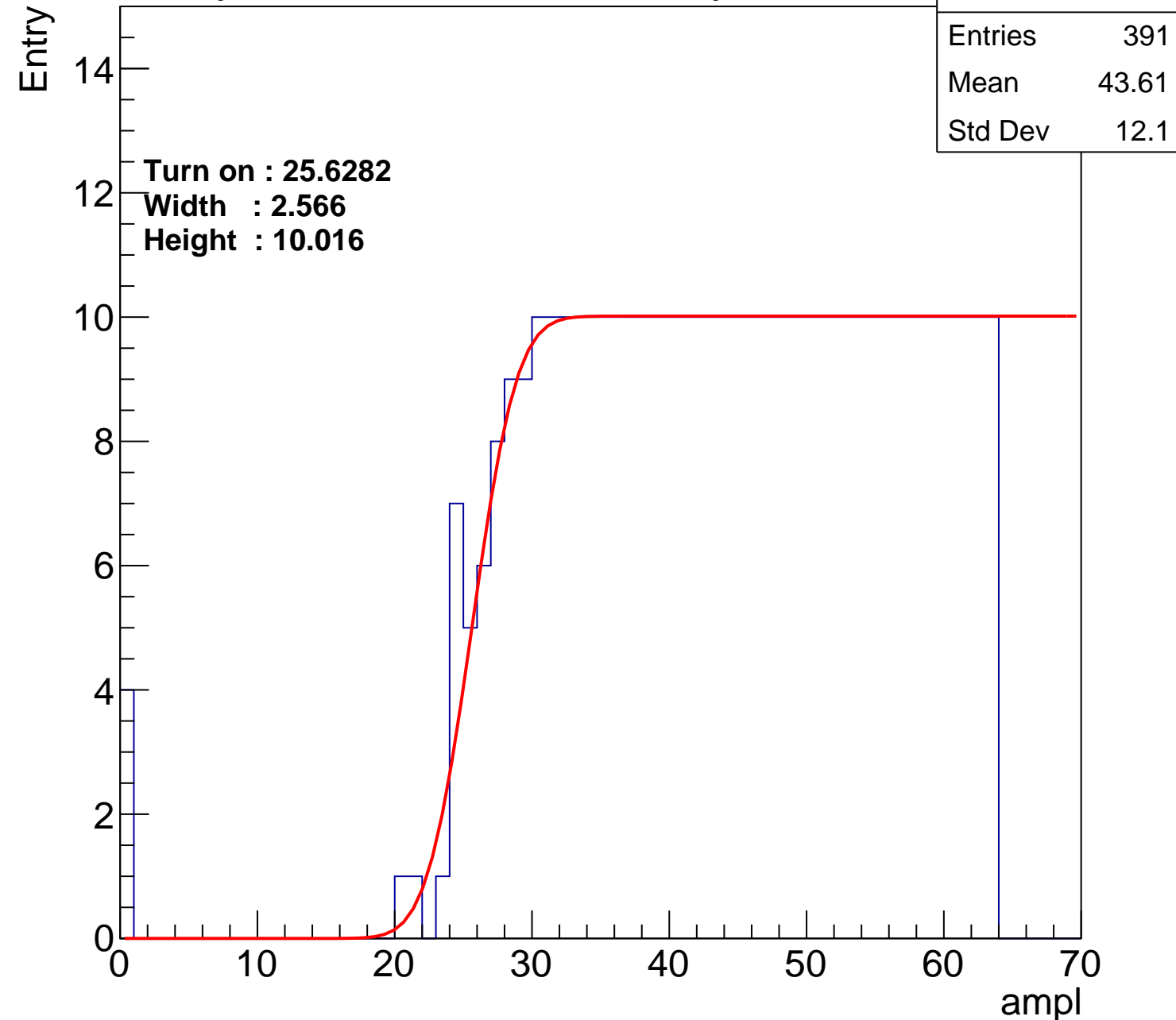
Width : 2.566

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch96

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	401
Mean	43.15
Std Dev	12.25

Turn on : 24.4225

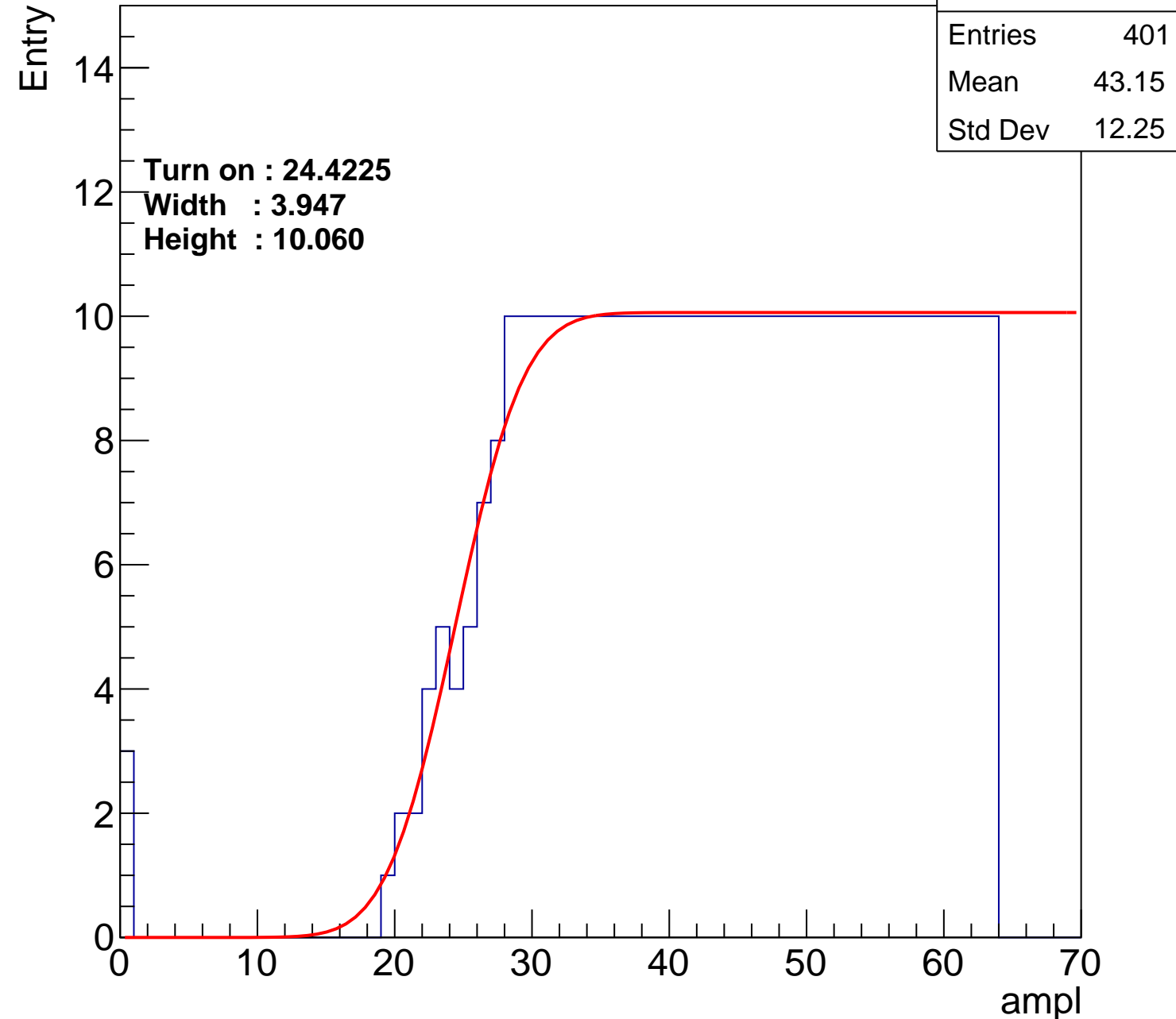
Width : 3.947

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch97

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	365
Mean	44.95
Std Dev	11.31

Turn on : 28.5948

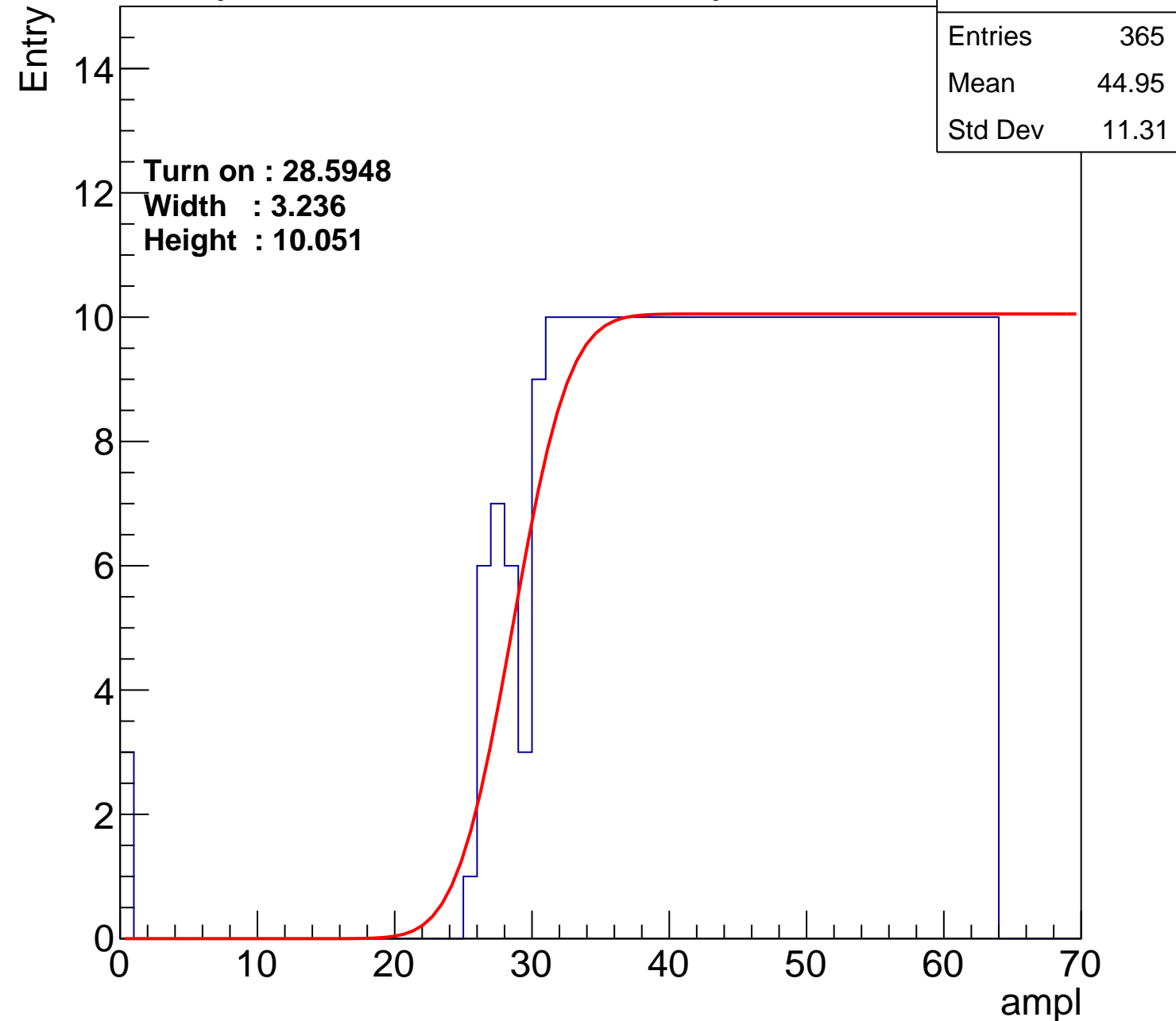
Width : 3.236

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch98

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.62
Std Dev	12.2

Turn on : 26.1445

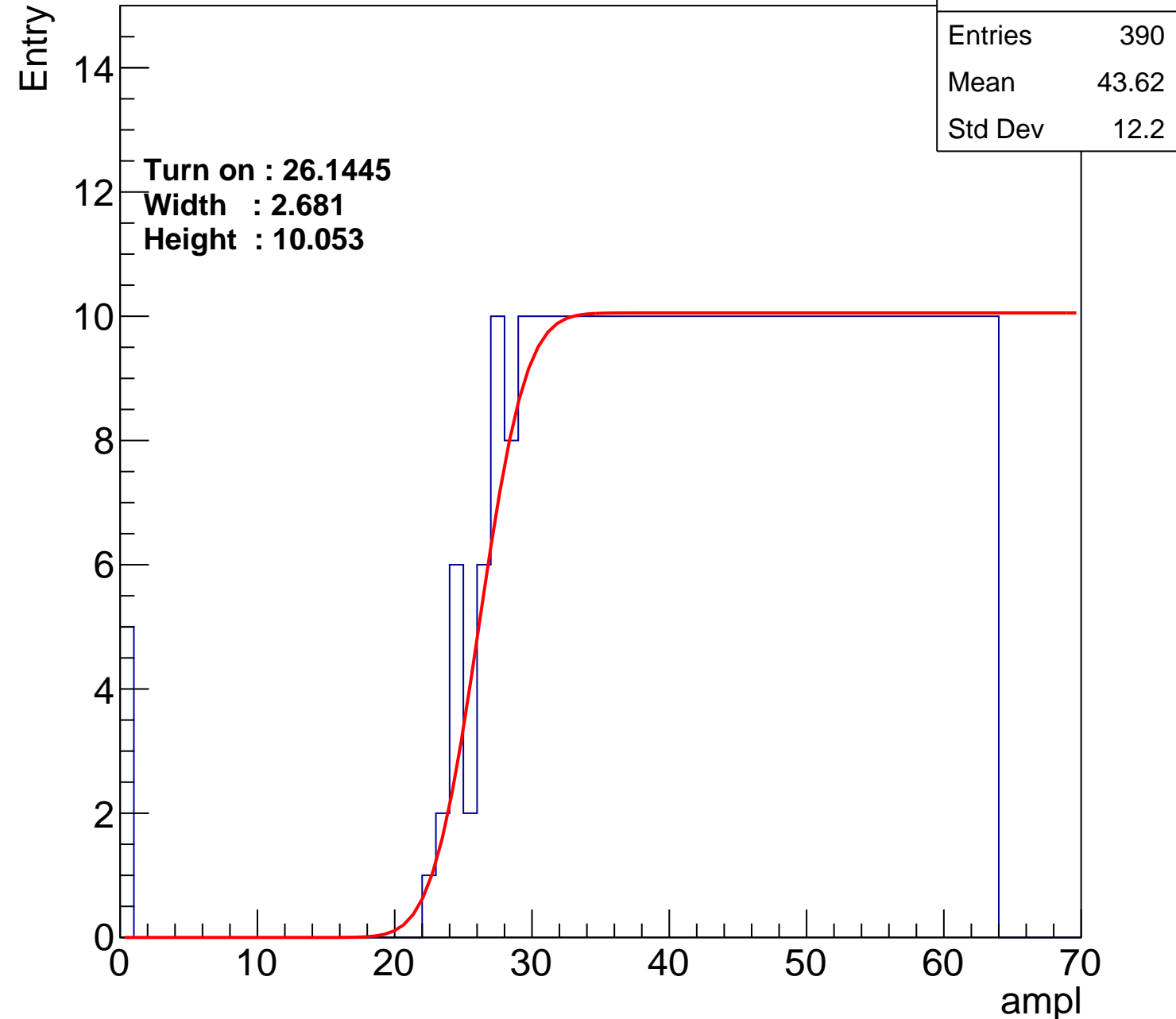
Width : 2.681

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch99

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	395
Mean	43.45
Std Dev	12.09

Turn on : 25.2399

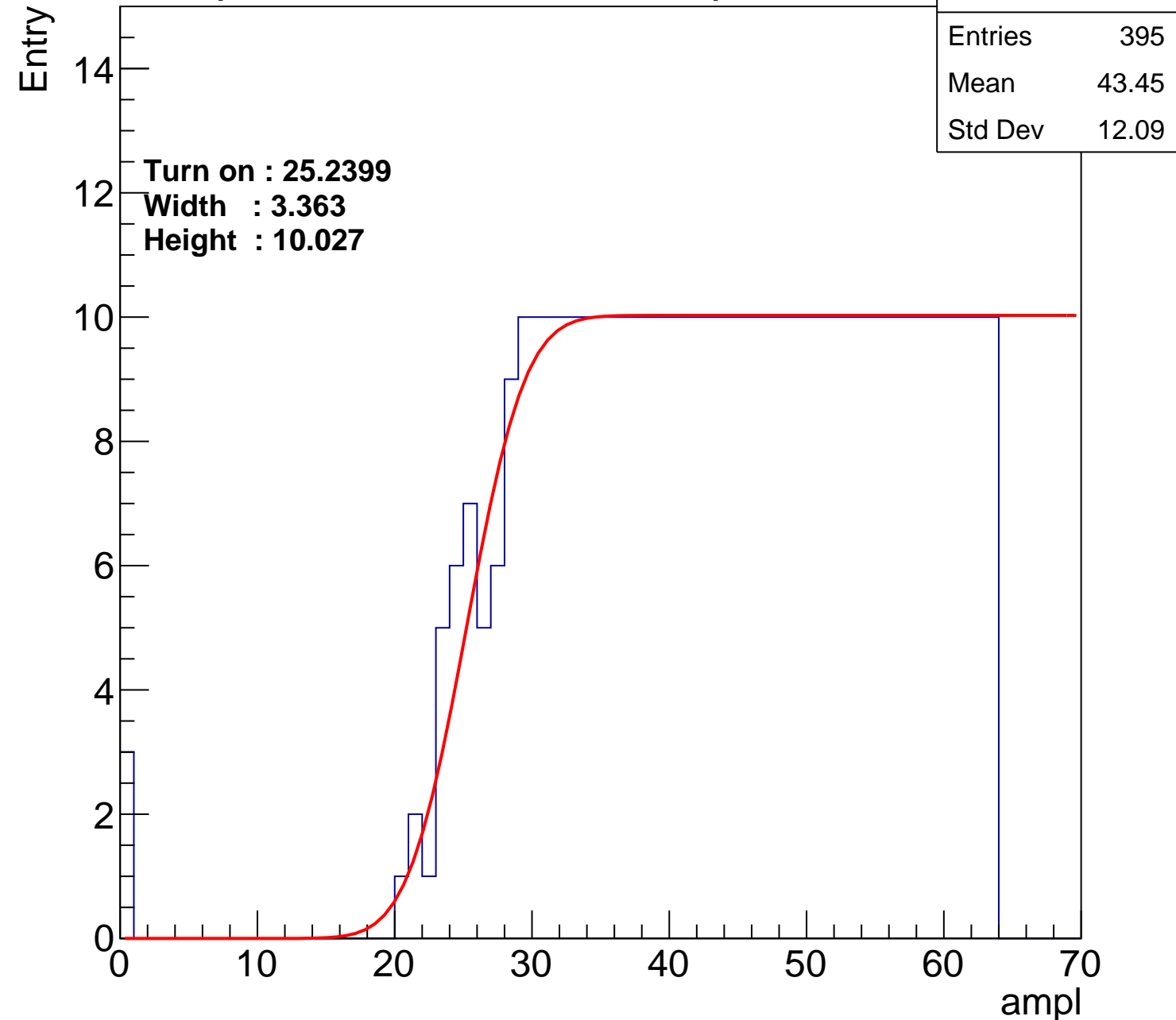
Width : 3.363

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch100

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.7479

Width : 2.632

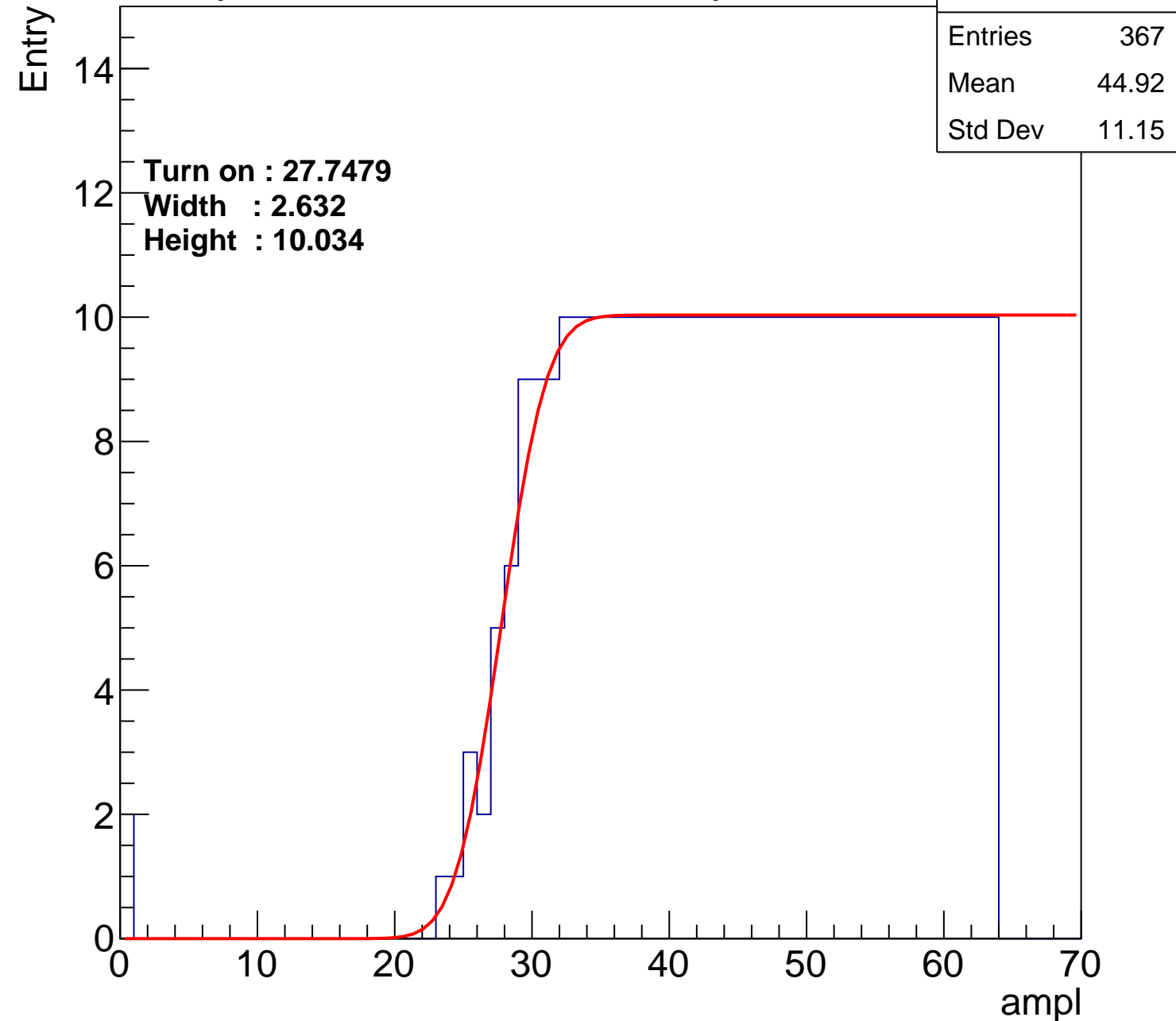
Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U12-ch101

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.65
Std Dev	11.69

Turn on : 24.6262

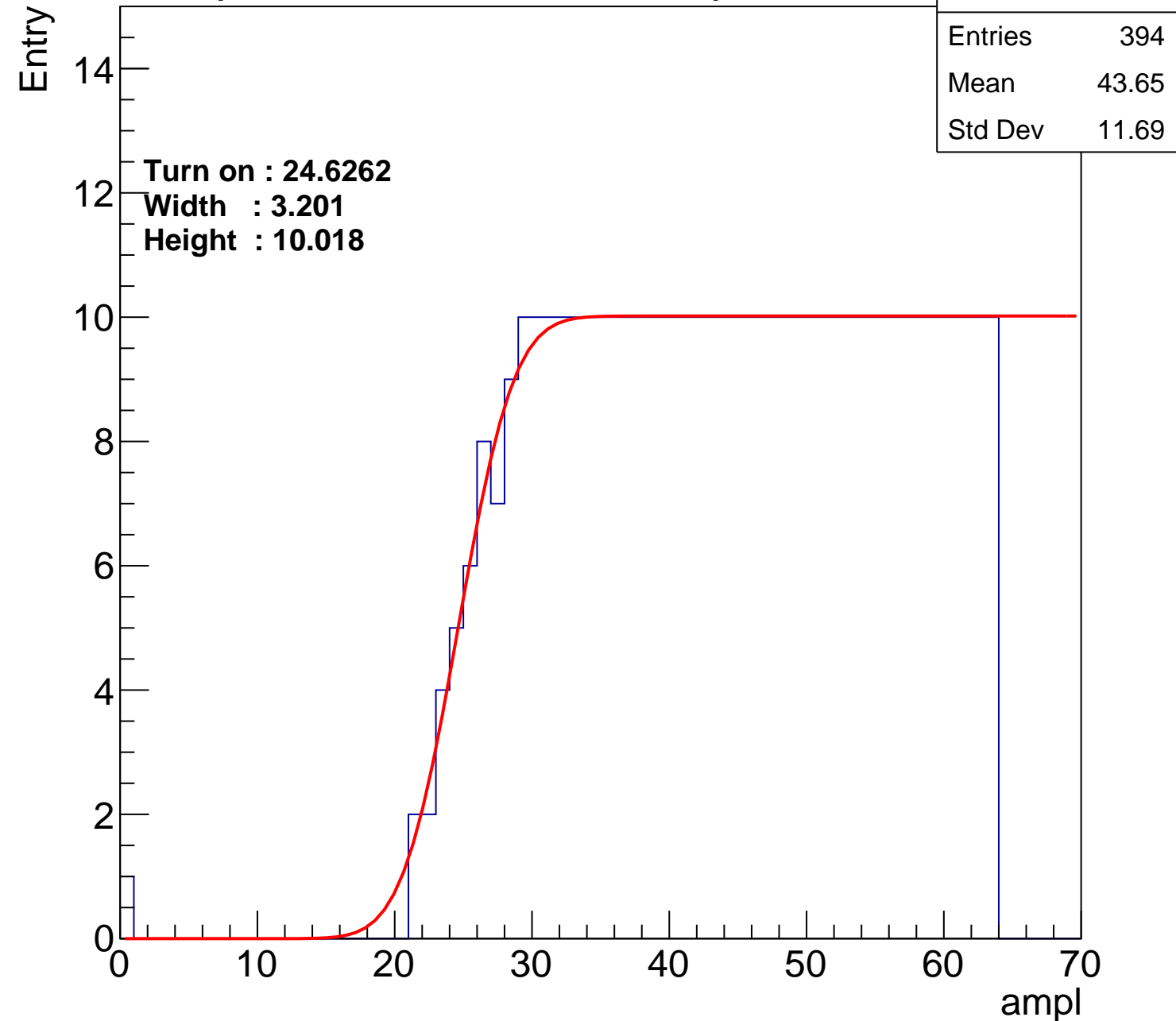
Width : 3.201

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch102

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.3

Turn on : 26.4424

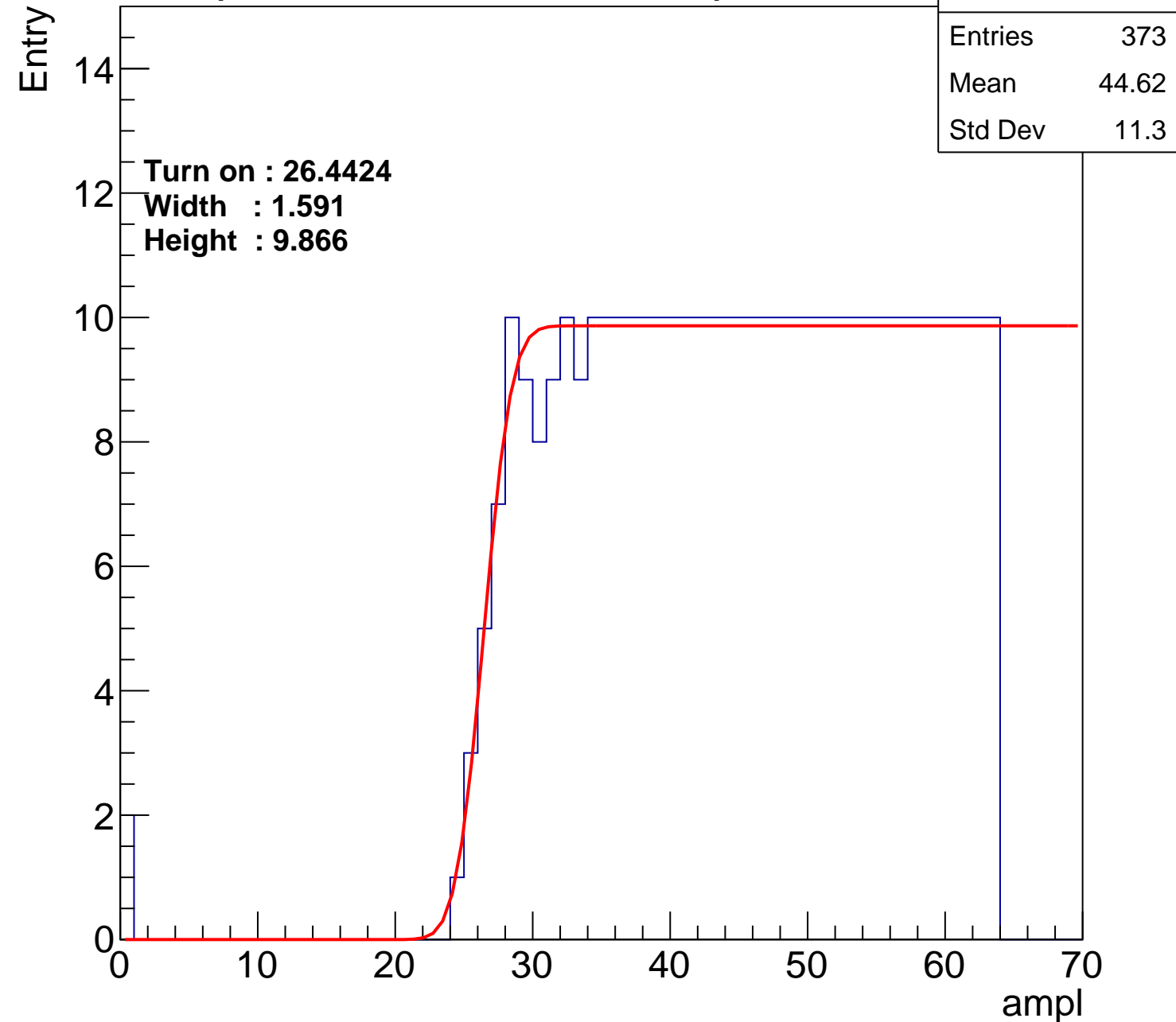
Width : 1.591

Height : 9.866

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch103

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.02
Std Dev	12.01

Turn on : 26.1336

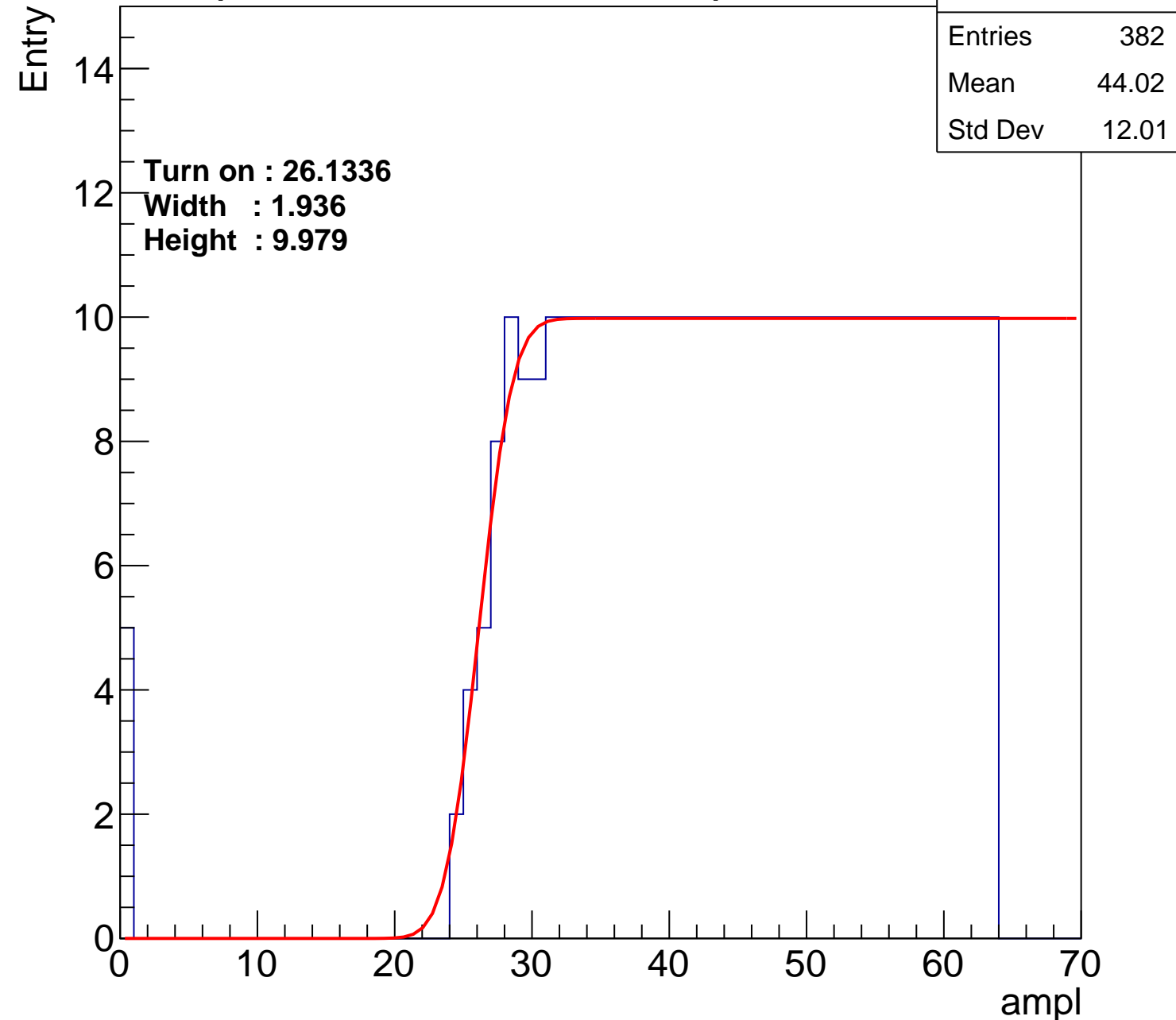
Width : 1.936

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch104

calib\_packv5\_042523\_0143.root, FC#10, port B3

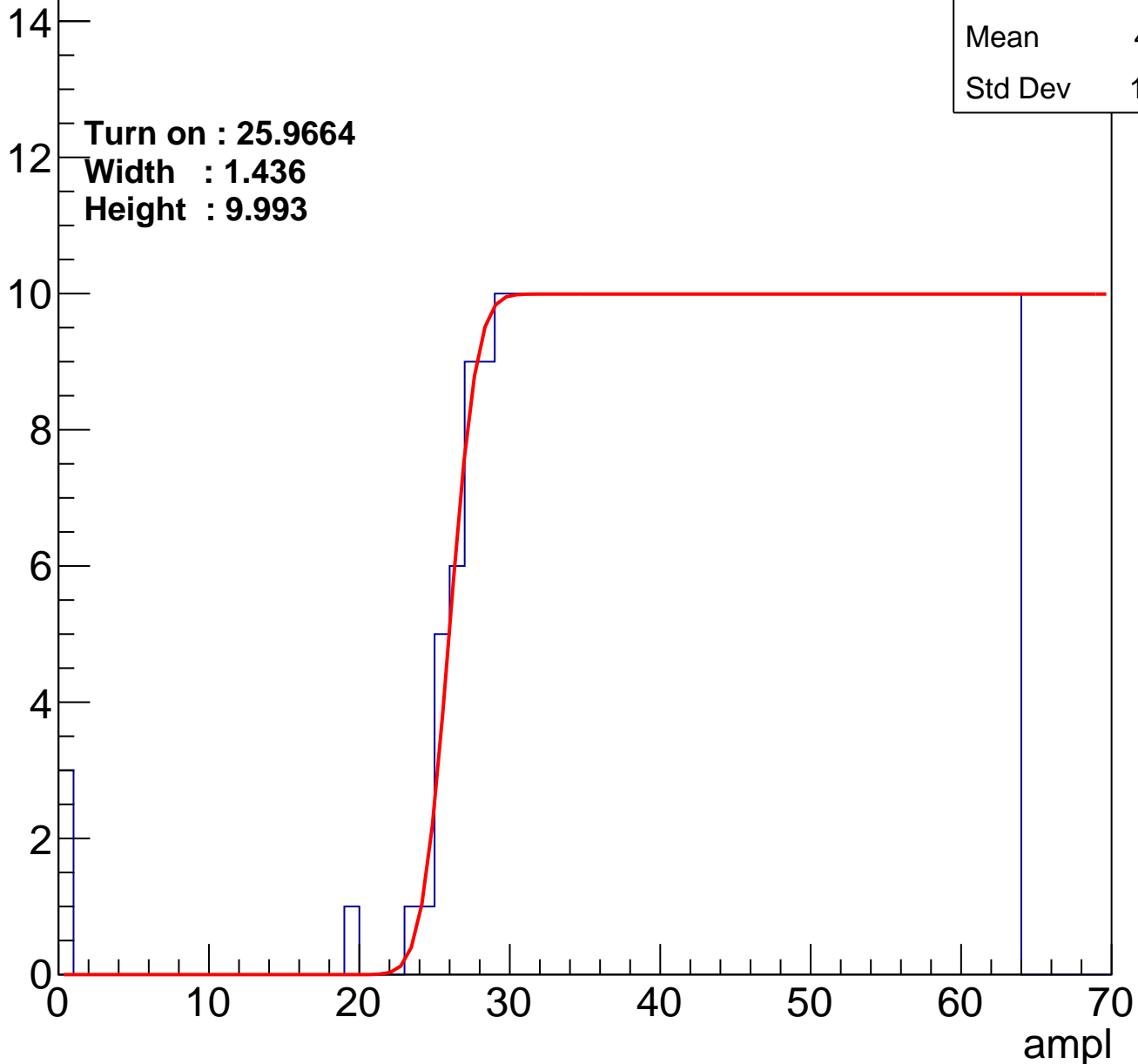
Entries	385
Mean	44.01
Std Dev	11.74

Turn on : 25.9664

Width : 1.436

Height : 9.993

Entry



# B1L002S, U12-ch105

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.9
Std Dev	11.83

Turn on : 25.5515

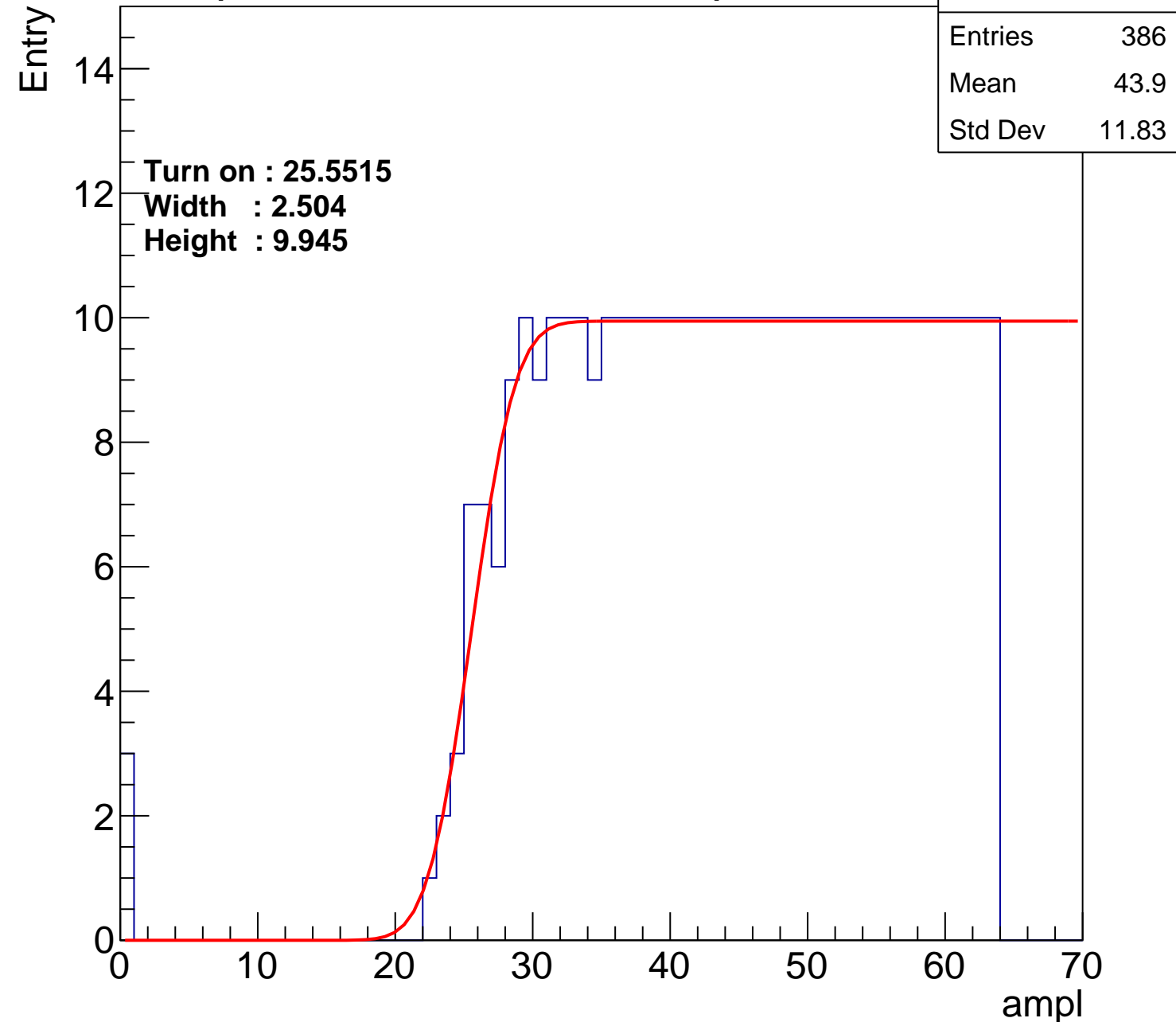
Width : 2.504

Height : 9.945

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch106

calib\_packv5\_042523\_0143.root, FC#10, port B3

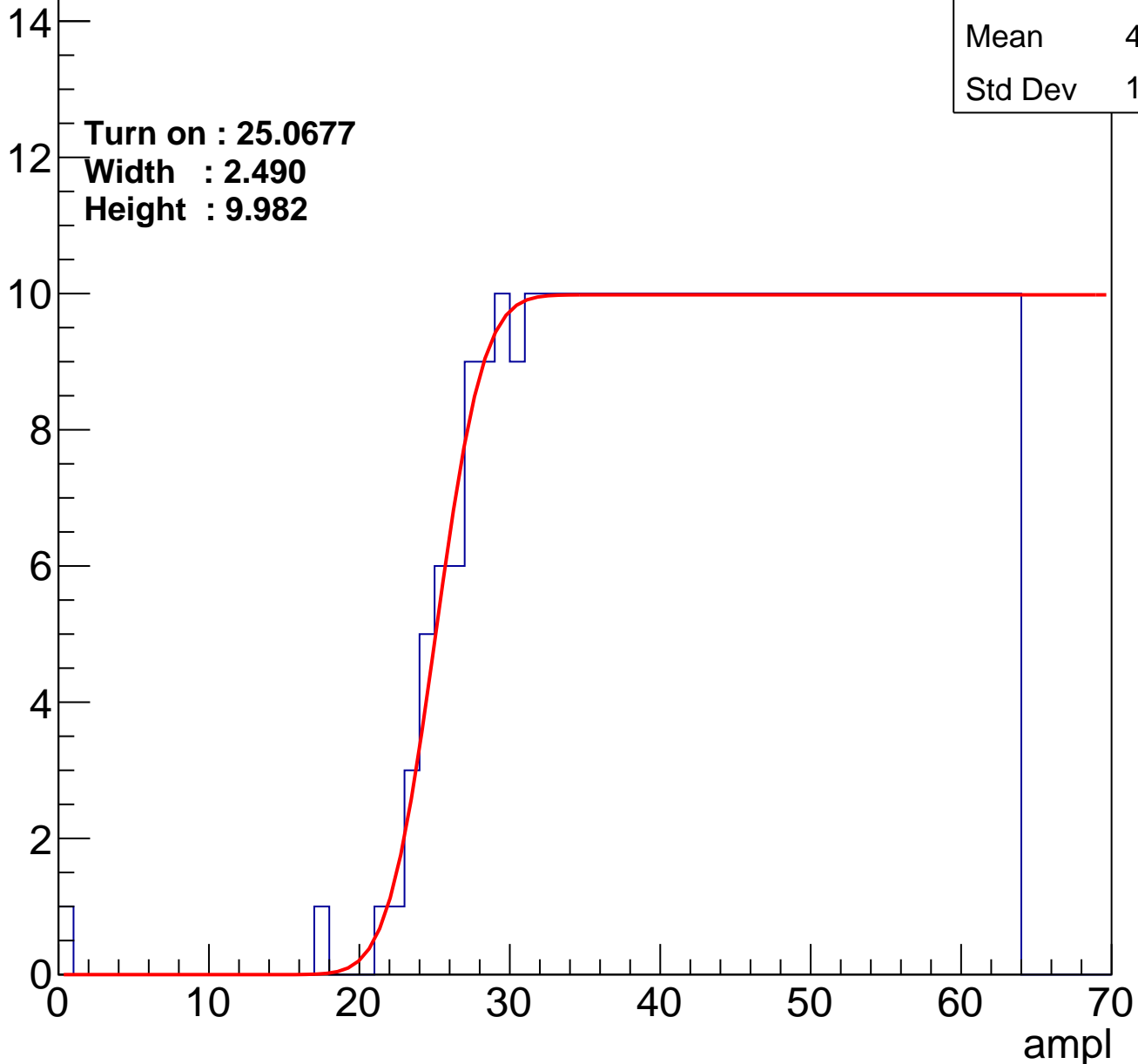
Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.0677

Width : 2.490

Height : 9.982

Entry



# B1L002S, U12-ch107

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.68
Std Dev	11.76

Turn on : 25.0916

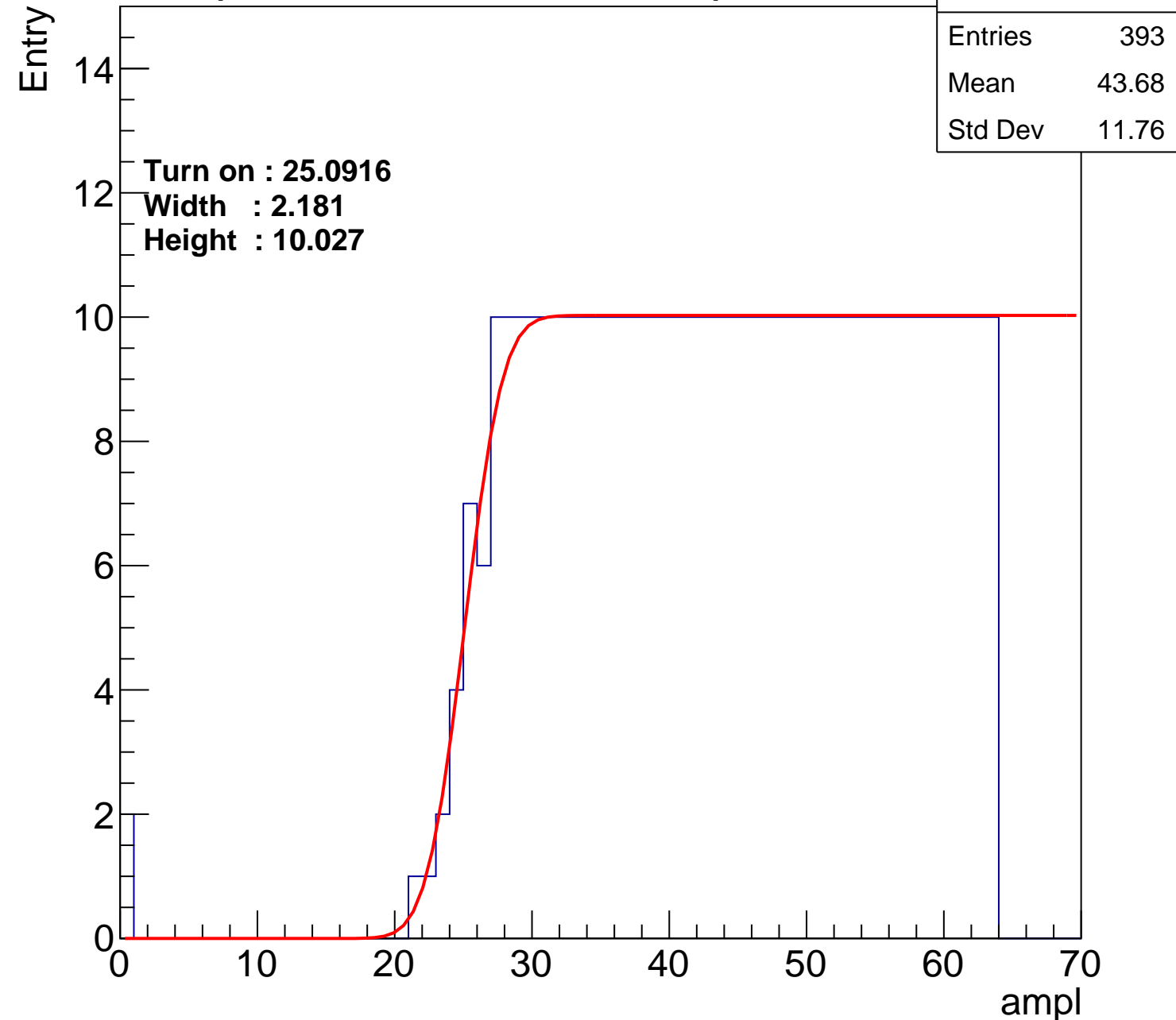
Width : 2.181

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch108

calib\_packv5\_042523\_0143.root, FC#10, port B3

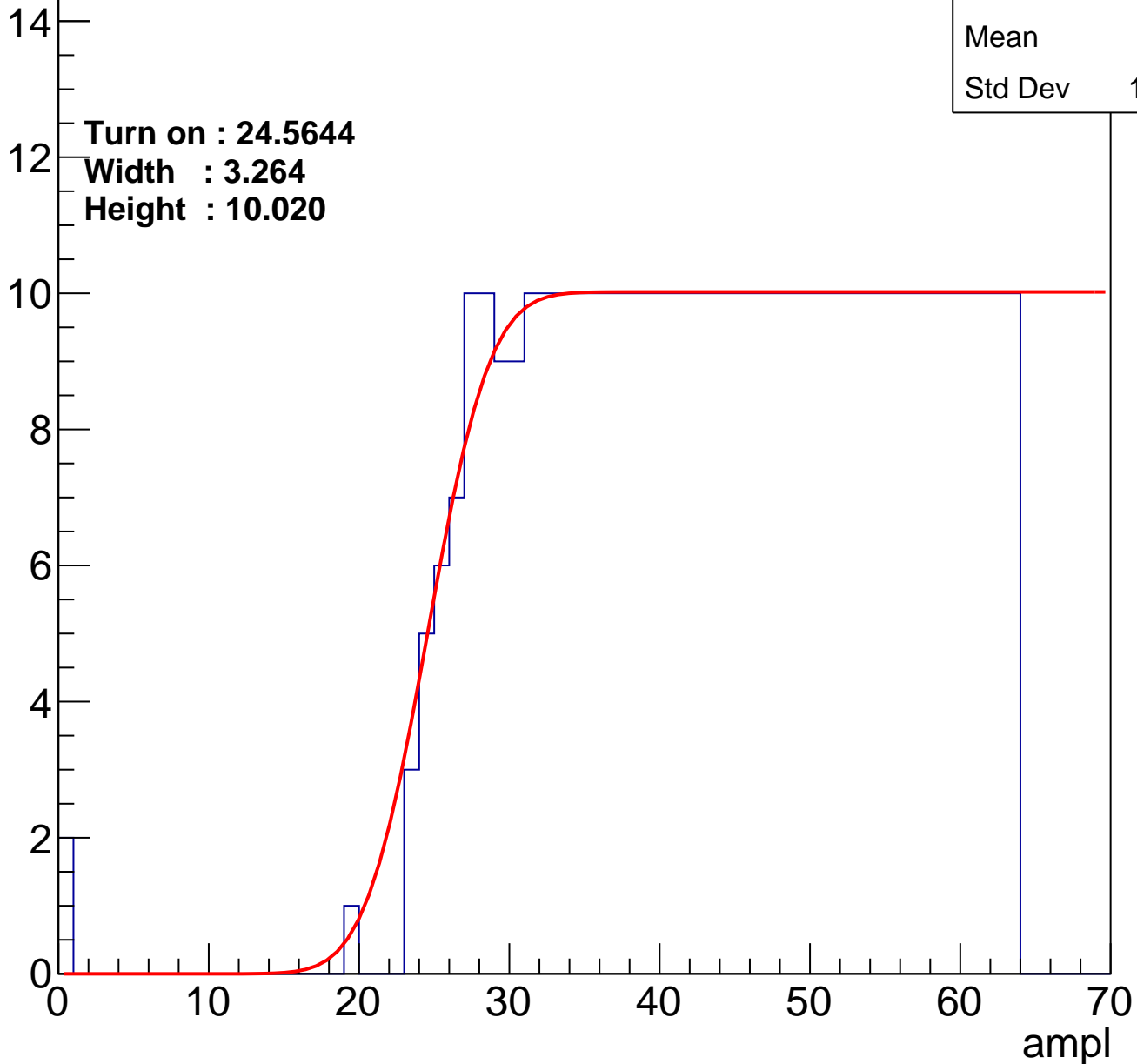
Entries	392
Mean	43.7
Std Dev	11.77

Turn on : 24.5644

Width : 3.264

Height : 10.020

Entry



# B1L002S, U12-ch109

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 26.3178

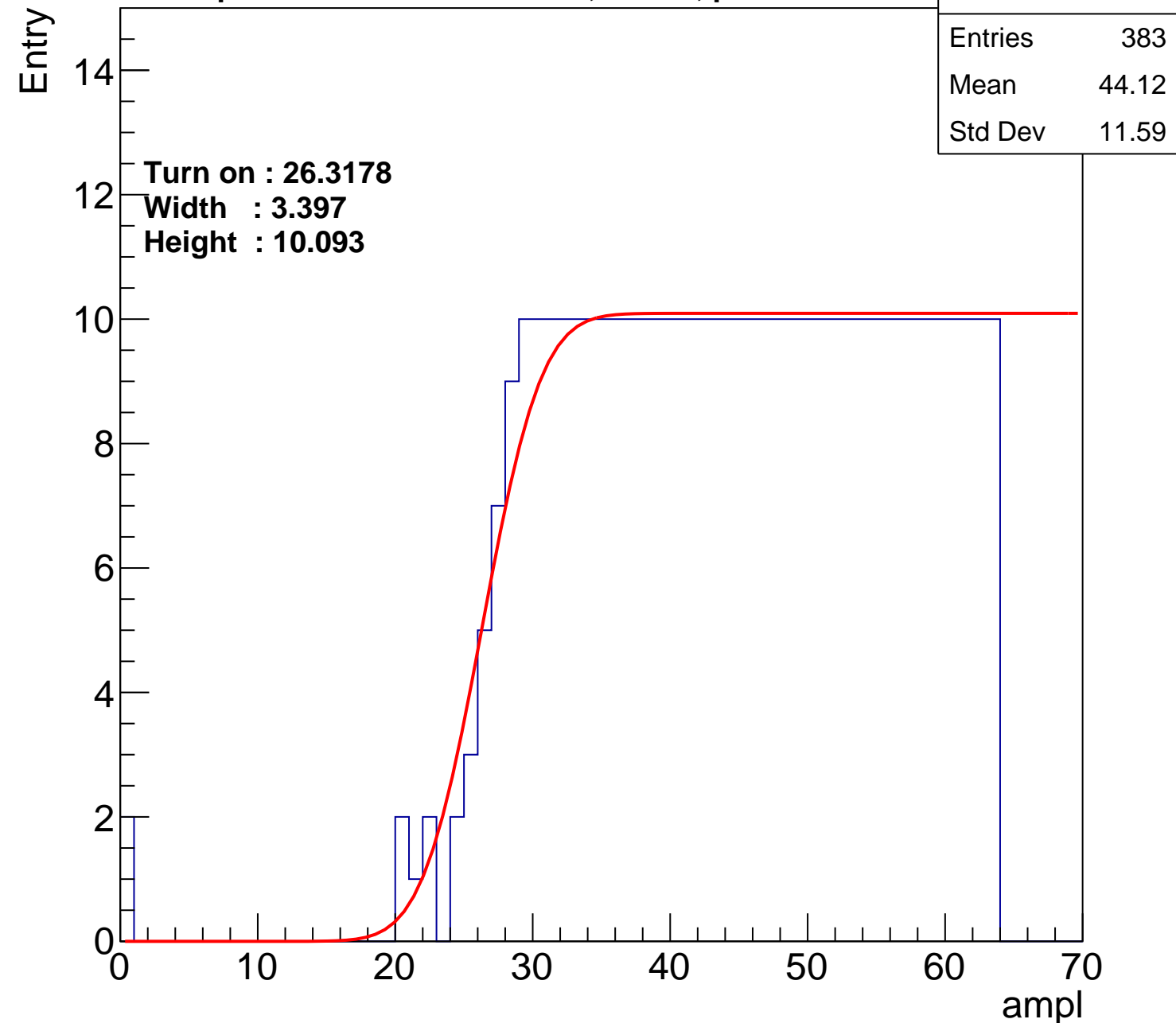
Width : 3.397

Height : 10.093

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch110

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.74

Turn on : 25.9194

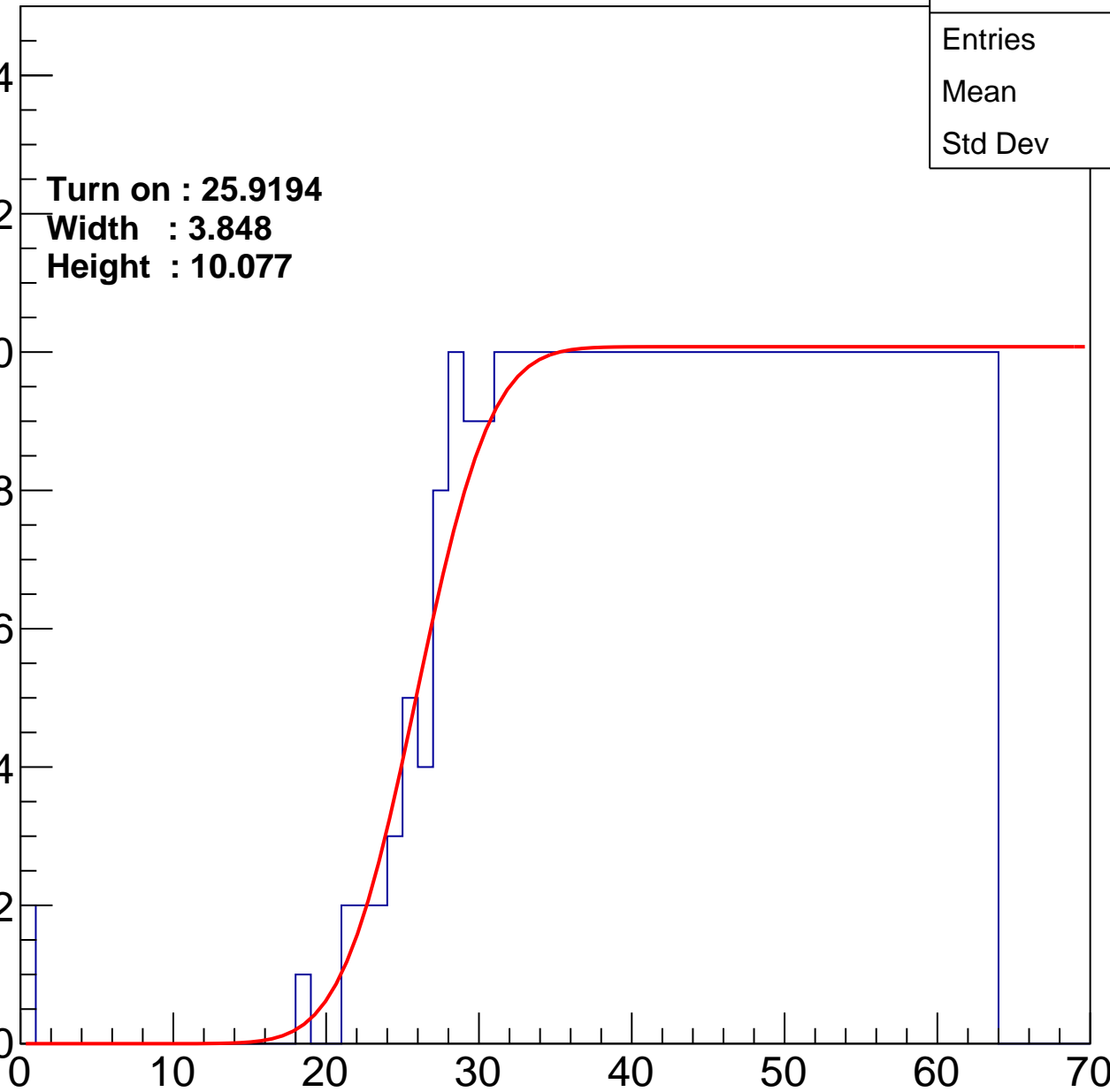
Width : 3.848

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch111

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.9766

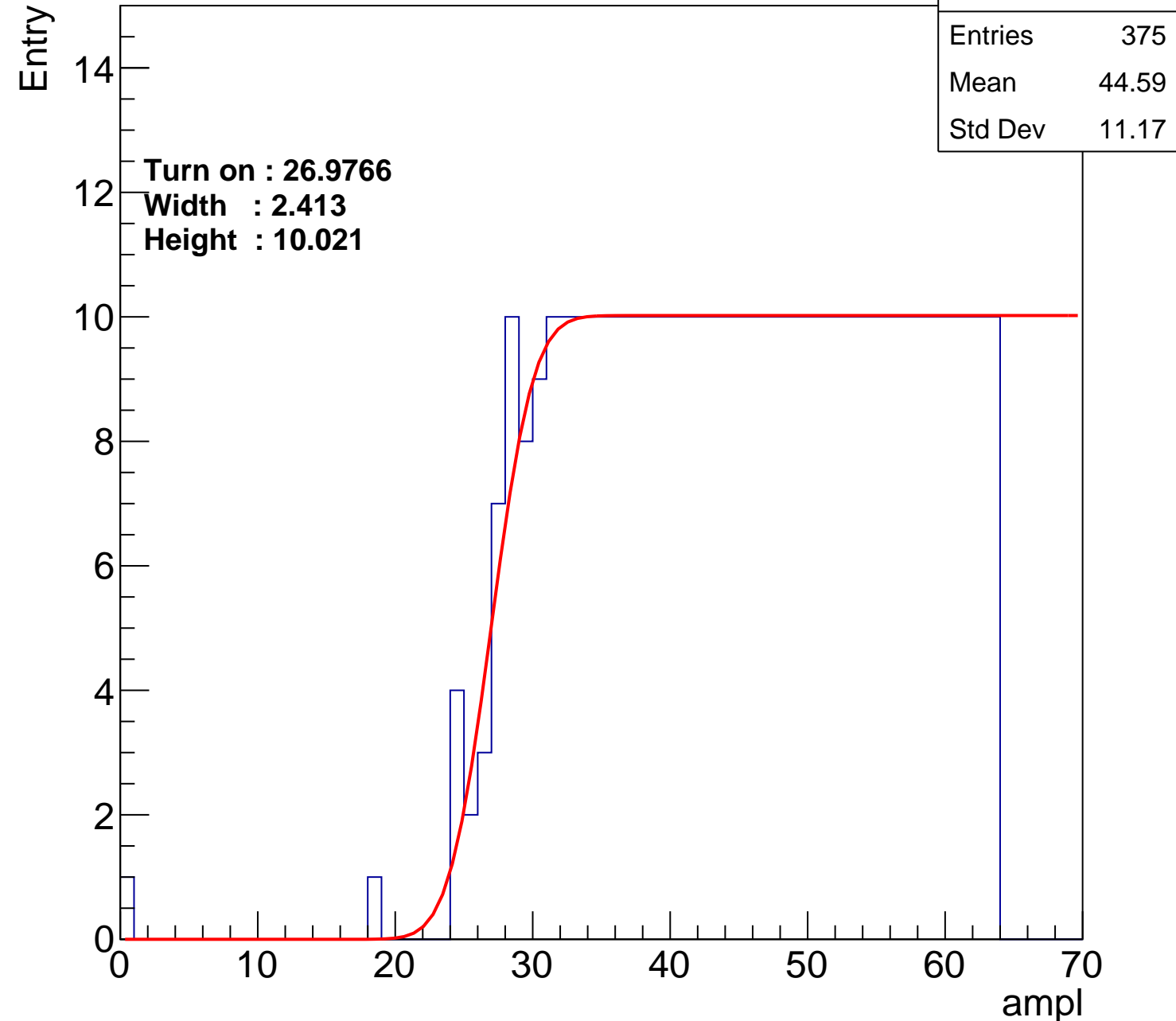
Width : 2.413

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch112

calib\_packv5\_042523\_0143.root, FC#10, port B3

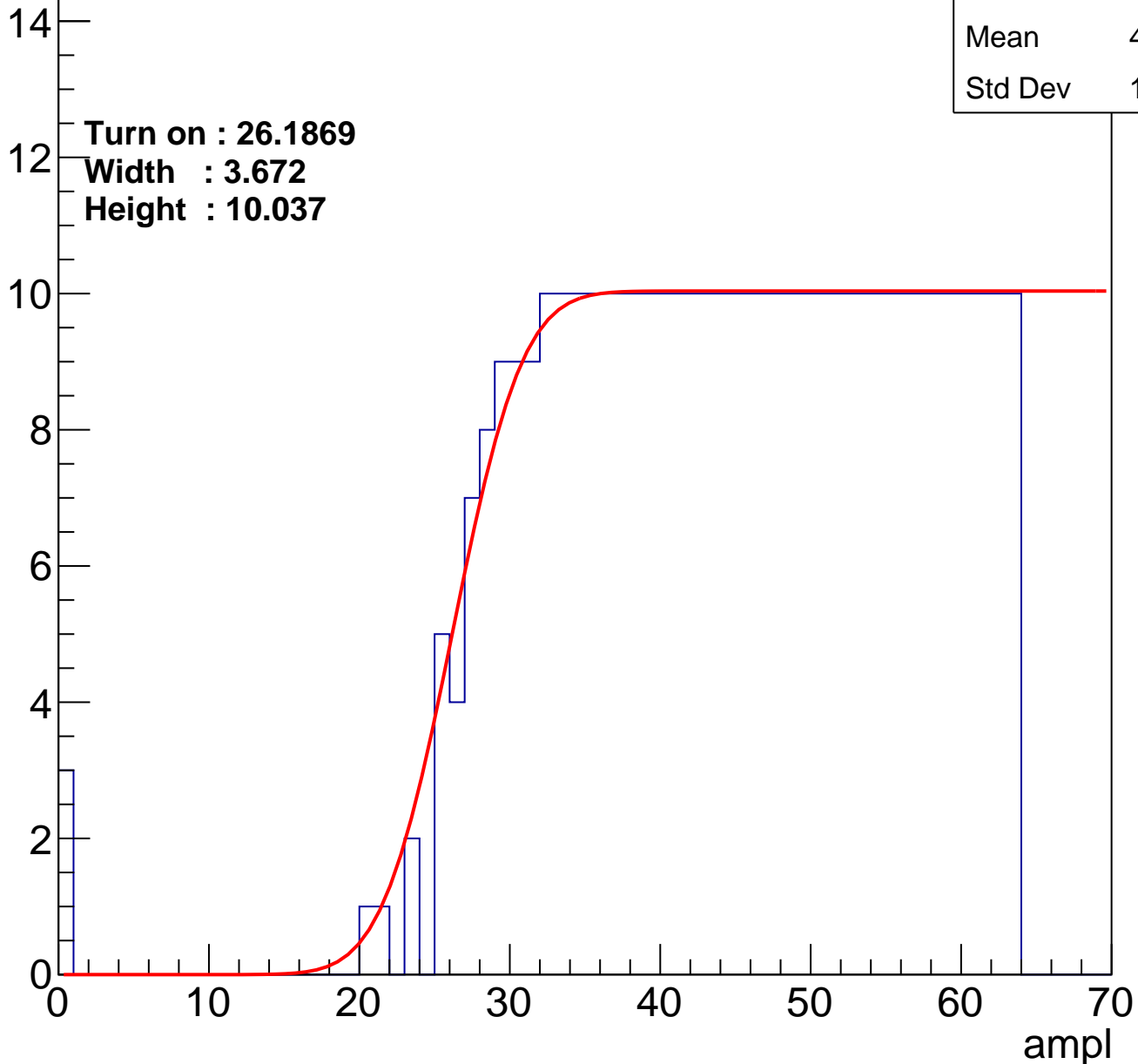
Entries	378
Mean	44.28
Std Dev	11.67

Turn on : 26.1869

Width : 3.672

Height : 10.037

Entry



# B1L002S, U12-ch113

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.81
Std Dev	12

Turn on : 25.1167

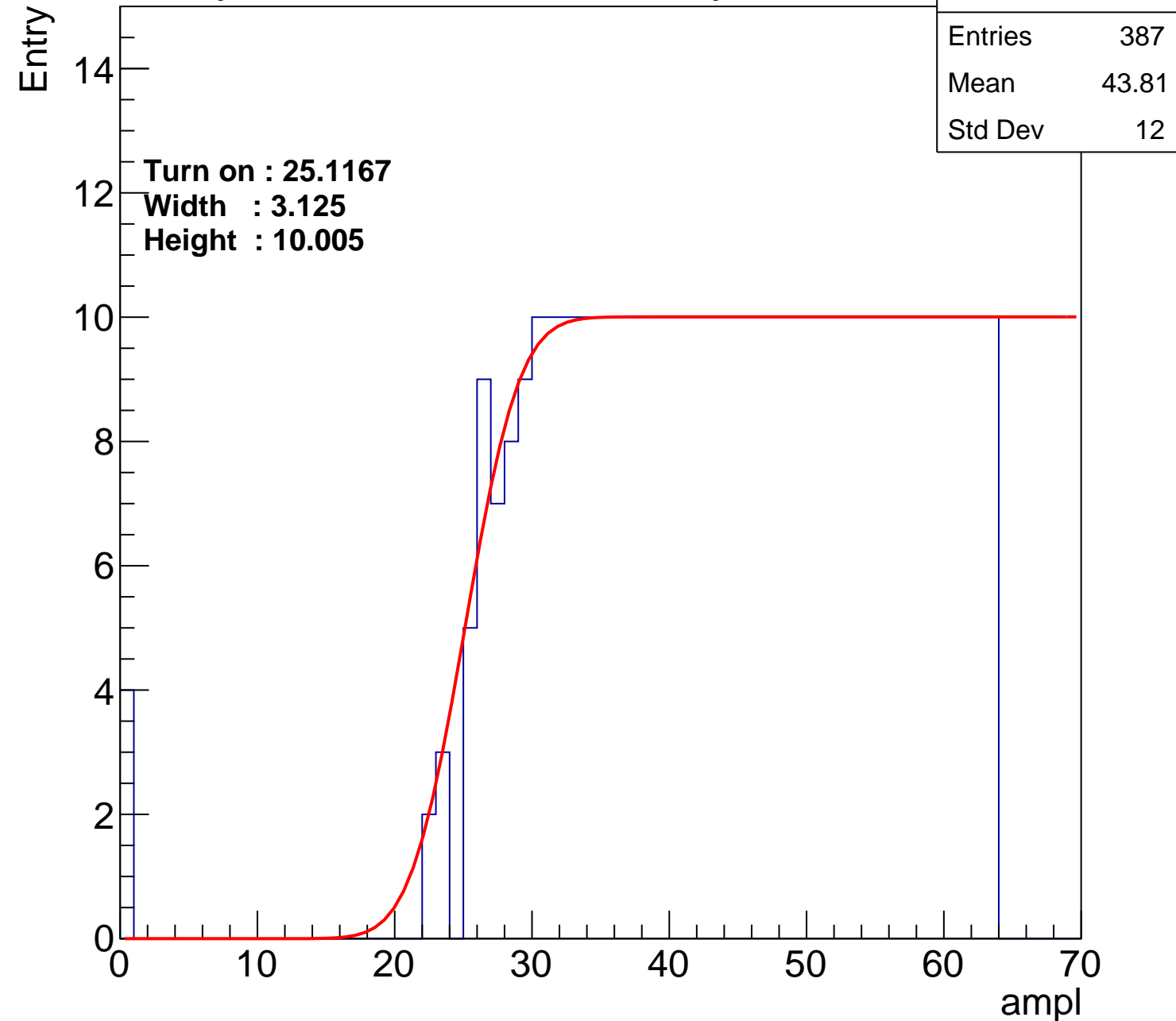
Width : 3.125

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch114

calib\_packv5\_042523\_0143.root, FC#10, port B3

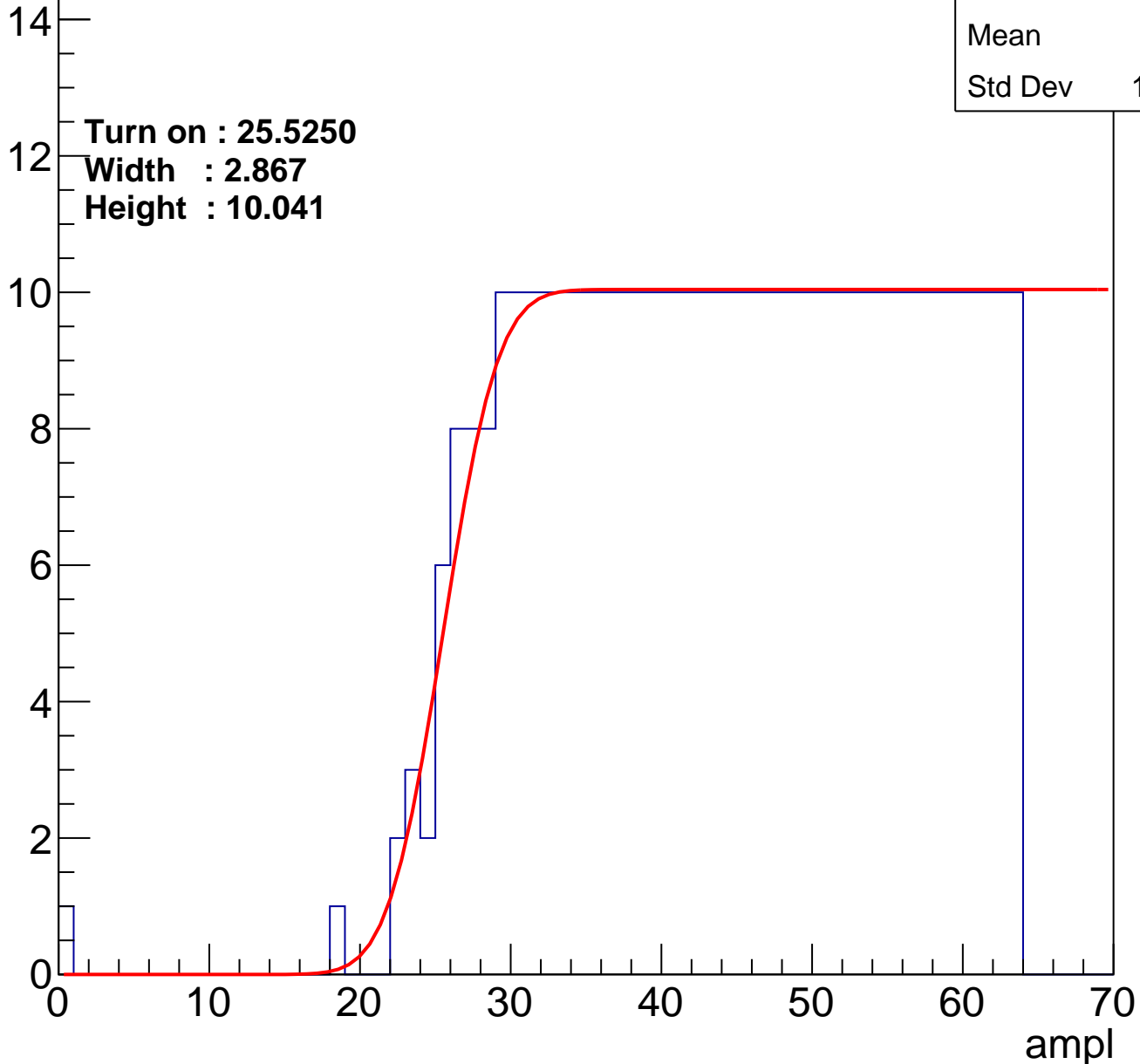
Entries	389
Mean	43.9
Std Dev	11.55

Turn on : 25.5250

Width : 2.867

Height : 10.041

Entry



# B1L002S, U12-ch115

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	43.83
Std Dev	12.13

Turn on : 25.9192

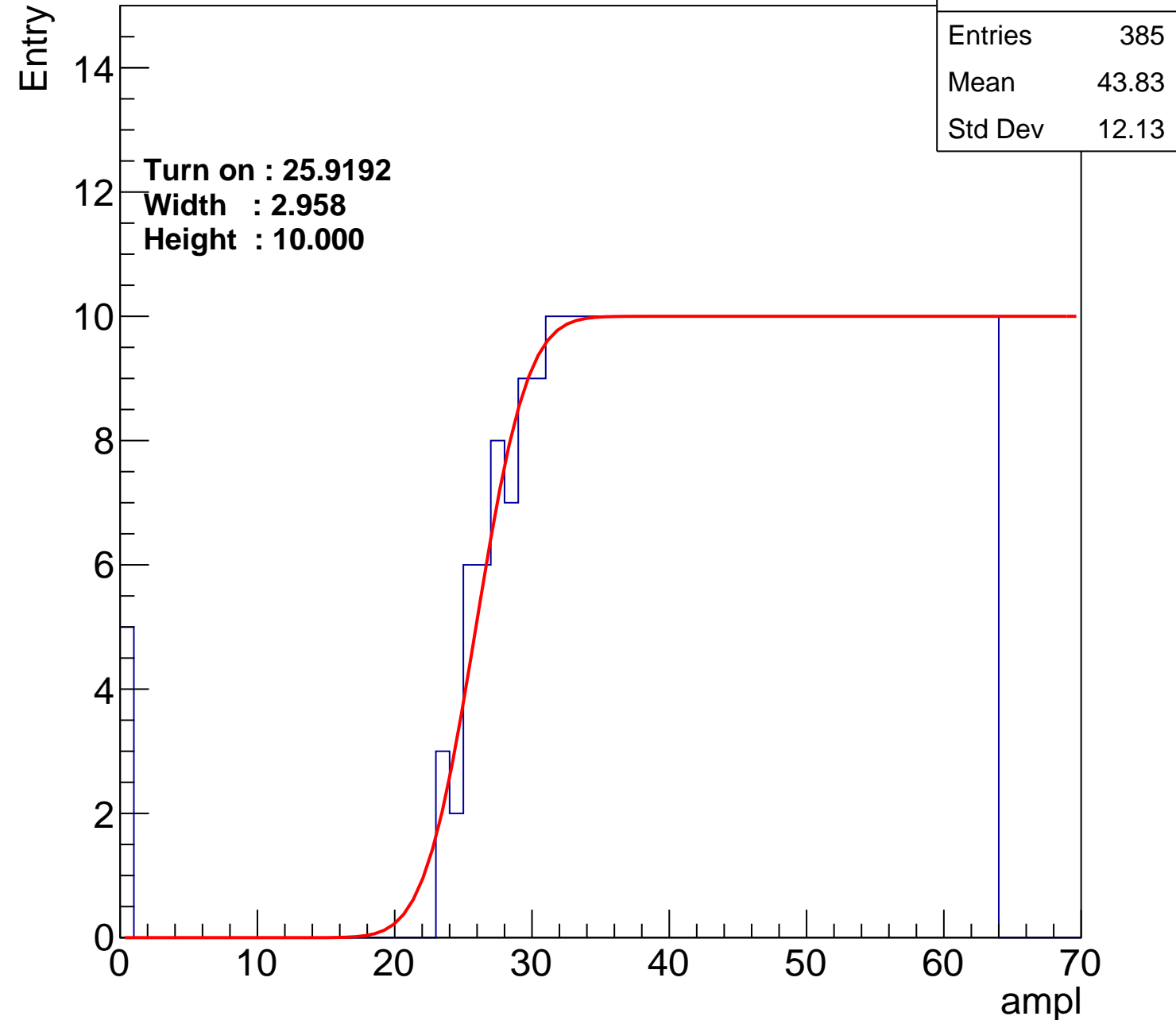
Width : 2.958

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch116

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.66
Std Dev	12.07

Turn on : 26.1735

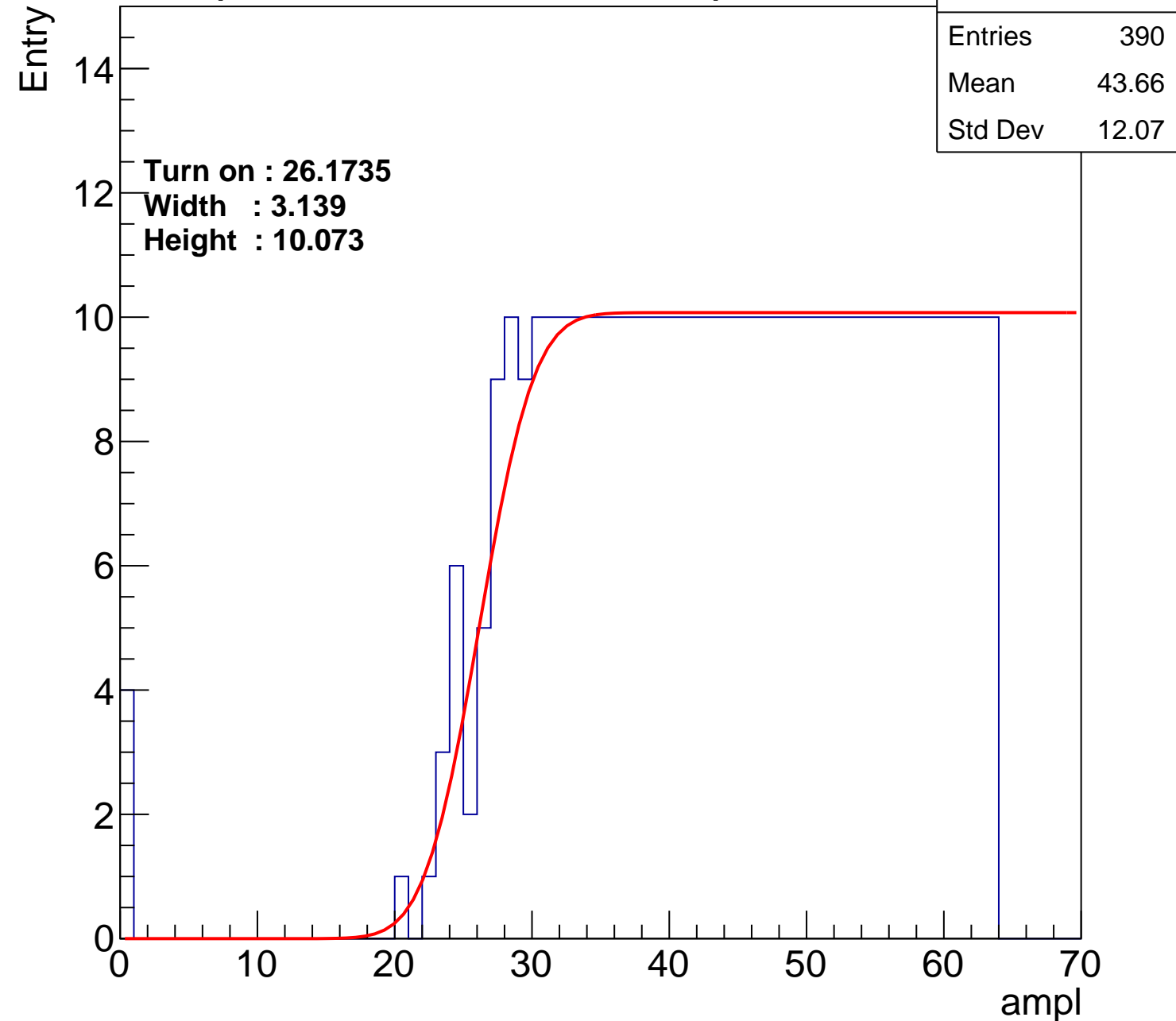
Width : 3.139

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch117

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.01
Std Dev	12.02

Turn on : 26.6533

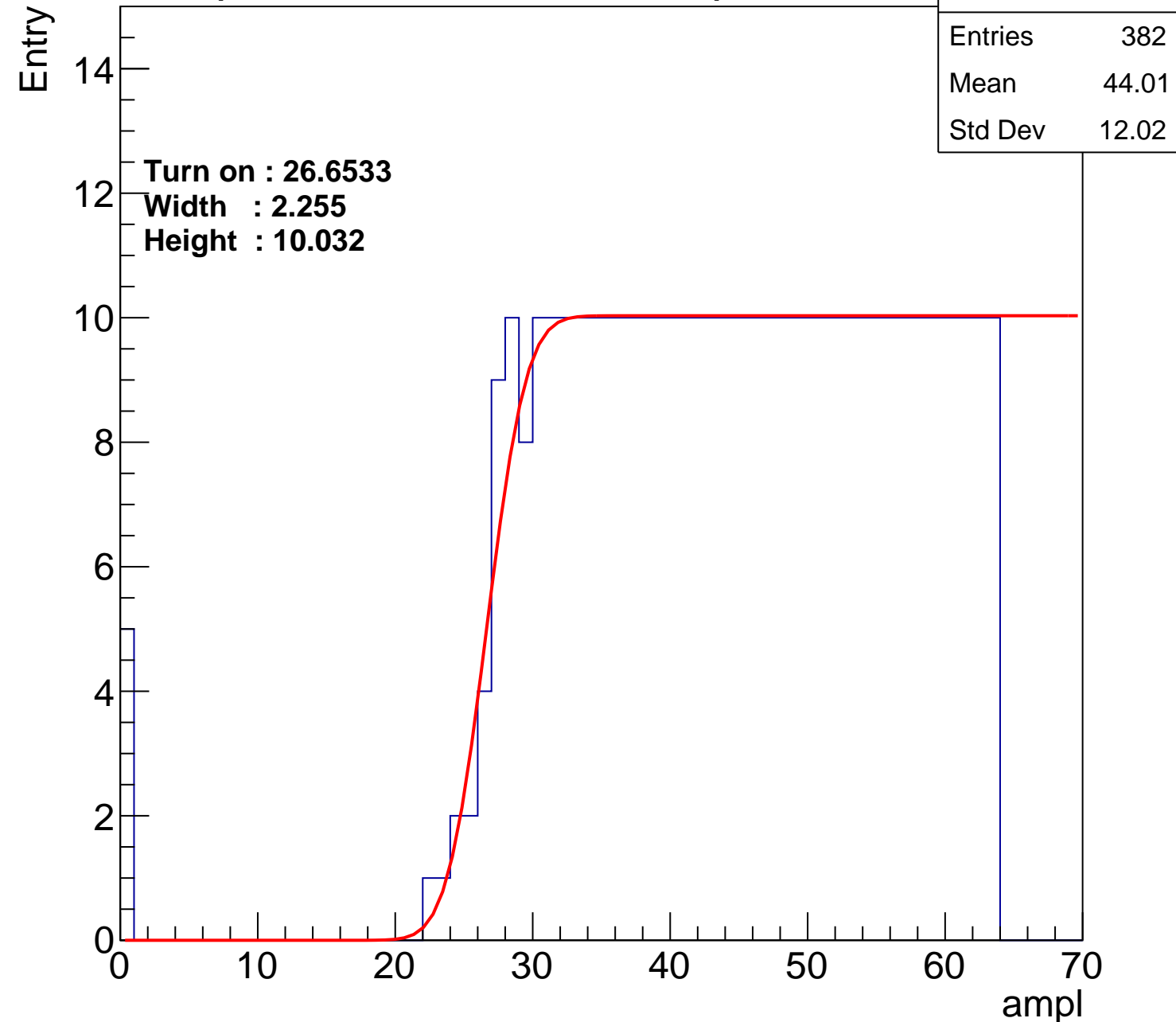
Width : 2.255

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch118

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.34
Std Dev	11.37

Turn on : 26.3709

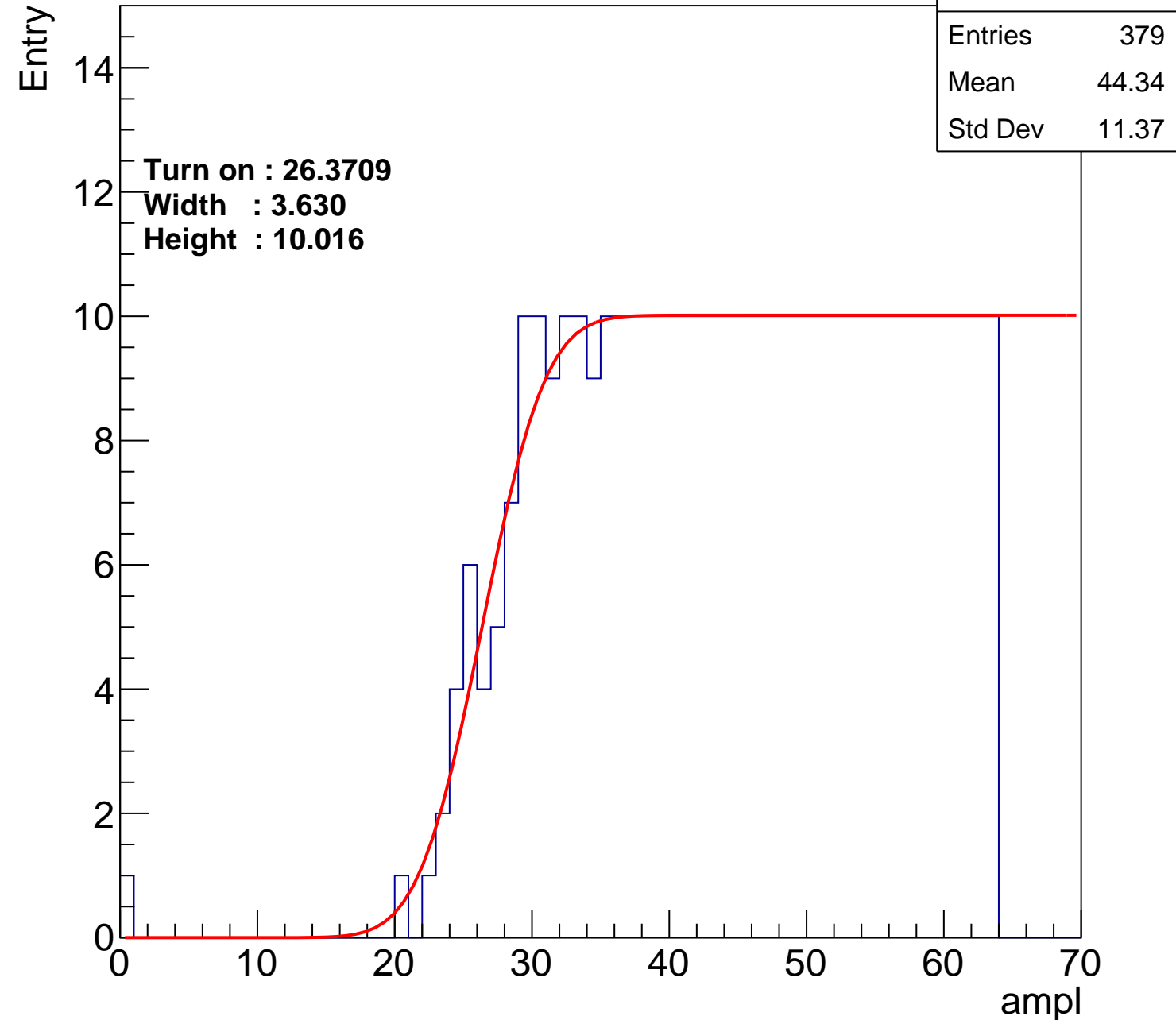
Width : 3.630

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch119

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.17
Std Dev	11.37

Turn on : 25.6757

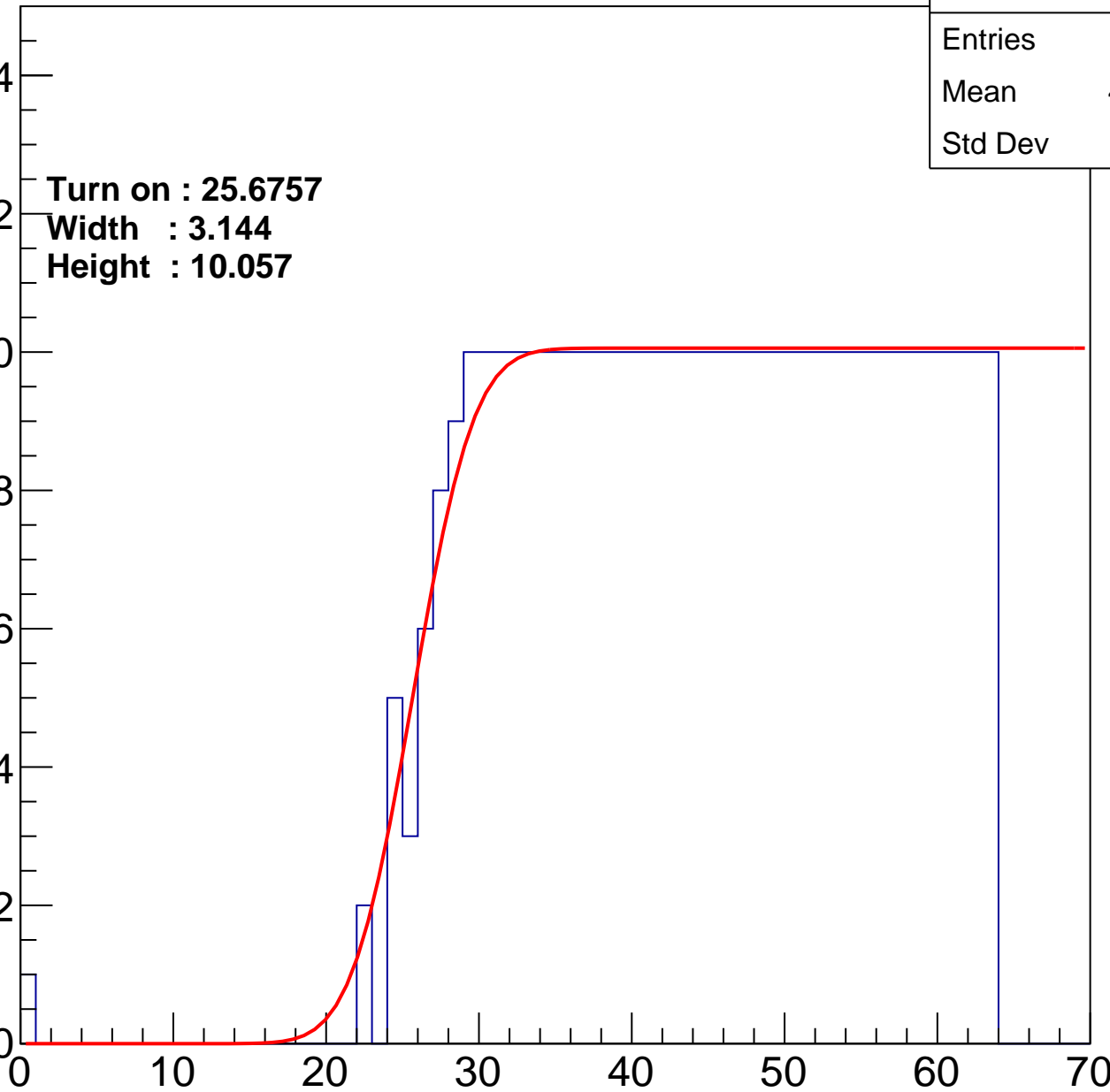
Width : 3.144

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch120

calib\_packv5\_042523\_0143.root, FC#10, port B3

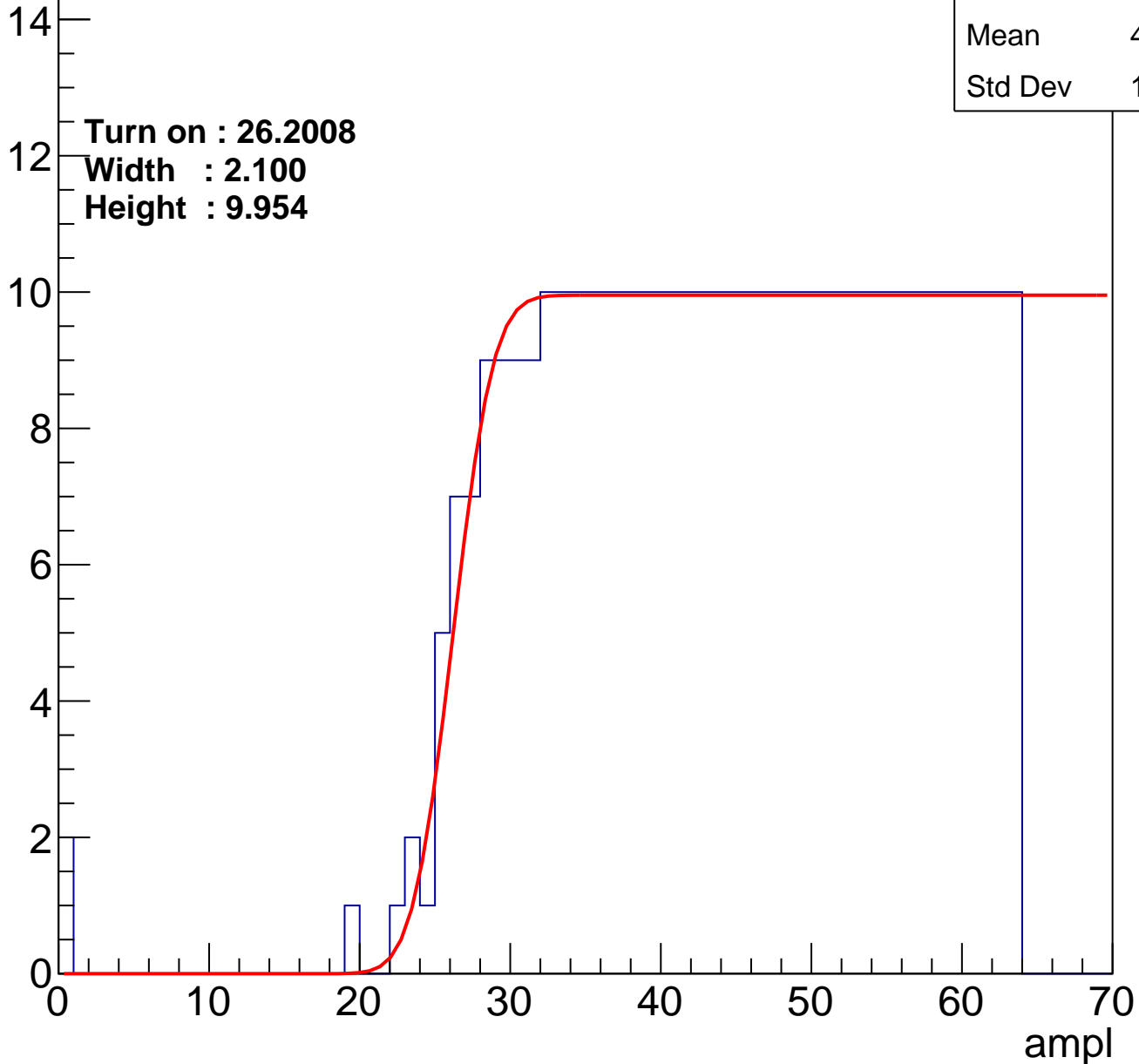
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.2008

Width : 2.100

Height : 9.954

Entry



# B1L002S, U12-ch121

calib\_packv5\_042523\_0143.root, FC#10, port B3

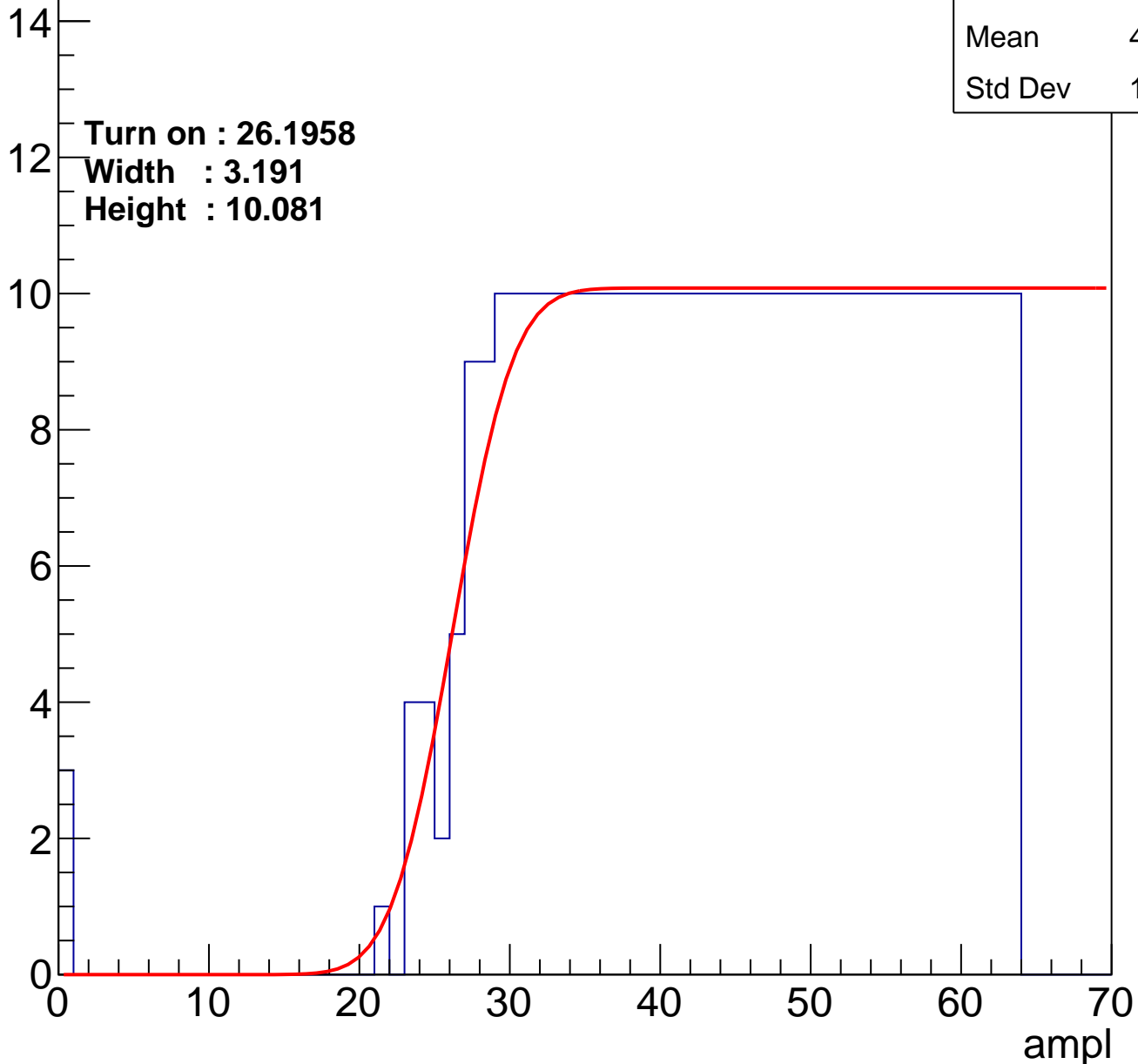
Entries	387
Mean	43.89
Std Dev	11.82

Turn on : 26.1958

Width : 3.191

Height : 10.081

Entry



# B1L002S, U12-ch122

calib\_packv5\_042523\_0143.root, FC#10, port B3

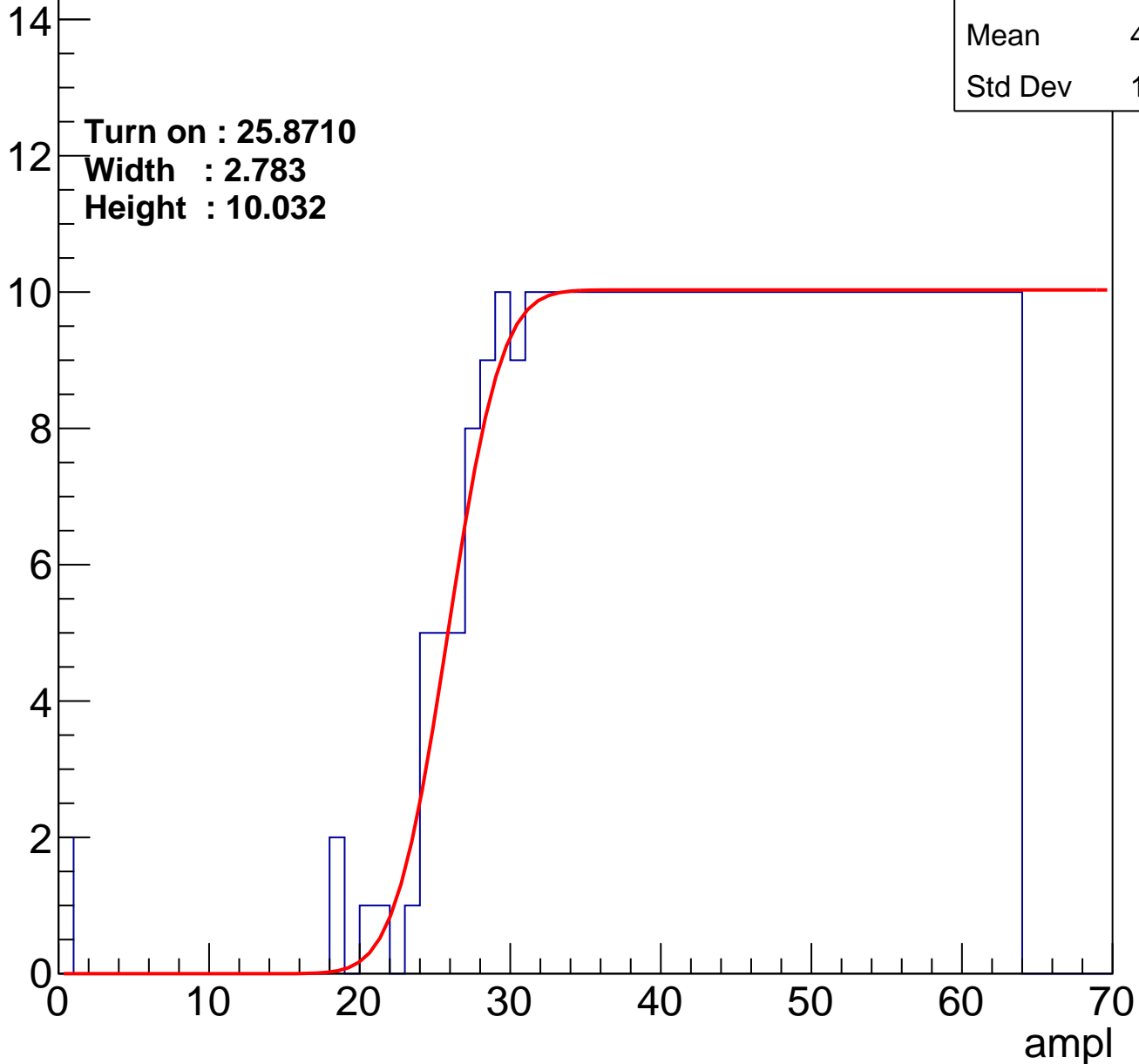
Entries	388
Mean	43.85
Std Dev	11.77

Turn on : 25.8710

Width : 2.783

Height : 10.032

Entry



# B1L002S, U12-ch123

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 25.9363

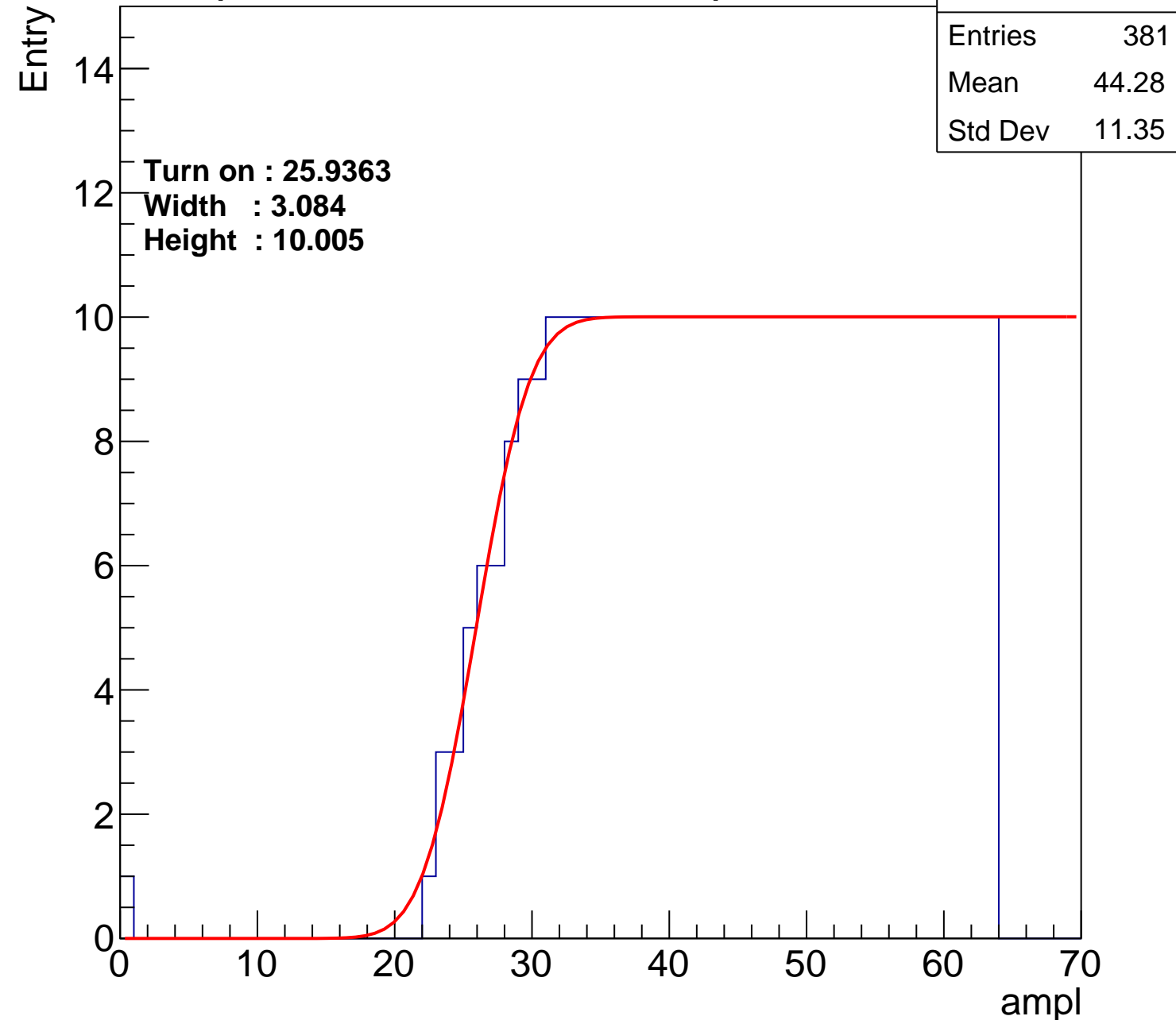
Width : 3.084

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch124

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.33
Std Dev	11.27

Turn on : 25.7234

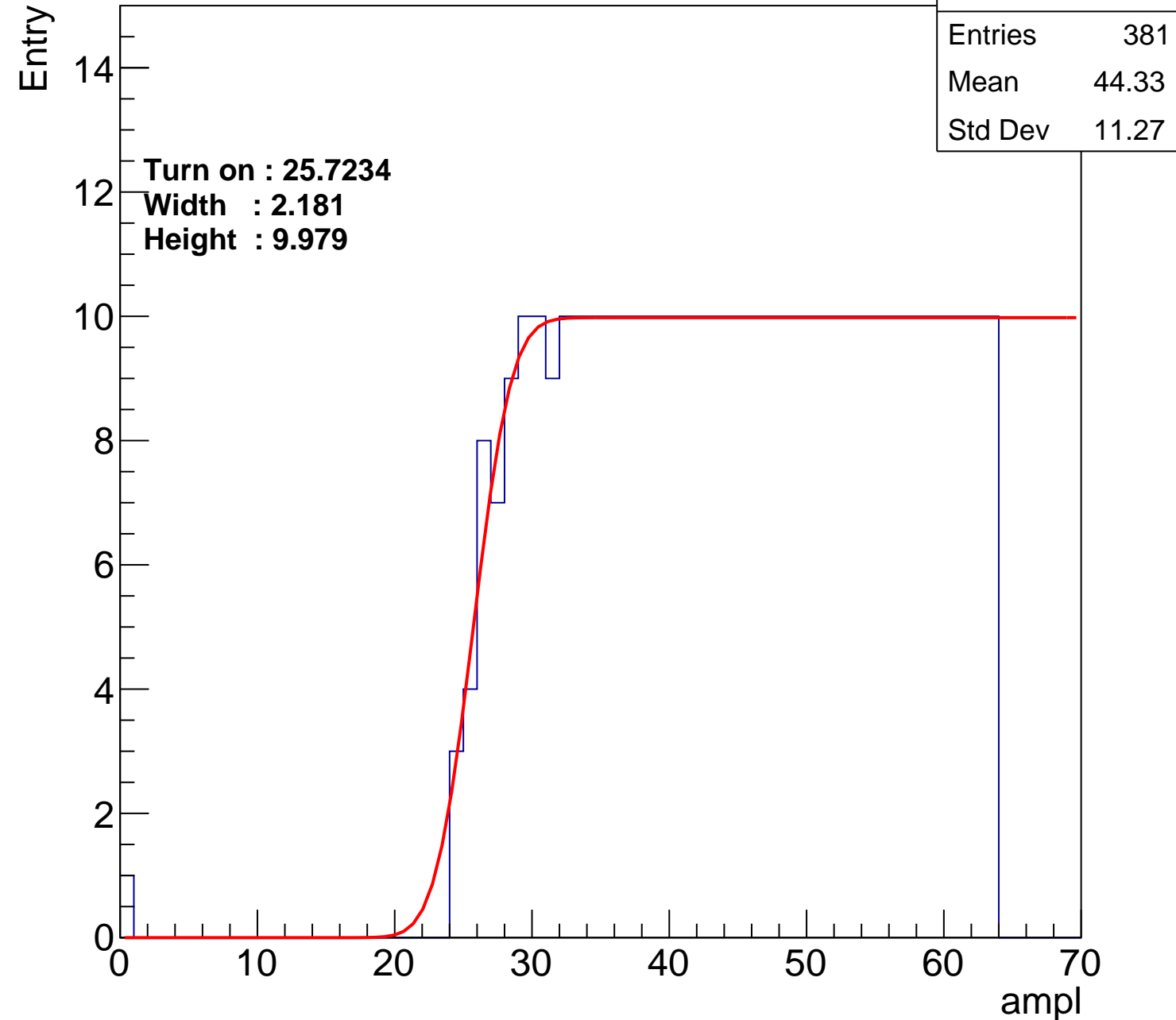
Width : 2.181

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch125

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.6601

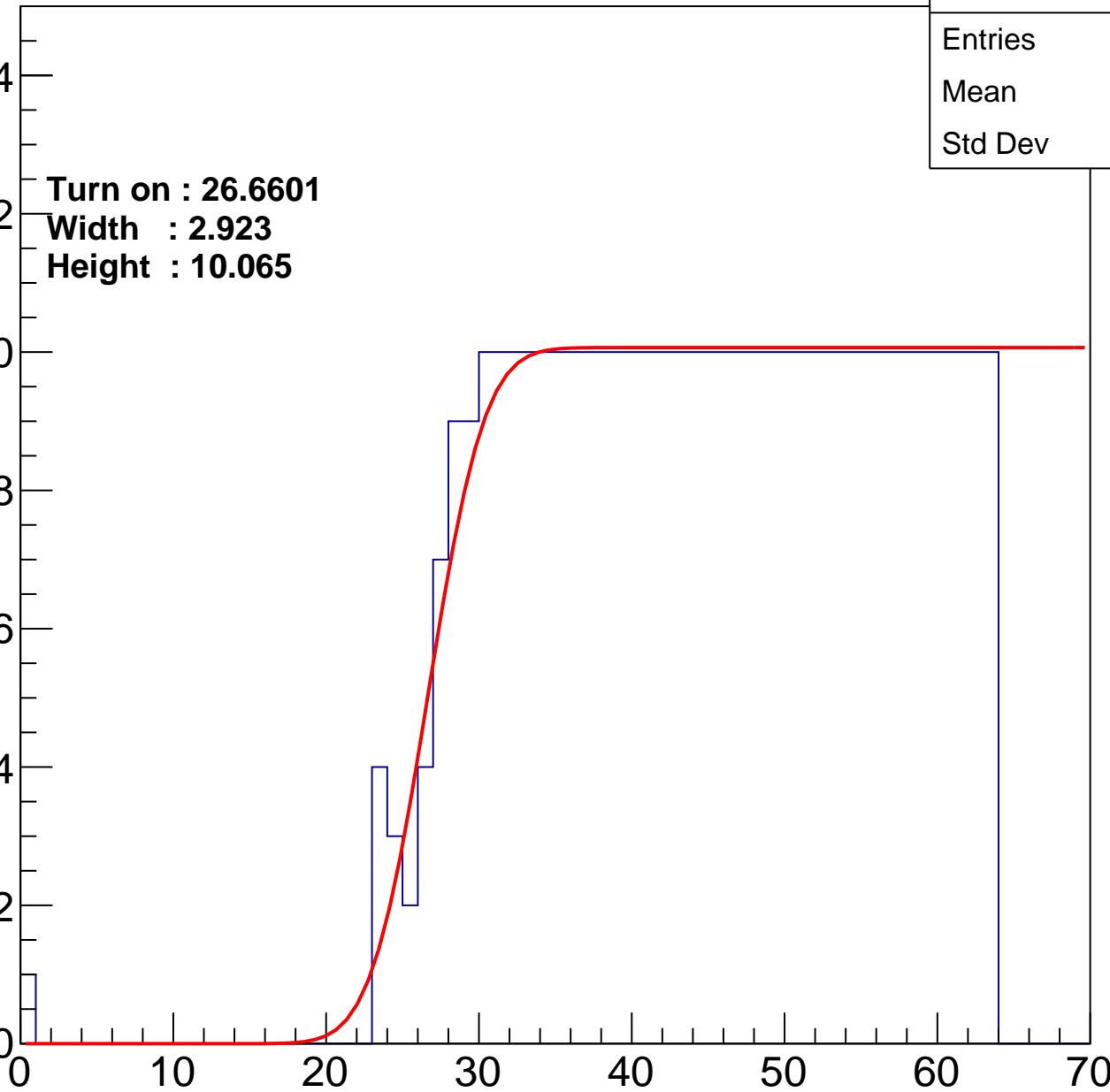
Width : 2.923

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch126

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.37
Std Dev	12.11

Turn on : 25.0955

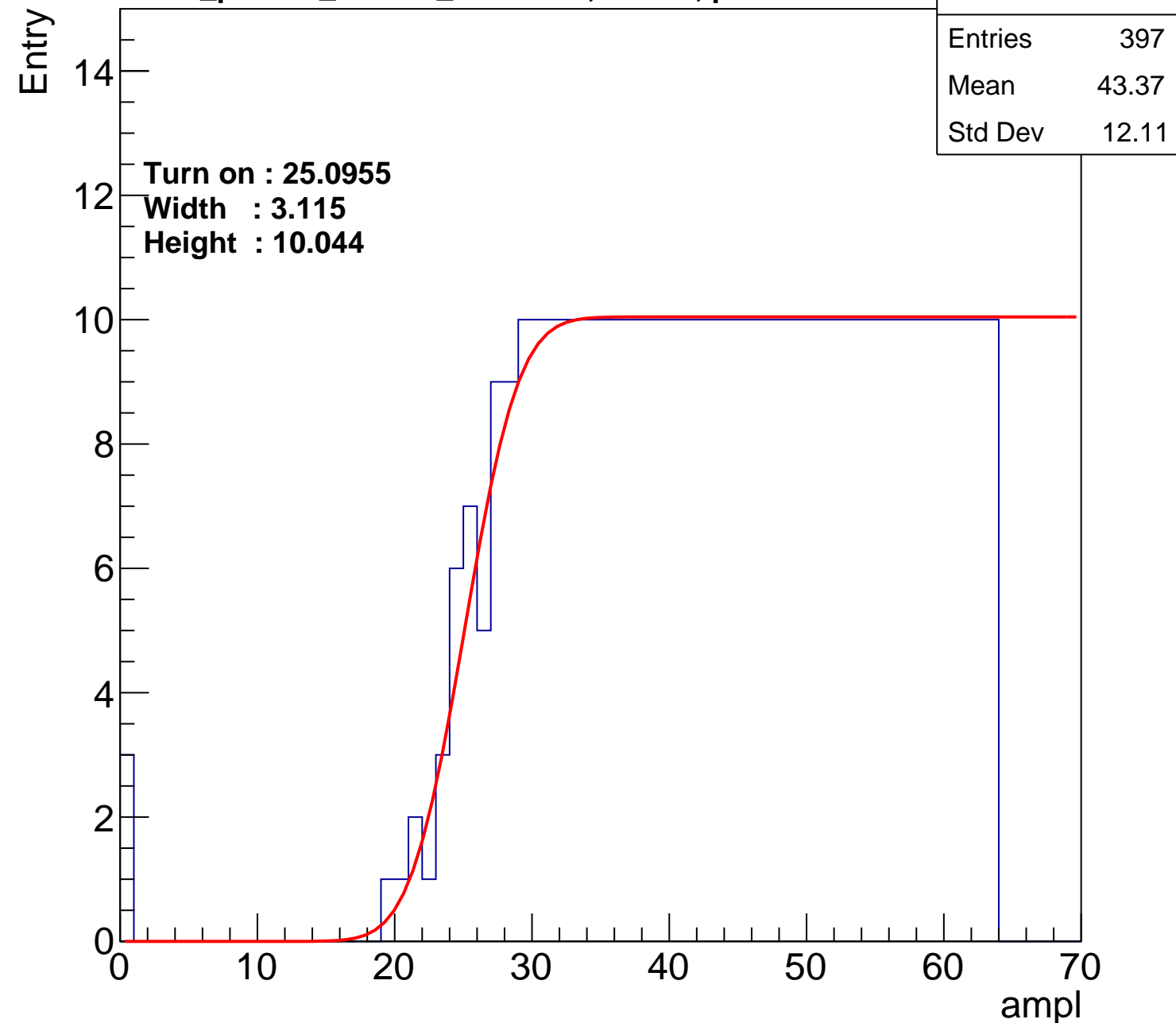
Width : 3.115

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U12-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.64
Std Dev	11.53

Turn on : 27.5836

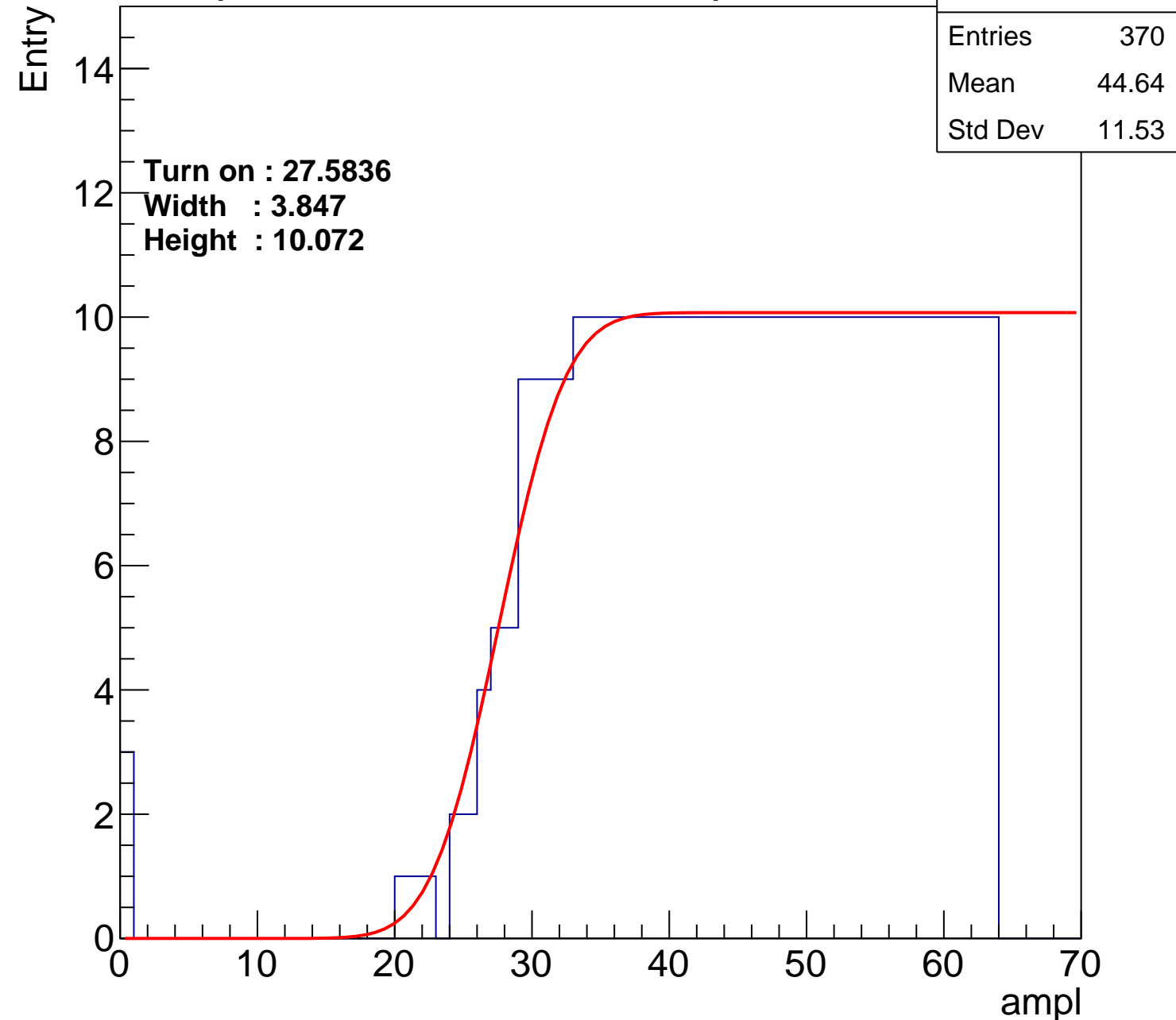
Width : 3.847

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U12-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.64
Std Dev	11.53

Turn on : 27.5836

Width : 3.847

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

