

B0L002S, U8-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.2
Std Dev	10.88

Turn on : 28.2616

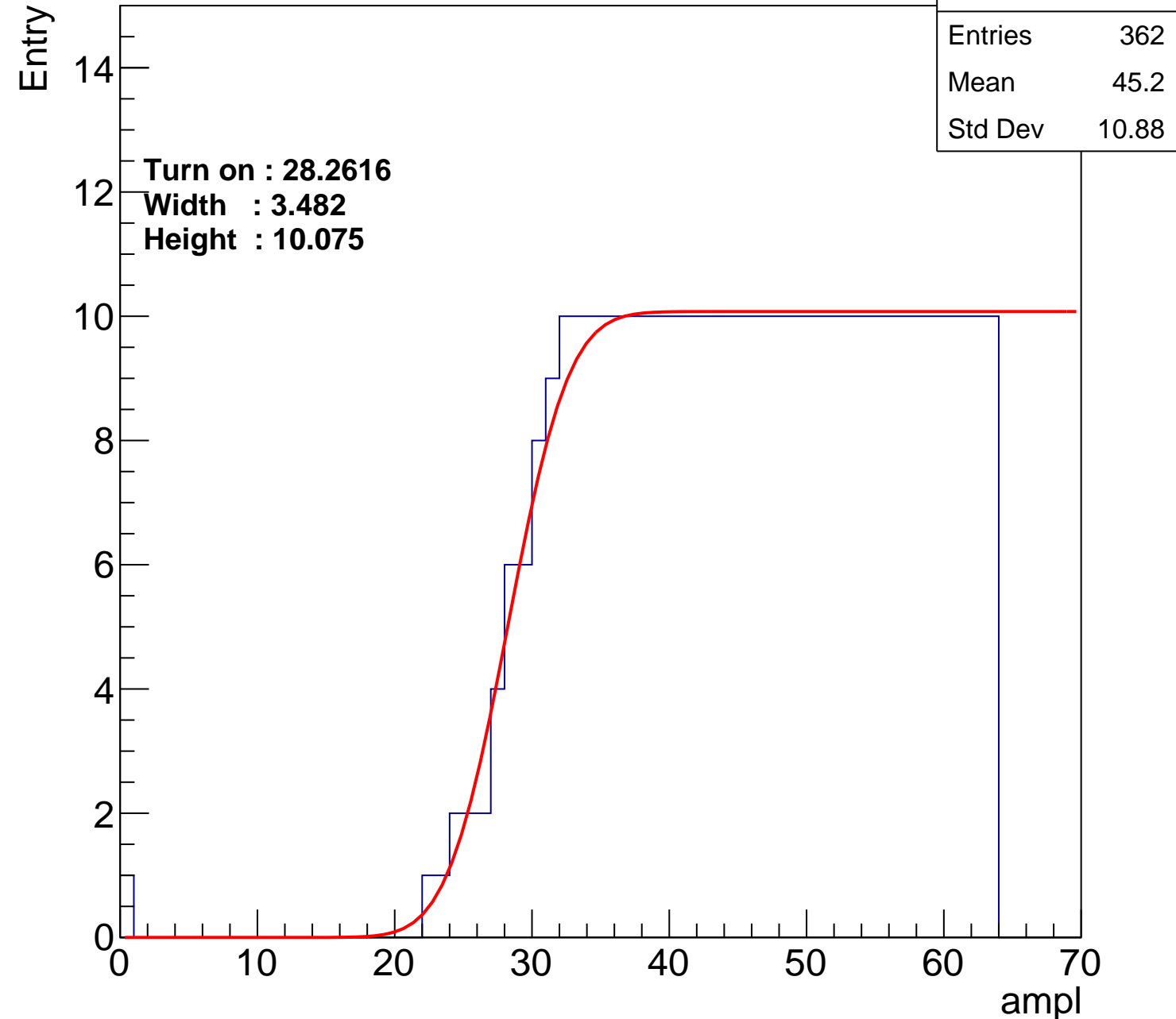
Width : 3.482

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch1

calib_packv5_042523_0143.root, FC#8, port C1

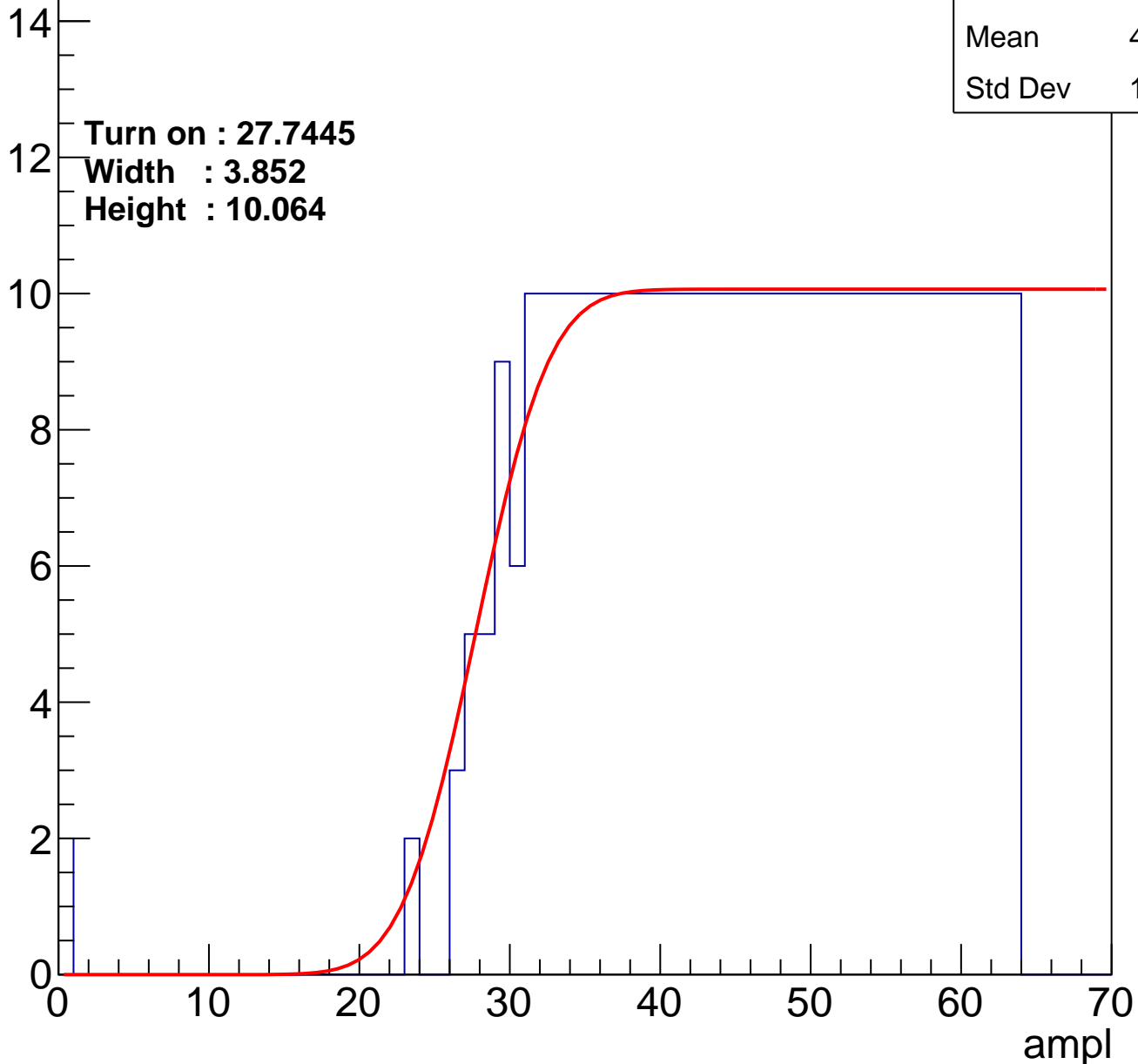
Entries	362
Mean	45.17
Std Dev	11.03

Turn on : 27.7445

Width : 3.852

Height : 10.064

Entry



B0L002S, U8-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.02
Std Dev	11.44

Turn on : 28.3344

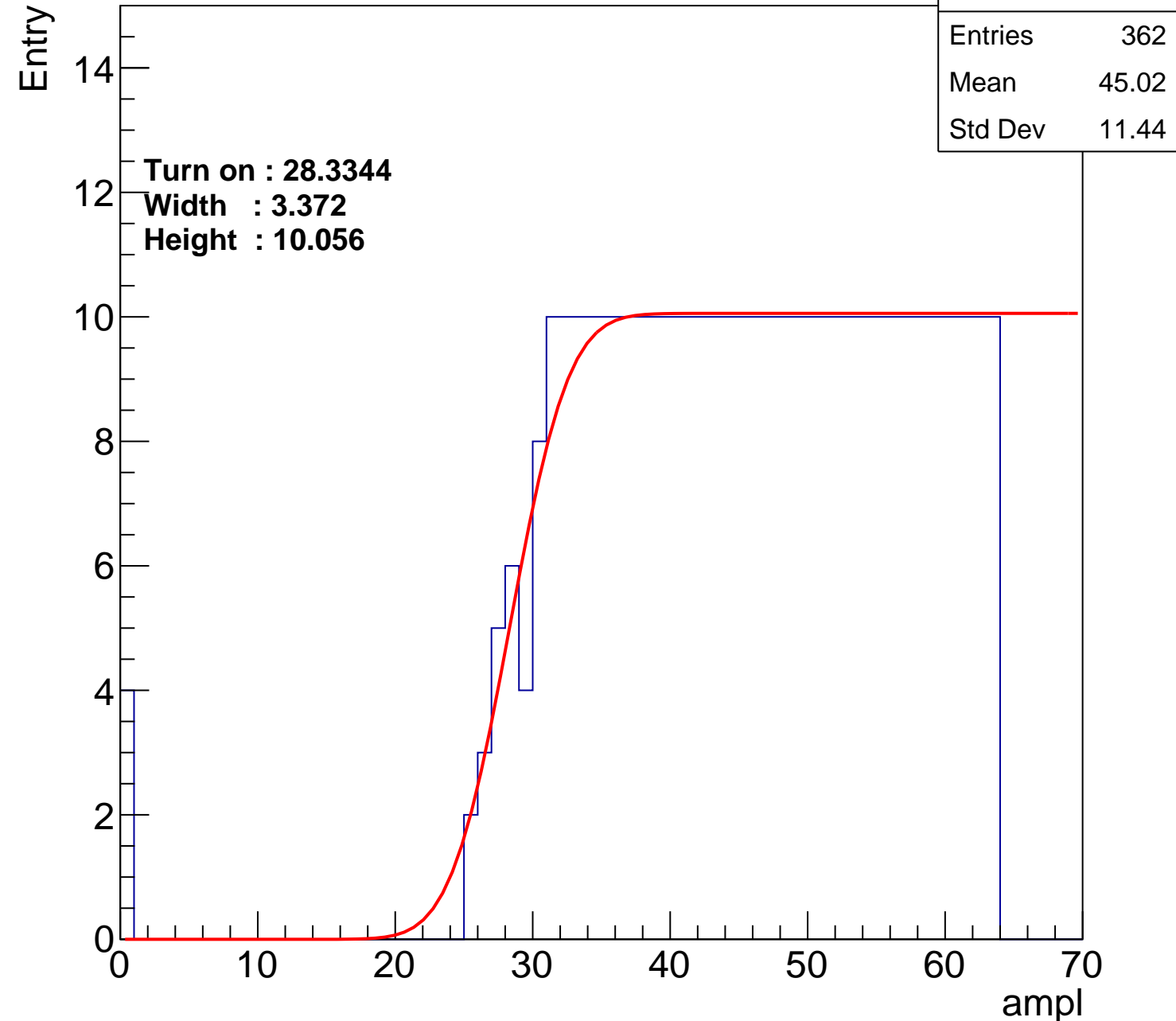
Width : 3.372

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch3

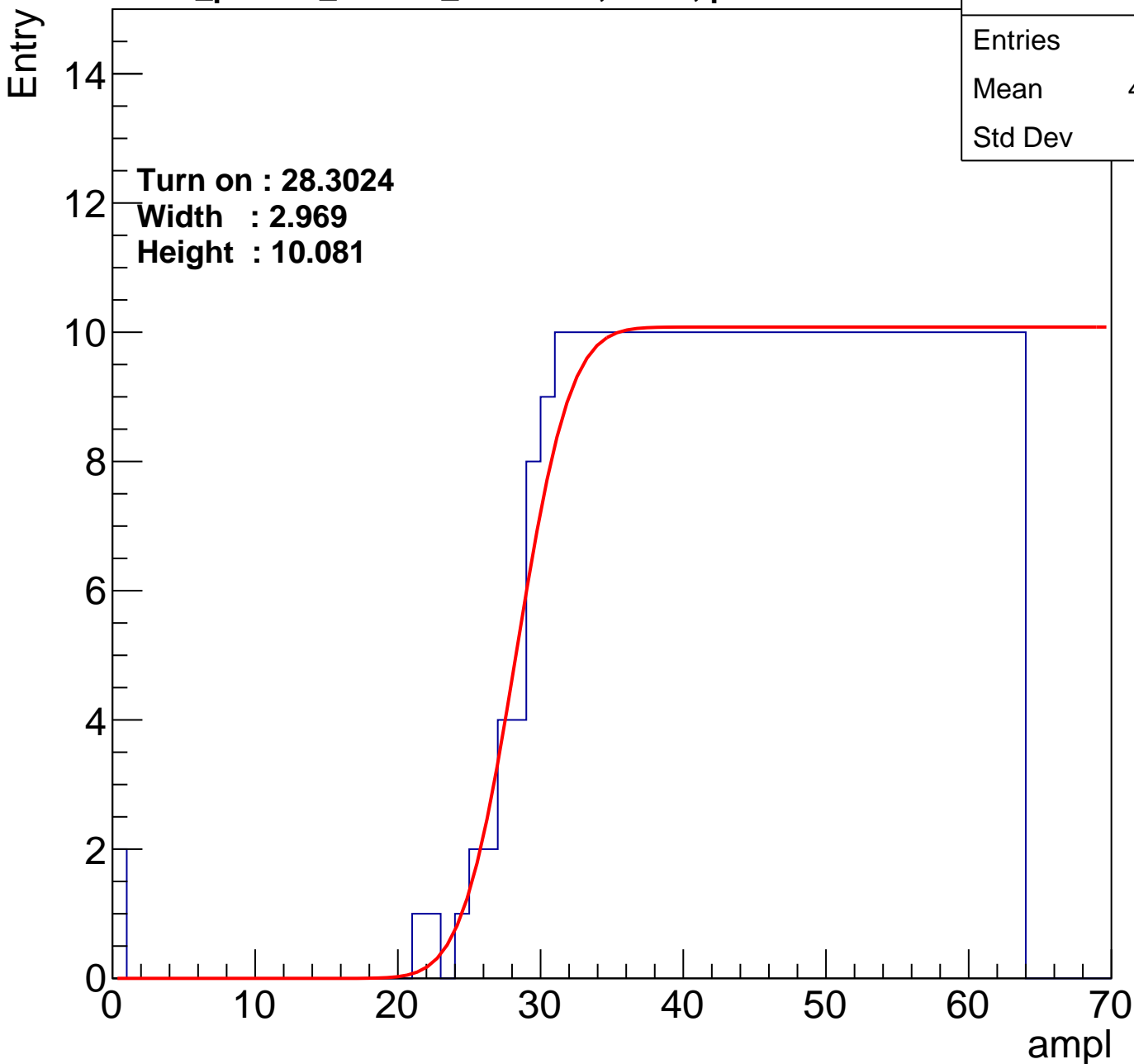
calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.06
Std Dev	11.1

Turn on : 28.3024

Width : 2.969

Height : 10.081



B0L002S, U8-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 29.8565

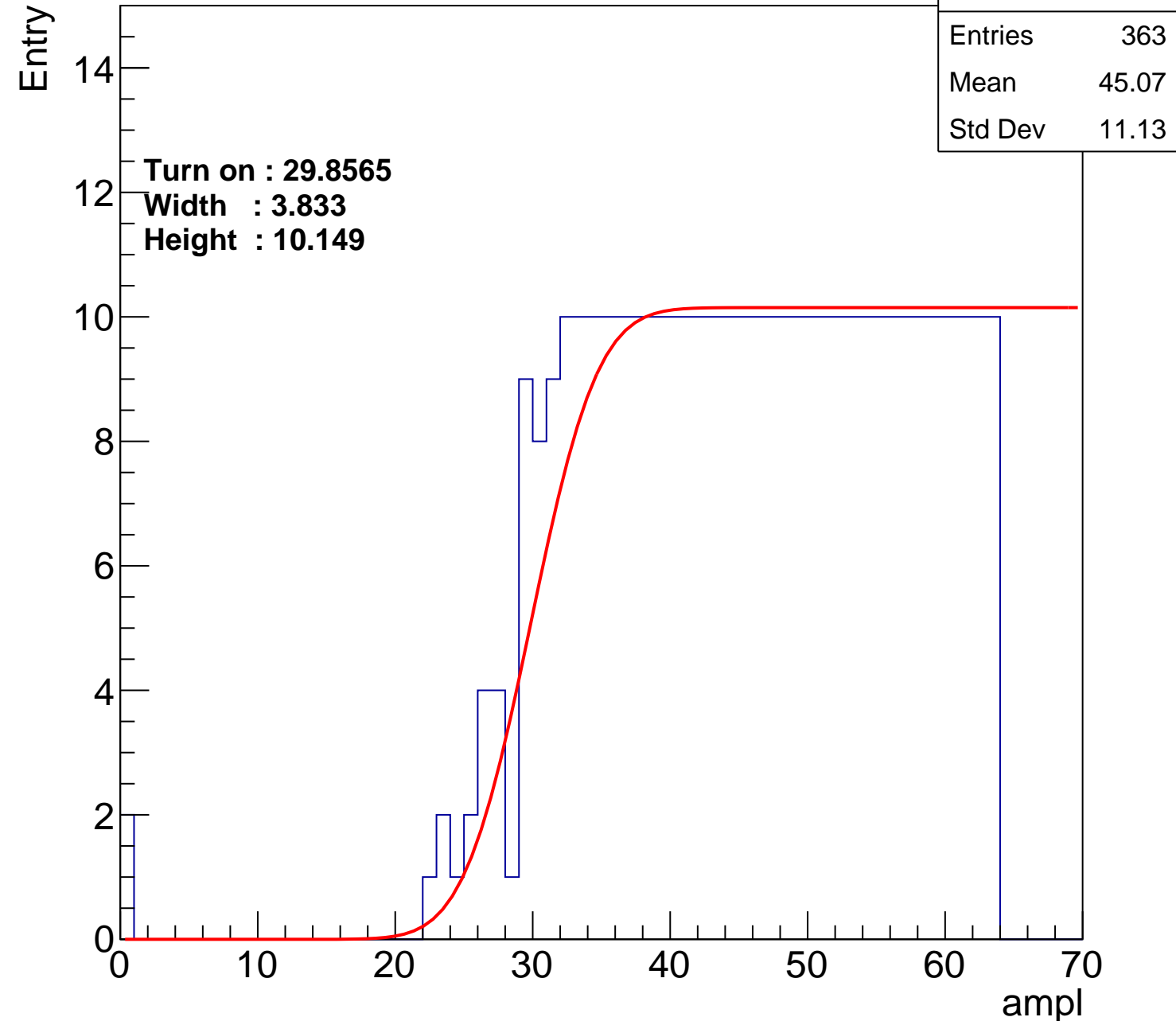
Width : 3.833

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.57
Std Dev	10.66

Turn on : 28.7481

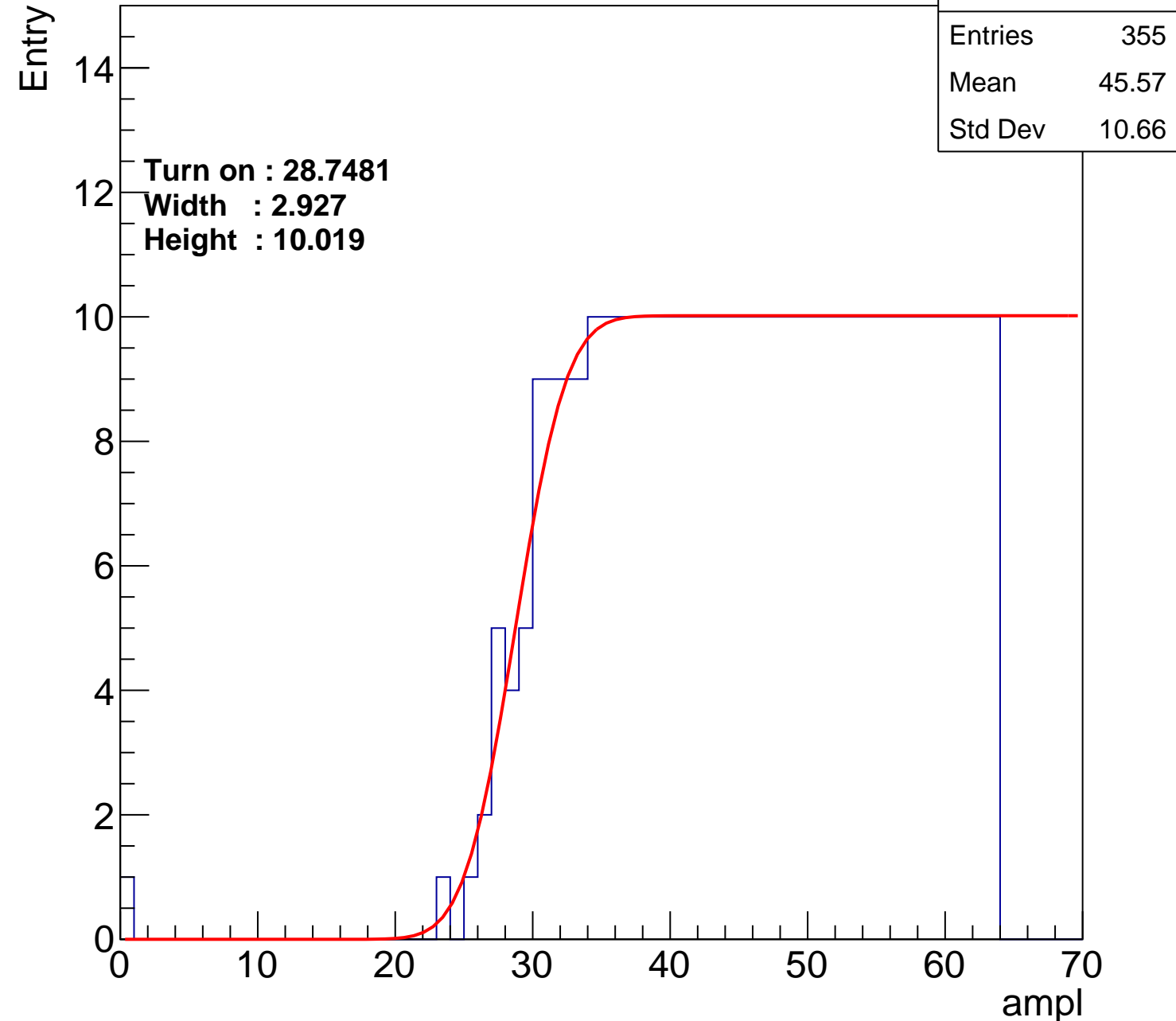
Width : 2.927

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.2
Std Dev	10.82

Turn on : 27.3912

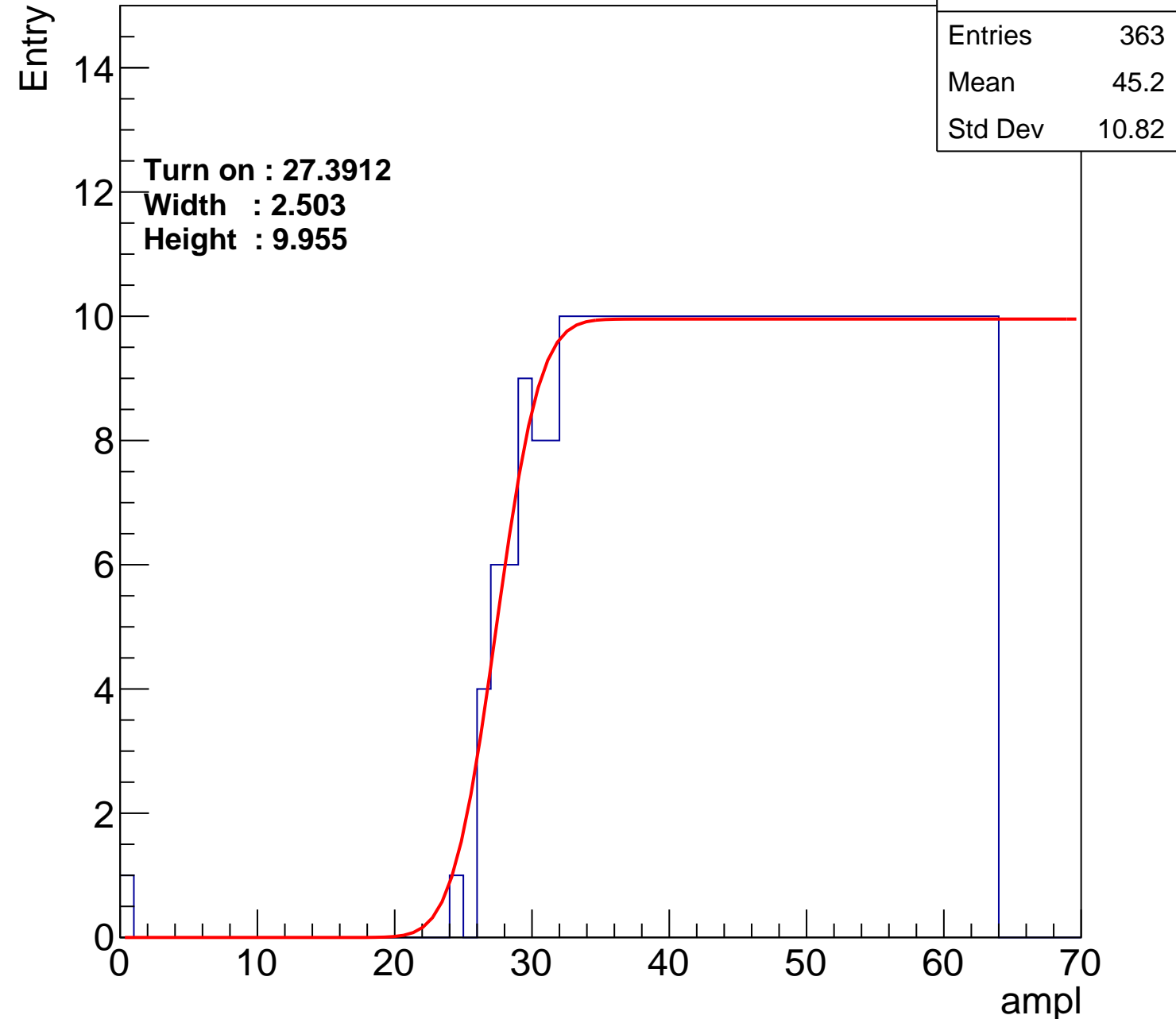
Width : 2.503

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.26
Std Dev	10.83

Turn on : 28.2549

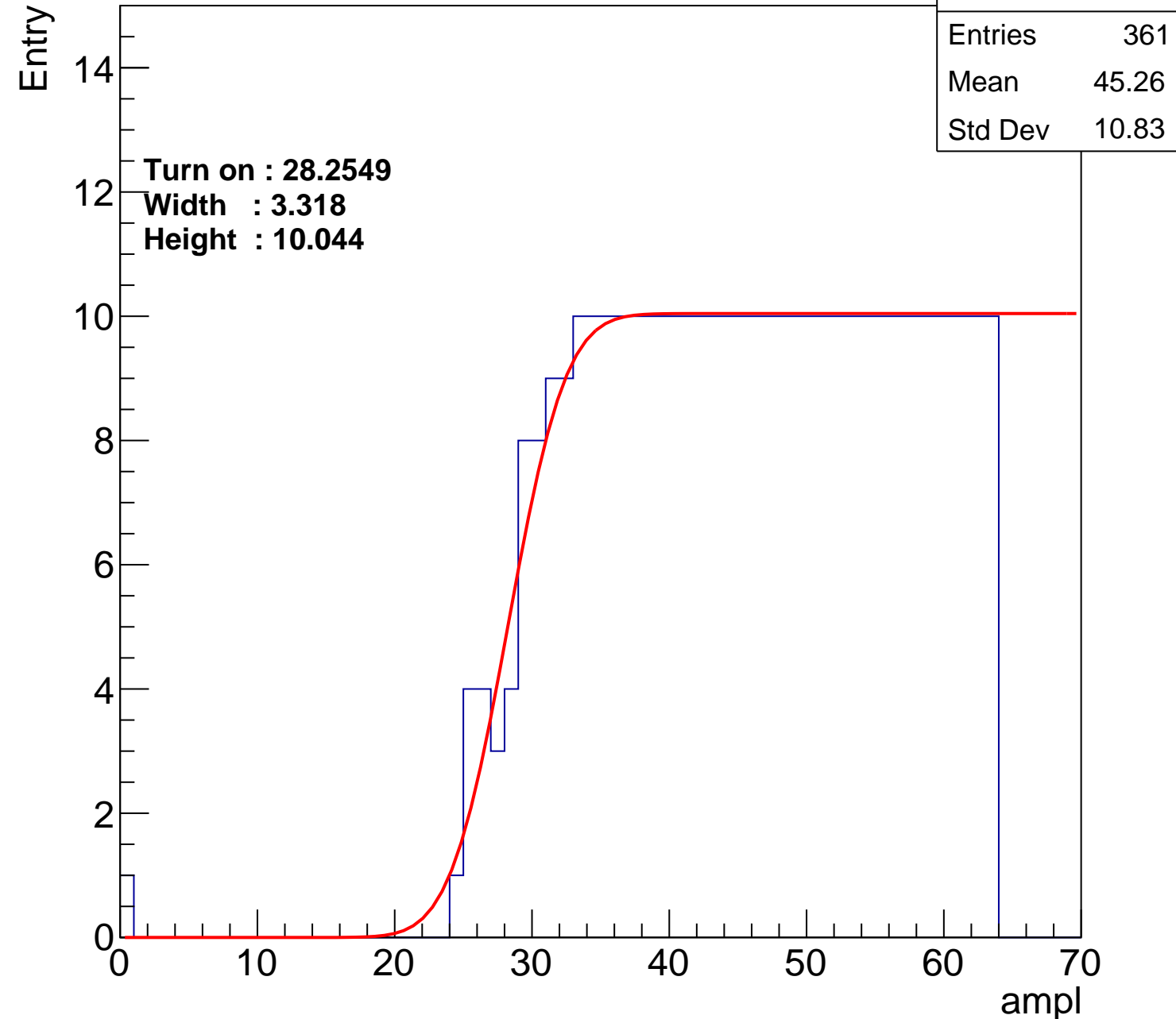
Width : 3.318

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.54
Std Dev	11.38

Turn on : 26.7610

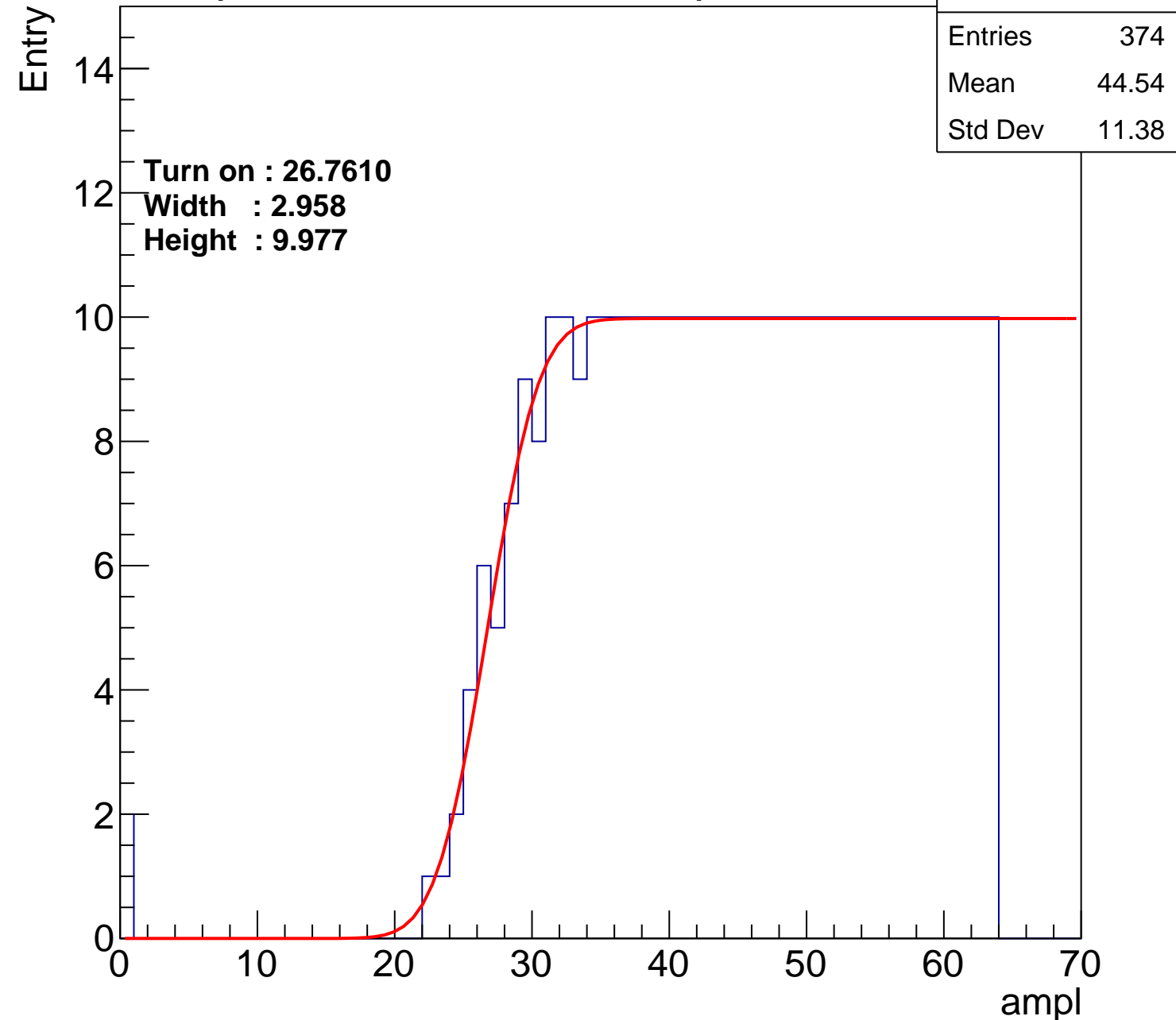
Width : 2.958

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 28.0688

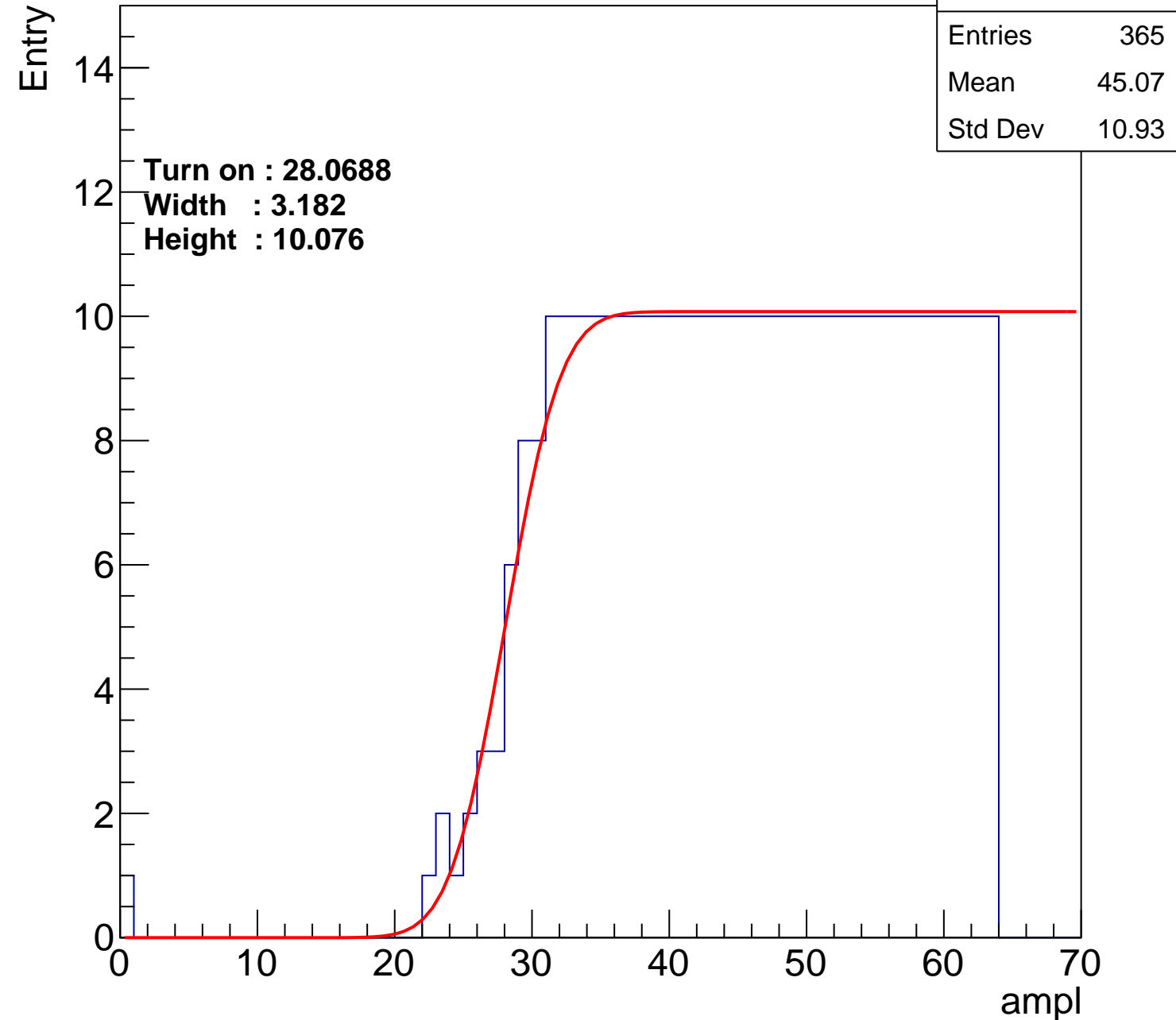
Width : 3.182

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.25
Std Dev	11.05

Turn on : 28.3955

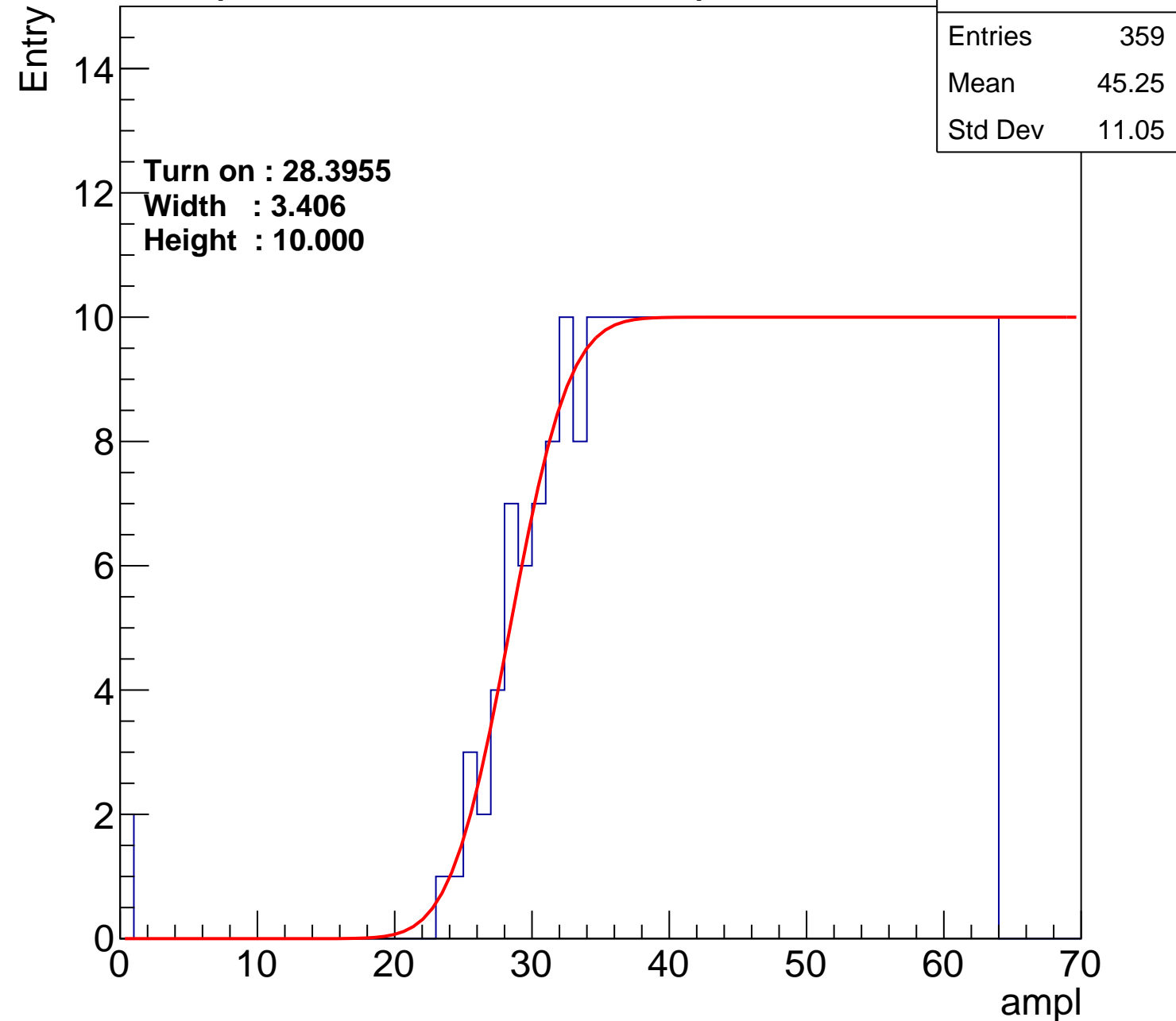
Width : 3.406

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.32
Std Dev	10.8

Turn on : 28.3012

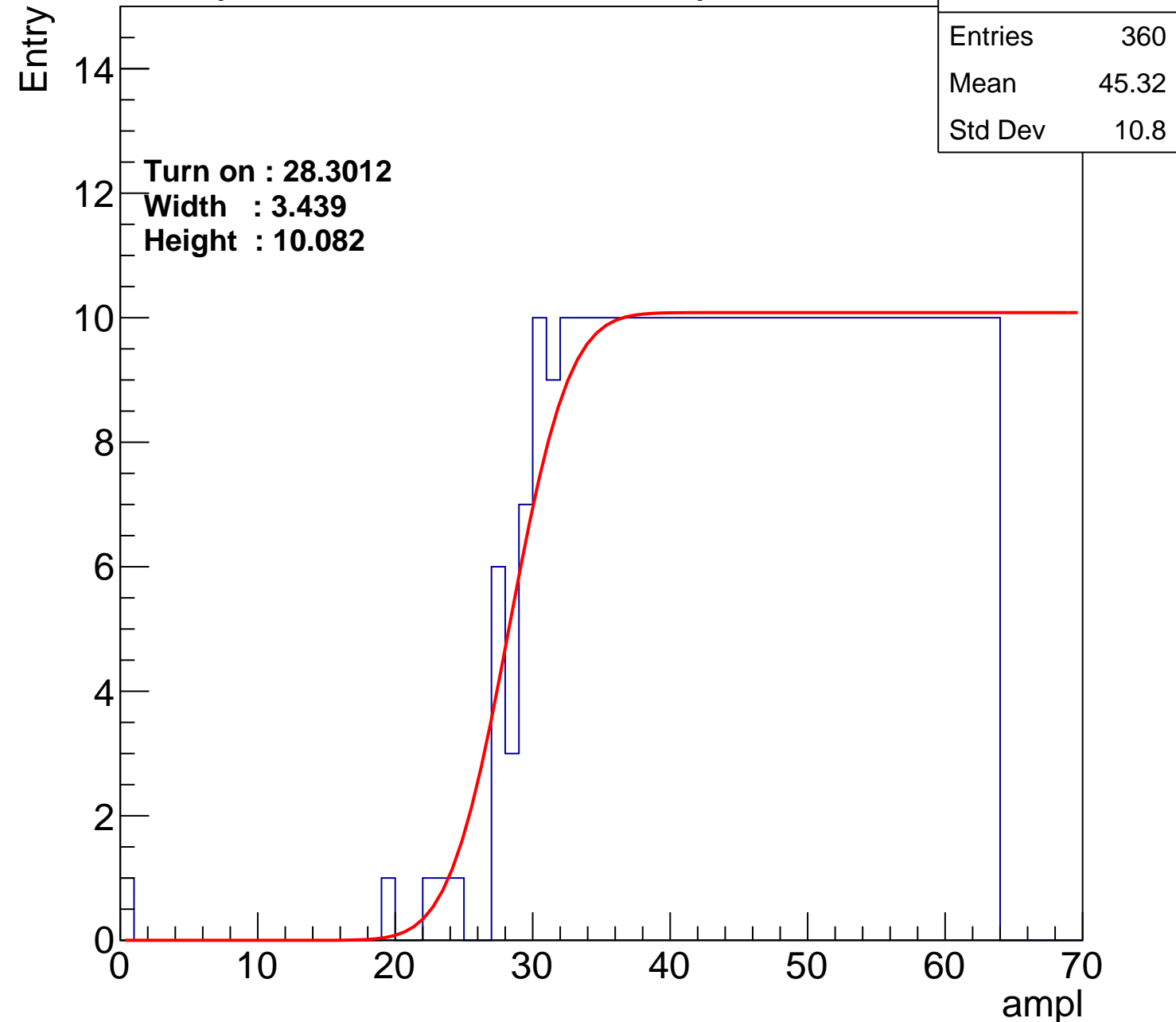
Width : 3.439

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch12

calib_packv5_042523_0143.root, FC#8, port C1

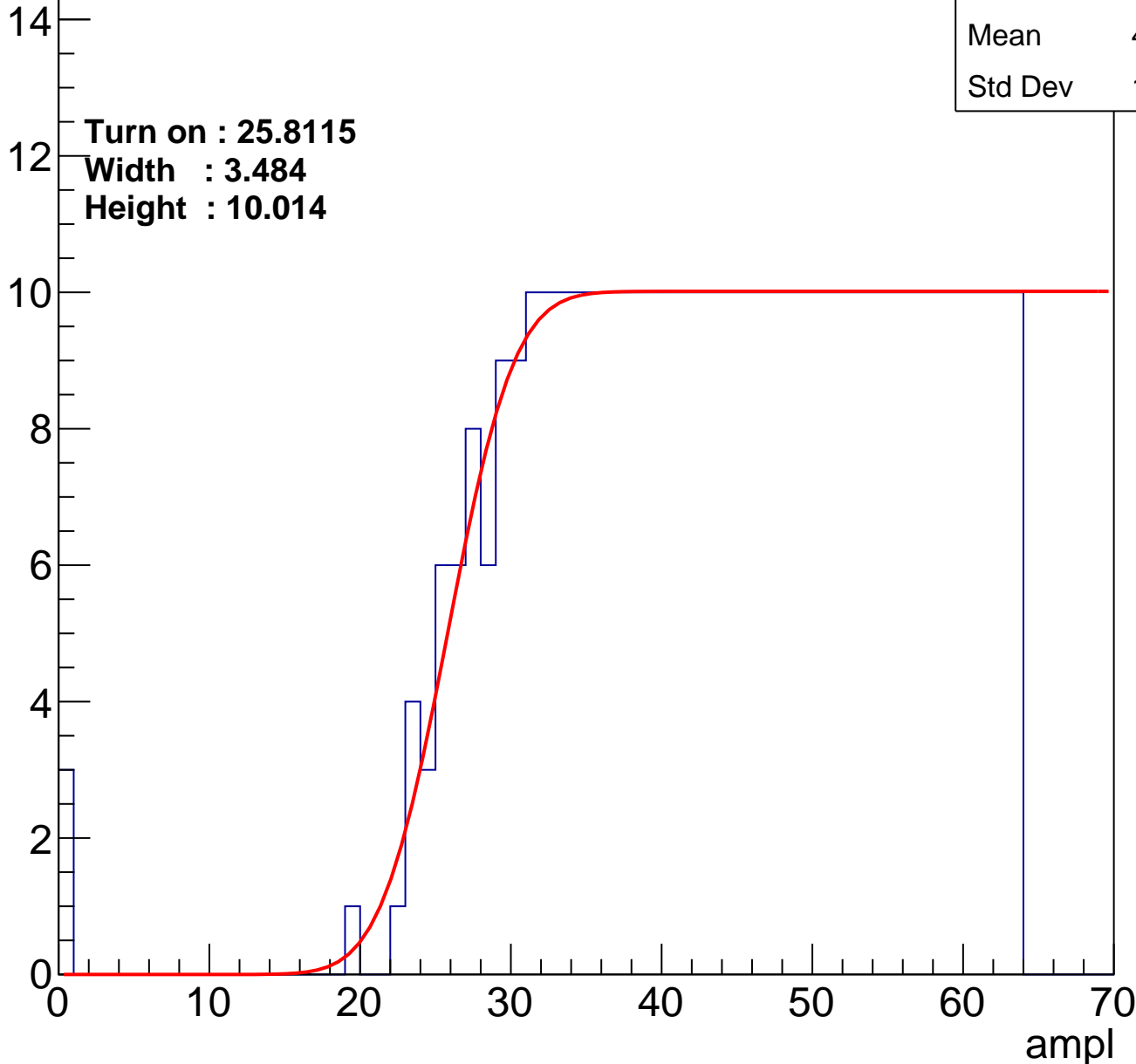
Entries	386
Mean	43.88
Std Dev	11.88

Turn on : 25.8115

Width : 3.484

Height : 10.014

Entry



B0L002S, U8-ch13

calib_packv5_042523_0143.root, FC#8, port C1

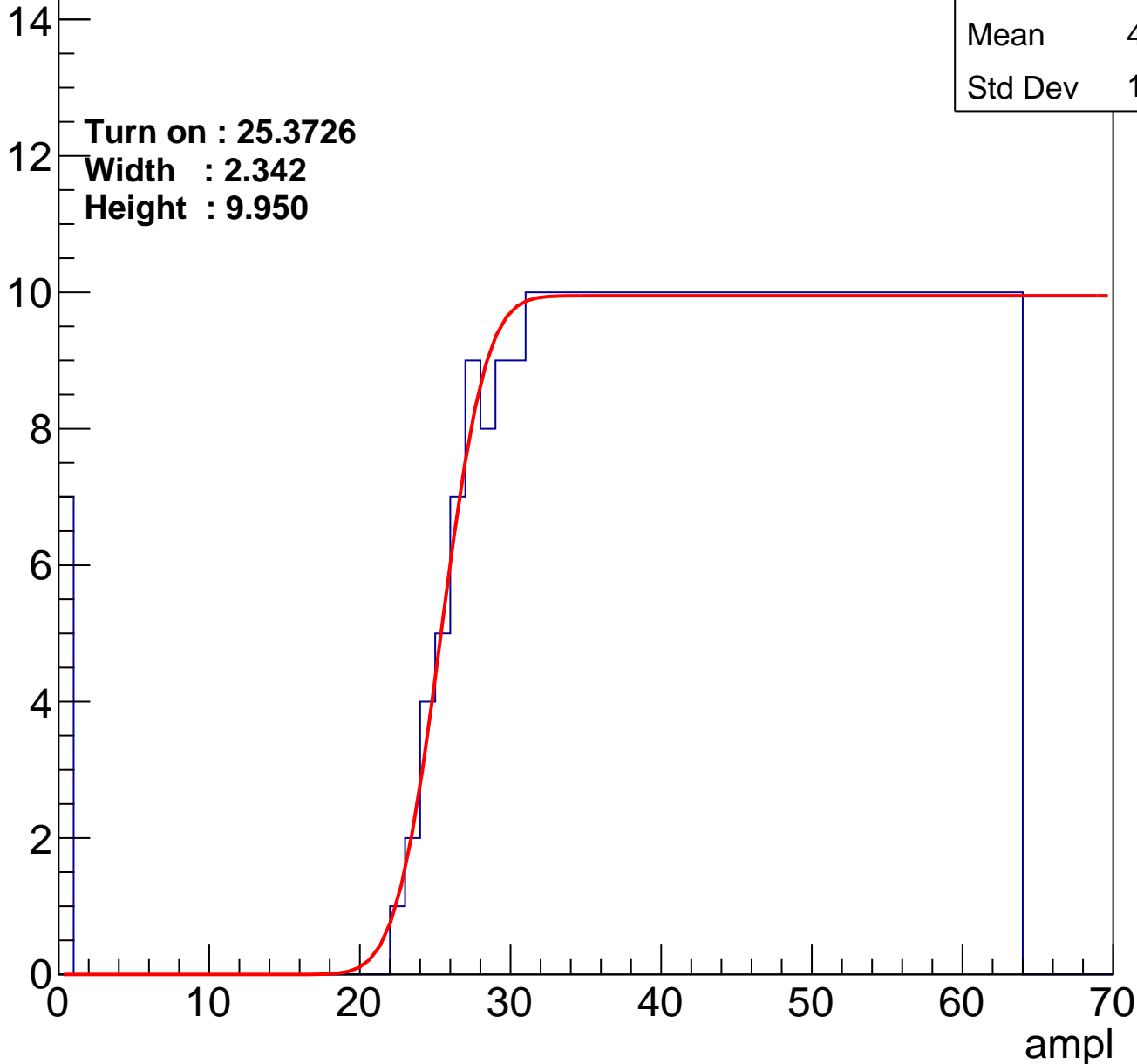
Entry

Entries	391
Mean	43.42
Std Dev	12.57

Turn on : 25.3726

Width : 2.342

Height : 9.950



B0L002S, U8-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 26.0095

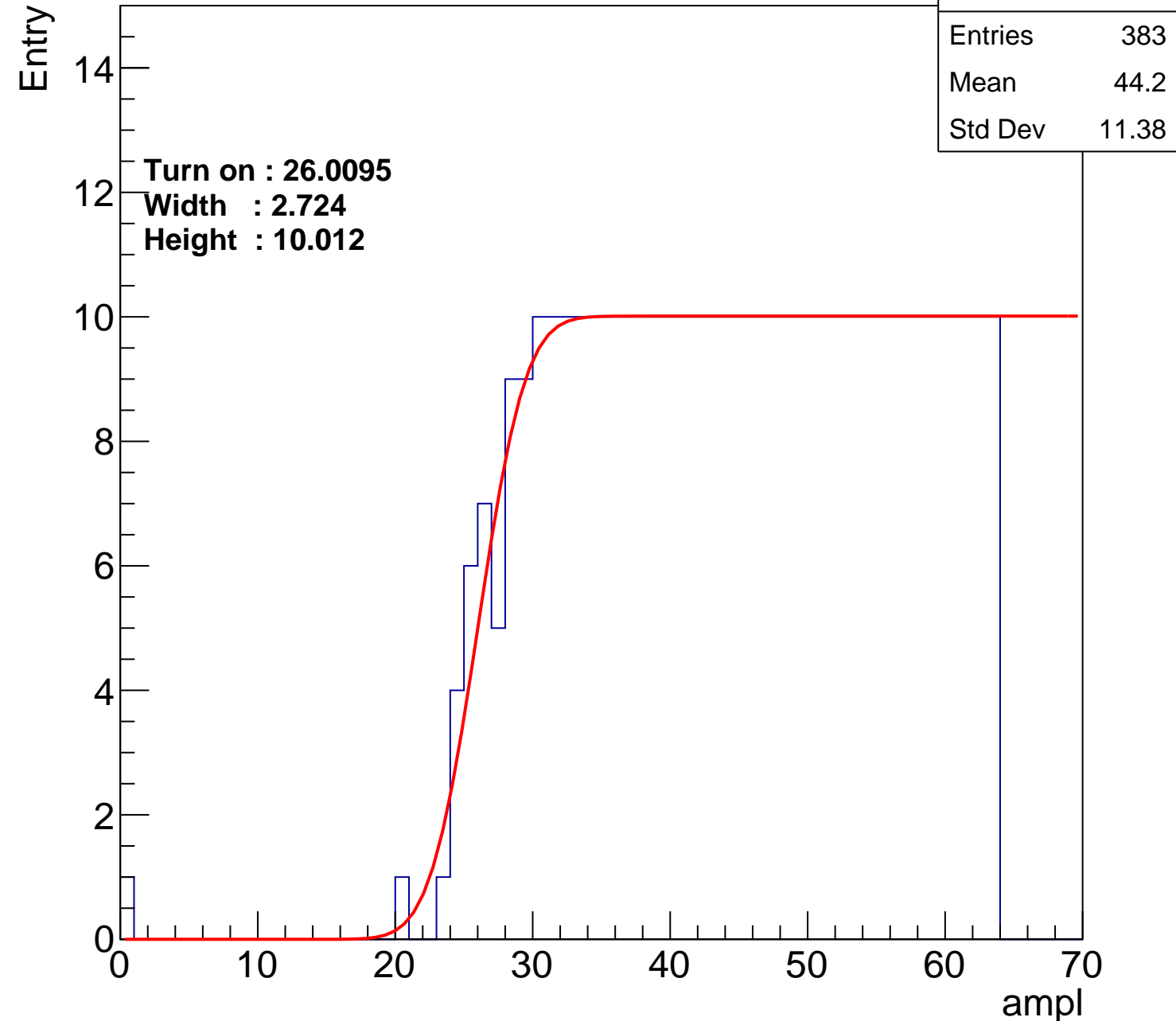
Width : 2.724

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch15

calib_packv5_042523_0143.root, FC#8, port C1

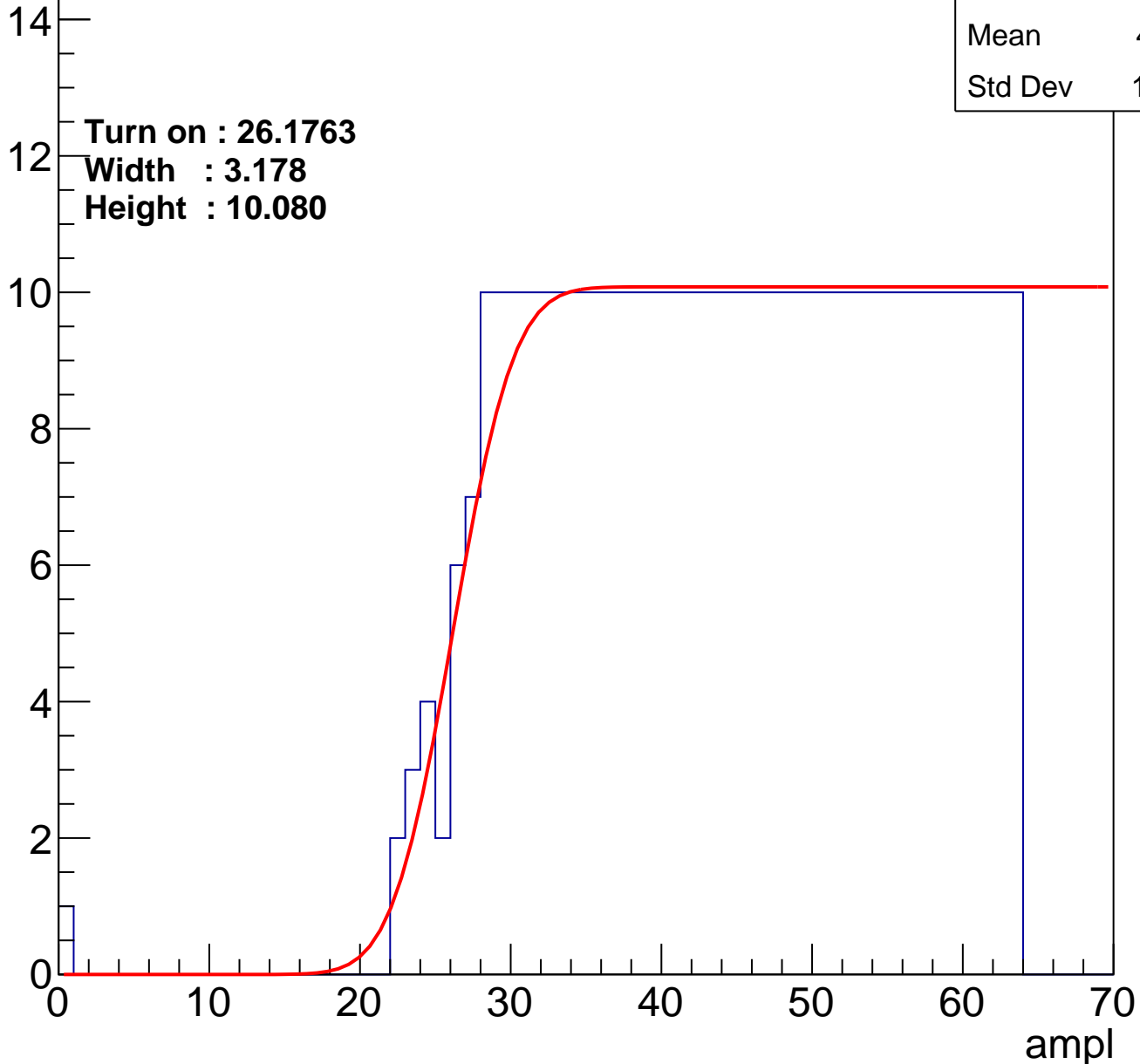
Entries	385
Mean	44.11
Std Dev	11.42

Turn on : 26.1763

Width : 3.178

Height : 10.080

Entry



B0L002S, U8-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.78
Std Dev	11.27

Turn on : 27.2073

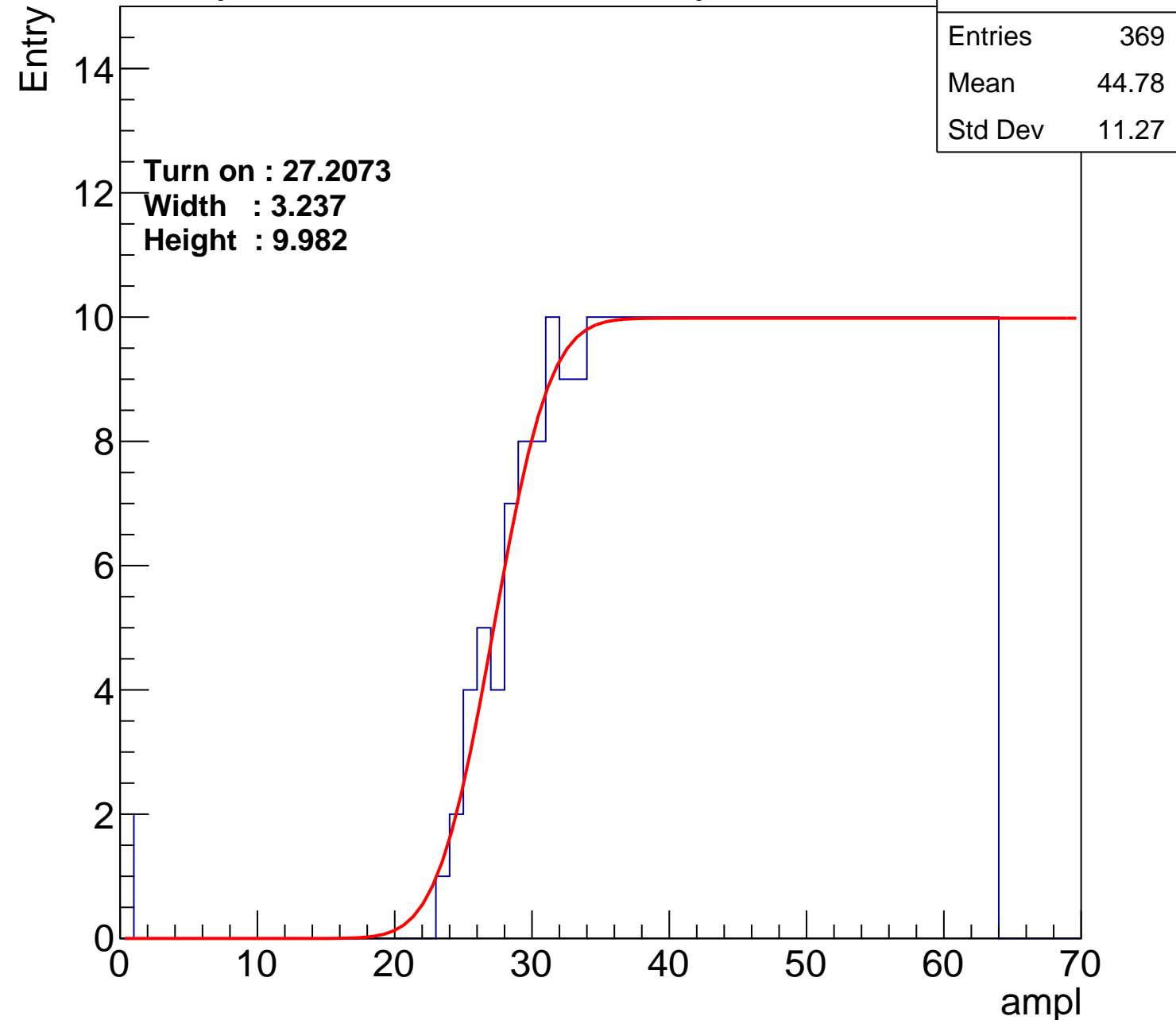
Width : 3.237

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.57
Std Dev	11.22

Turn on : 26.8954

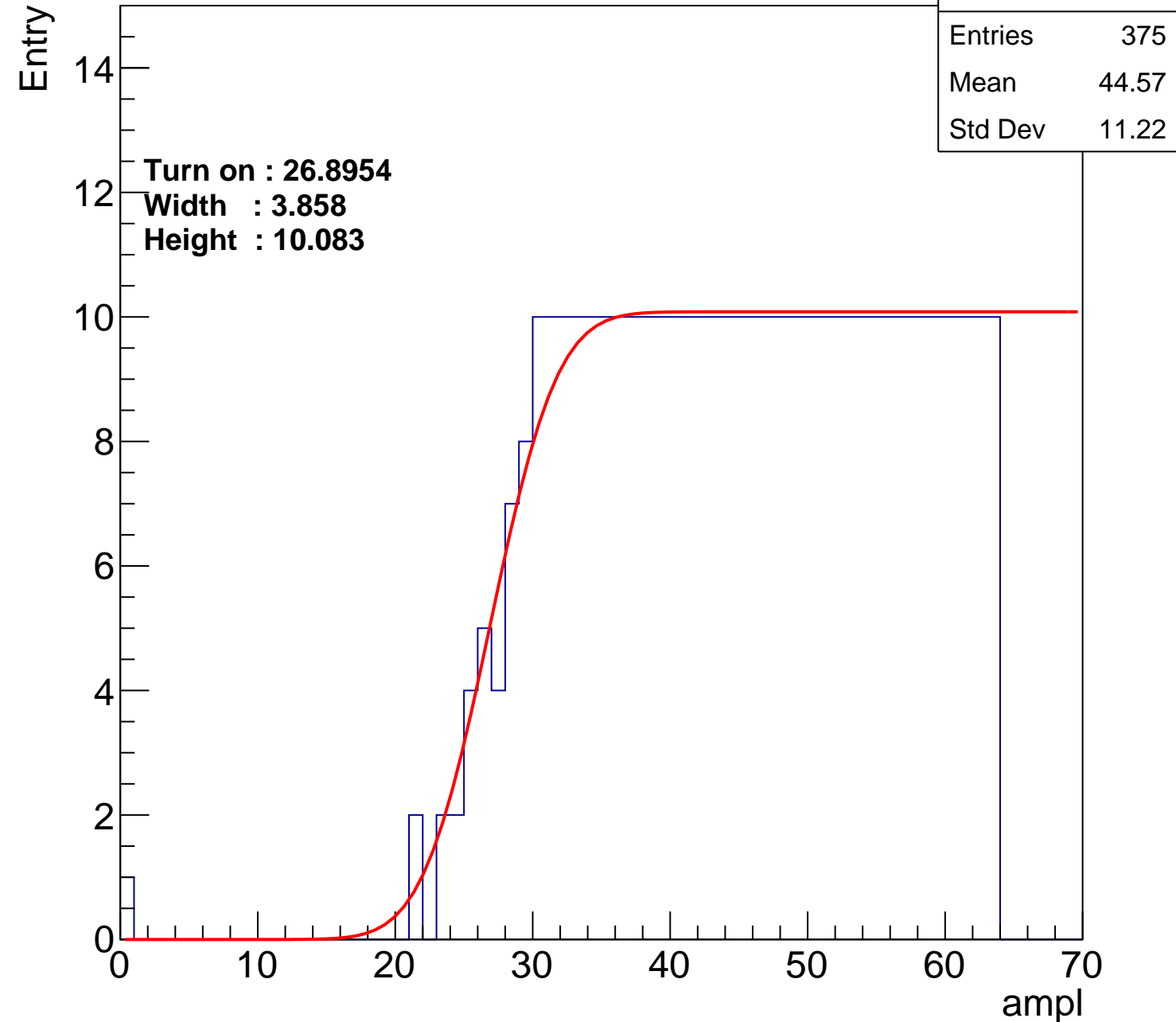
Width : 3.858

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.36
Std Dev	11.86

Turn on : 26.7952

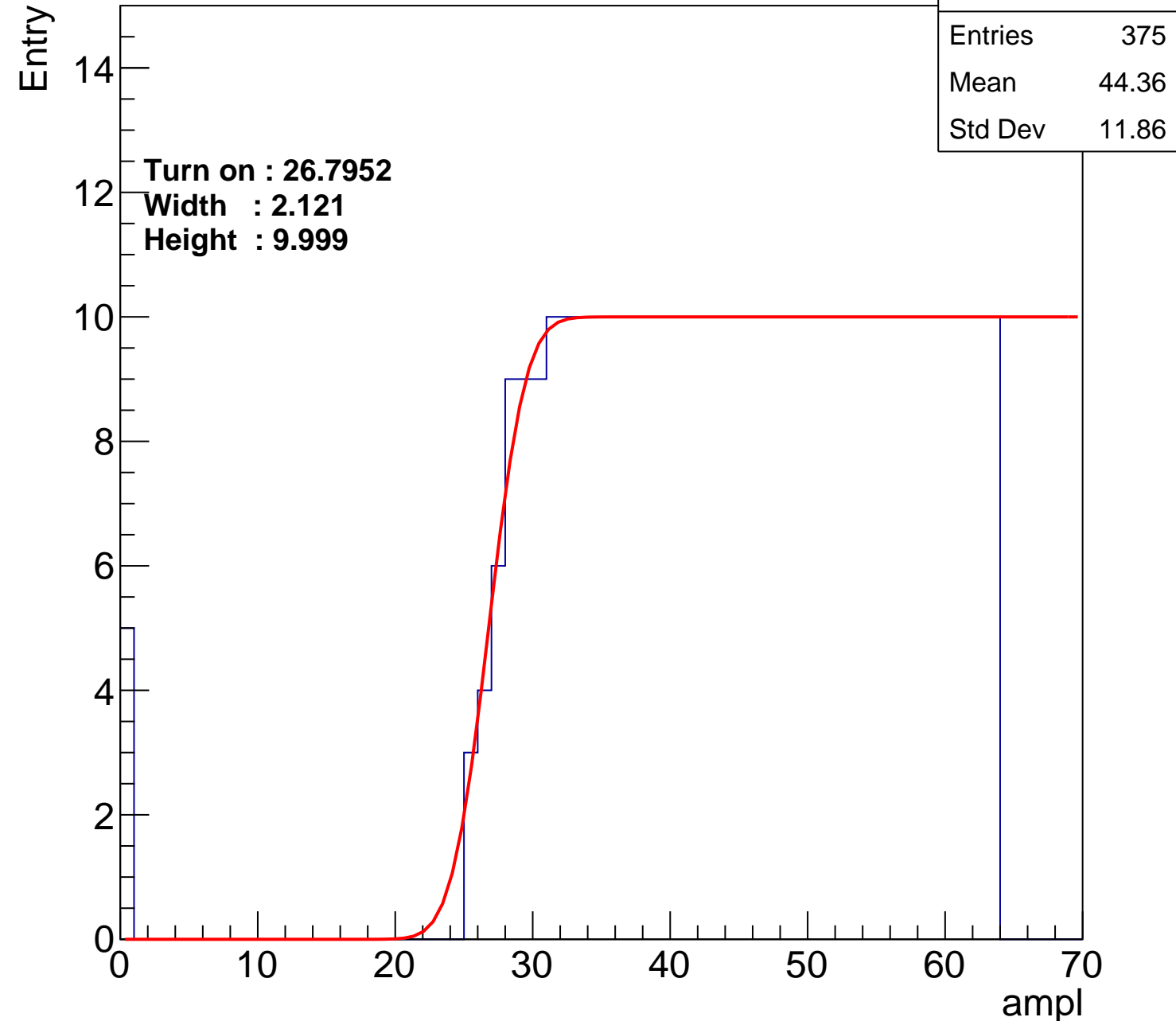
Width : 2.121

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.6677

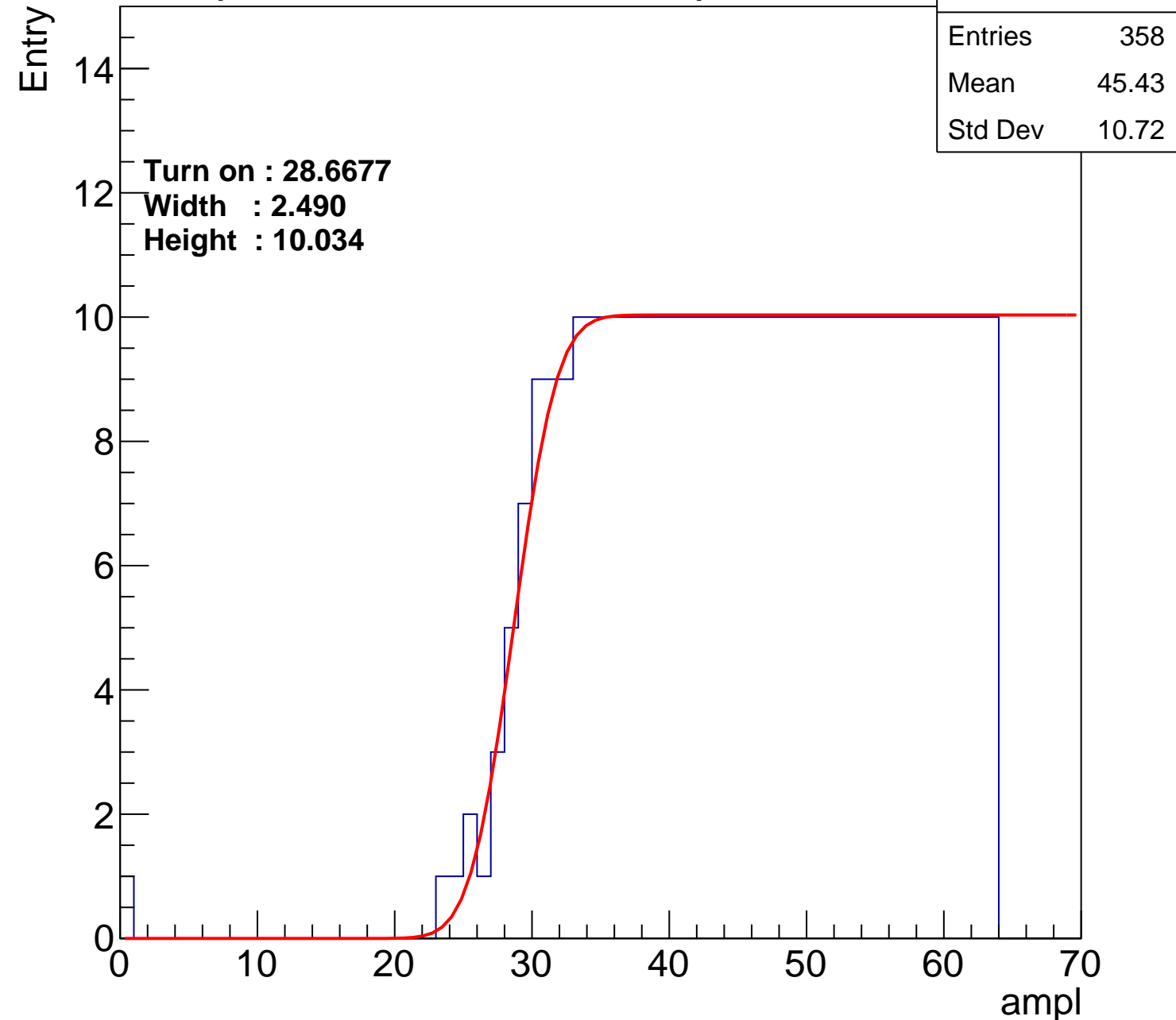
Width : 2.490

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch20

calib_packv5_042523_0143.root, FC#8, port C1

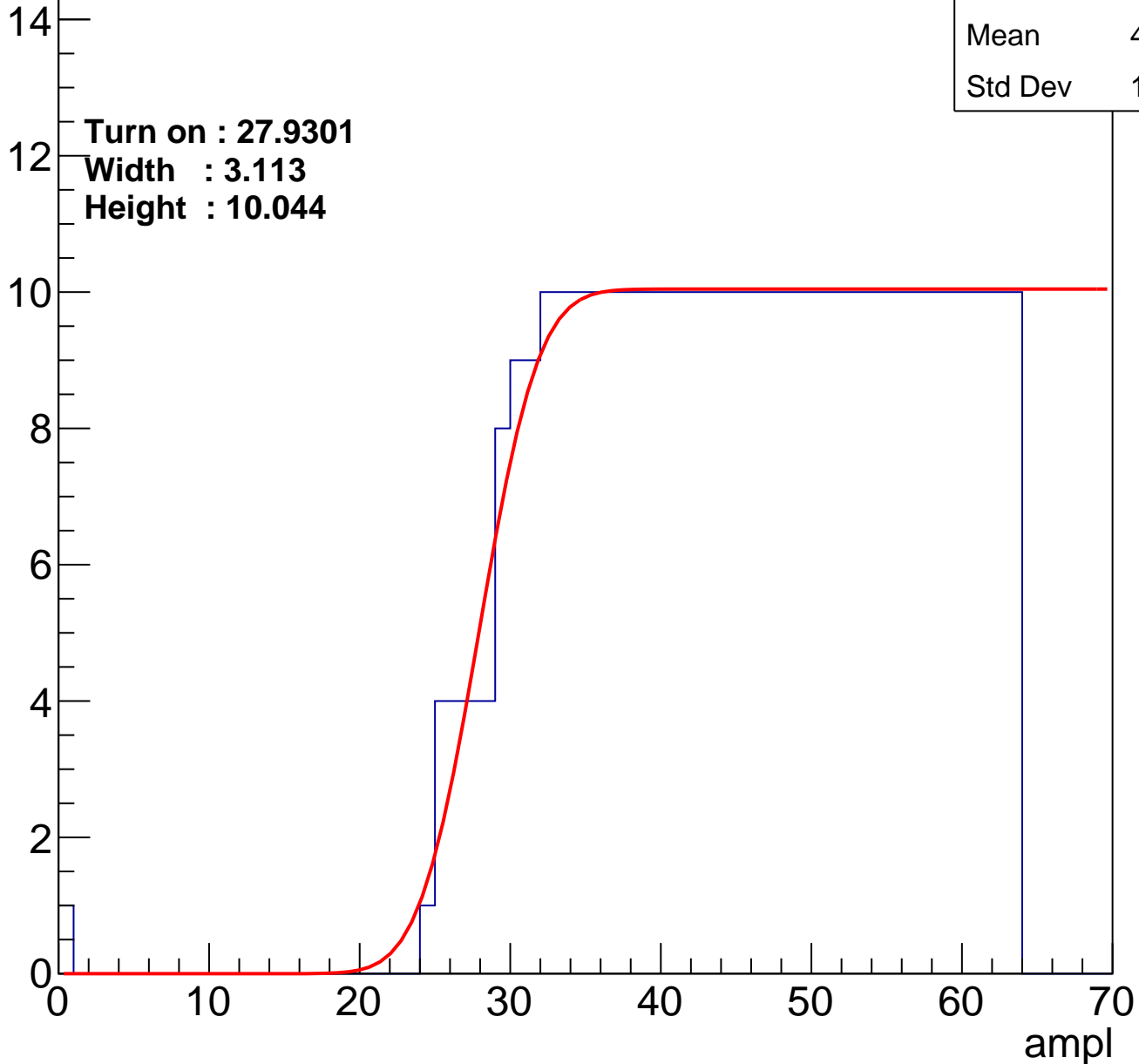
Entries	364
Mean	45.13
Std Dev	10.88

Turn on : 27.9301

Width : 3.113

Height : 10.044

Entry



B0L002S, U8-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
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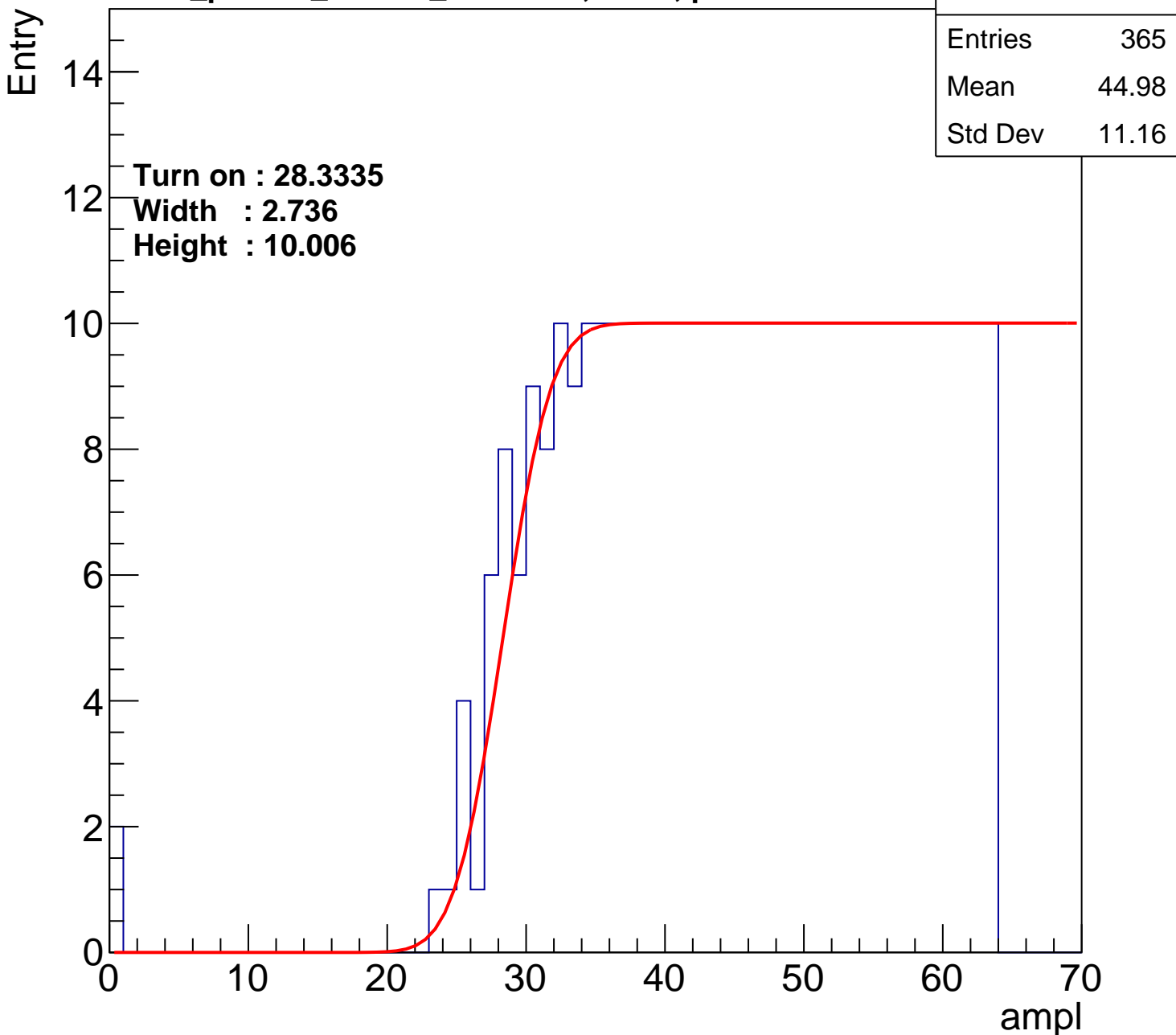
Mean	44.98
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Std Dev	11.16
---------	-------

Turn on : 28.3335

Width : 2.736

Height : 10.006



B0L002S, U8-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.05
Std Dev	11.05

Turn on : 27.4400

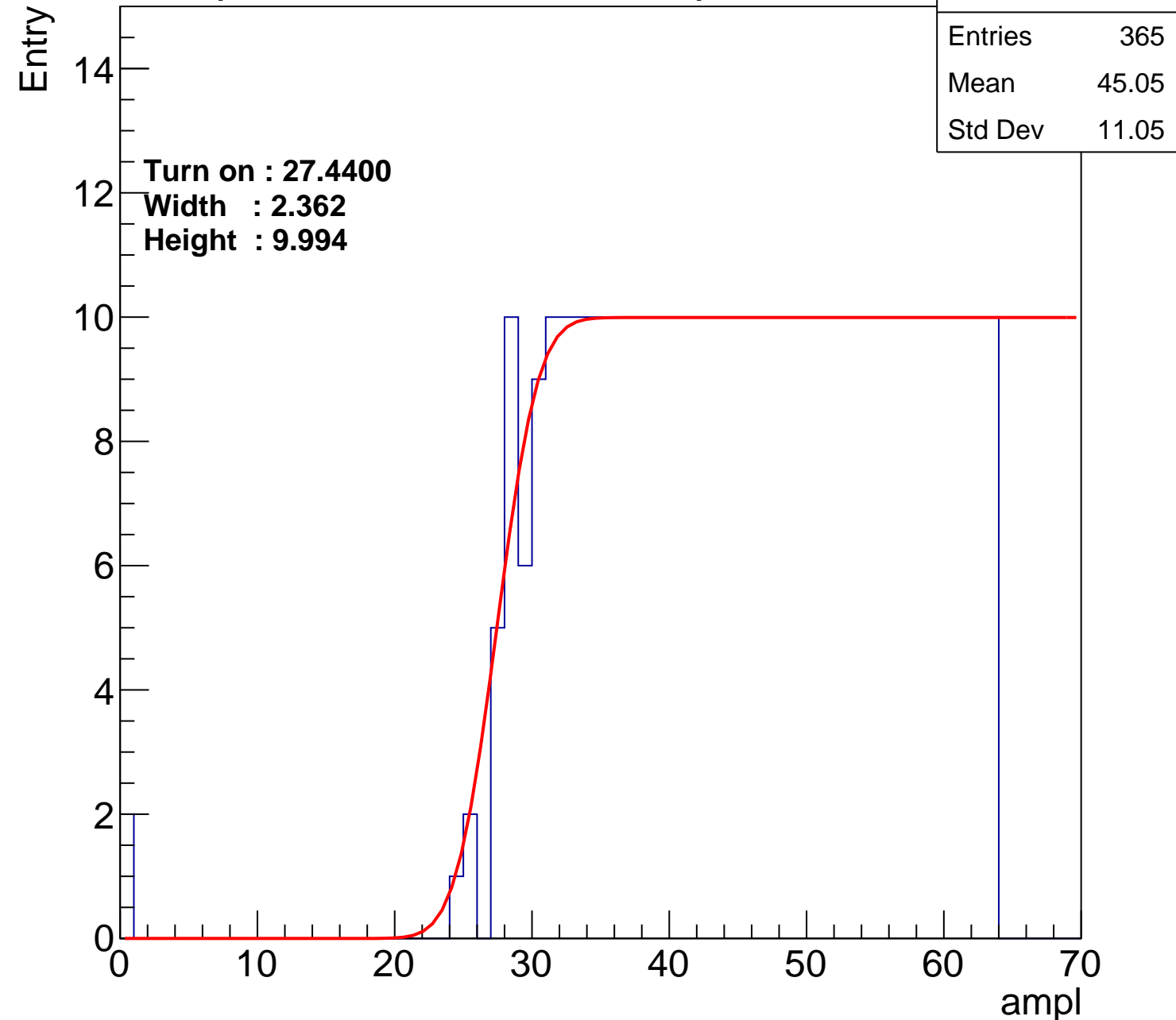
Width : 2.362

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.49
Std Dev	10.83

Turn on : 28.6733

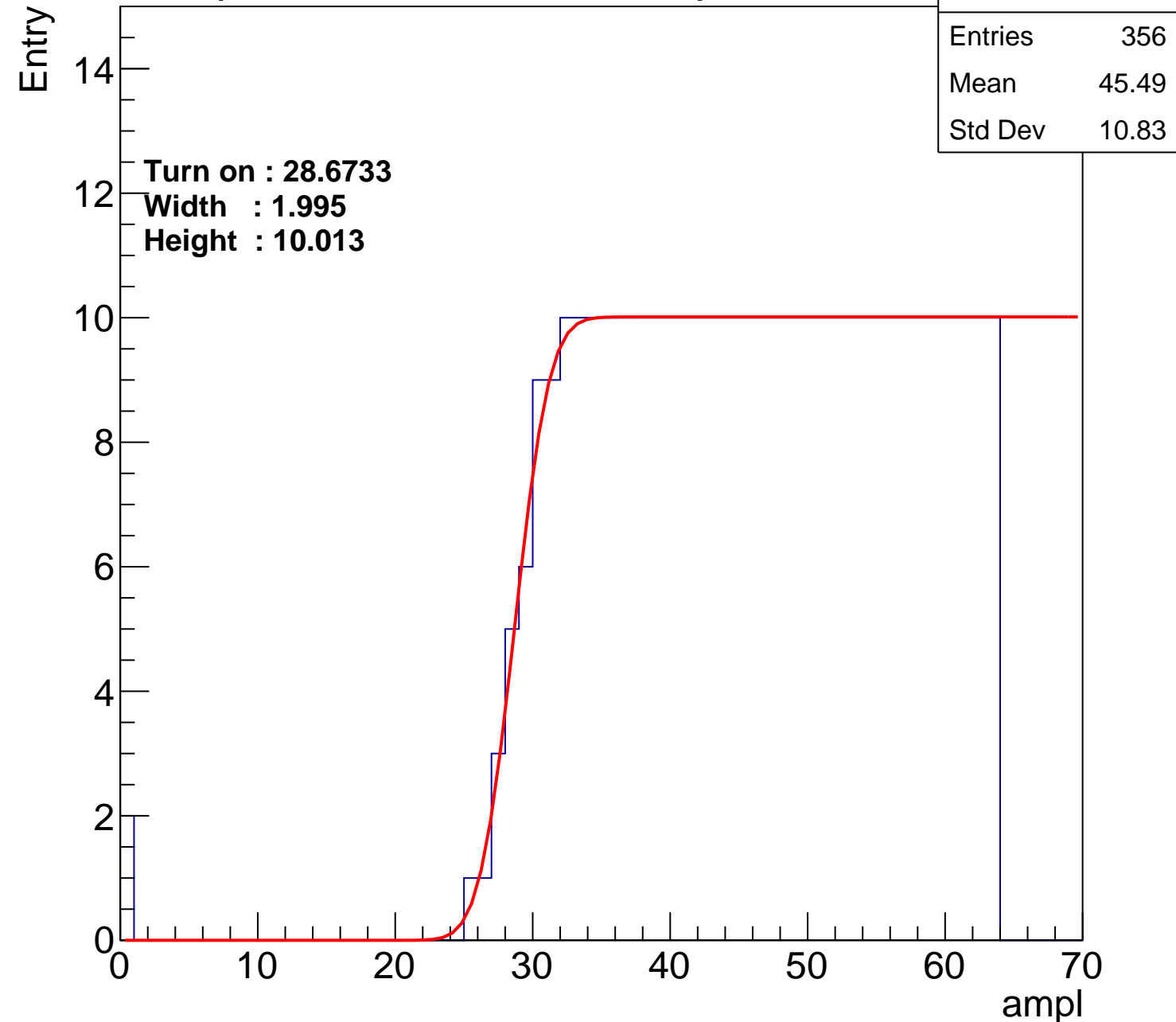
Width : 1.995

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.47
Std Dev	11.22

Turn on : 26.6533

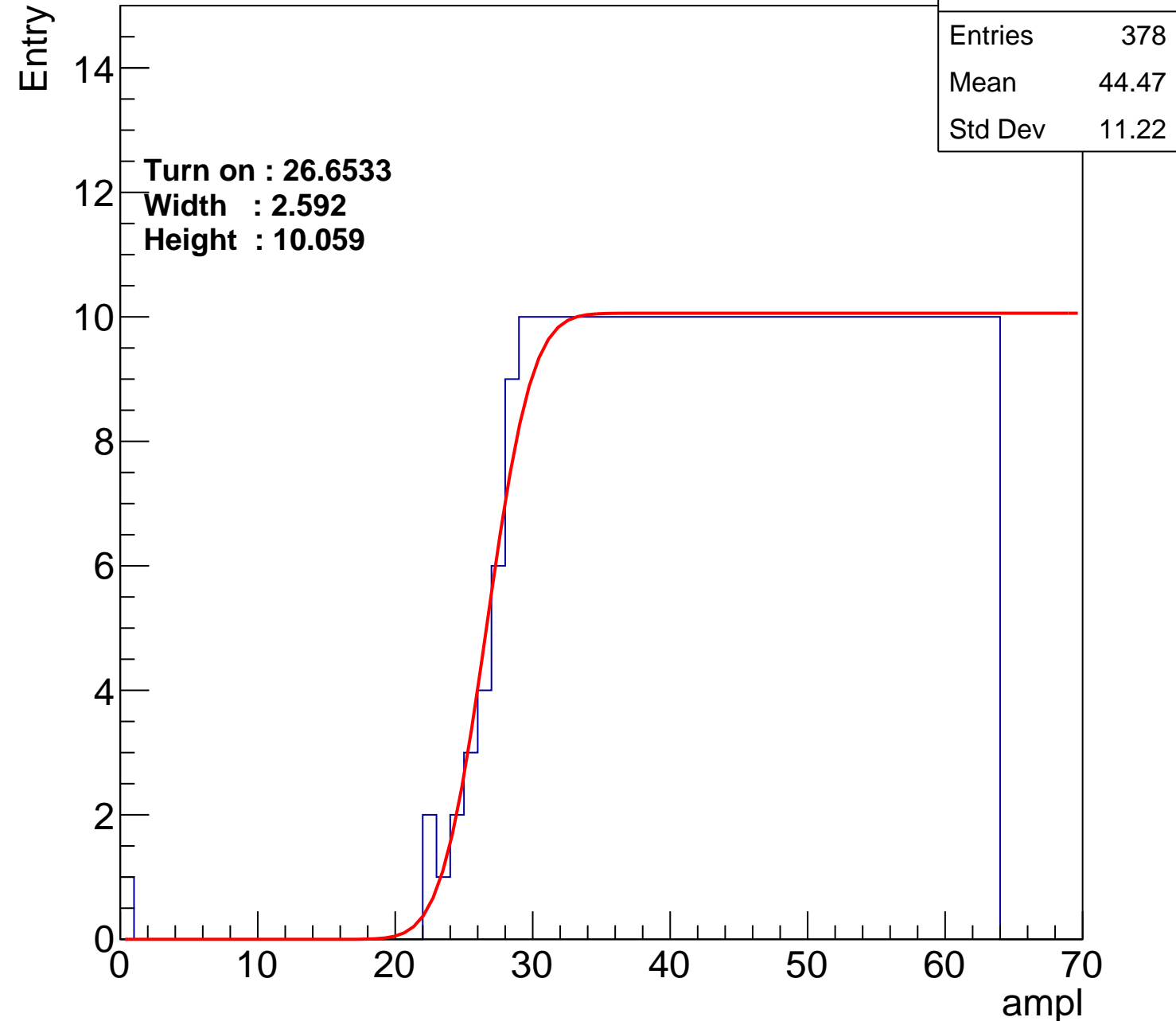
Width : 2.592

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.89
Std Dev	11.02

Turn on : 27.5649

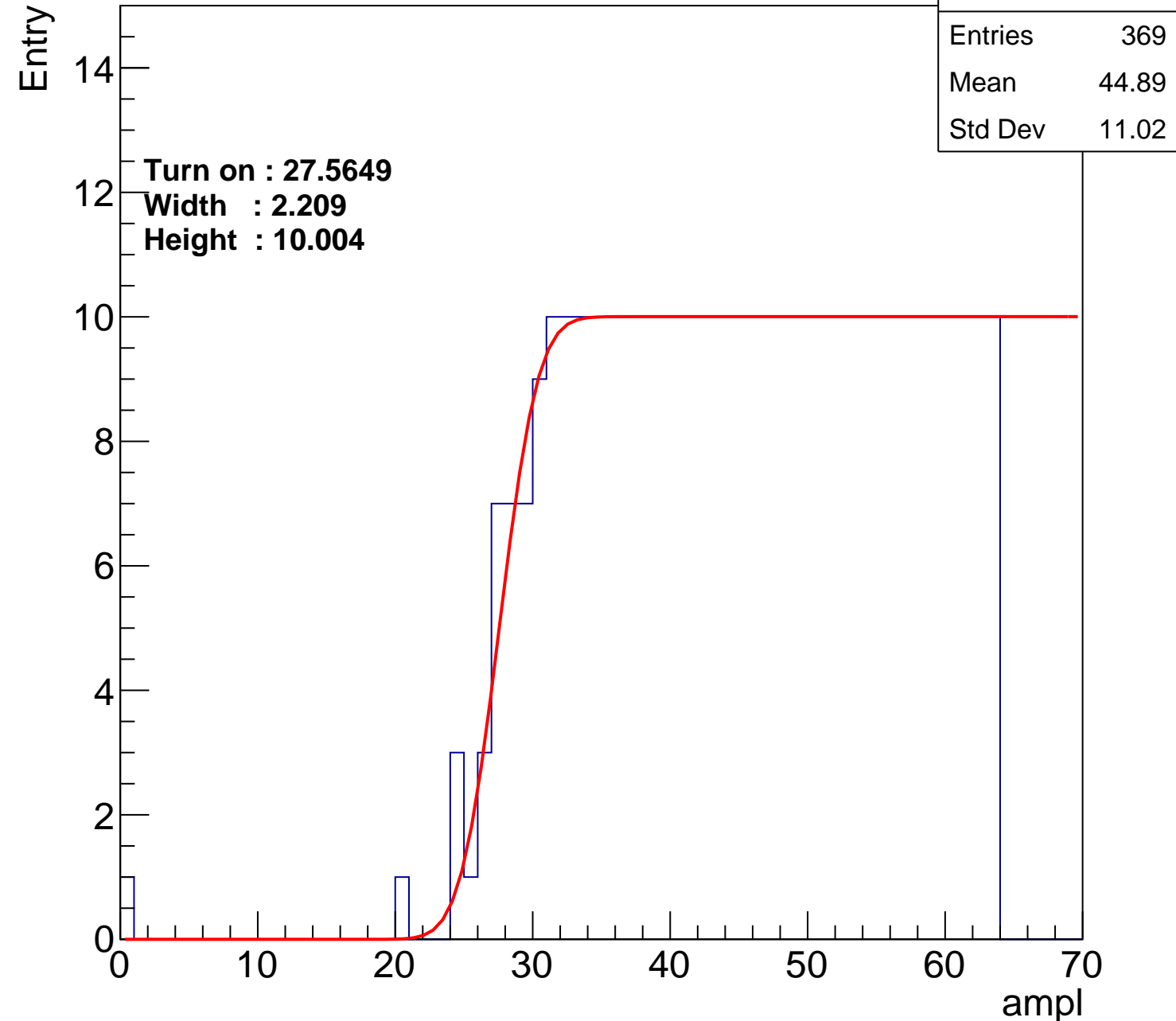
Width : 2.209

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch26

calib_packv5_042523_0143.root, FC#8, port C1

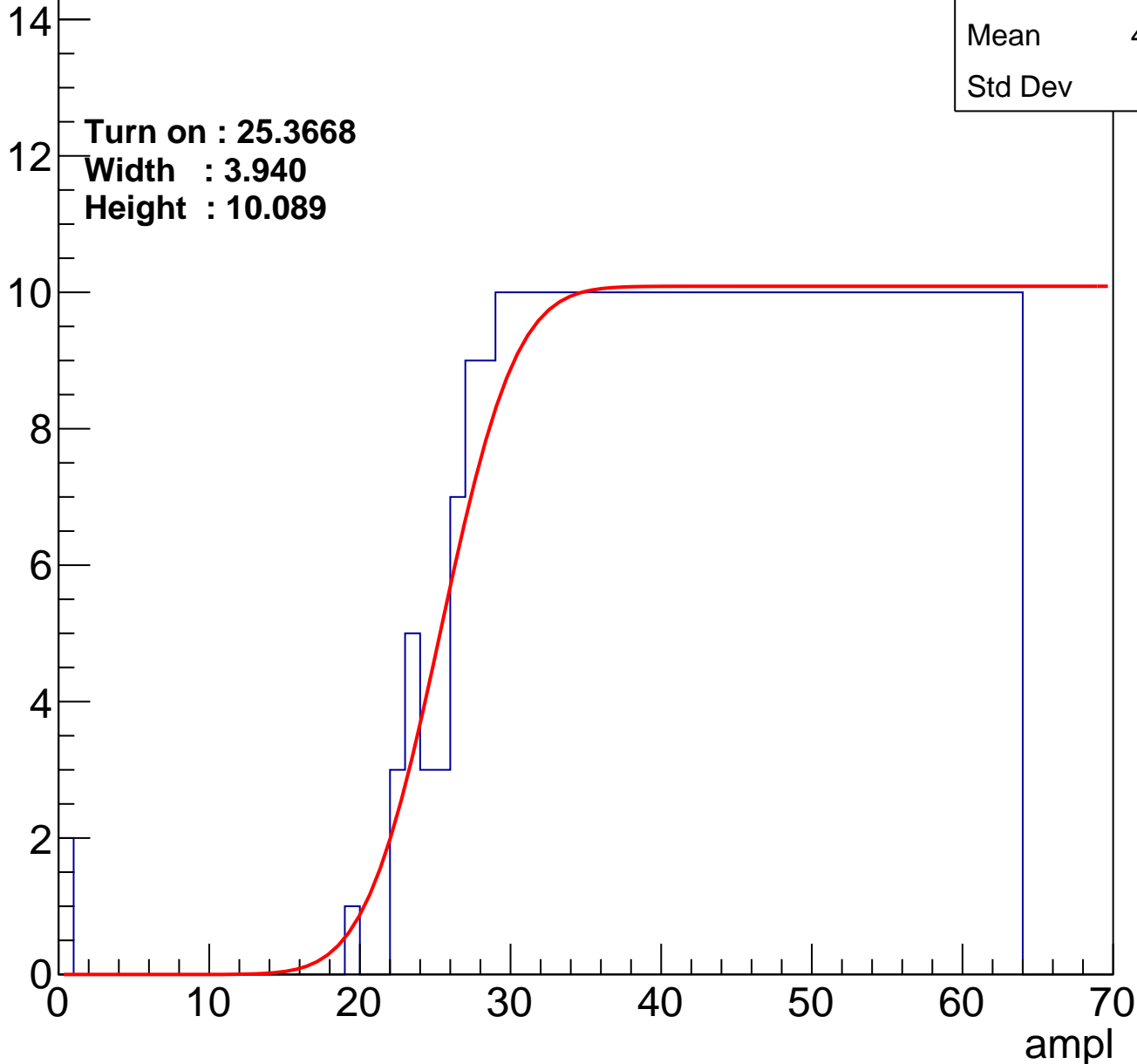
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.3668

Width : 3.940

Height : 10.089

Entry



B0L002S, U8-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 26.1214

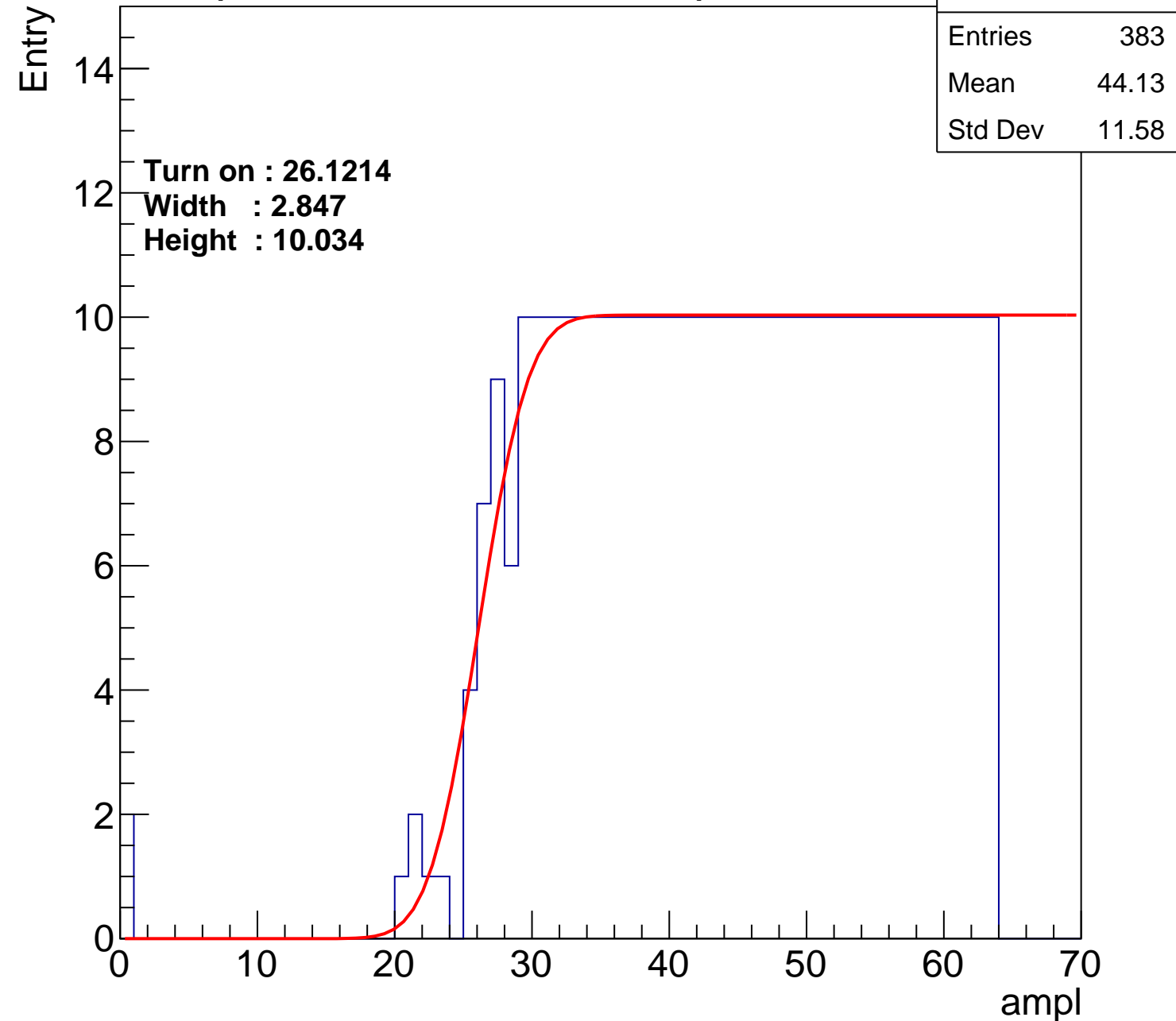
Width : 2.847

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.56
Std Dev	10.88

Turn on : 29.1358

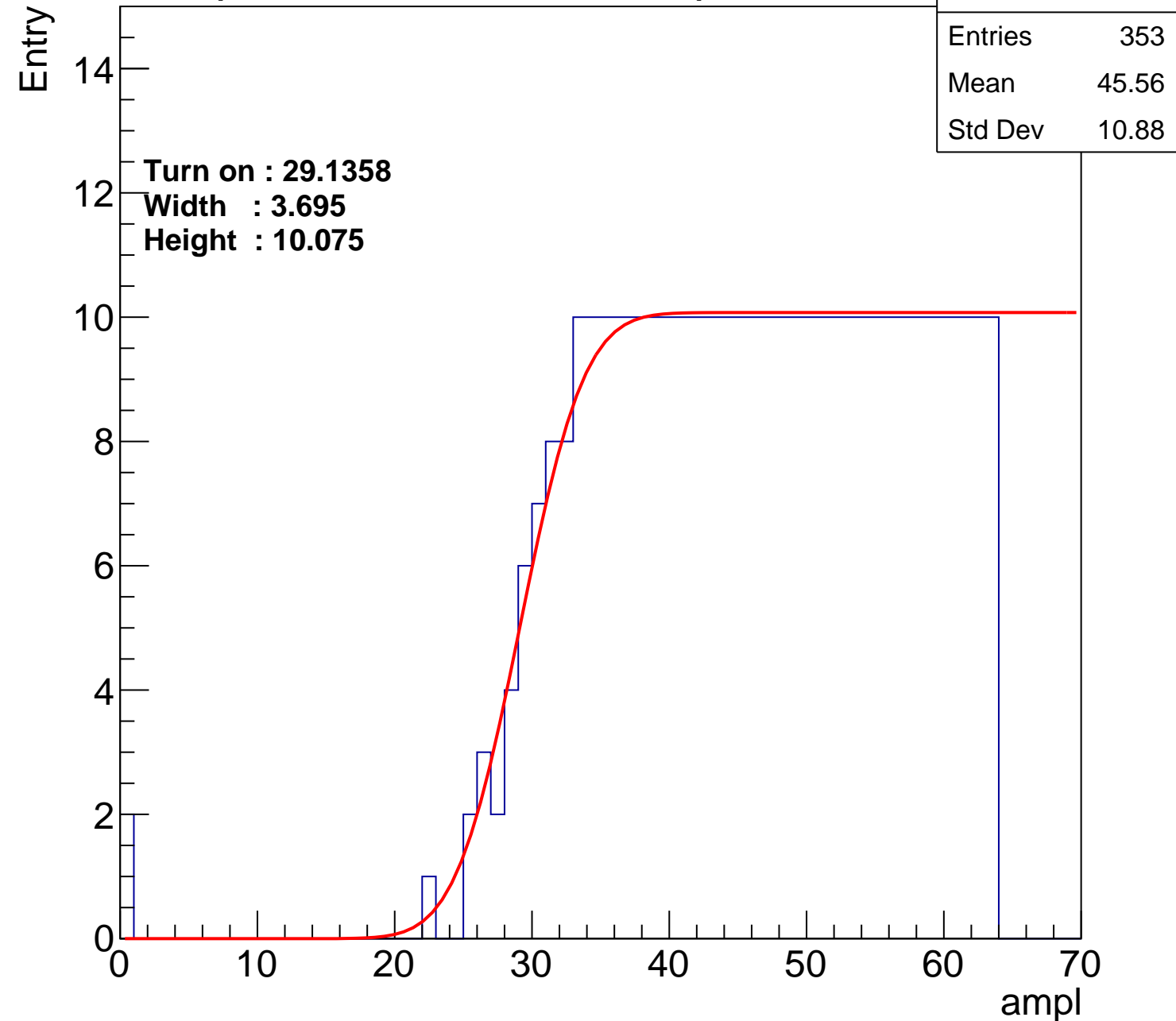
Width : 3.695

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.46
Std Dev	11.54

Turn on : 27.2352

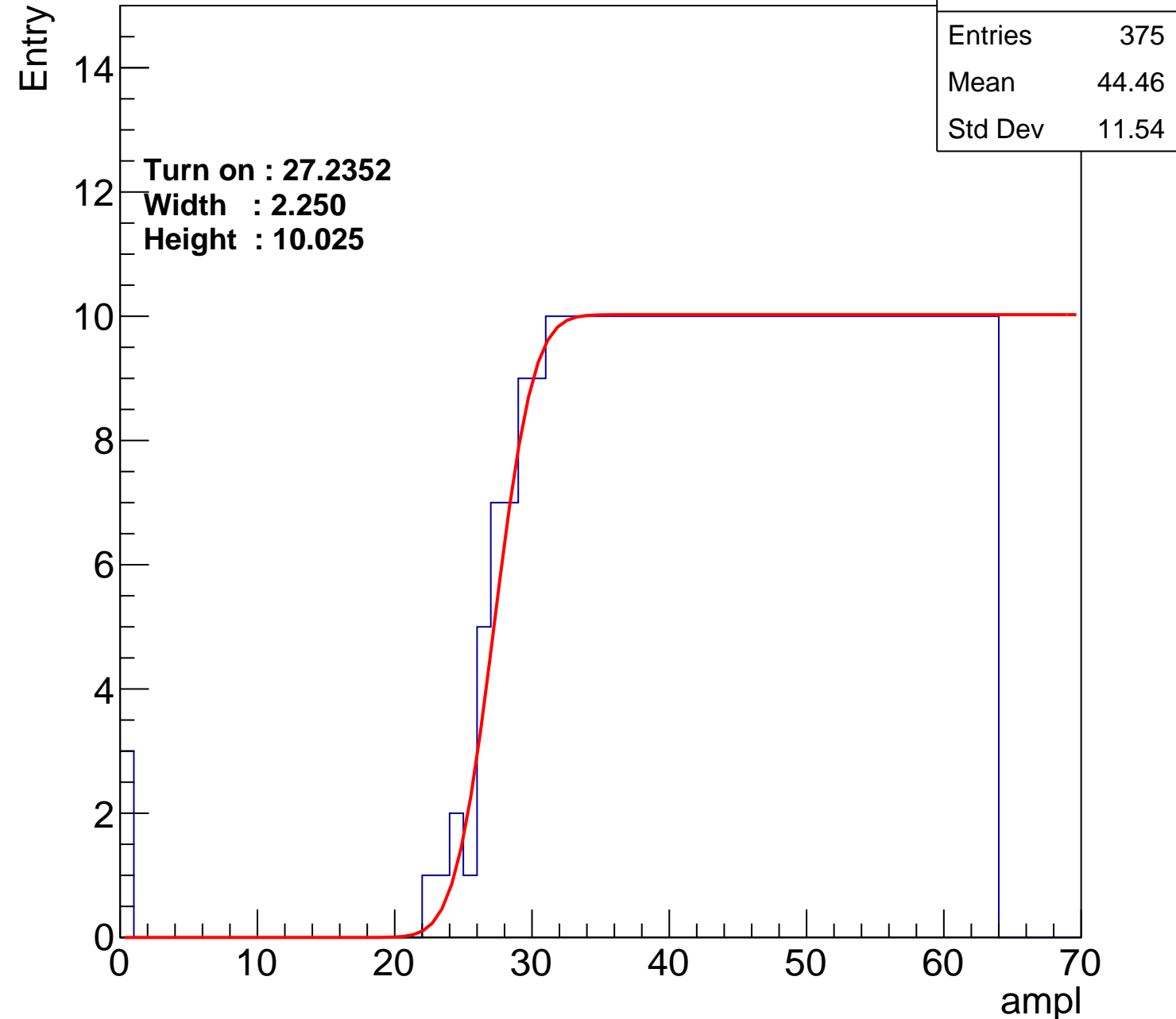
Width : 2.250

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.6747

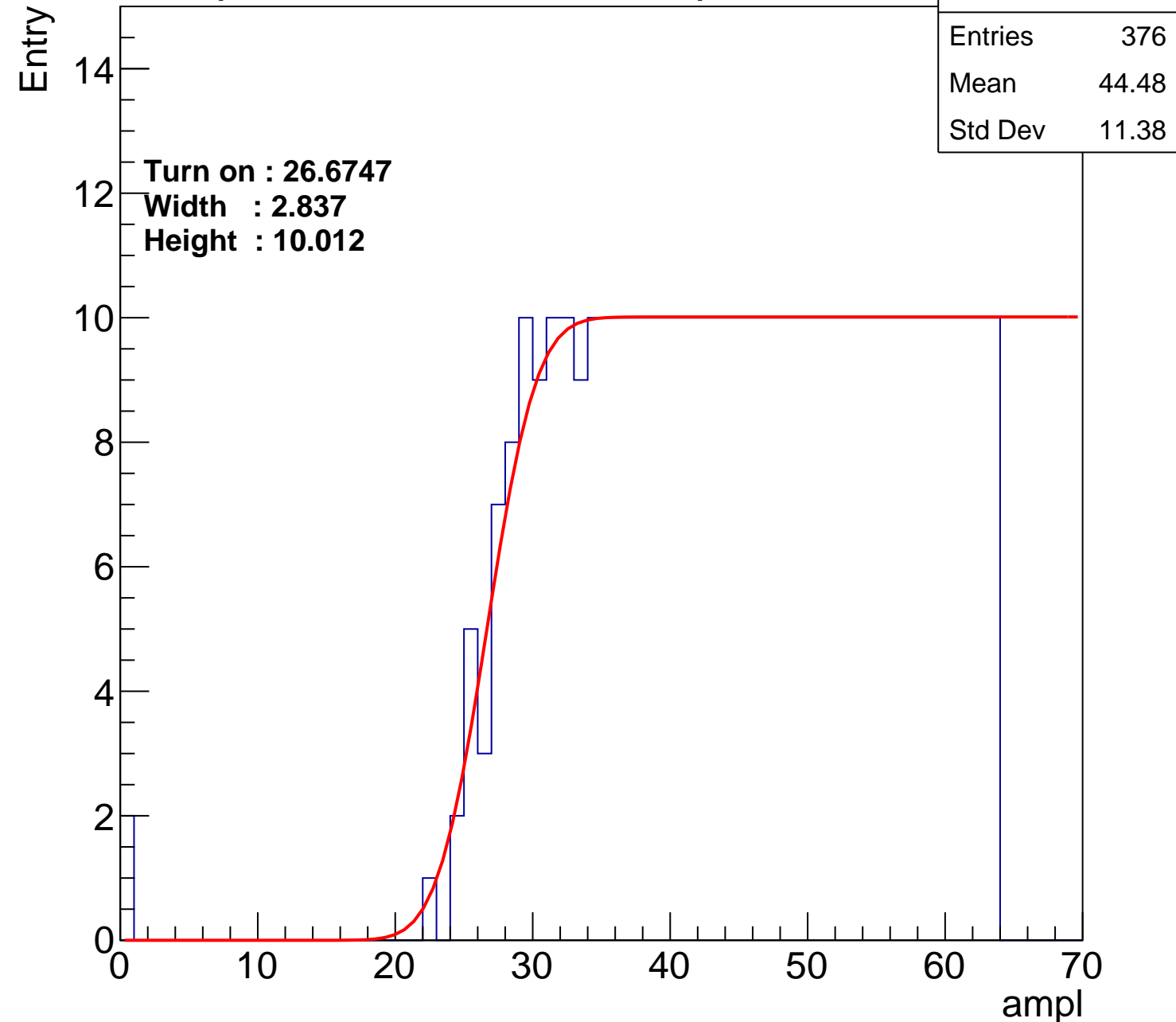
Width : 2.837

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.2220

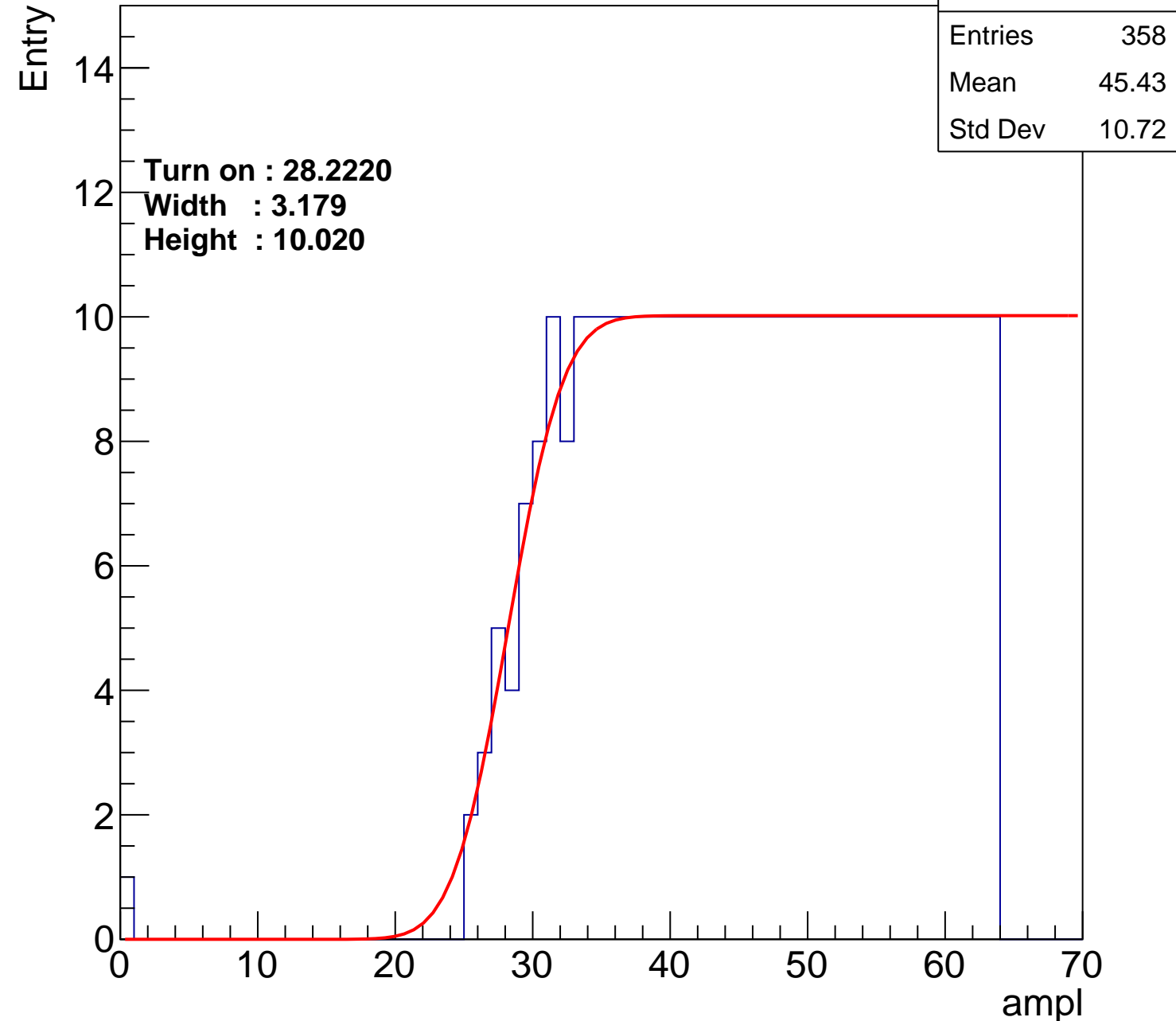
Width : 3.179

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch32

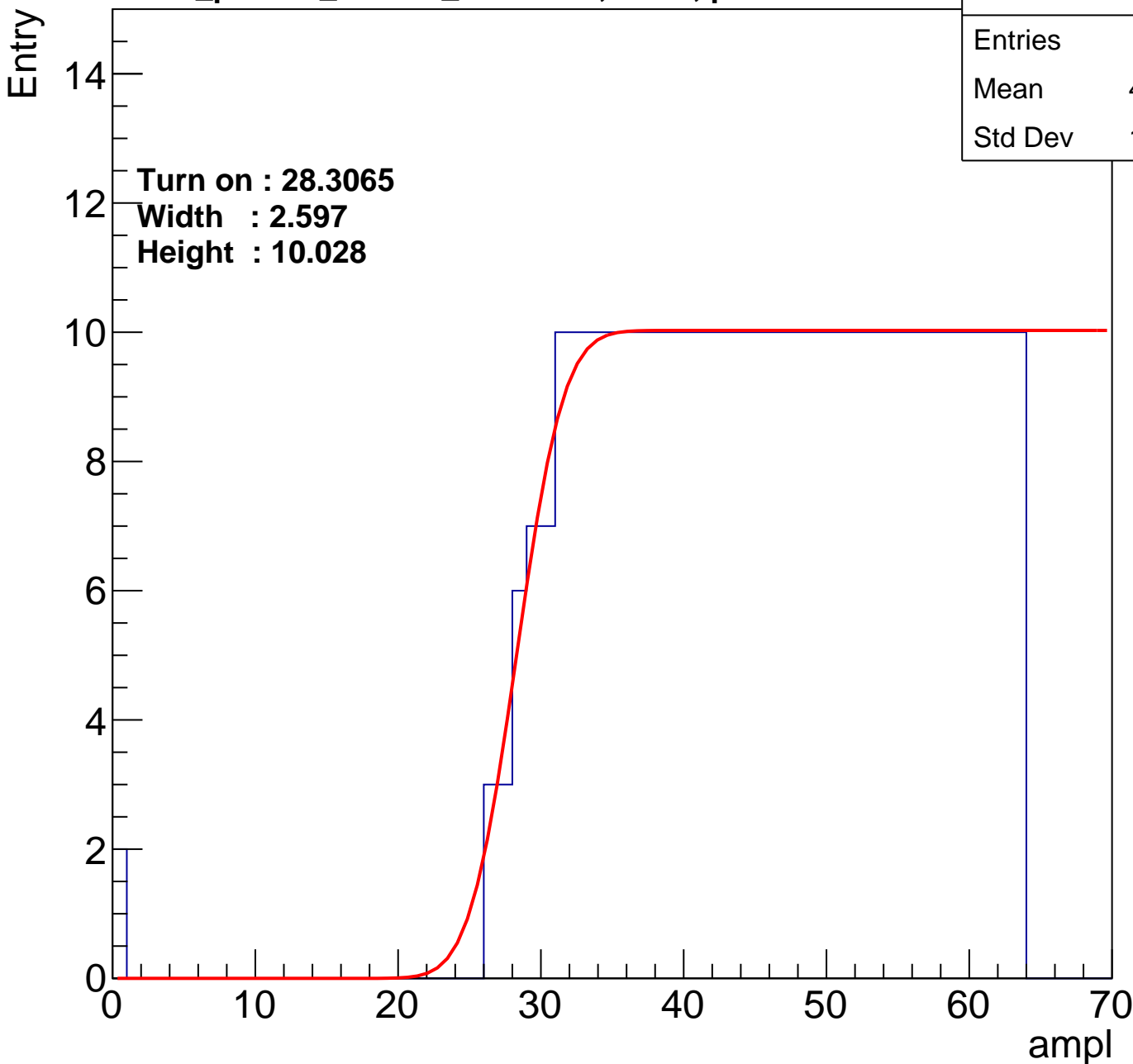
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.3065

Width : 2.597

Height : 10.028

Entries	358
Mean	45.39
Std Dev	10.88



B0L002S, U8-ch33

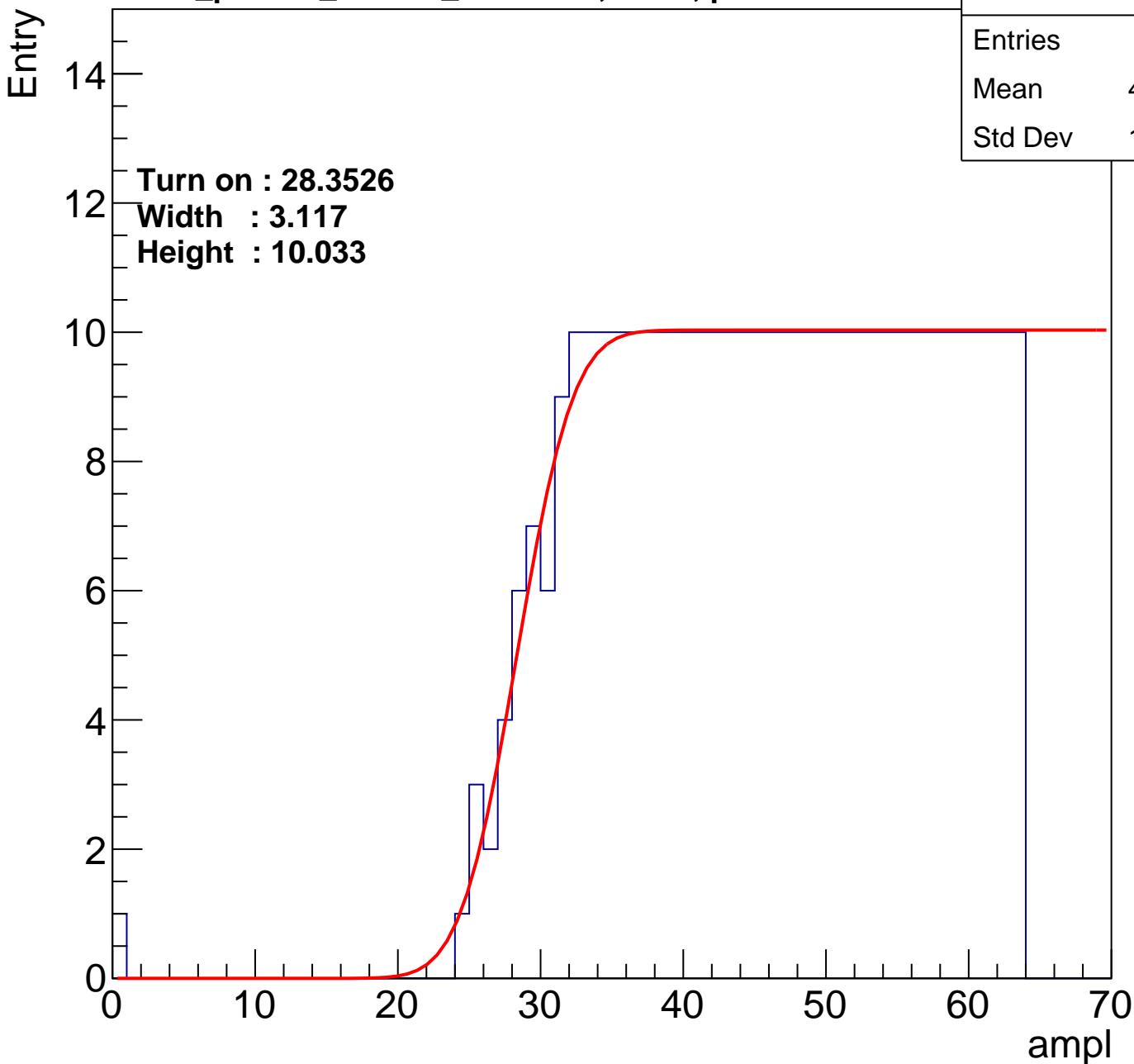
calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.37
Std Dev	10.76

Turn on : 28.3526

Width : 3.117

Height : 10.033



B0L002S, U8-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.92
Std Dev	11.49

Turn on : 28.4219

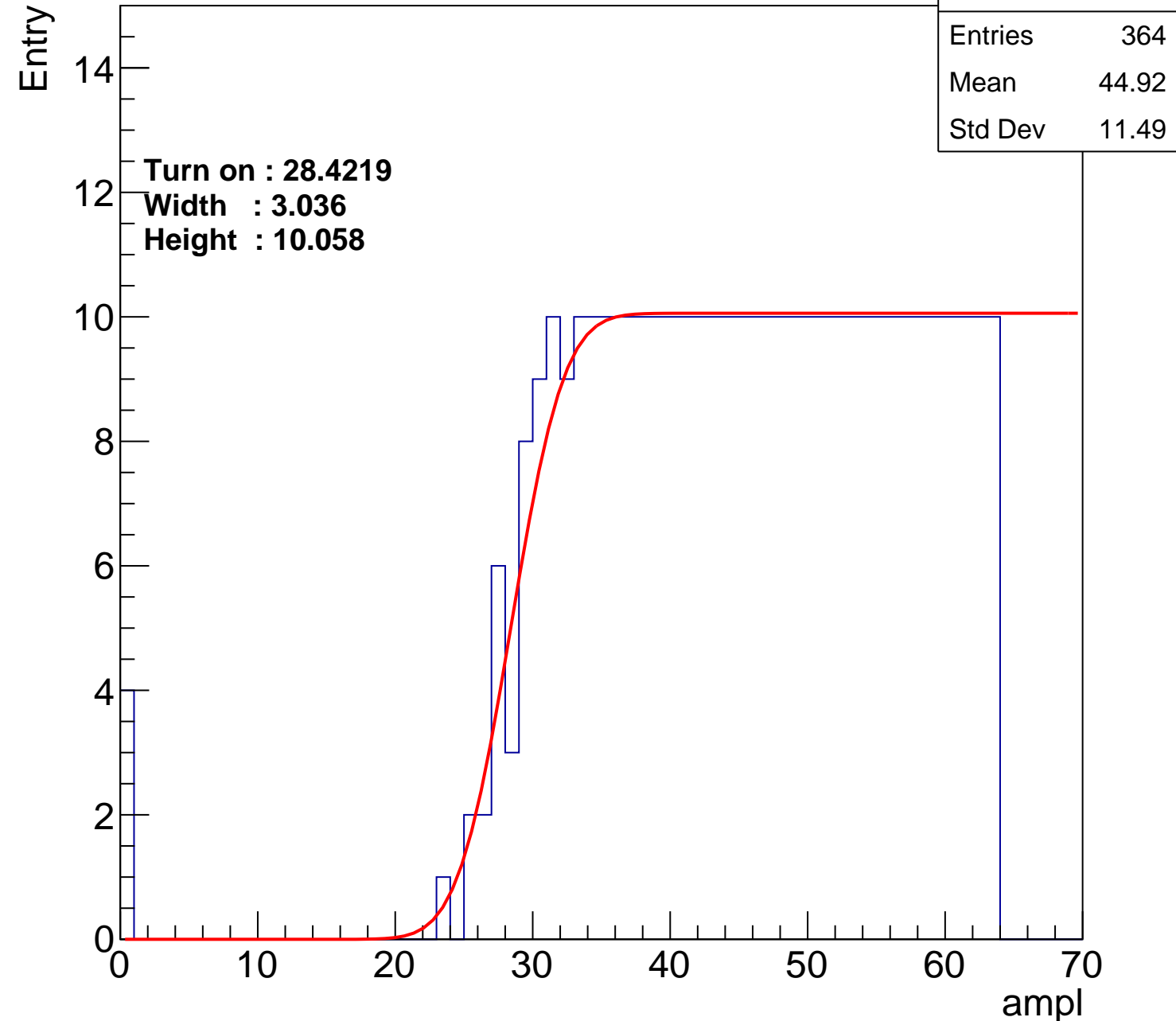
Width : 3.036

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	44.93
Std Dev	11.86

Turn on : 28.6053

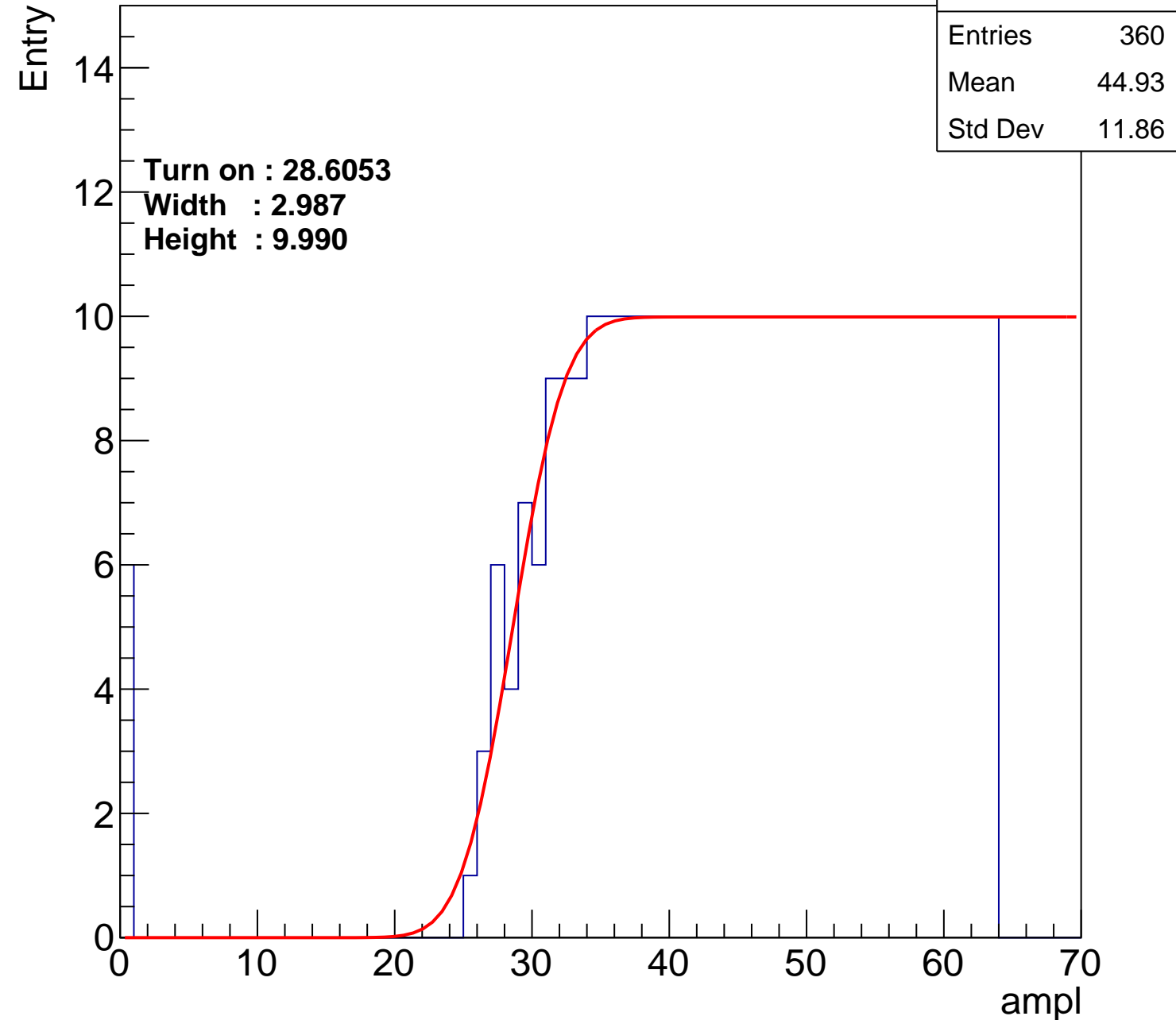
Width : 2.987

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.56
Std Dev	10.61

Turn on : 28.9646

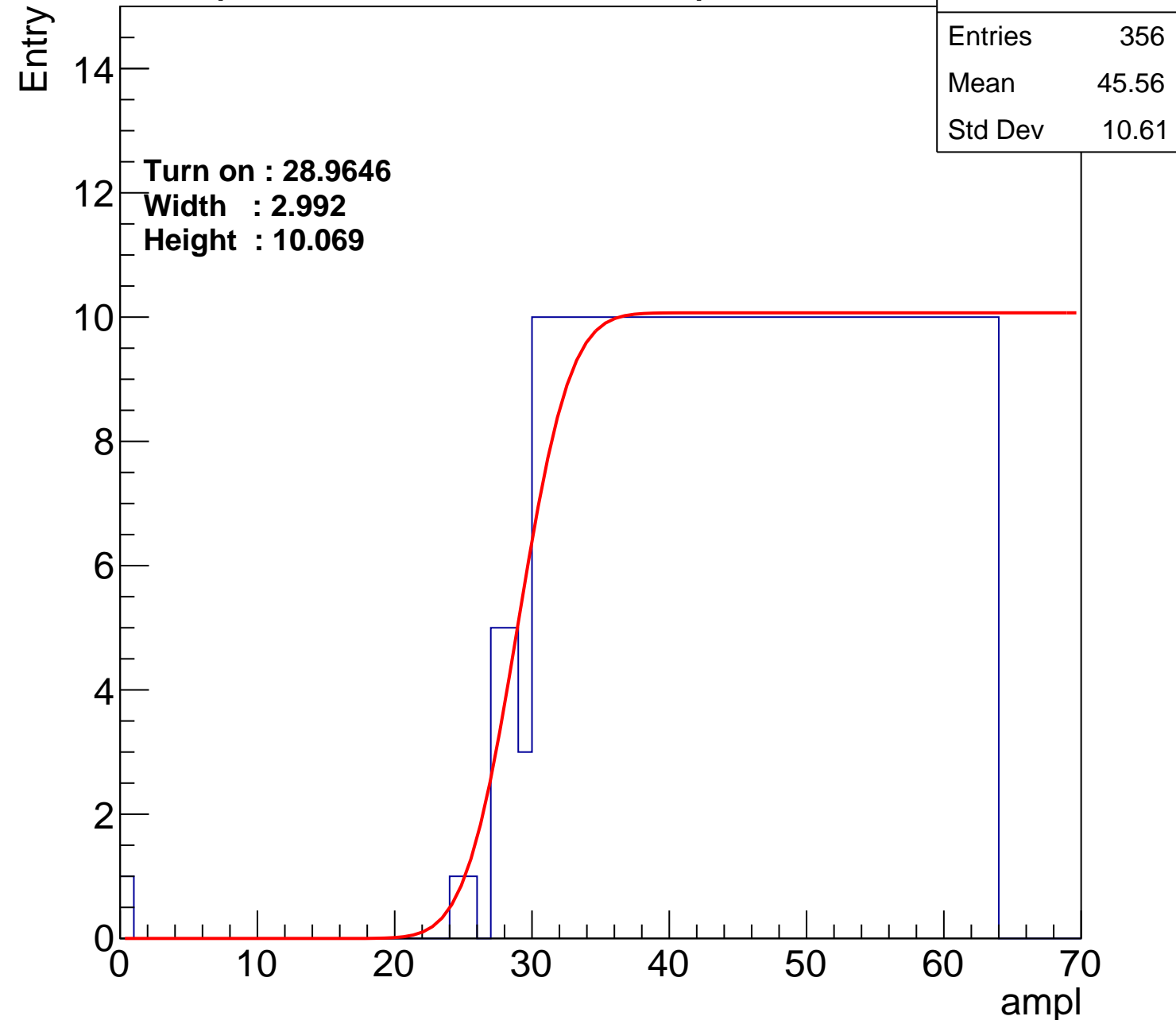
Width : 2.992

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.66
Std Dev	10.77

Turn on : 29.4758

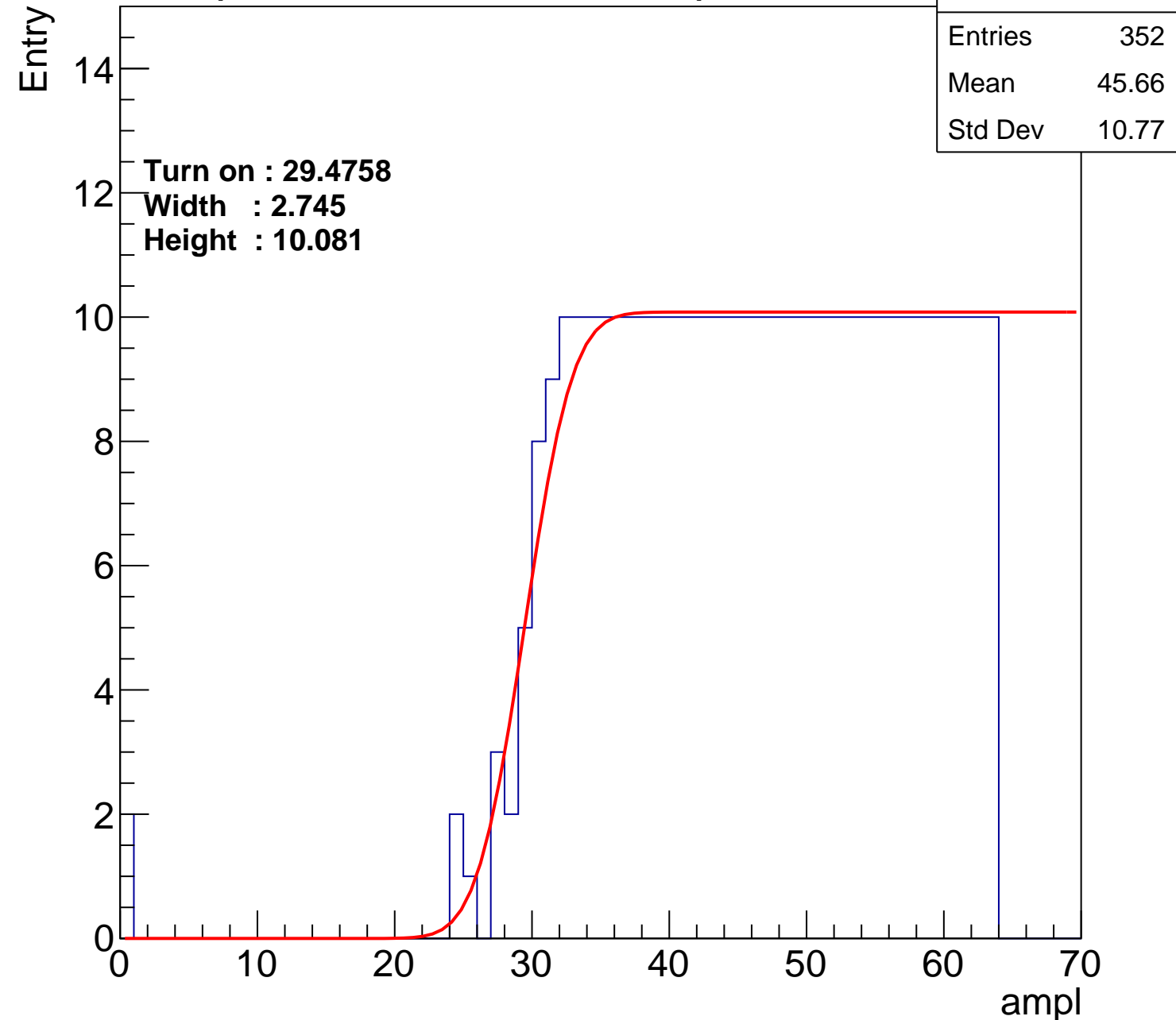
Width : 2.745

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.7490

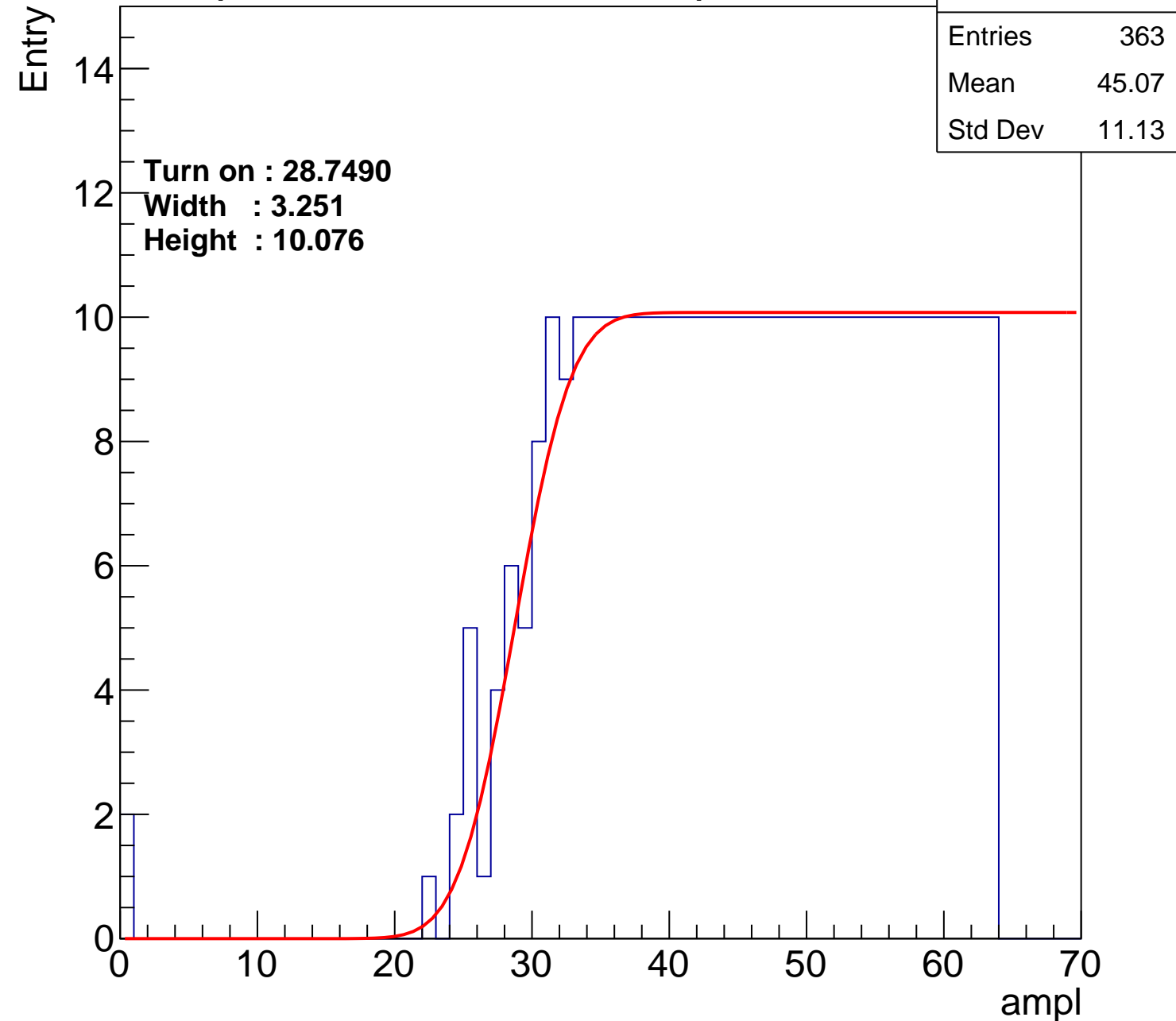
Width : 3.251

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch39

calib_packv5_042523_0143.root, FC#8, port C1

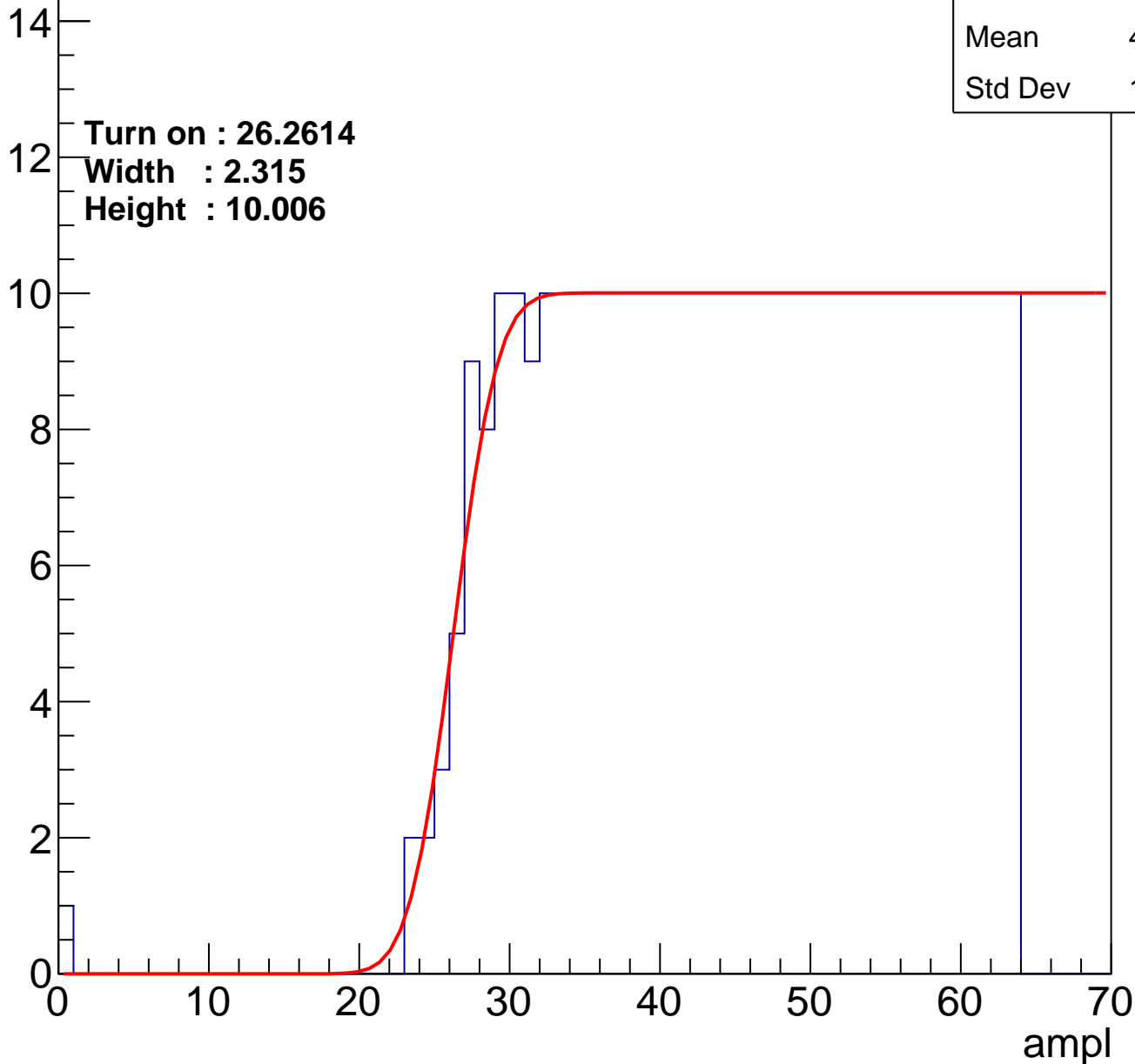
Entry

Entries	379
Mean	44.42
Std Dev	11.23

Turn on : 26.2614

Width : 2.315

Height : 10.006



B0L002S, U8-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.02
Std Dev	11.29

Turn on : 28.3252

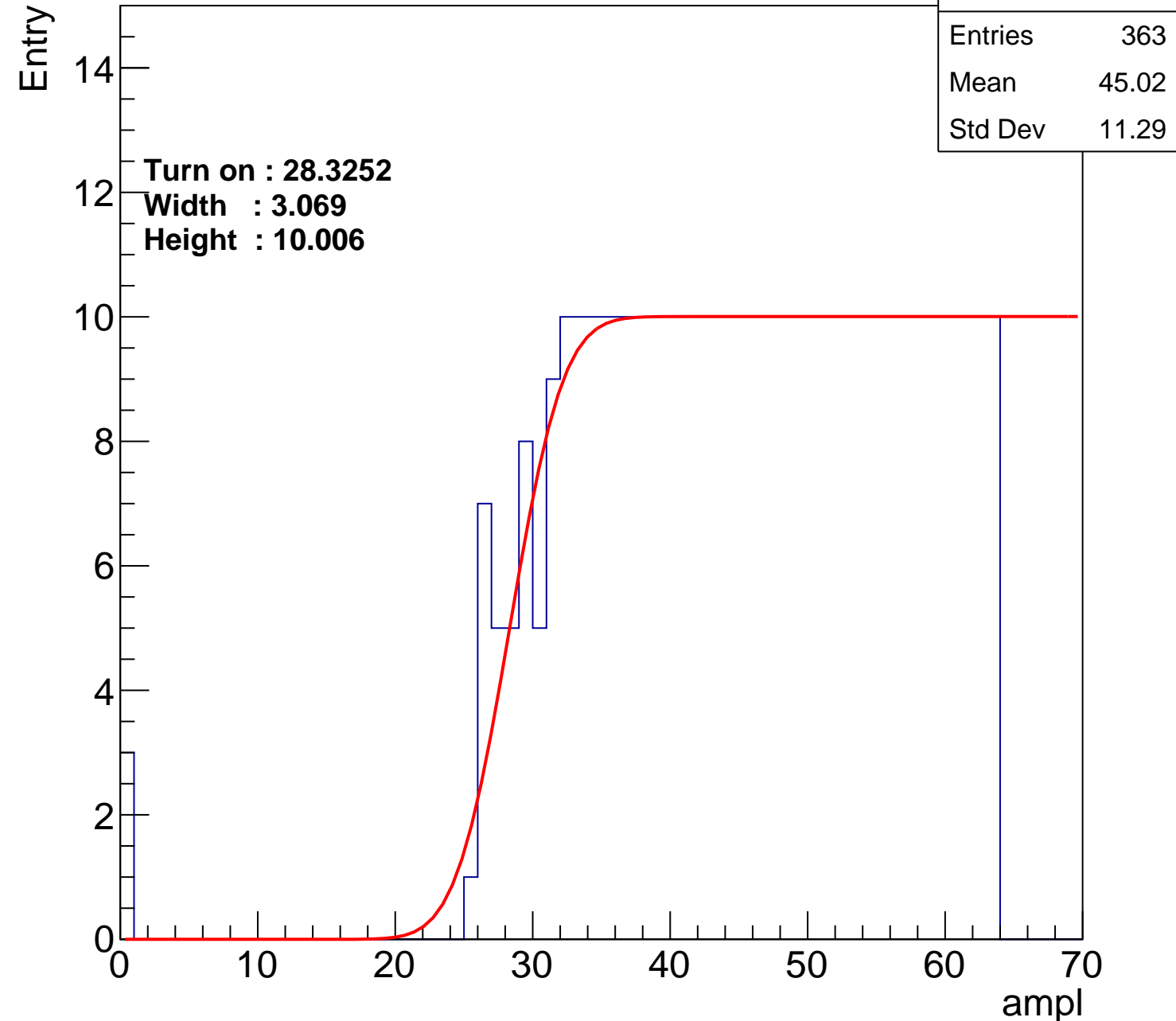
Width : 3.069

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.41
Std Dev	11.06

Turn on : 28.4708

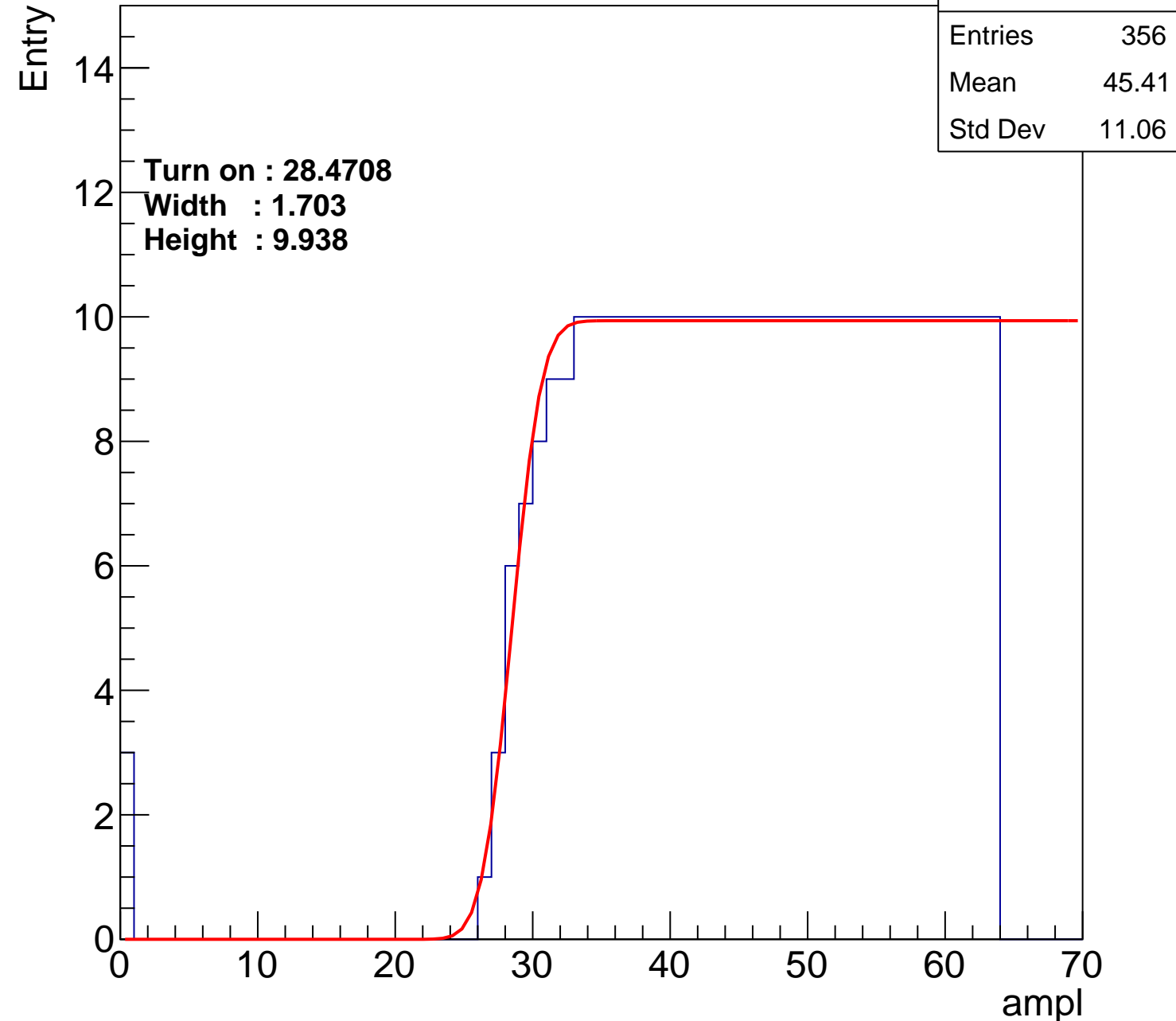
Width : 1.703

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.42
Std Dev	10.88

Turn on : 28.5965

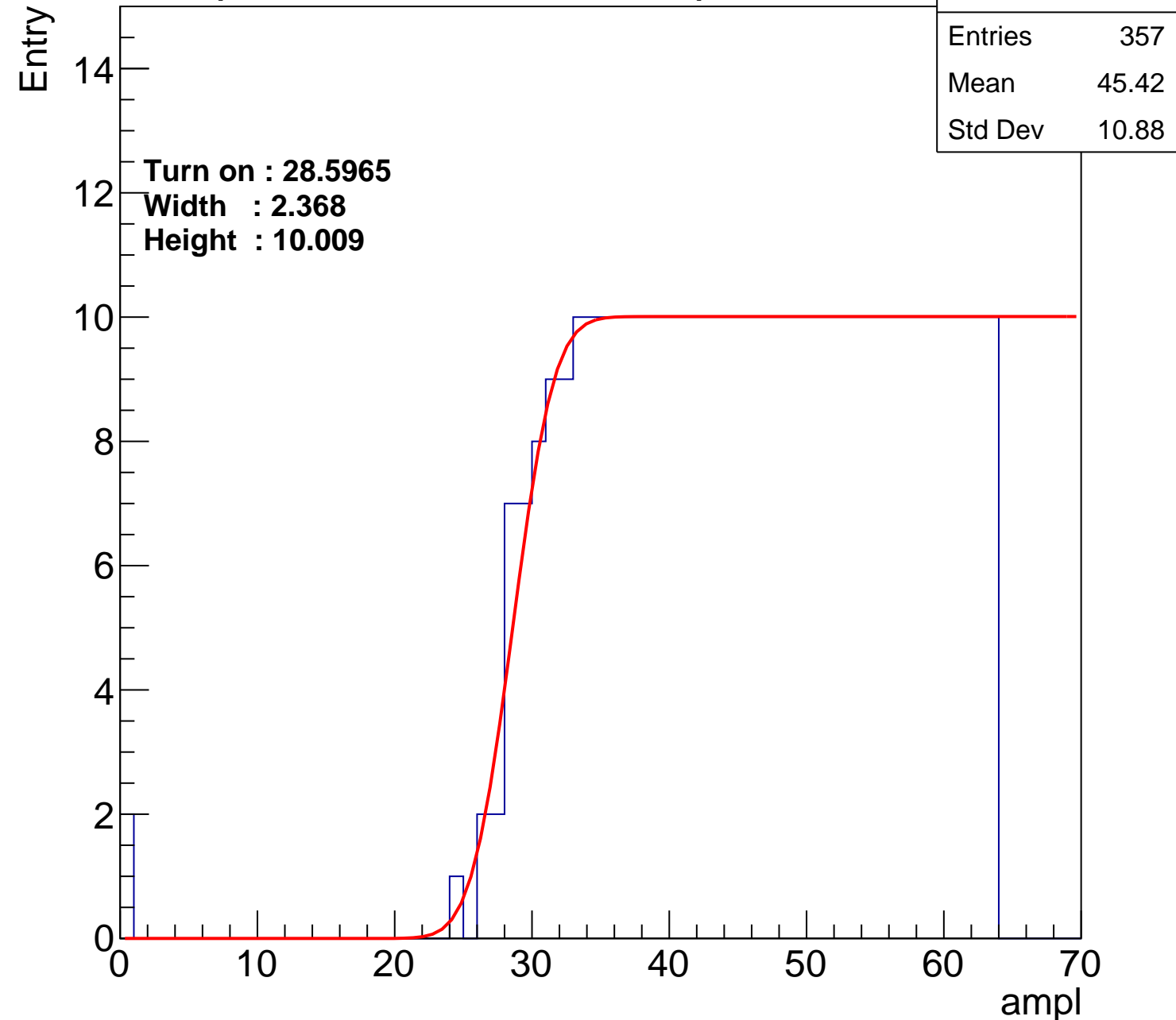
Width : 2.368

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.16
Std Dev	11.18

Turn on : 27.8715

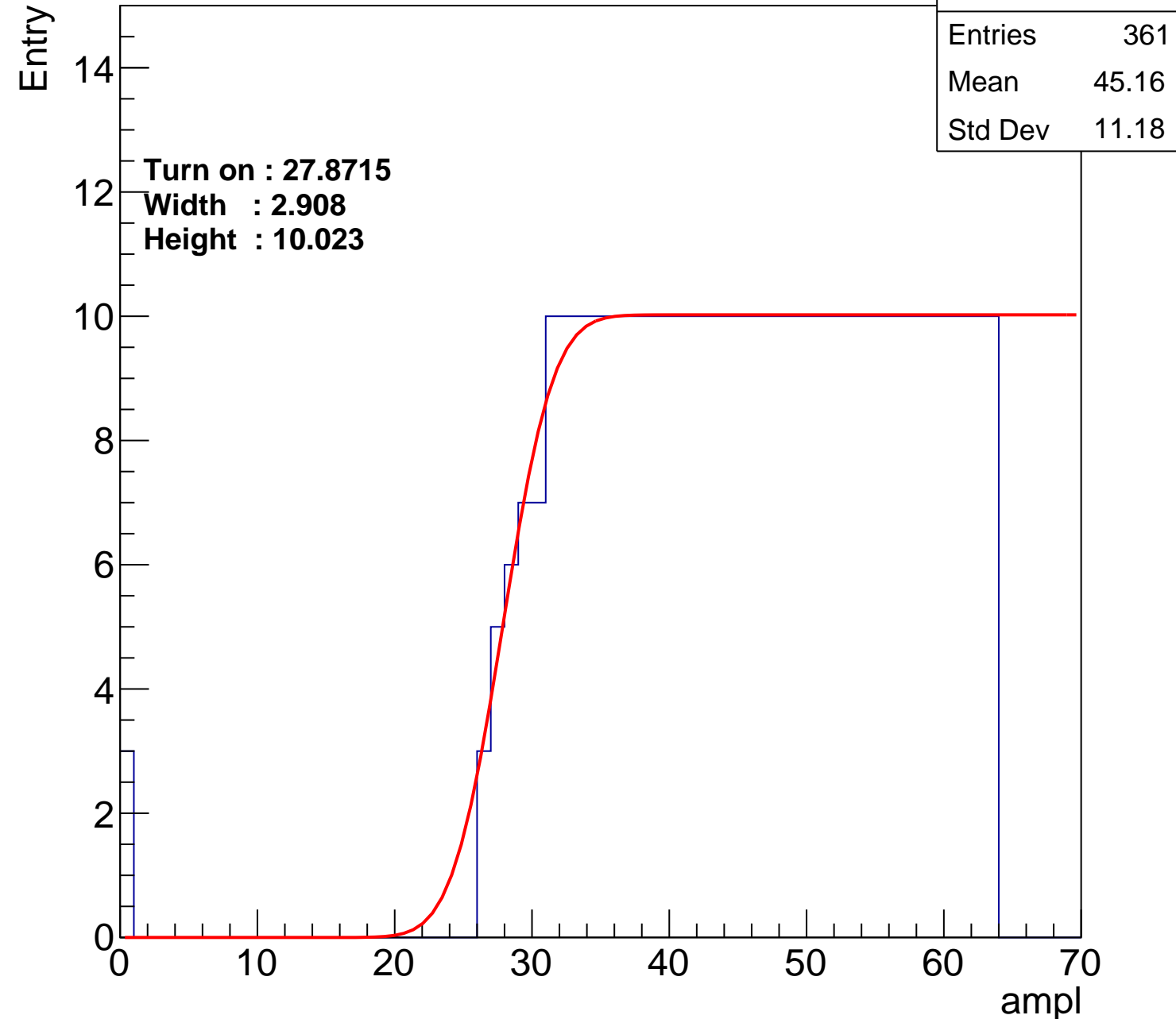
Width : 2.908

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.46
Std Dev	10.73

Turn on : 28.4319

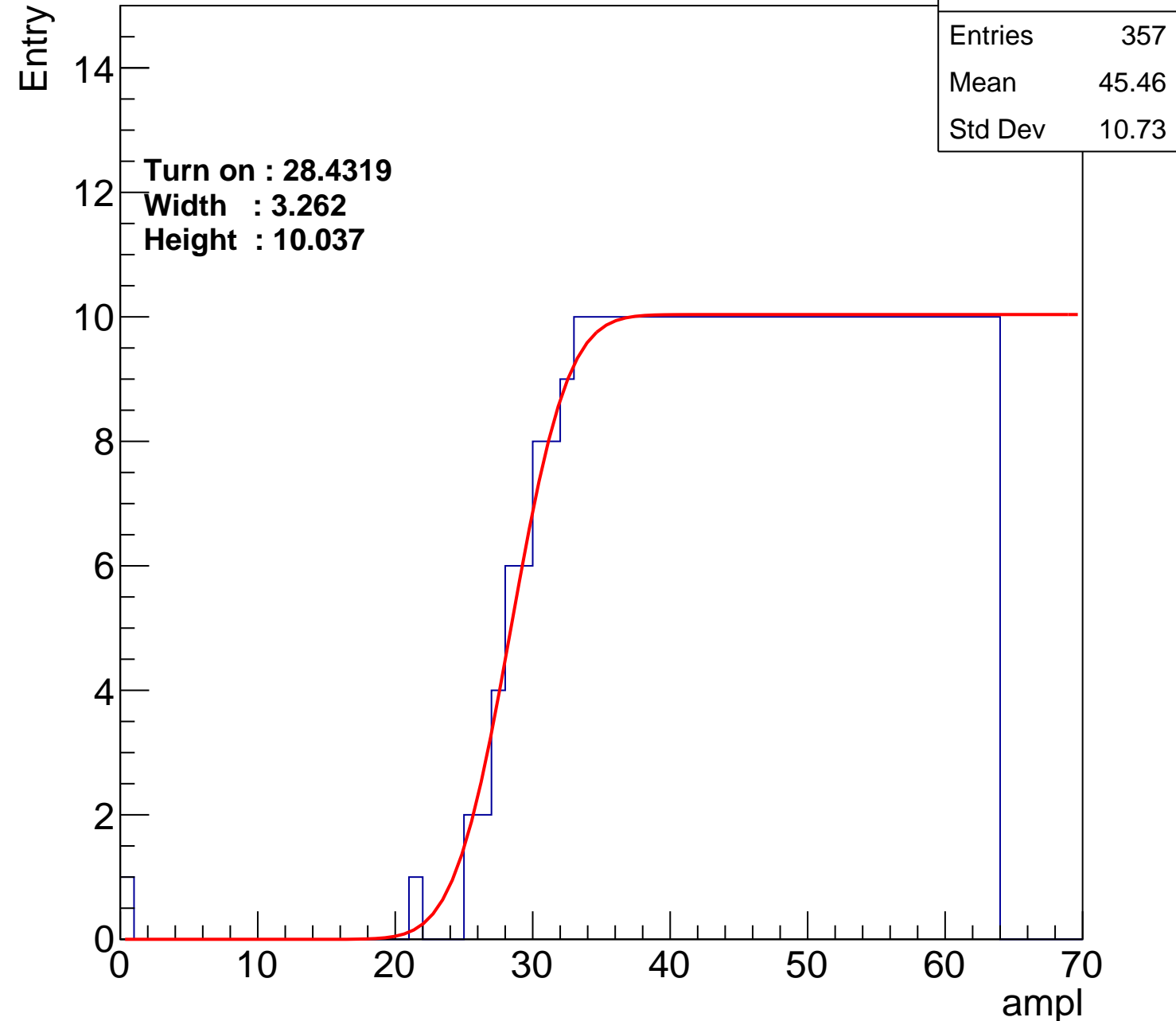
Width : 3.262

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.1699

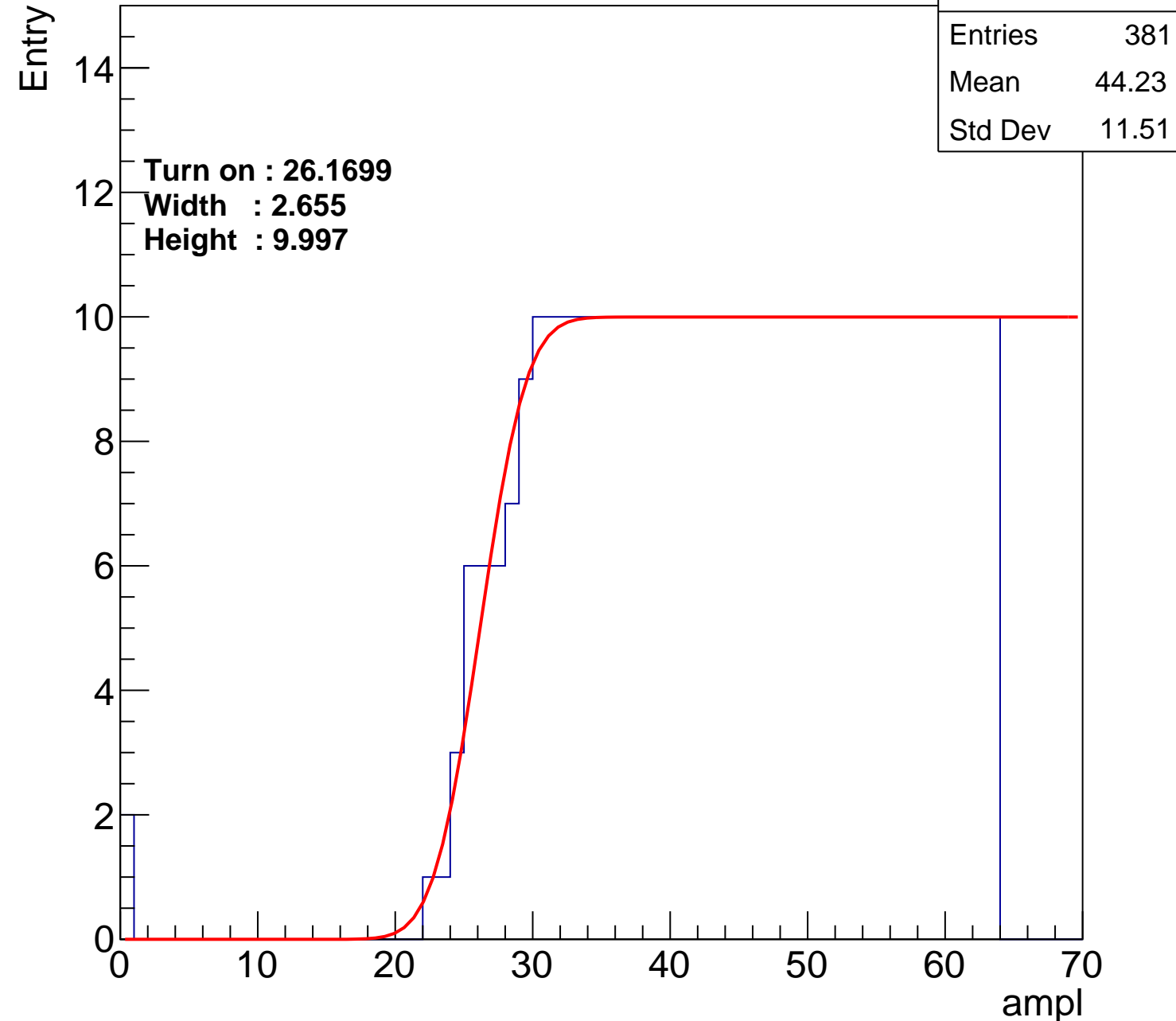
Width : 2.655

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.29
Std Dev	11.58

Turn on : 26.4688

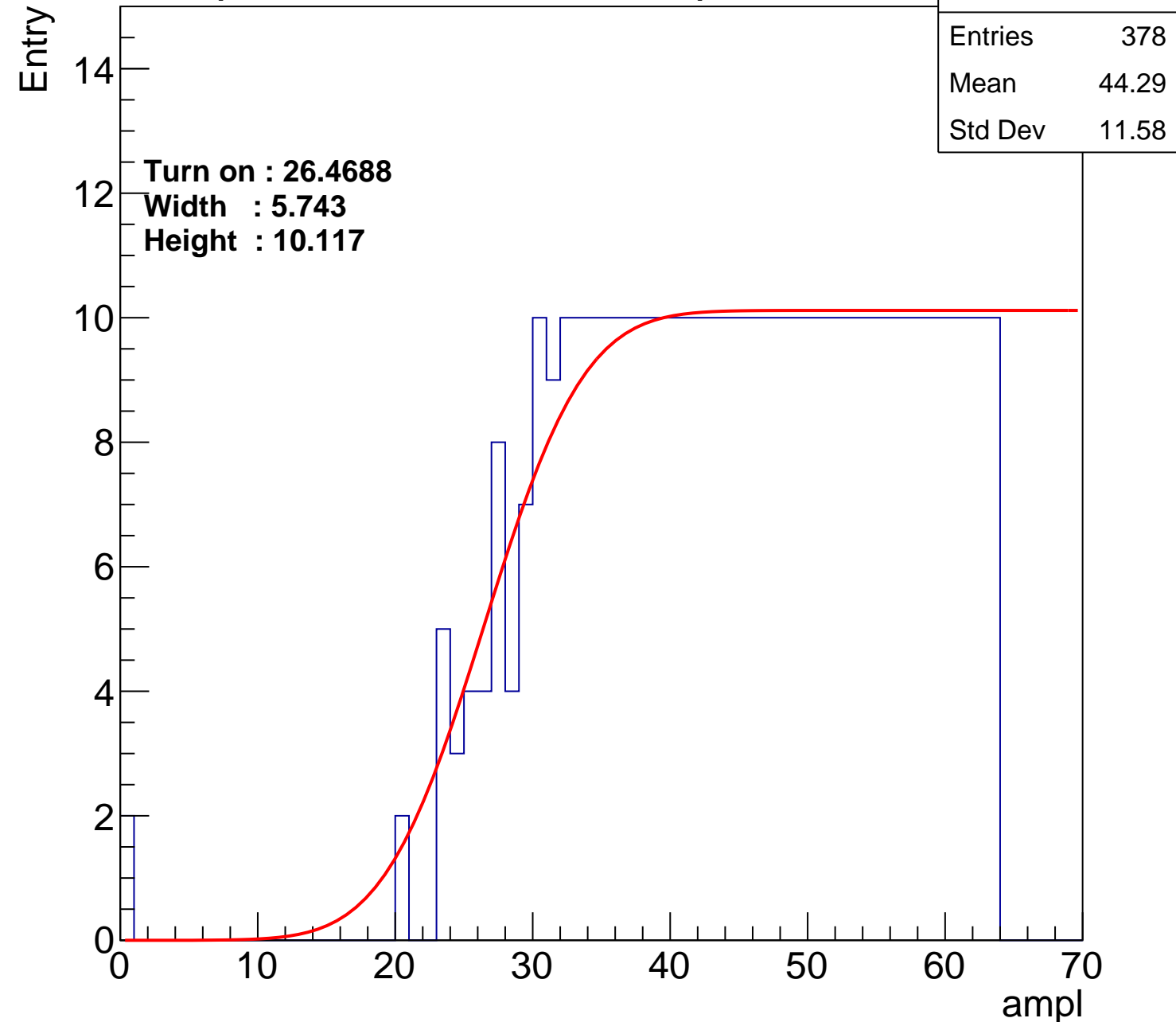
Width : 5.743

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.06
Std Dev	11.28

Turn on : 28.0599

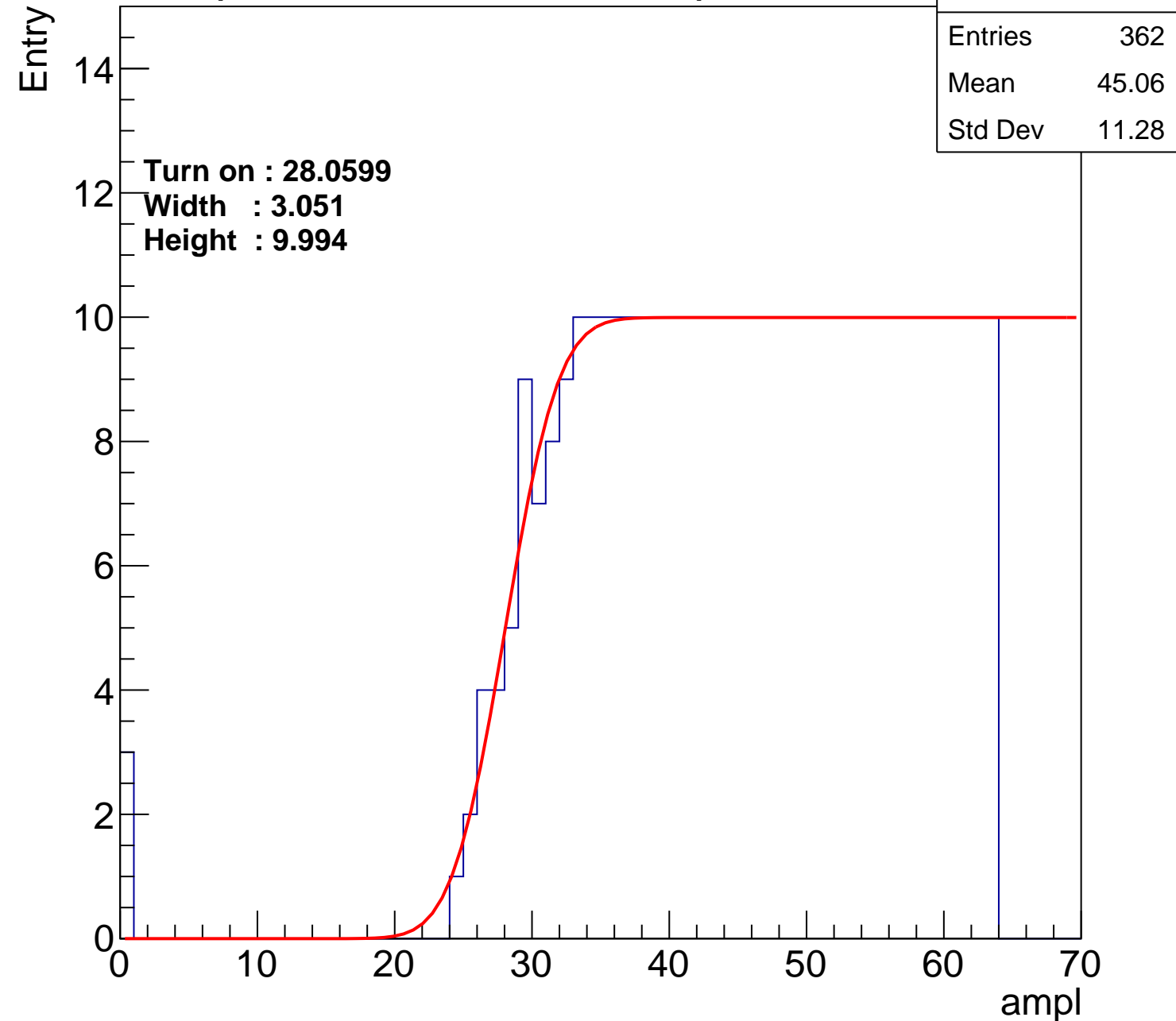
Width : 3.051

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.09
Std Dev	11.09

Turn on : 27.9234

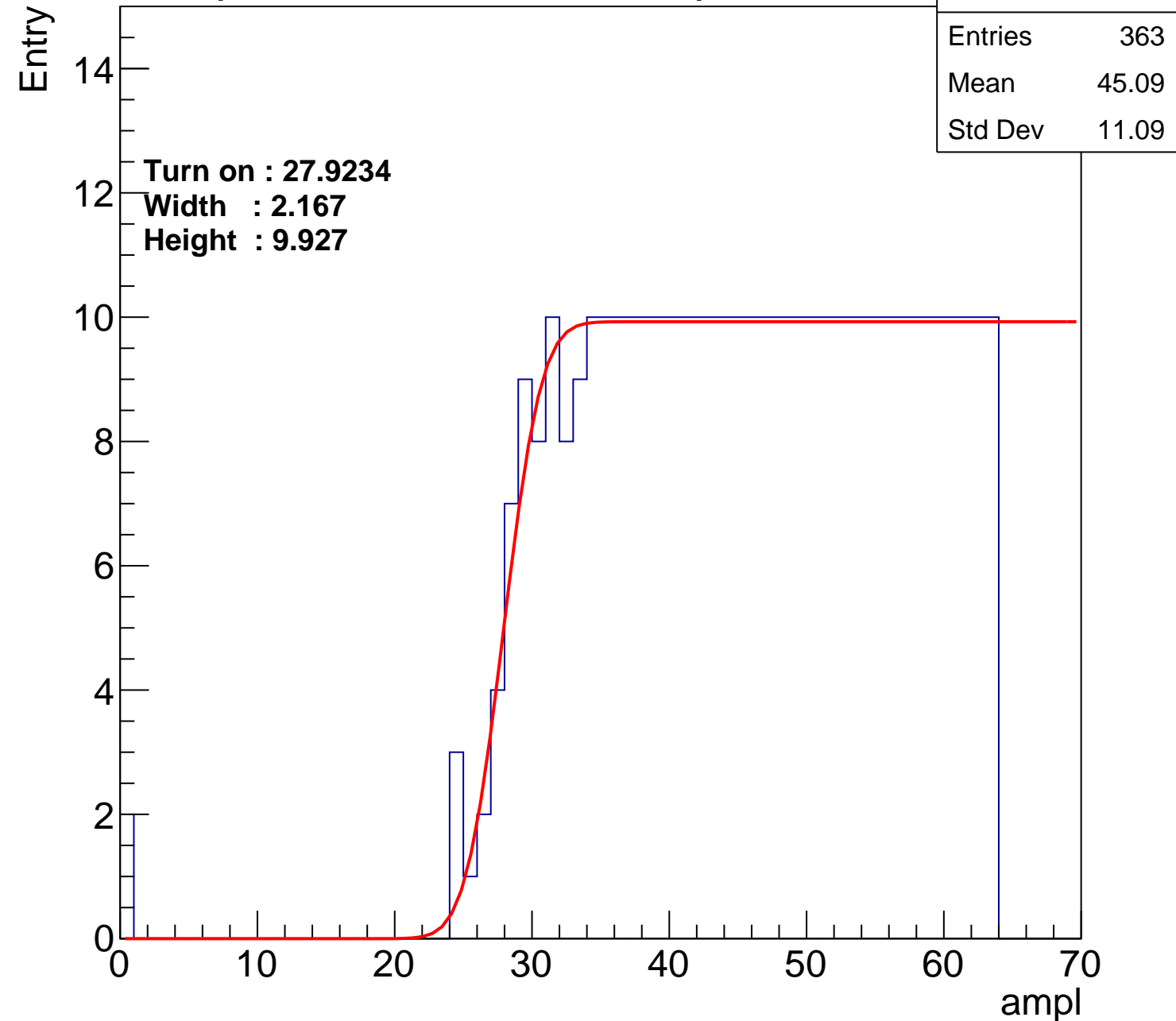
Width : 2.167

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch49

calib_packv5_042523_0143.root, FC#8, port C1

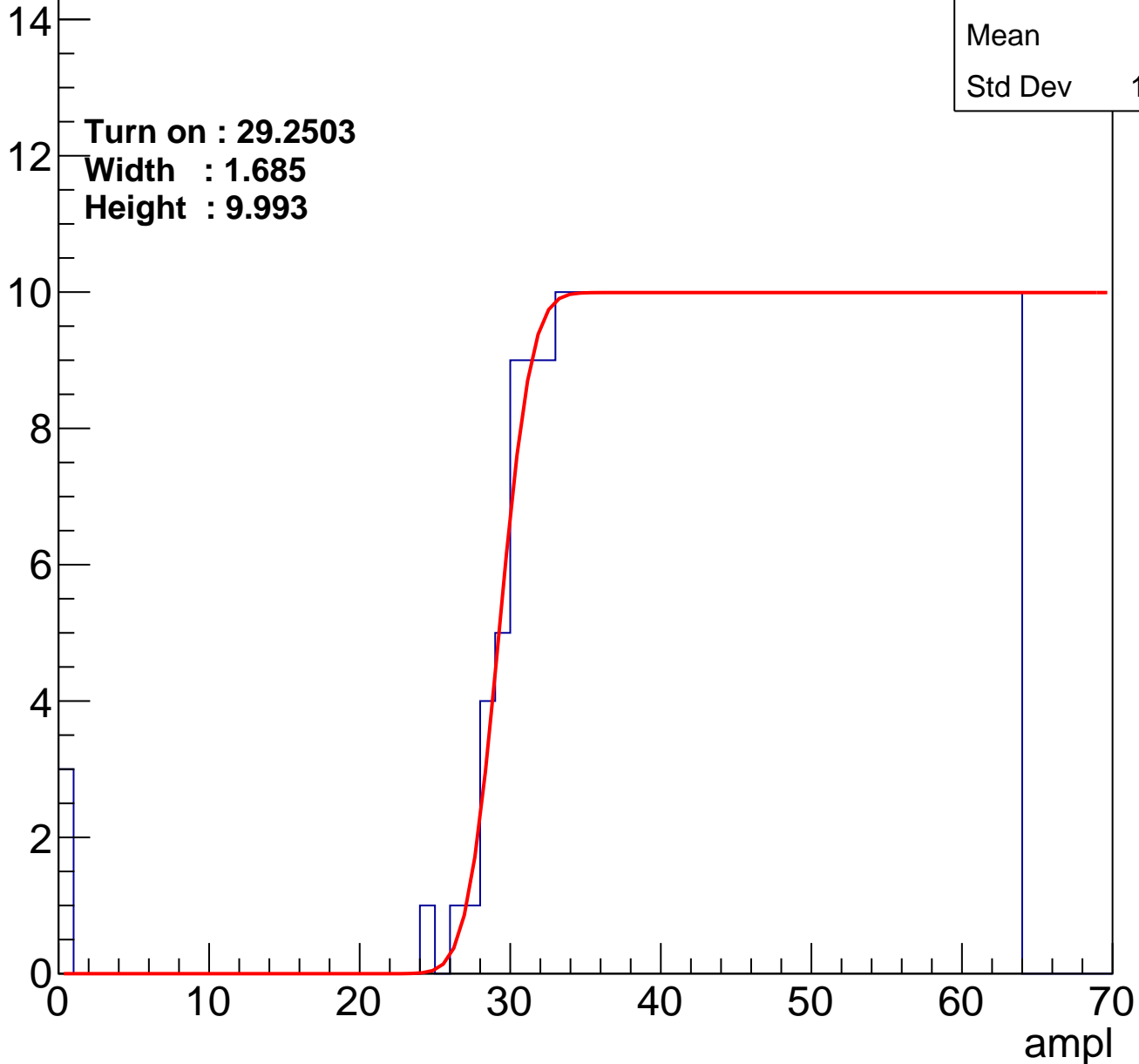
Entries	352
Mean	45.6
Std Dev	10.98

Turn on : 29.2503

Width : 1.685

Height : 9.993

Entry



B0L002S, U8-ch50

calib_packv5_042523_0143.root, FC#8, port C1

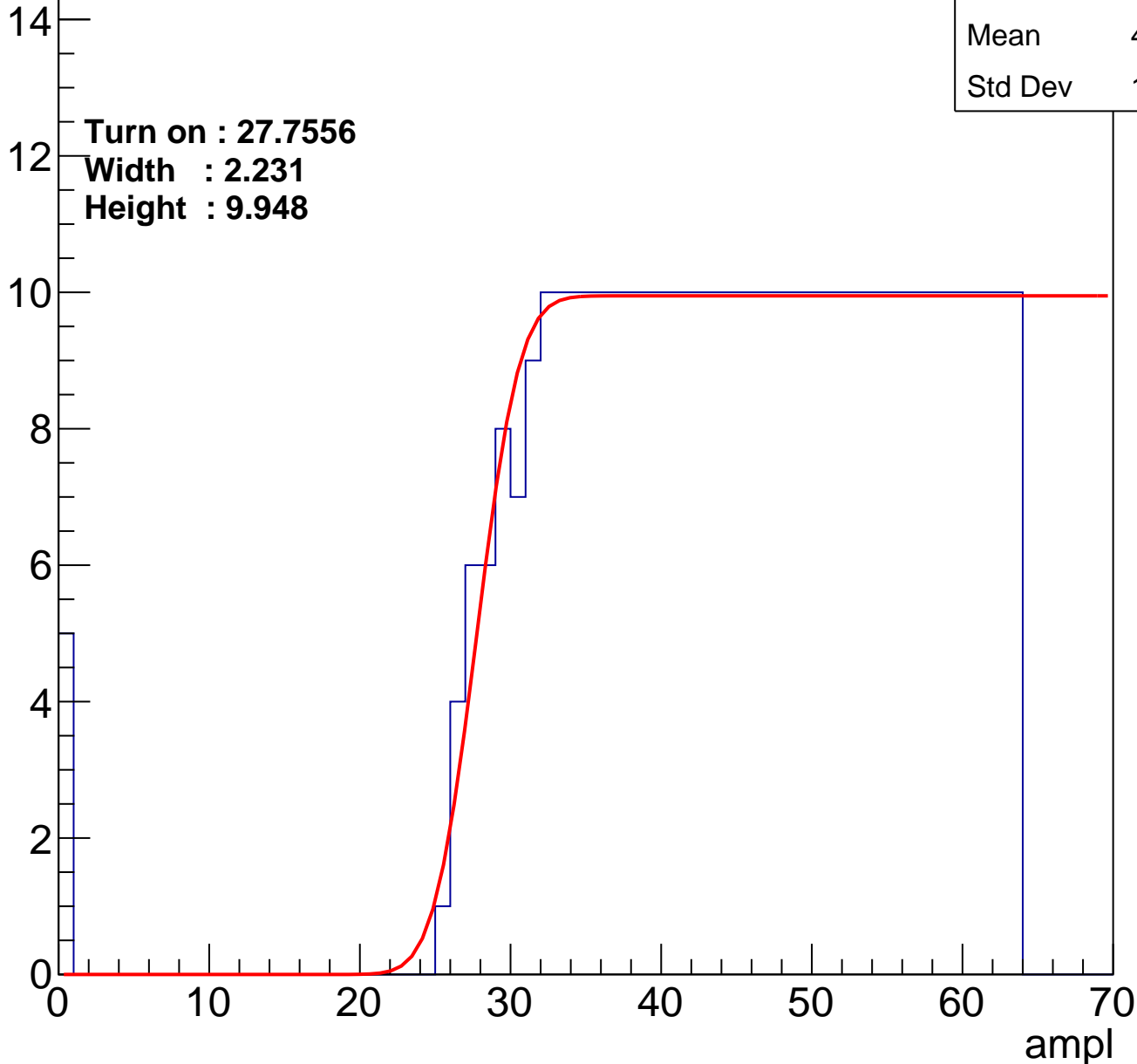
Entry

Entries	366
Mean	44.75
Std Dev	11.72

Turn on : 27.7556

Width : 2.231

Height : 9.948



B0L002S, U8-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.68
Std Dev	11.47

Turn on : 27.8447

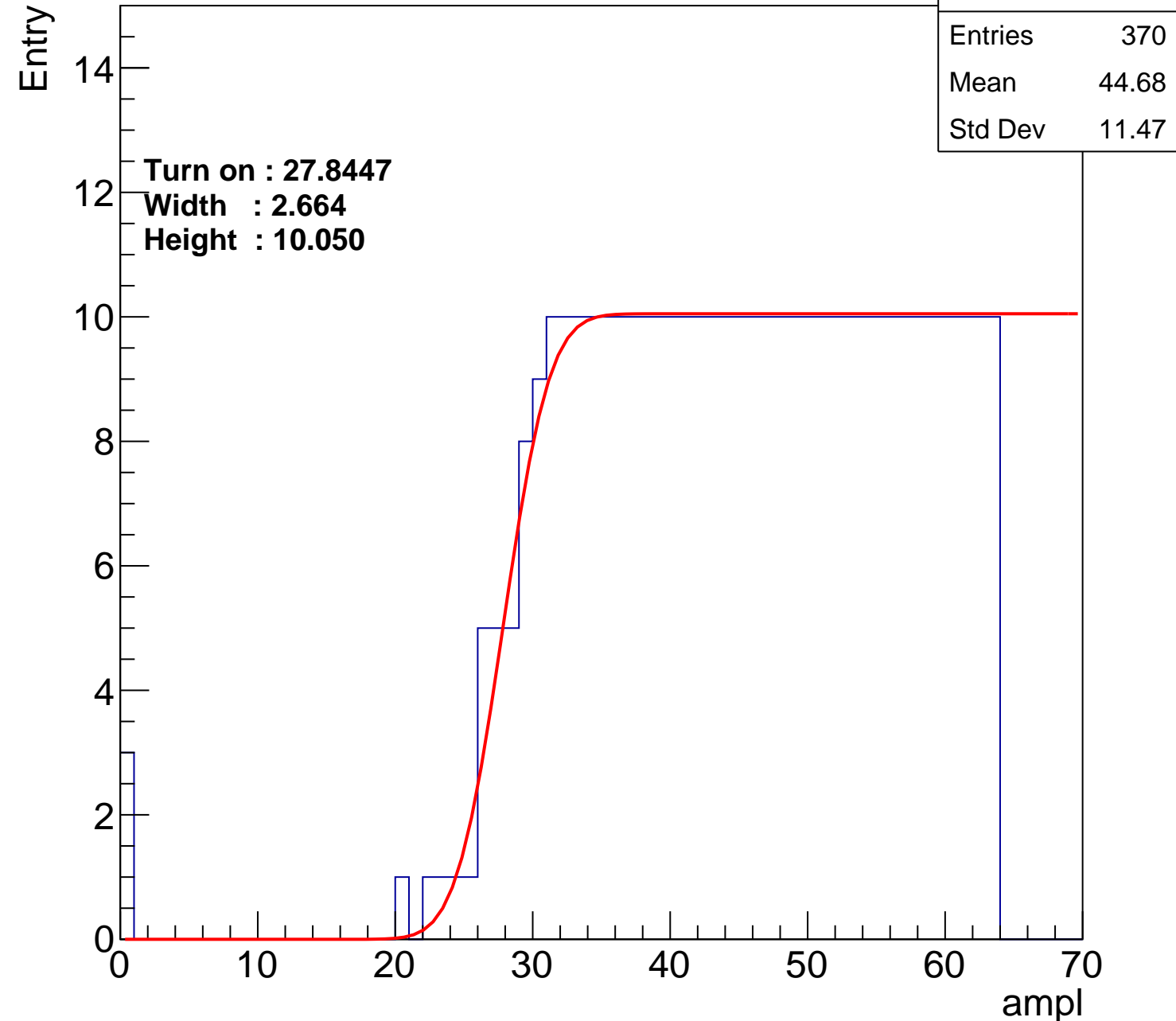
Width : 2.664

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.48
Std Dev	11.23

Turn on : 26.0628

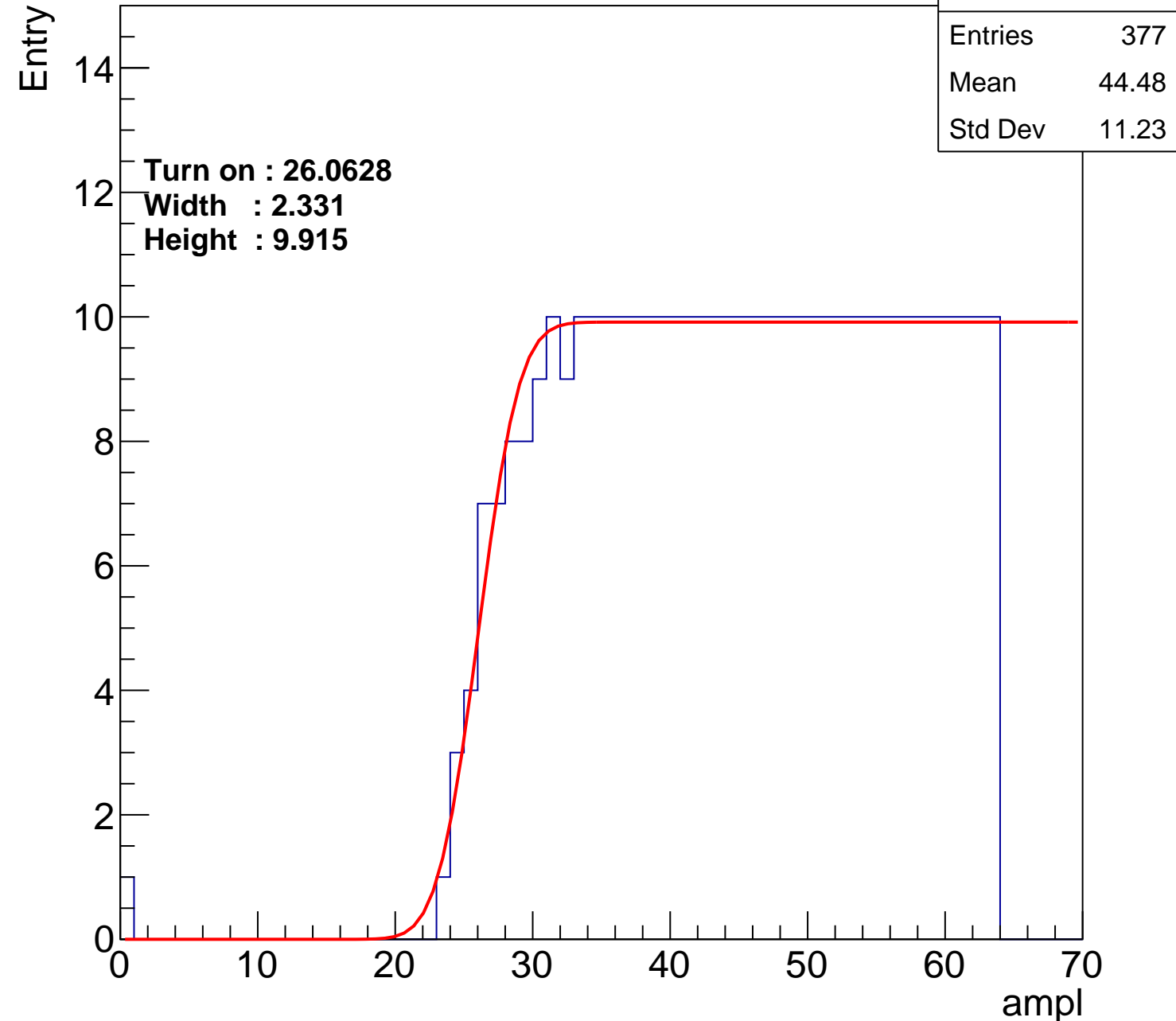
Width : 2.331

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.52
Std Dev	11.43

Turn on : 26.8449

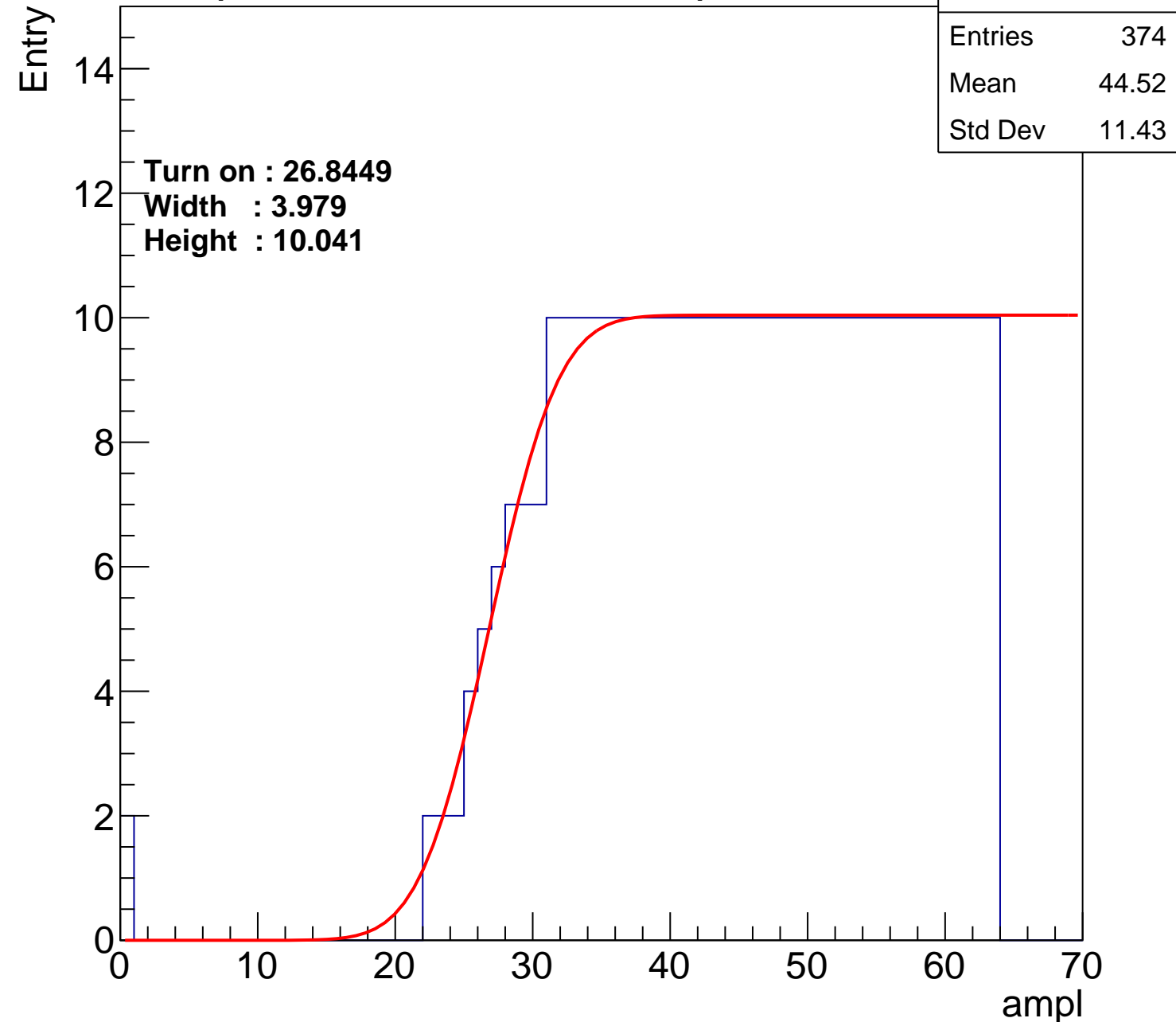
Width : 3.979

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.4
Std Dev	10.96

Turn on : 28.4161

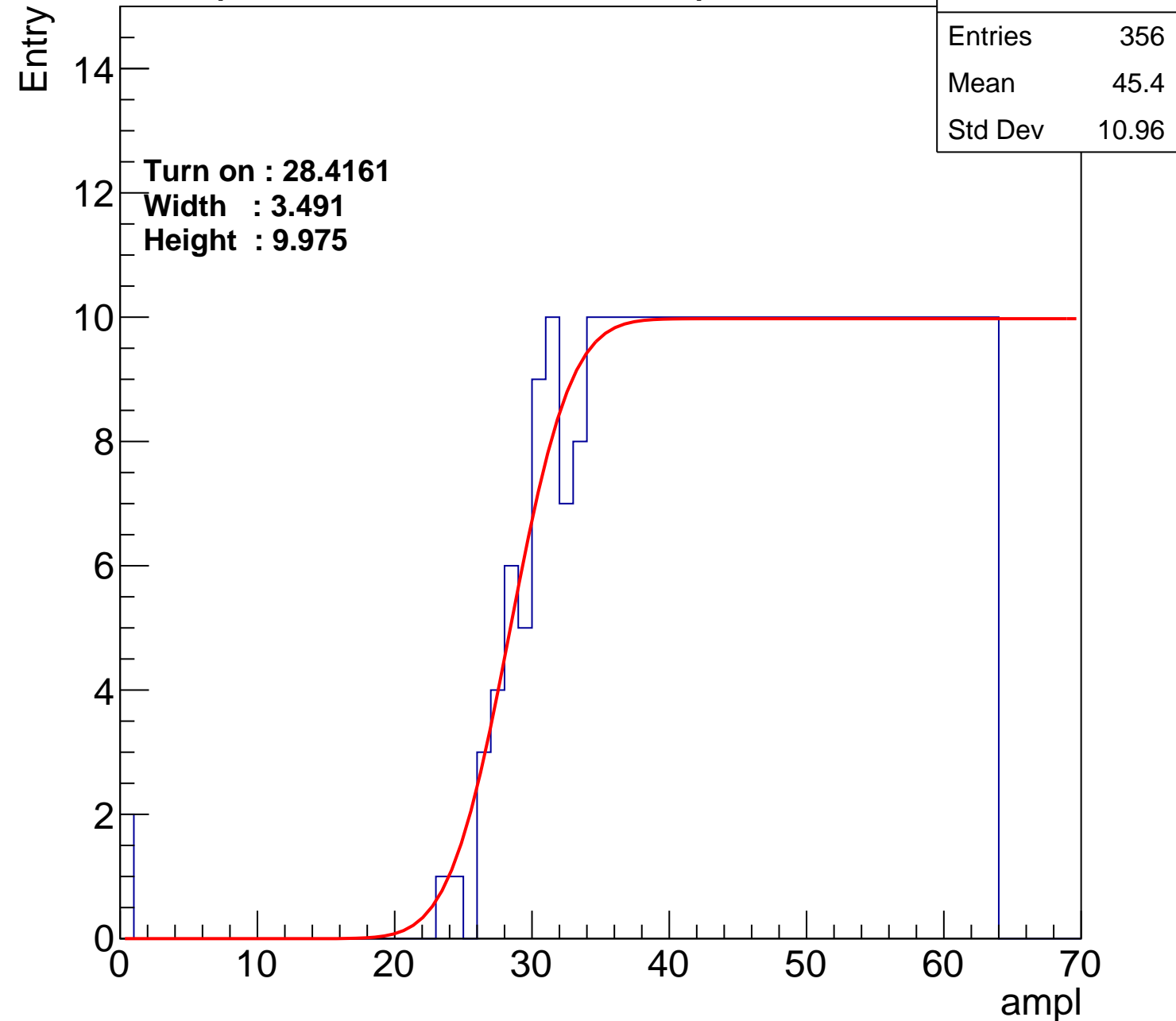
Width : 3.491

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch55

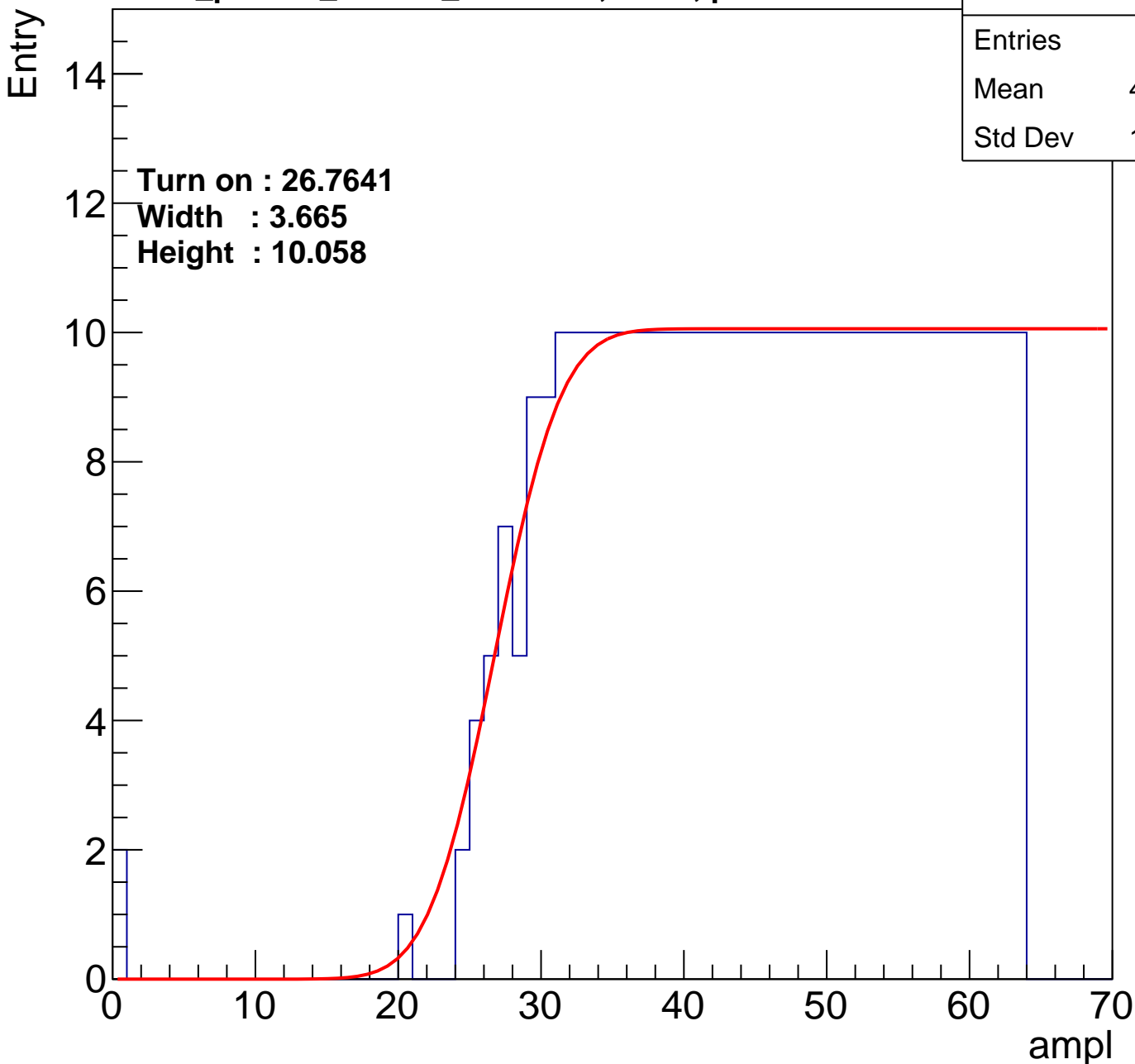
calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 26.7641

Width : 3.665

Height : 10.058



B0L002S, U8-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 26.8929

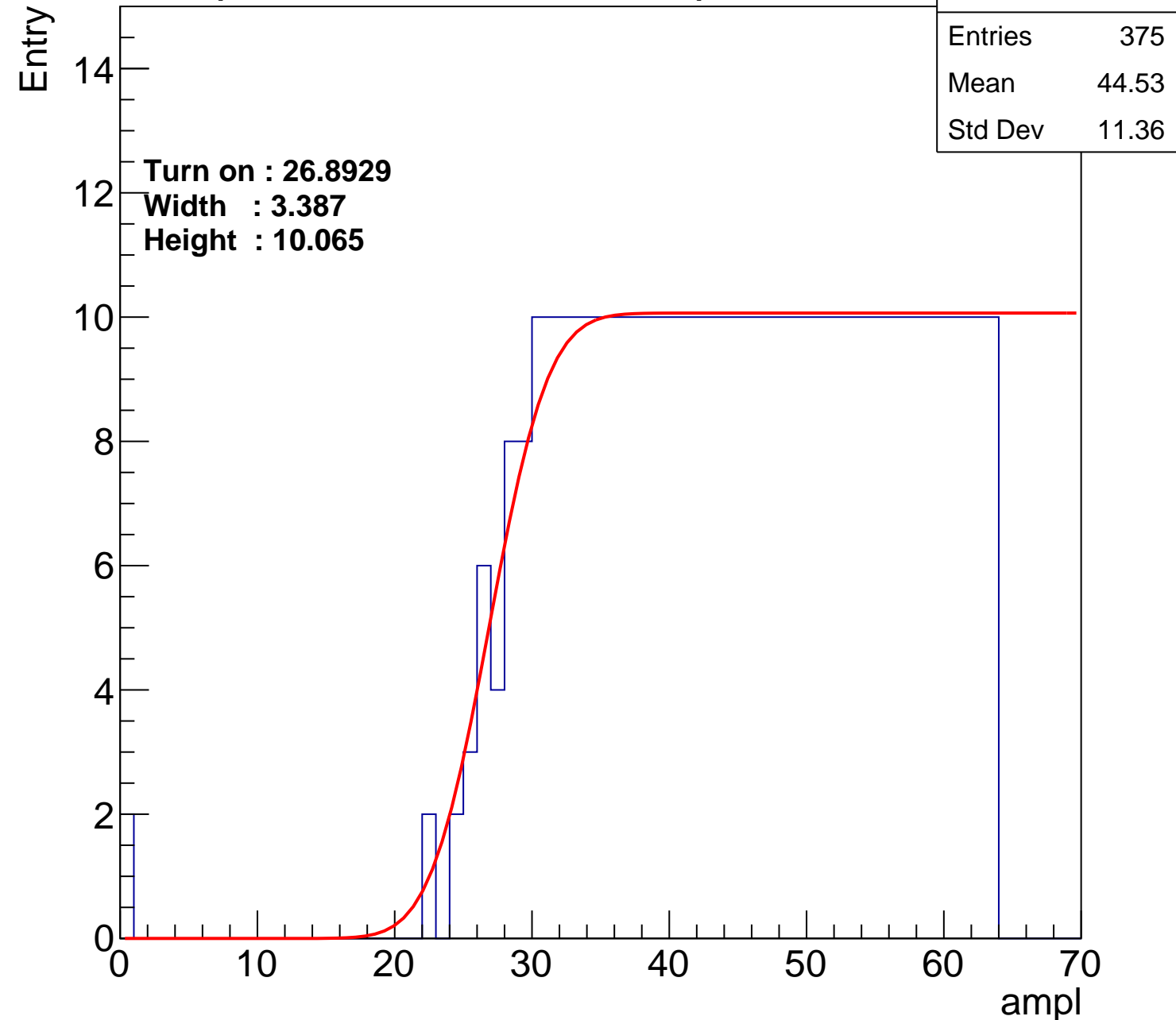
Width : 3.387

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch57

calib_packv5_042523_0143.root, FC#8, port C1

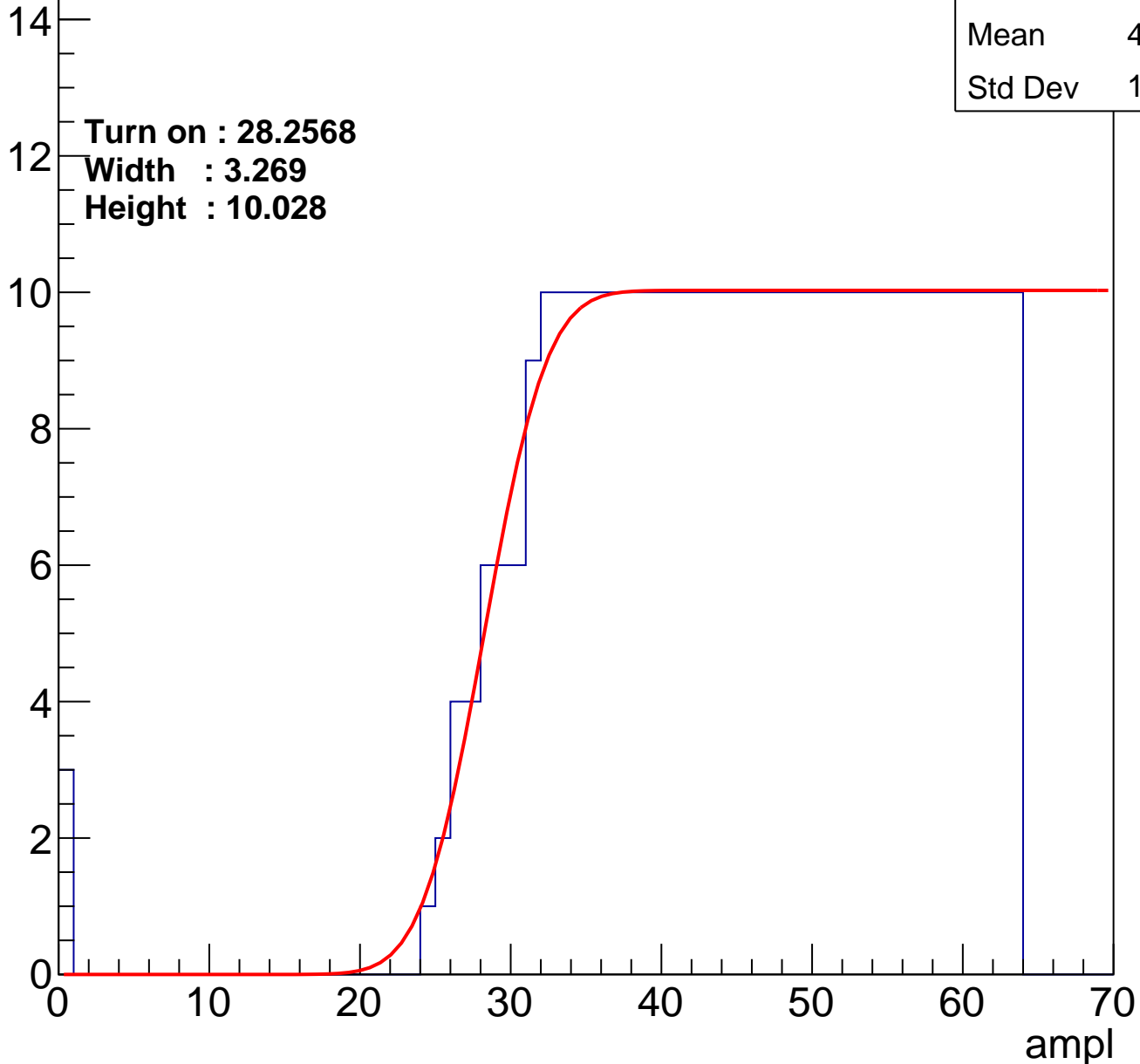
Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.2568

Width : 3.269

Height : 10.028

Entry



B0L002S, U8-ch58

calib_packv5_042523_0143.root, FC#8, port C1

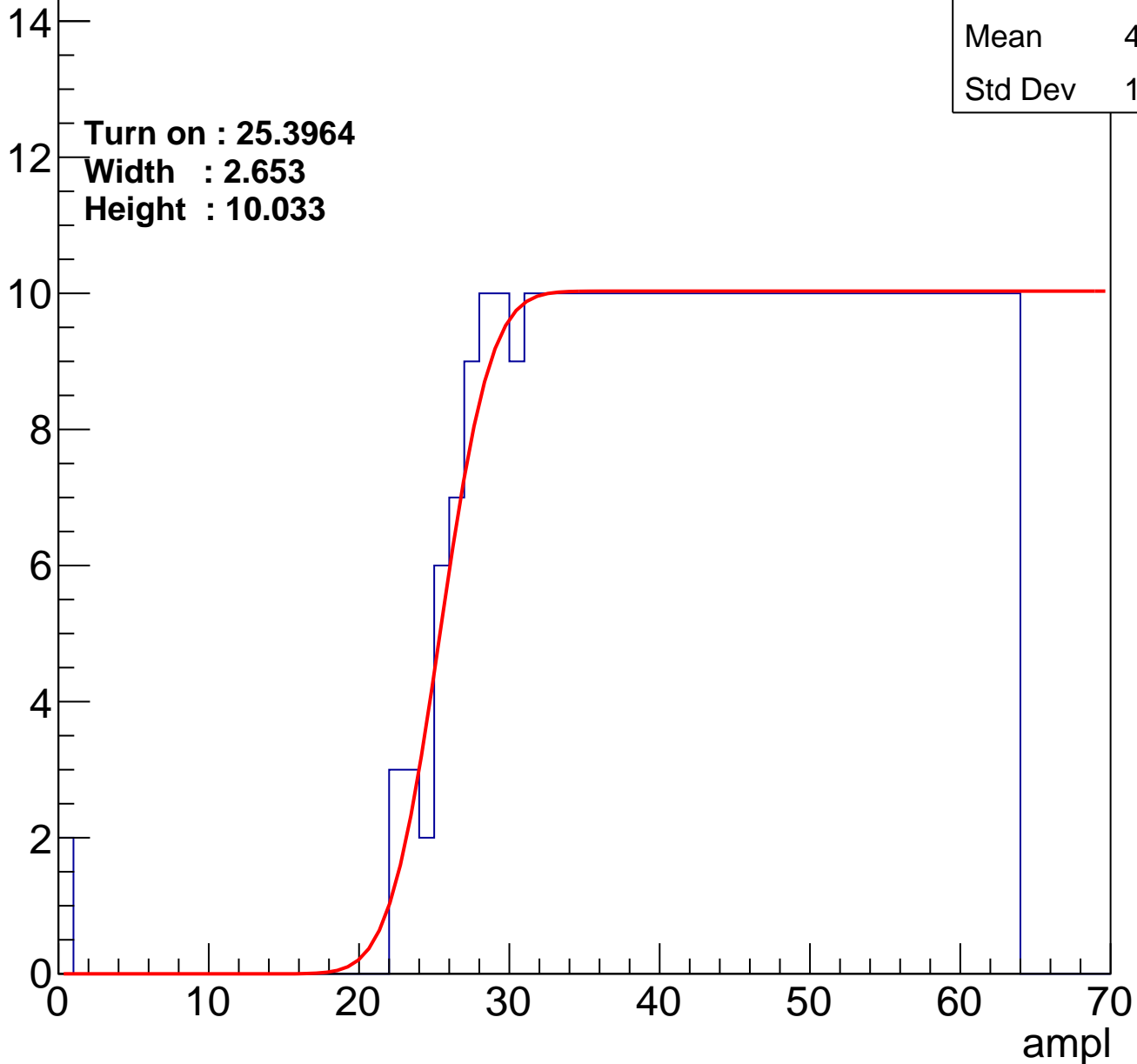
Entries	391
Mean	43.75
Std Dev	11.74

Turn on : 25.3964

Width : 2.653

Height : 10.033

Entry



B0L002S, U8-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.4183

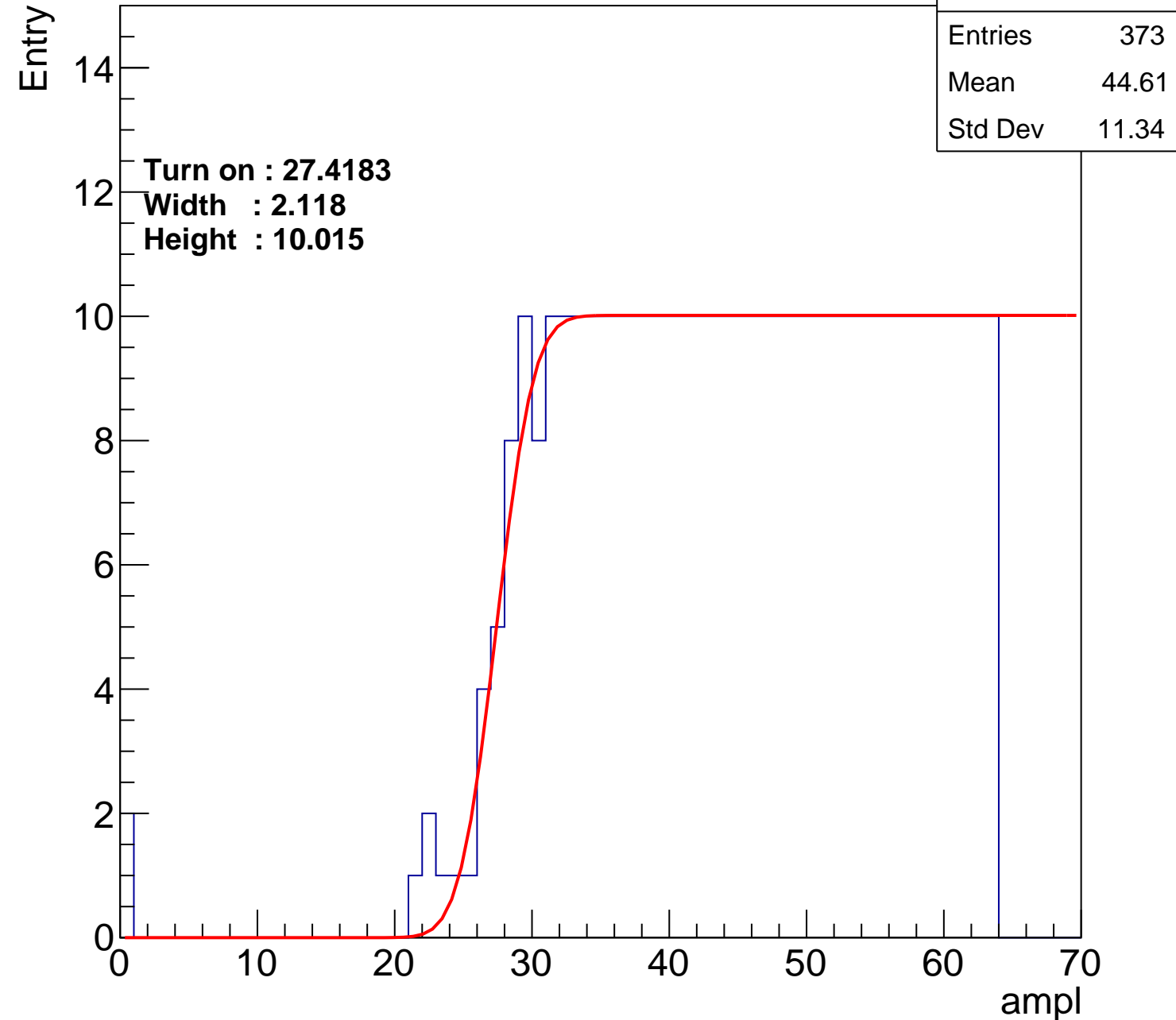
Width : 2.118

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.39
Std Dev	11.61

Turn on : 27.1734

Width : 3.699

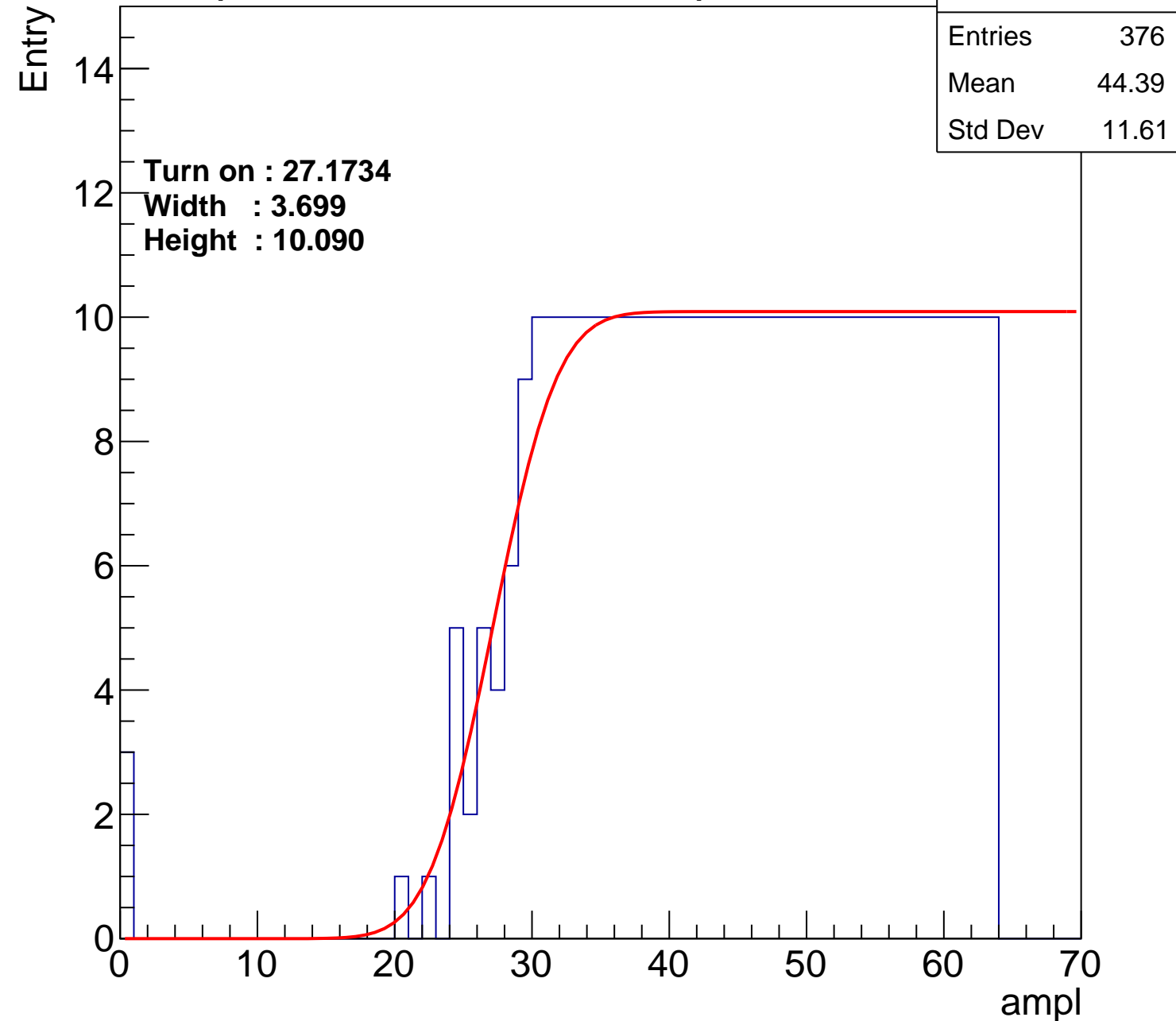
Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U8-ch61

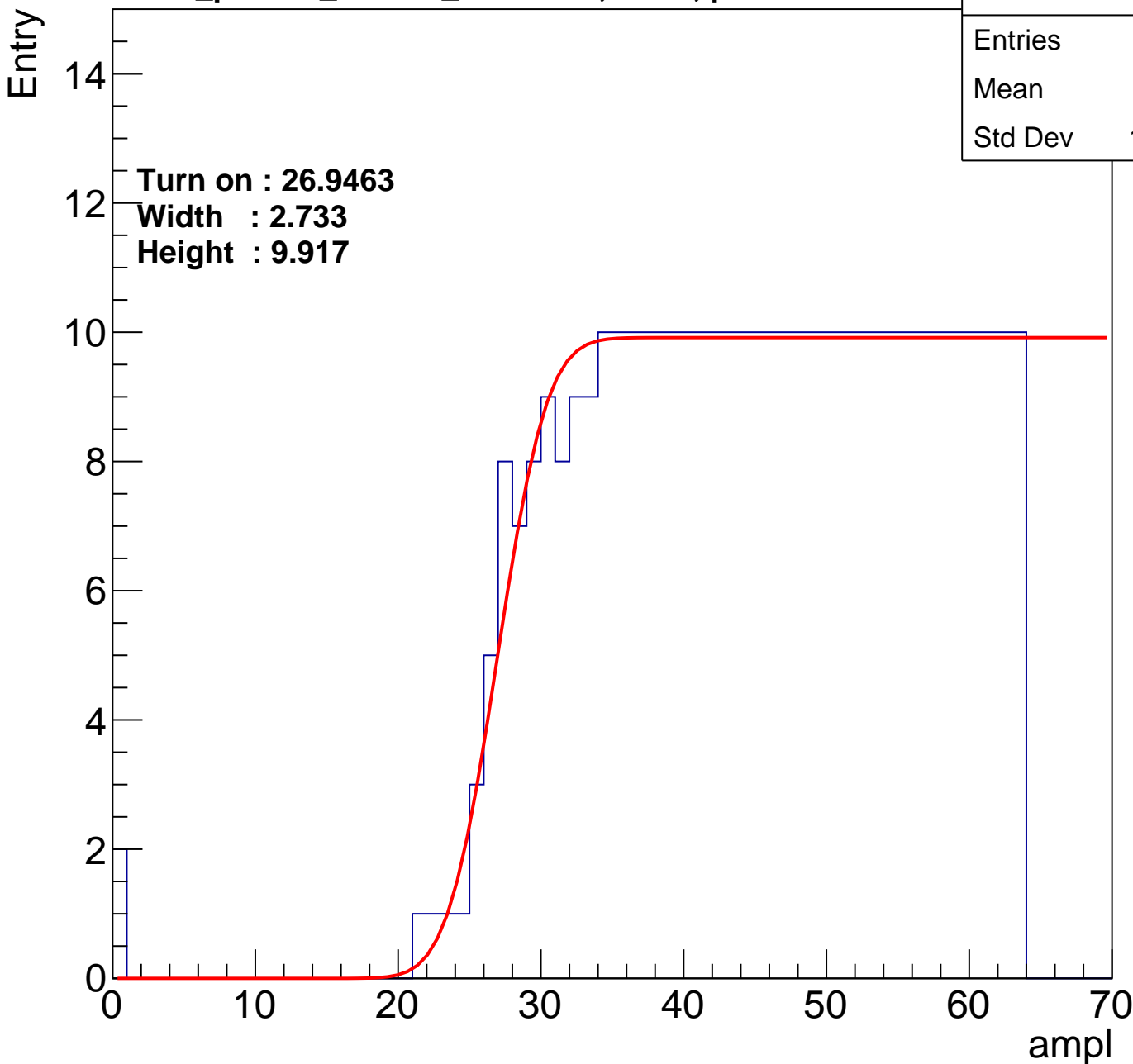
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 26.9463

Width : 2.733

Height : 9.917

Entries	372
Mean	44.6
Std Dev	11.39



B0L002S, U8-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.71
Std Dev	11.1

Turn on : 26.7797

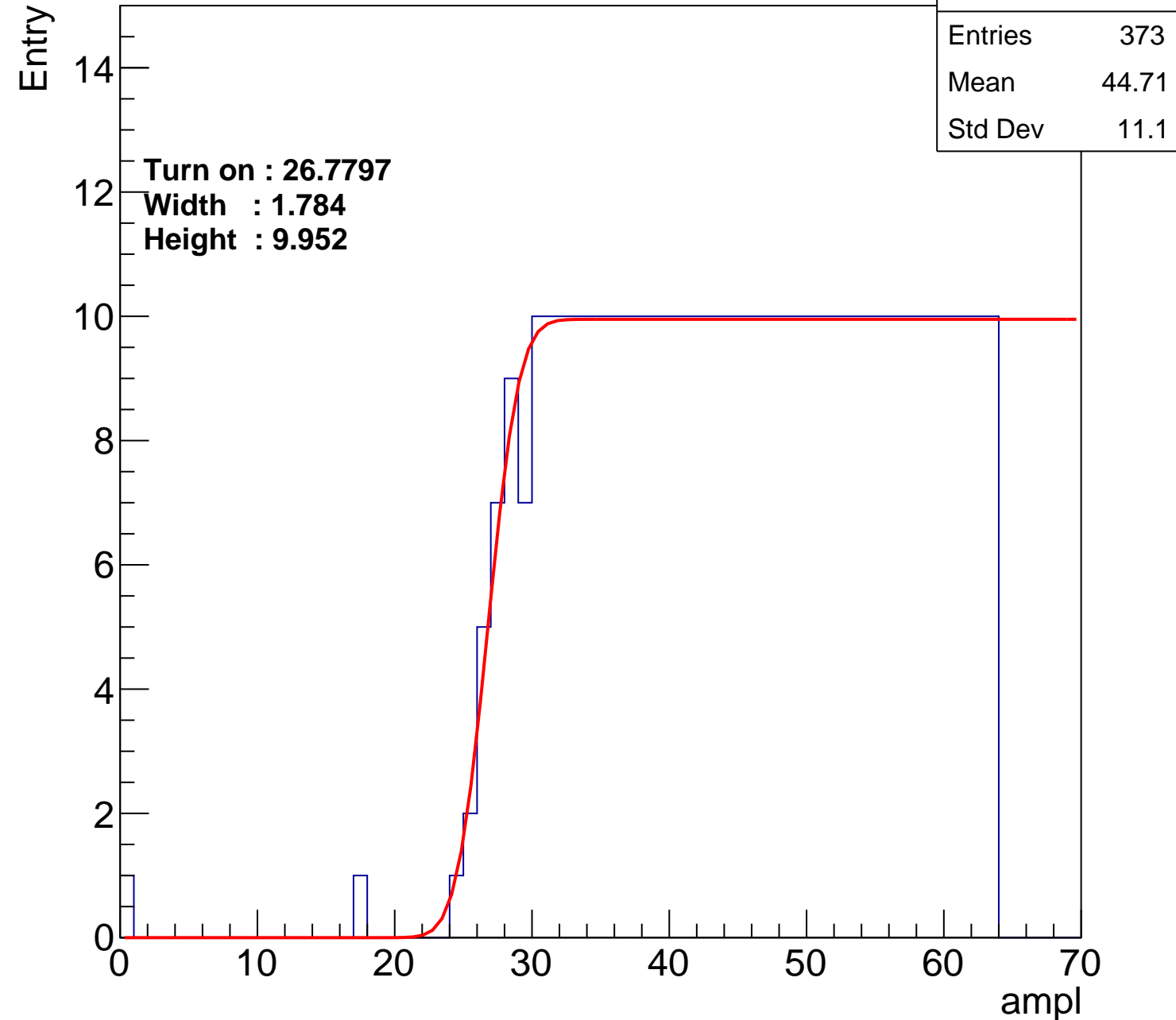
Width : 1.784

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch63

calib_packv5_042523_0143.root, FC#8, port C1

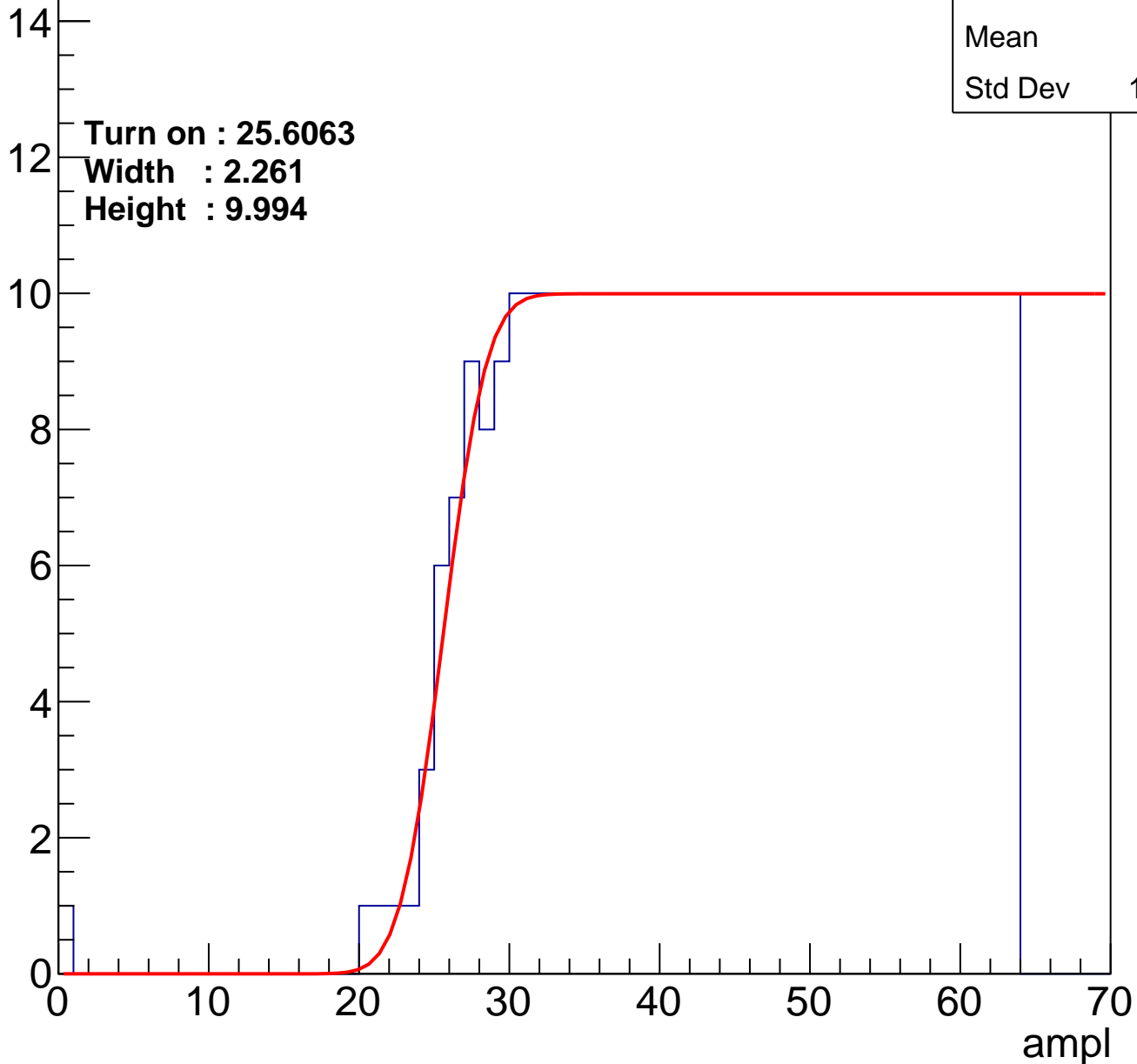
Entry

Entries	387
Mean	44
Std Dev	11.49

Turn on : 25.6063

Width : 2.261

Height : 9.994



B0L002S, U8-ch64

calib_packv5_042523_0143.root, FC#8, port C1

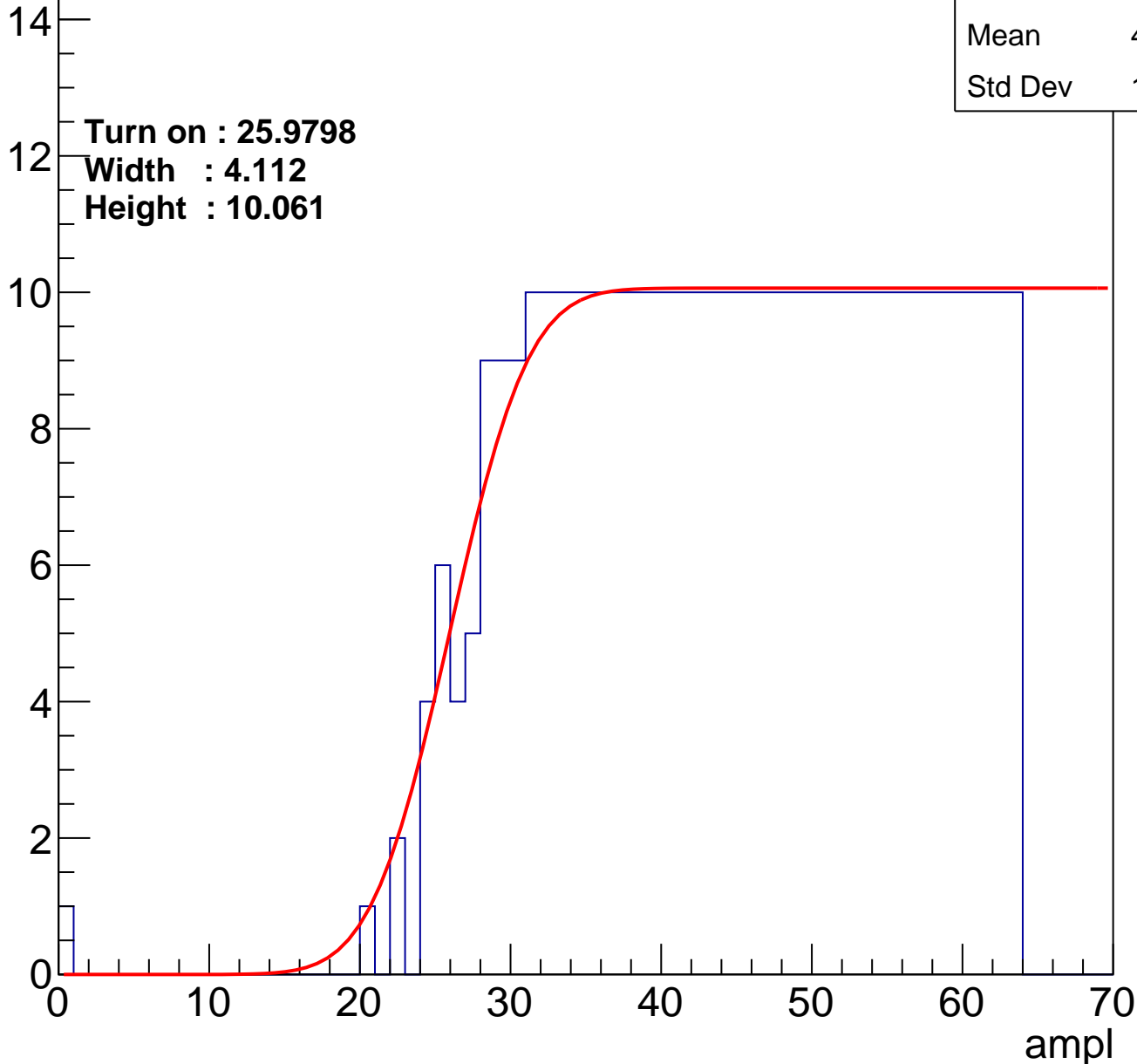
Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 25.9798

Width : 4.112

Height : 10.061

Entry



B0L002S, U8-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 28.5185

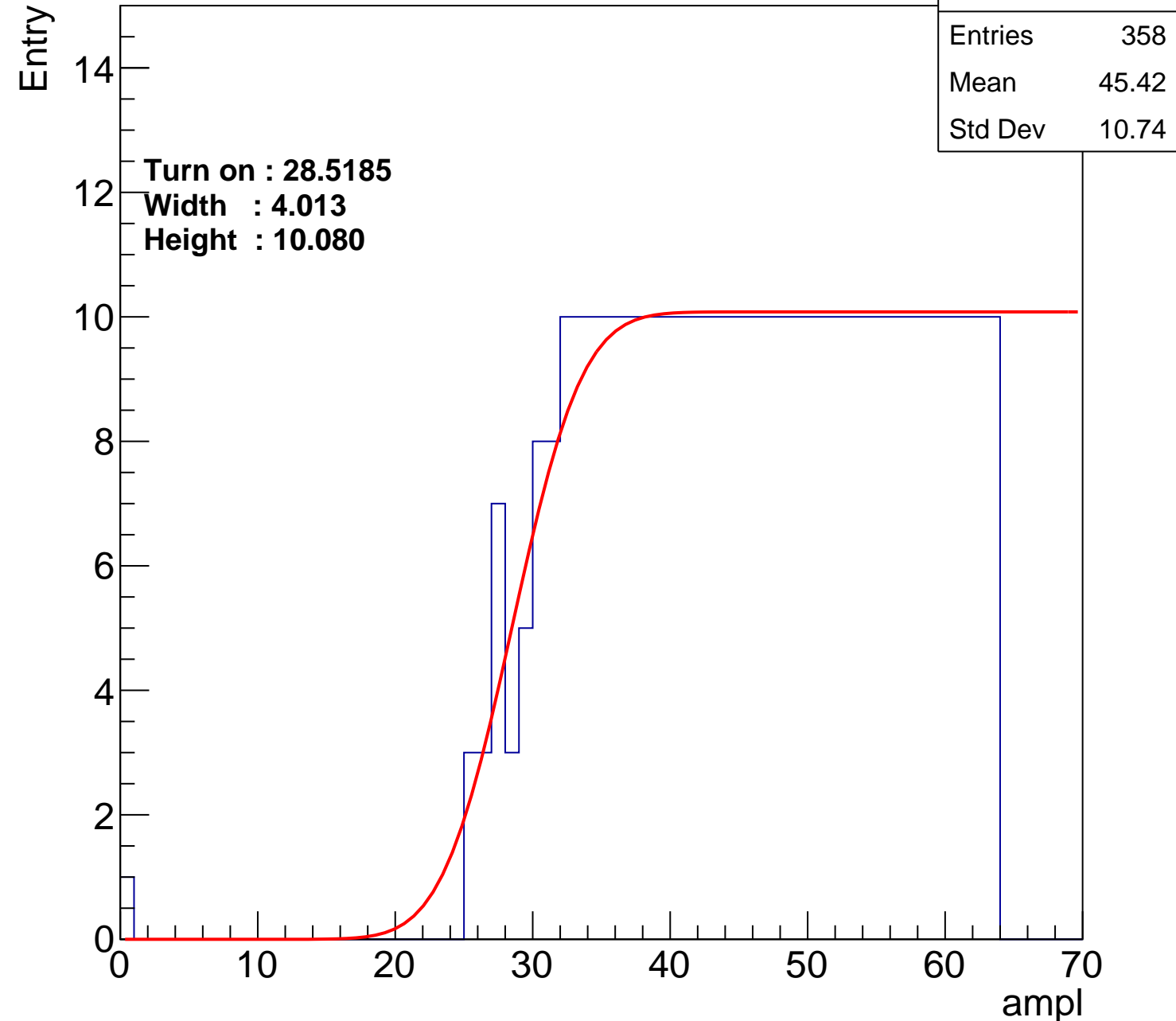
Width : 4.013

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.98
Std Dev	11.11

Turn on : 27.7436

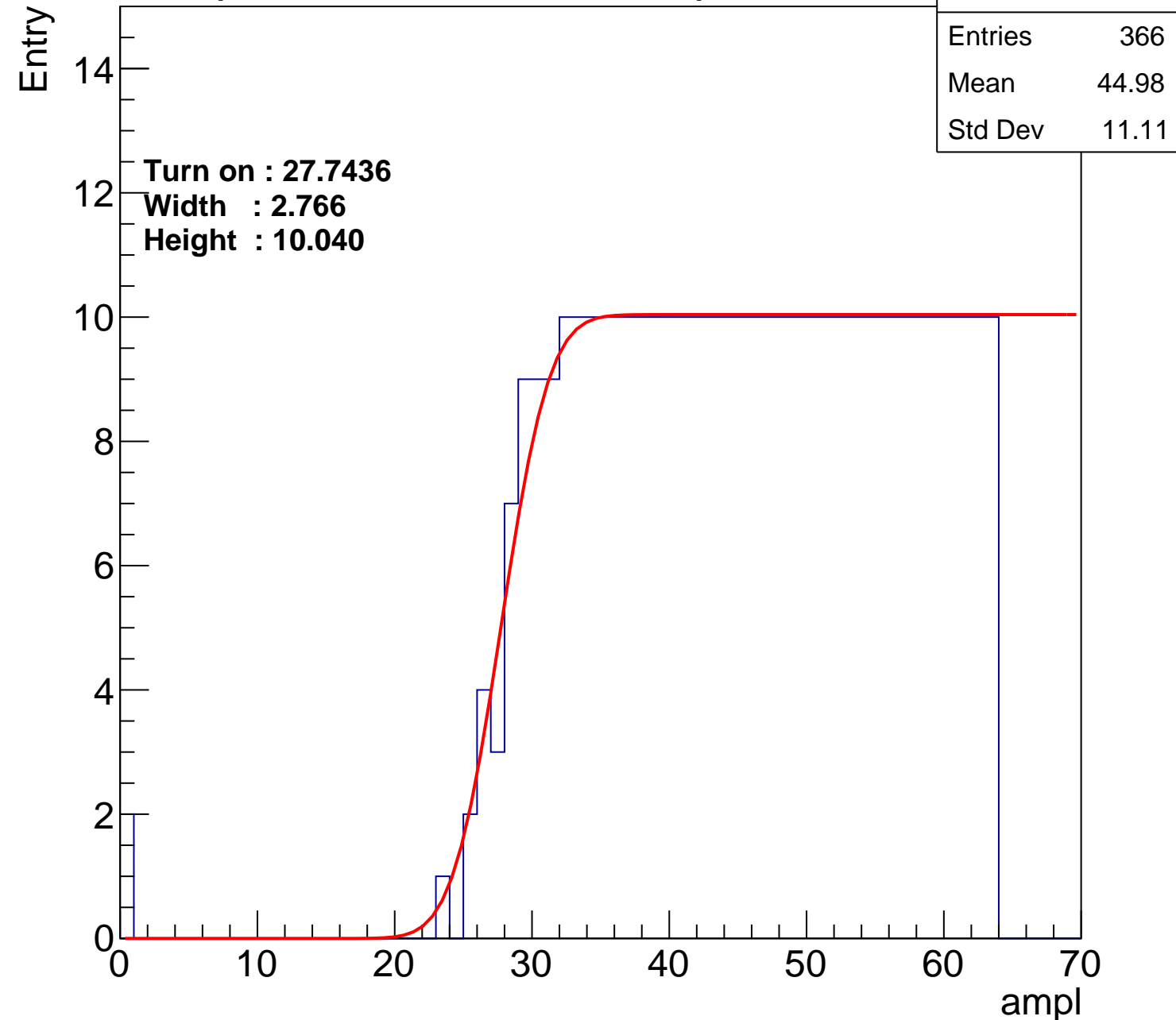
Width : 2.766

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.3441

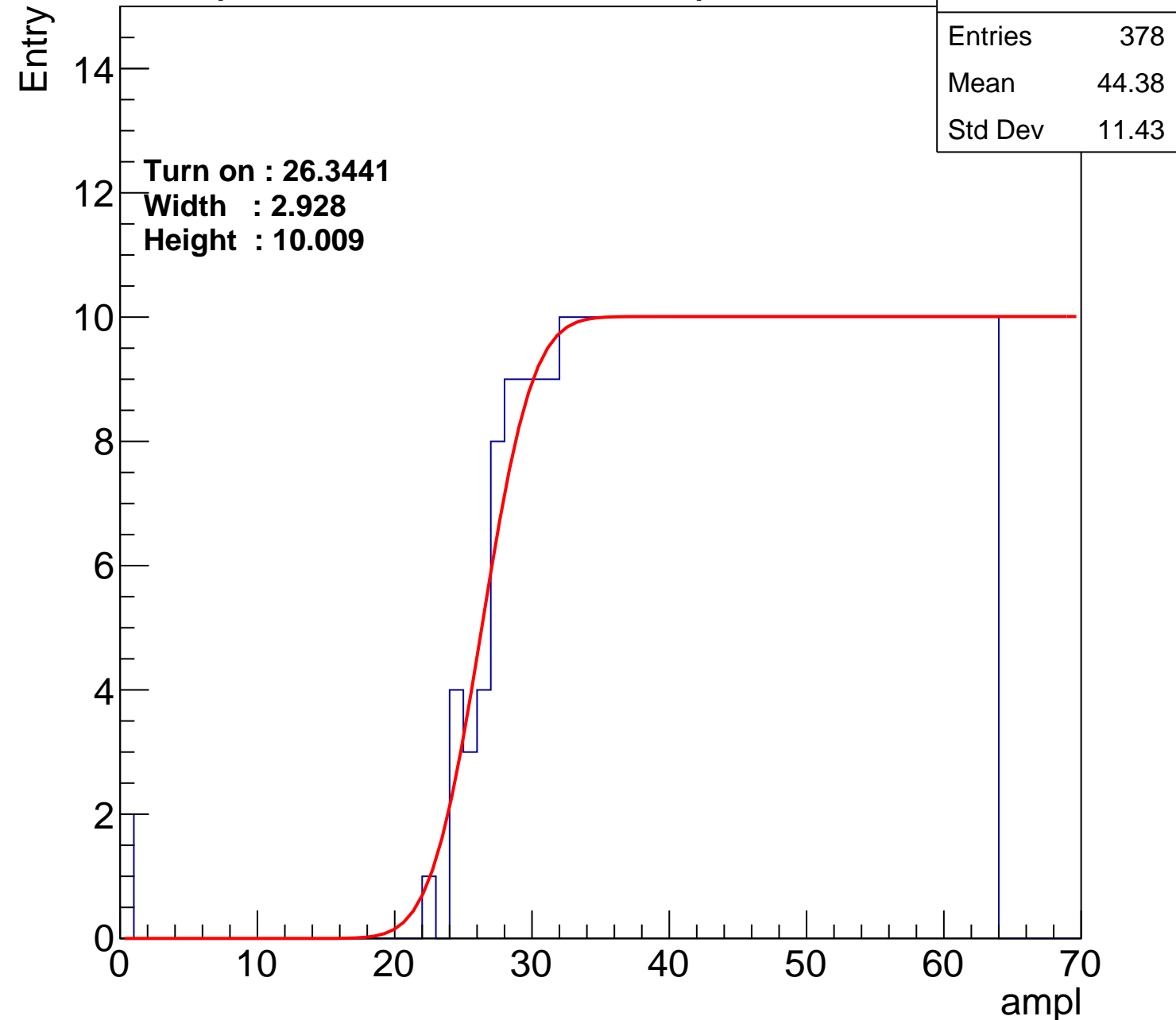
Width : 2.928

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.99
Std Dev	11.09

Turn on : 27.7308

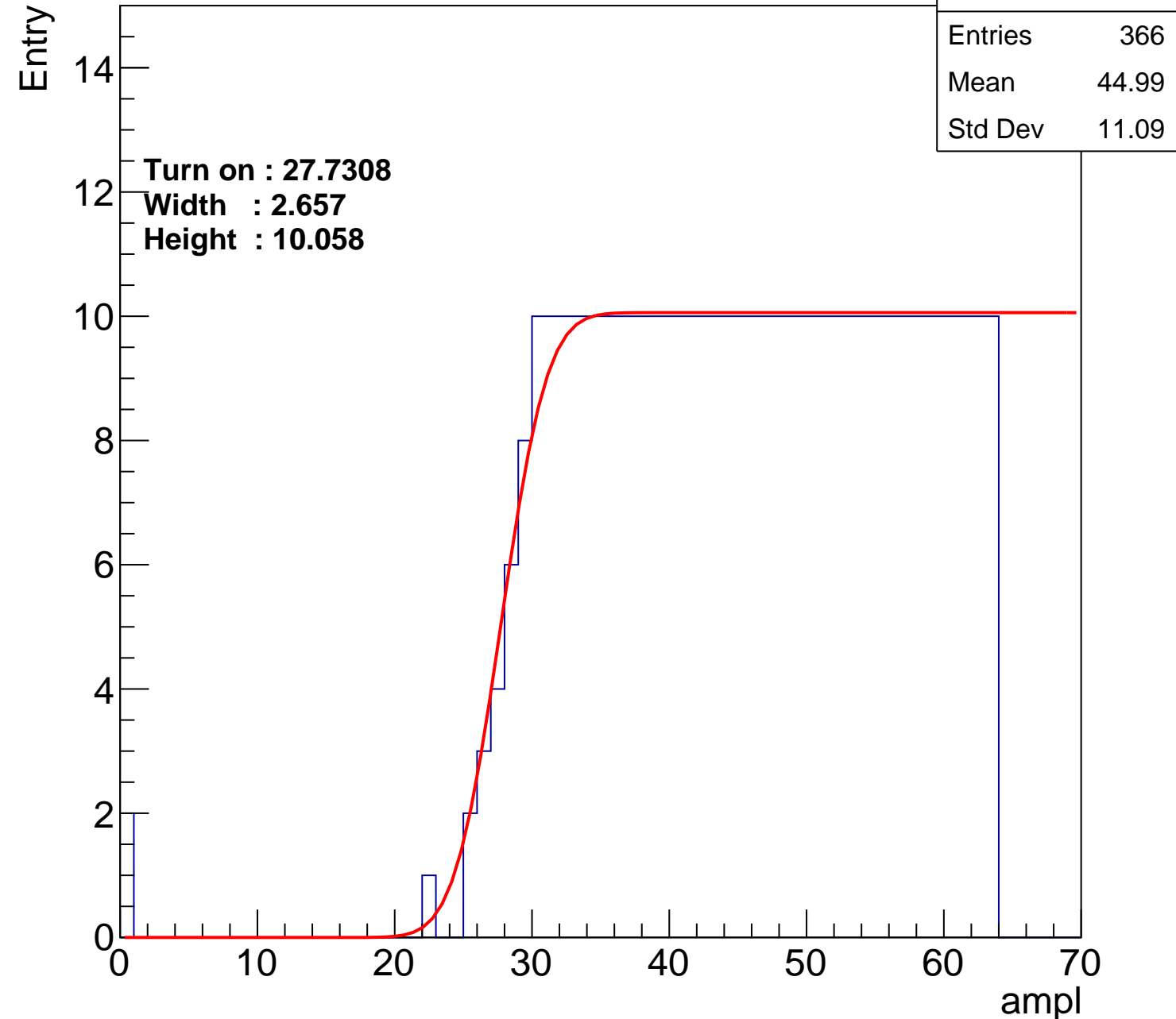
Width : 2.657

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch69

calib_packv5_042523_0143.root, FC#8, port C1

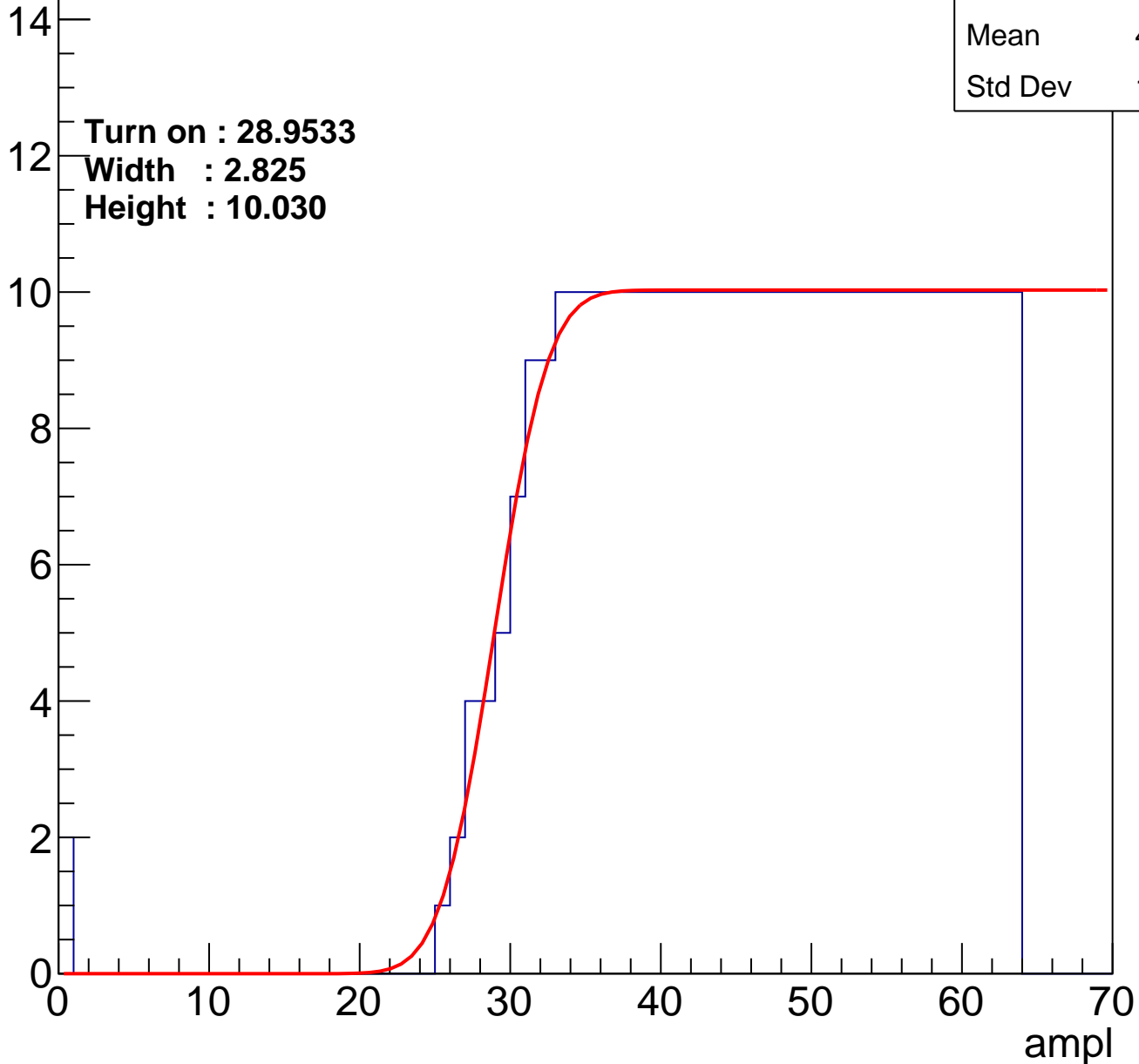
Entries	353
Mean	45.61
Std Dev	10.81

Turn on : 28.9533

Width : 2.825

Height : 10.030

Entry



B0L002S, U8-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.61
Std Dev	10.79

Turn on : 28.9355

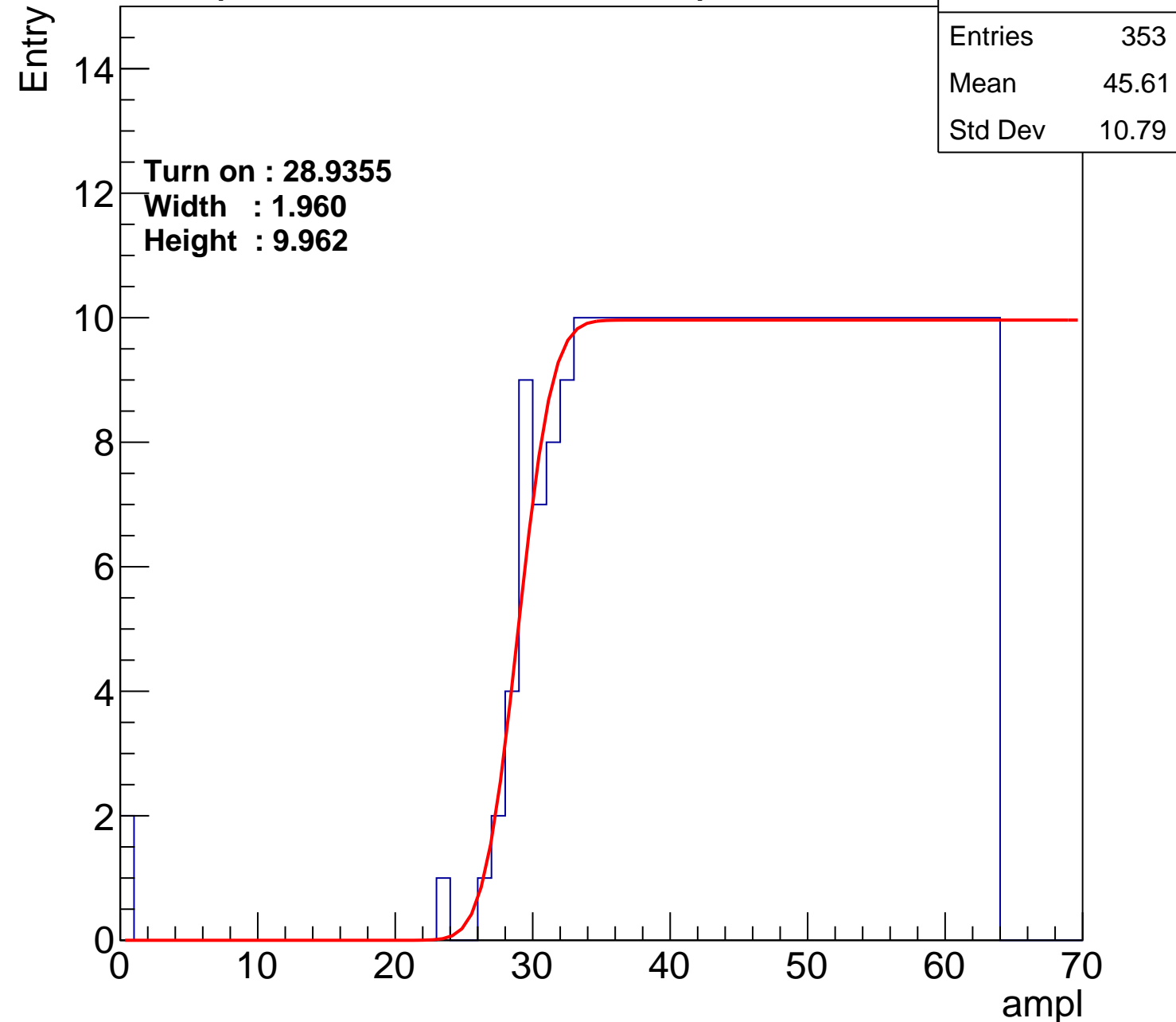
Width : 1.960

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.82
Std Dev	11.06

Turn on : 29.9606

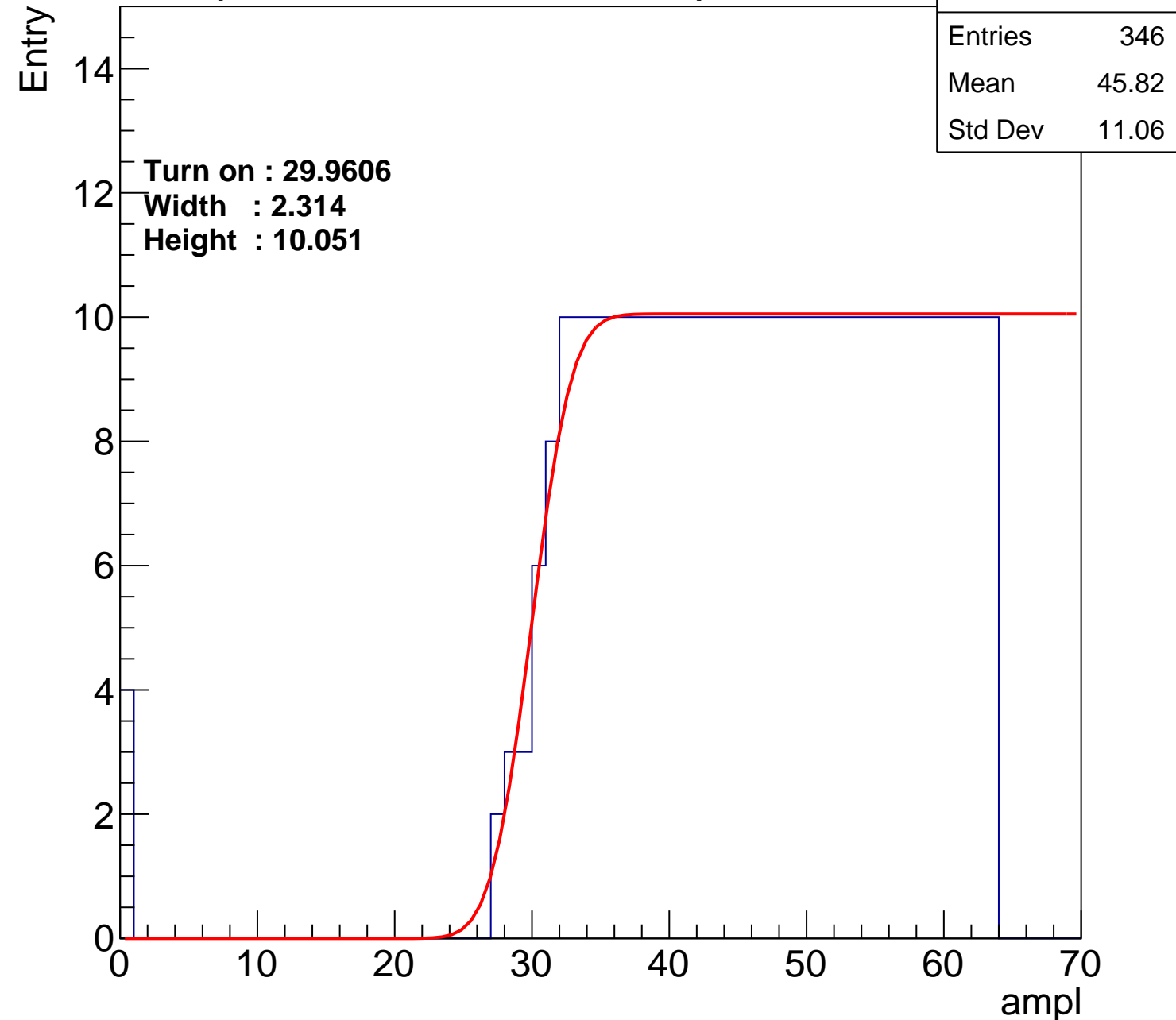
Width : 2.314

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.88
Std Dev	11.22

Turn on : 28.3673

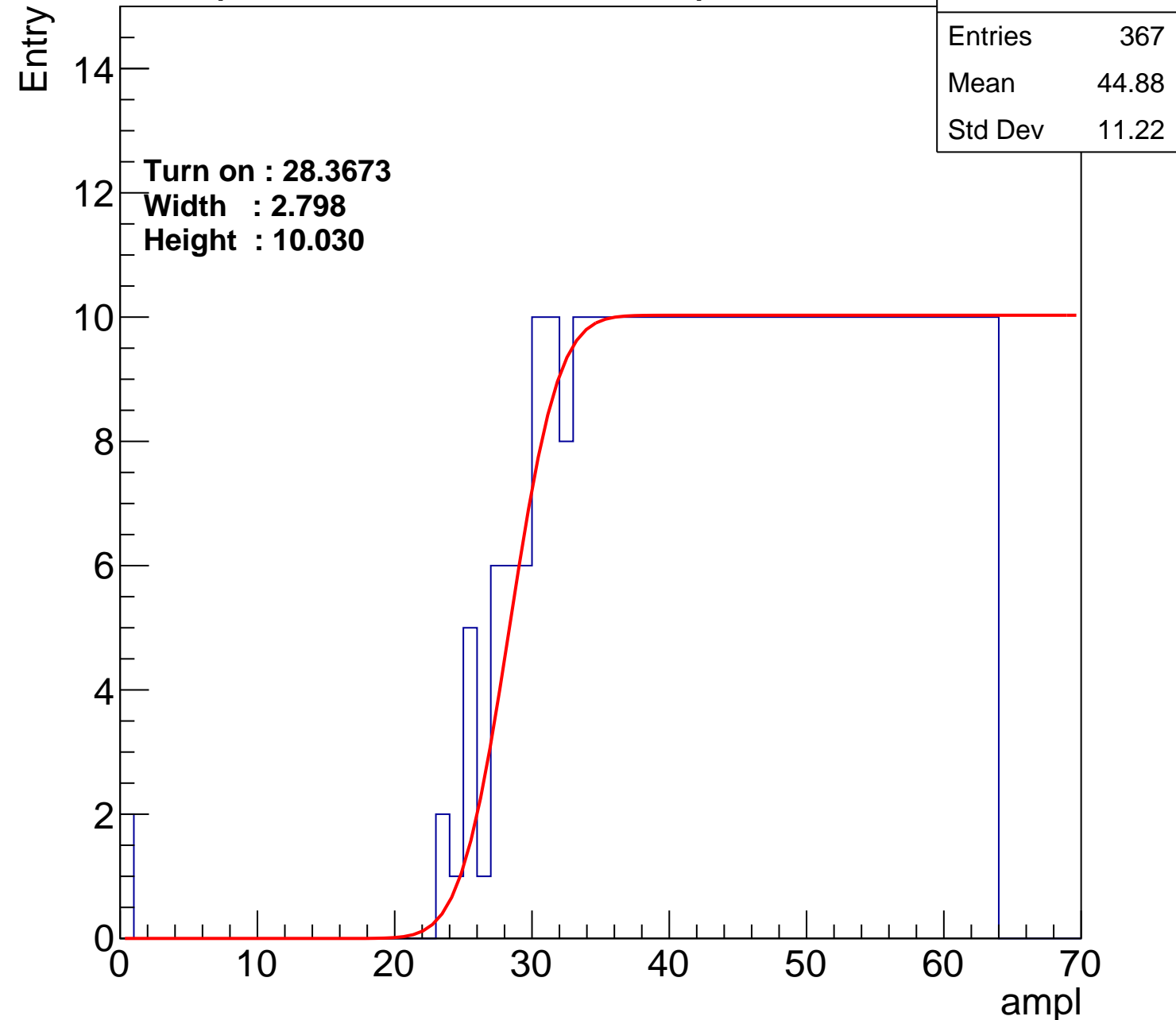
Width : 2.798

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.74
Std Dev	10.6

Turn on : 28.7885

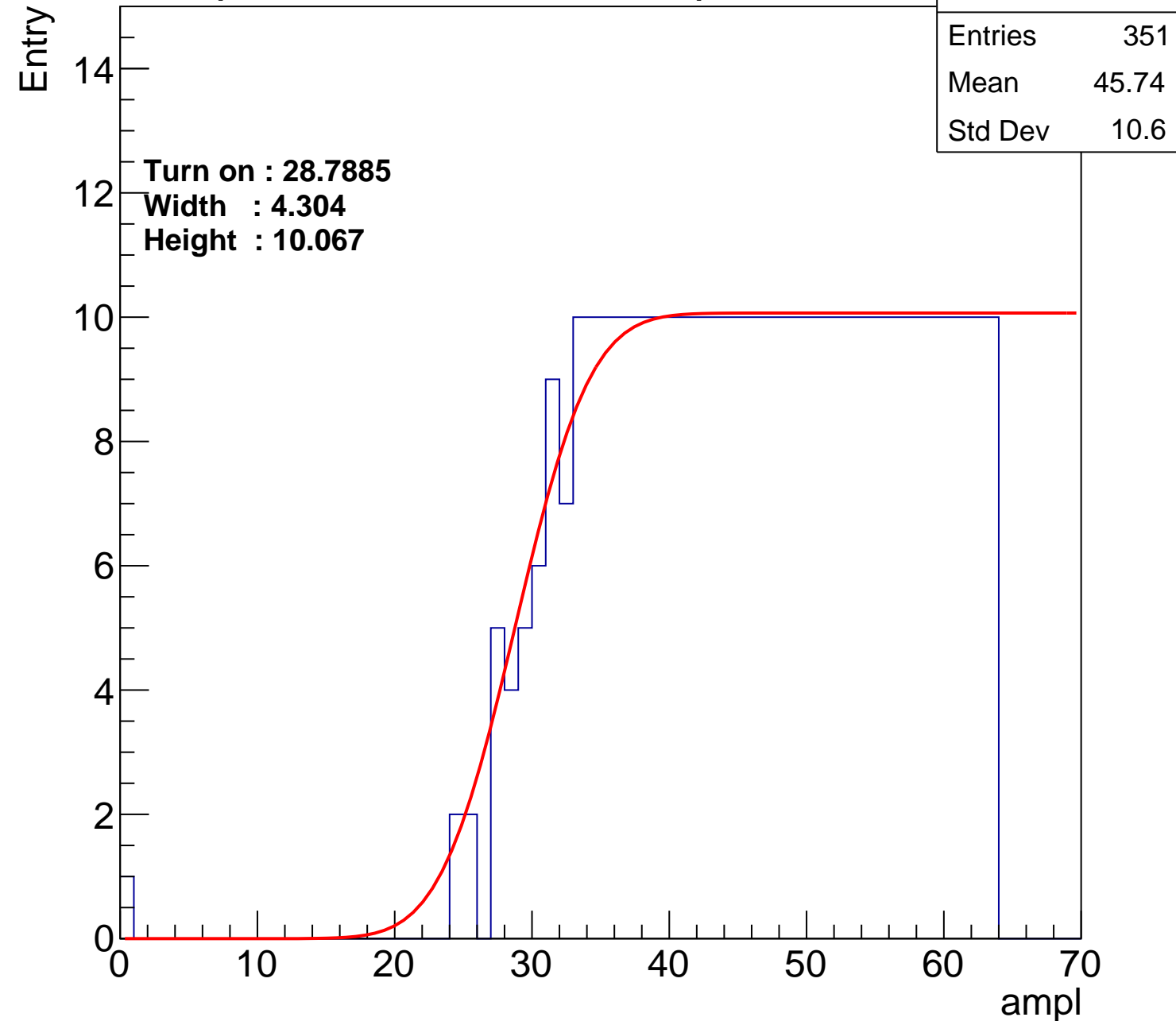
Width : 4.304

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch74

calib_packv5_042523_0143.root, FC#8, port C1

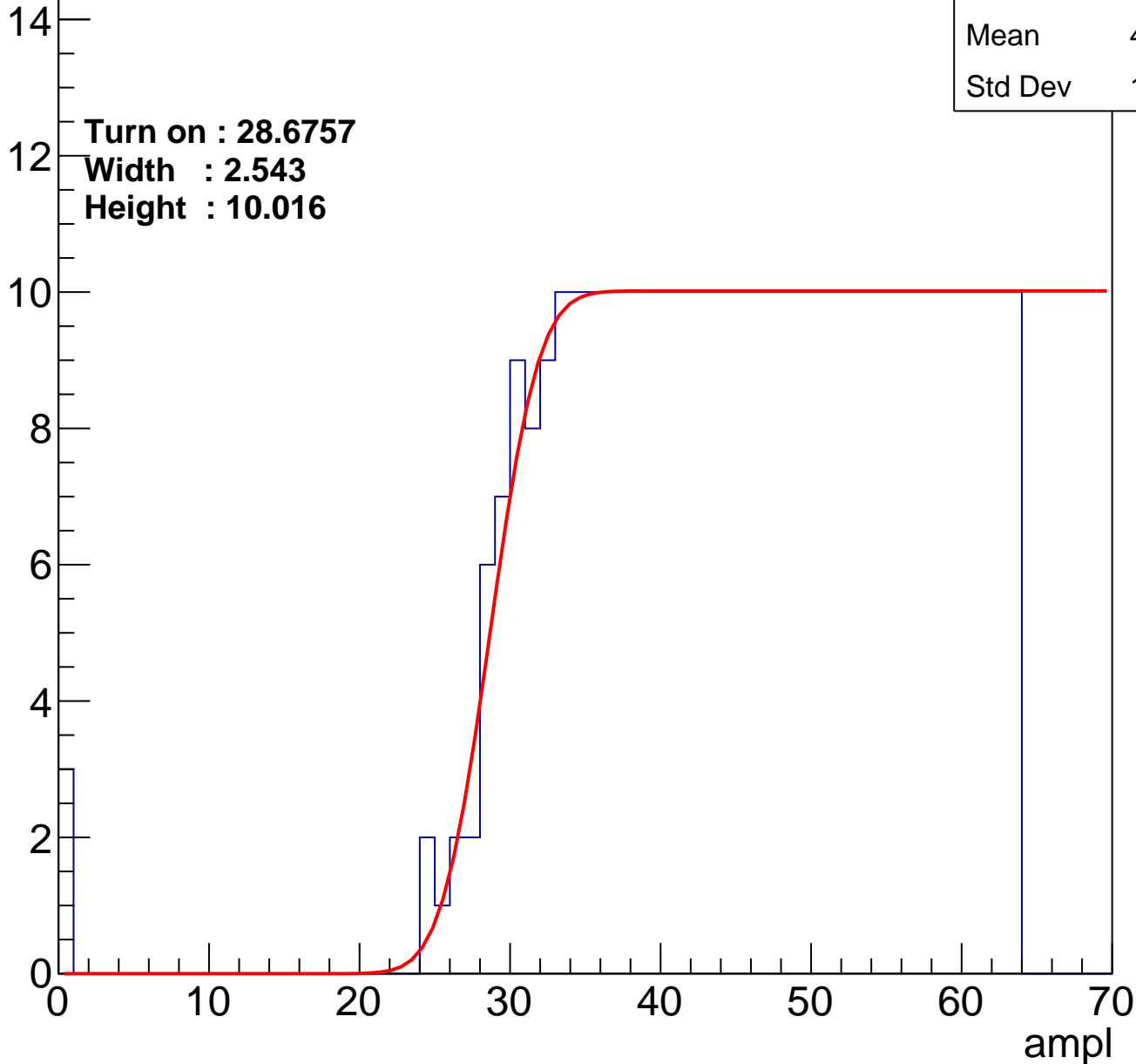
Entry

Entries	359
Mean	45.23
Std Dev	11.19

Turn on : 28.6757

Width : 2.543

Height : 10.016



B0L002S, U8-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.93
Std Dev	11.13

Turn on : 27.6791

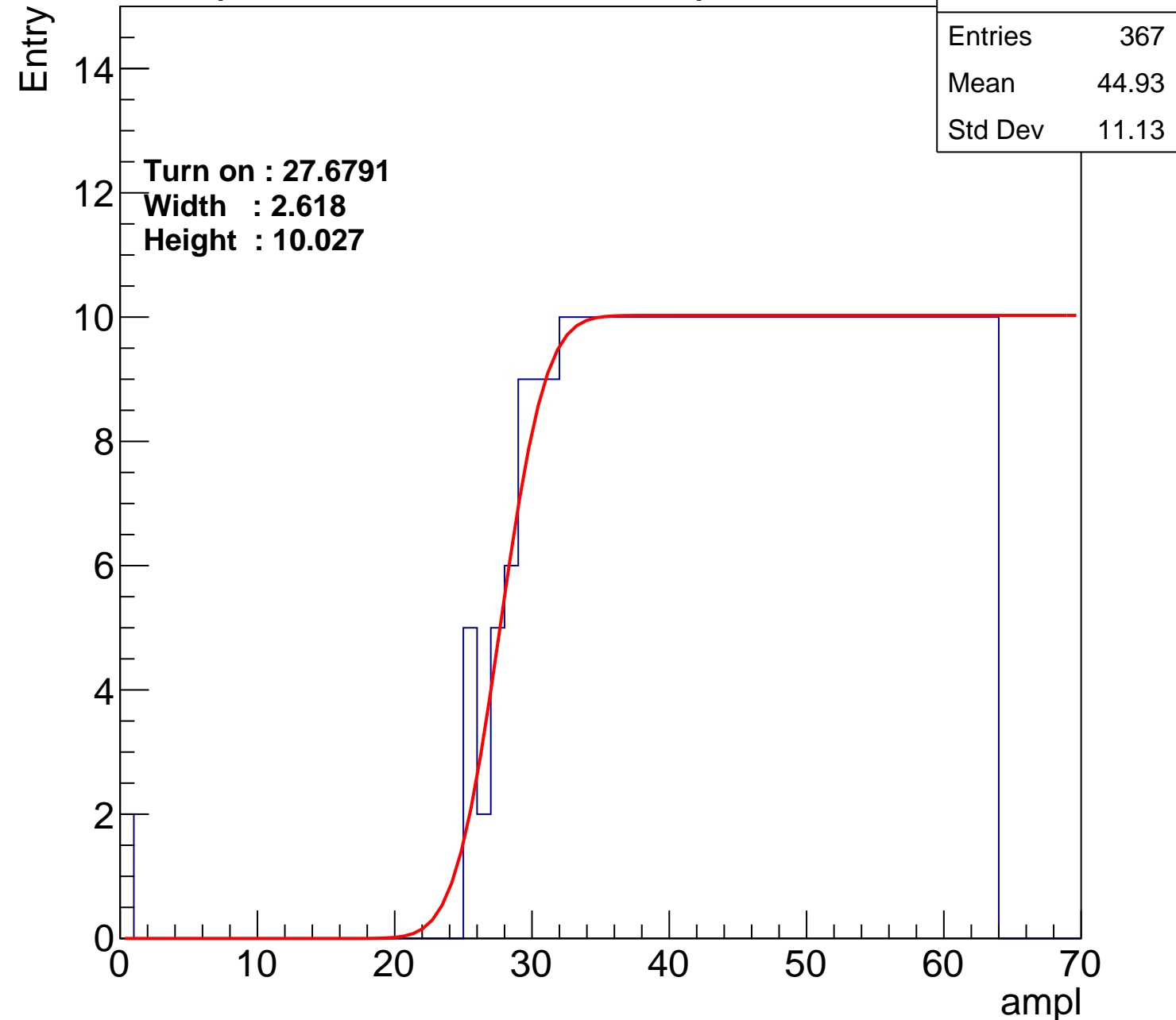
Width : 2.618

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.04
Std Dev	11.12

Turn on : 28.2907

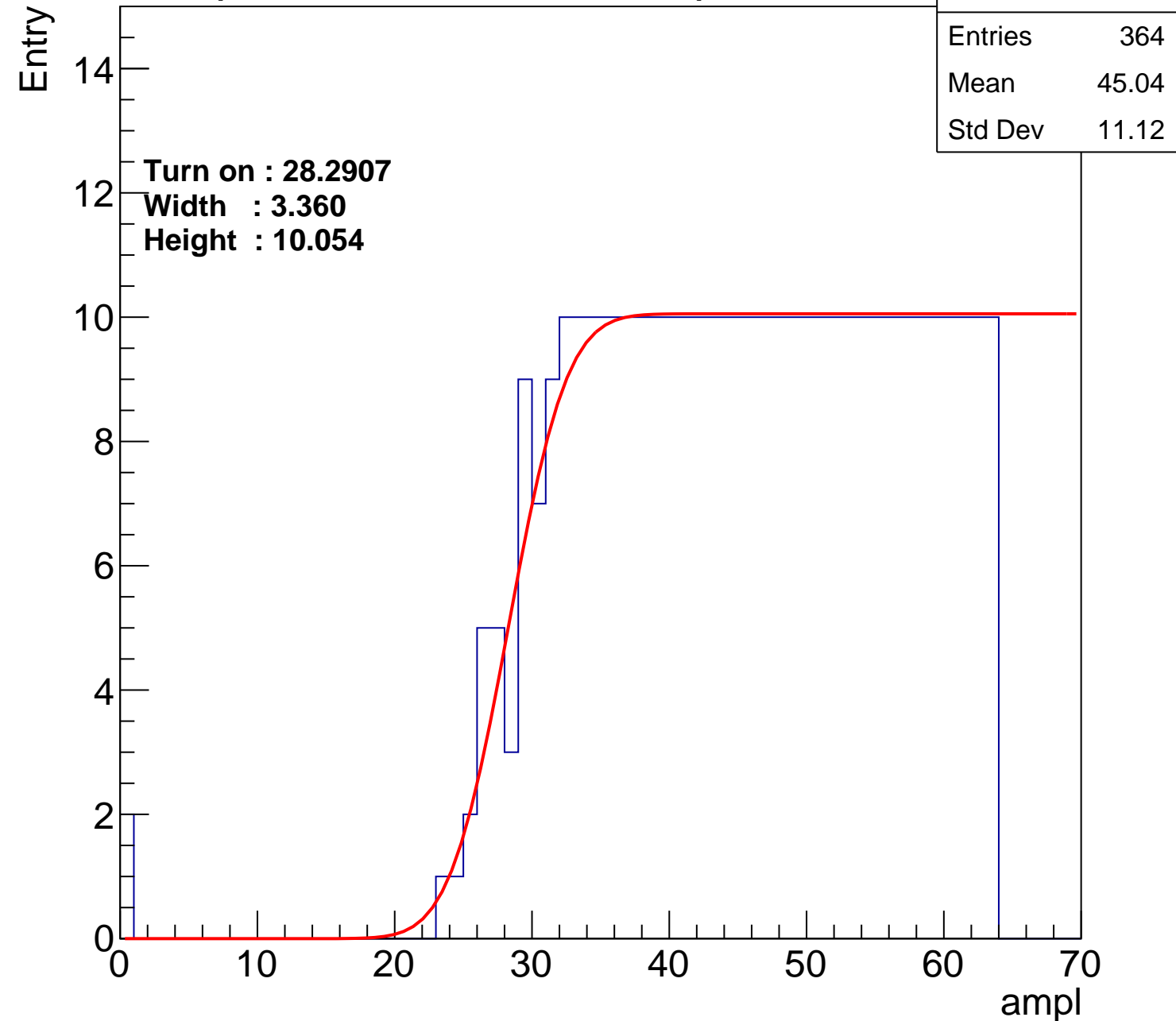
Width : 3.360

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.9444

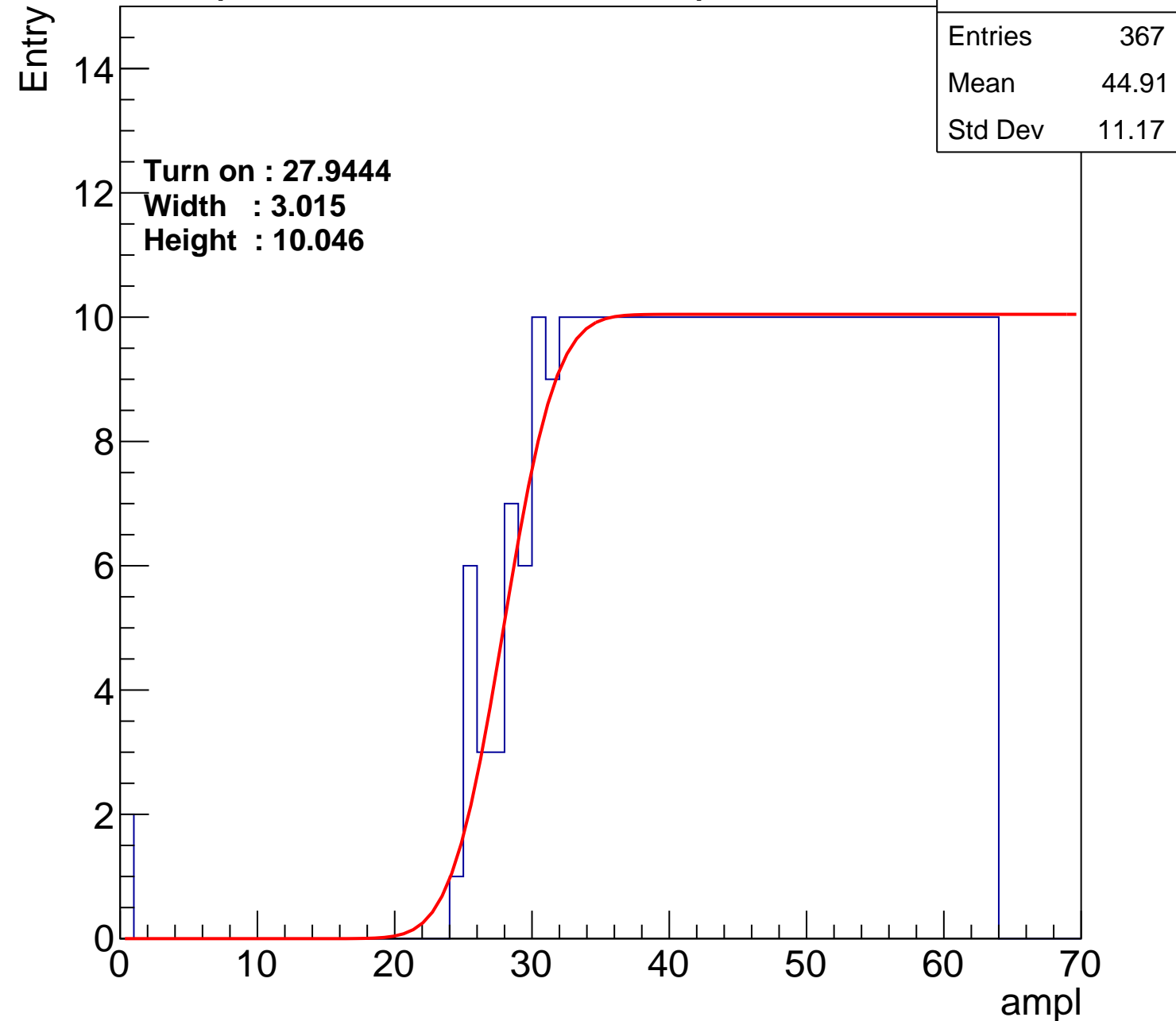
Width : 3.015

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch78

calib_packv5_042523_0143.root, FC#8, port C1

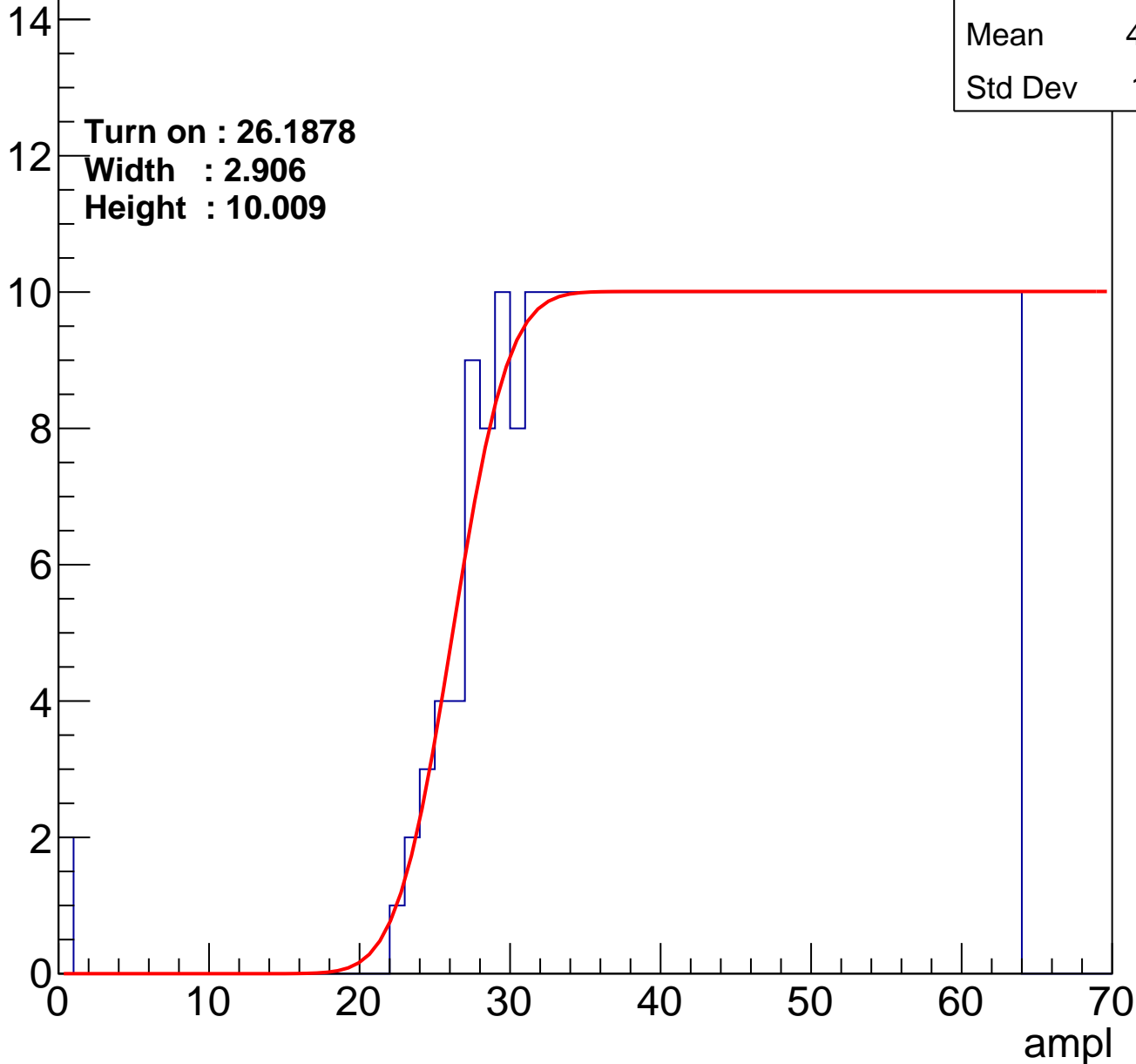
Entry

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.1878

Width : 2.906

Height : 10.009



B0L002S, U8-ch79

calib_packv5_042523_0143.root, FC#8, port C1

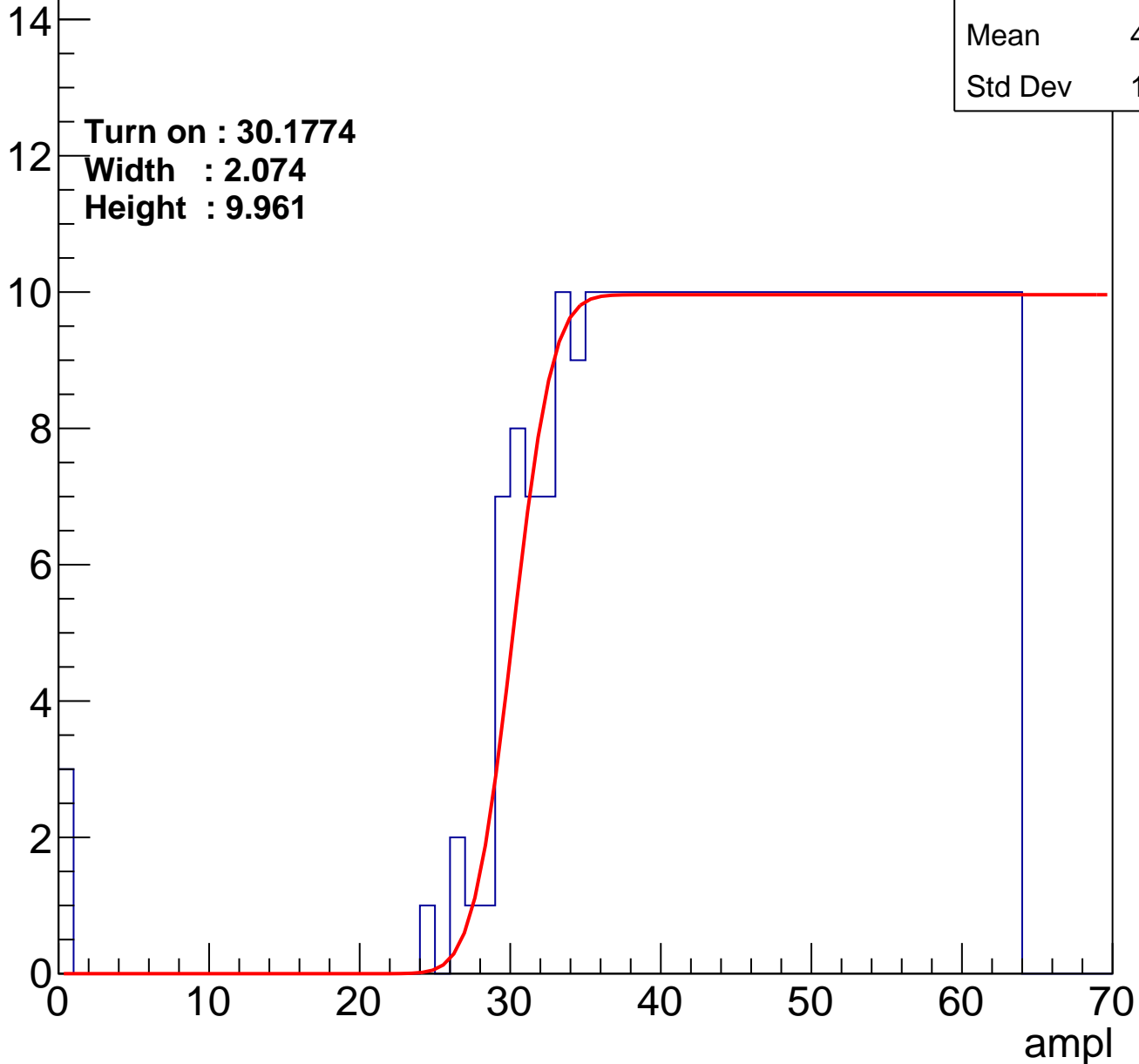
Entries	346
Mean	45.84
Std Dev	10.92

Turn on : 30.1774

Width : 2.074

Height : 9.961

Entry



B0L002S, U8-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.91
Std Dev	11.7

Turn on : 25.5878

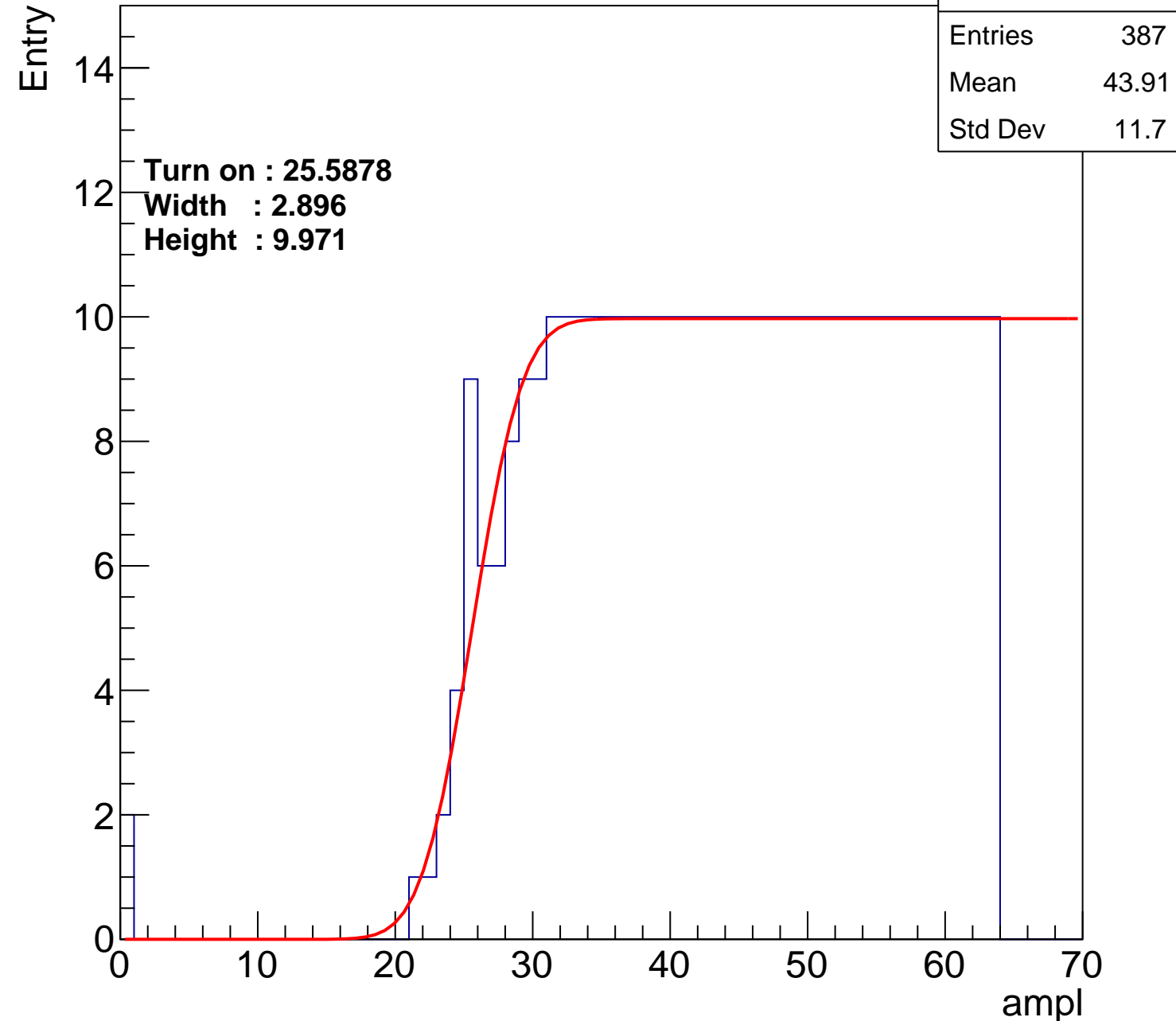
Width : 2.896

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch81

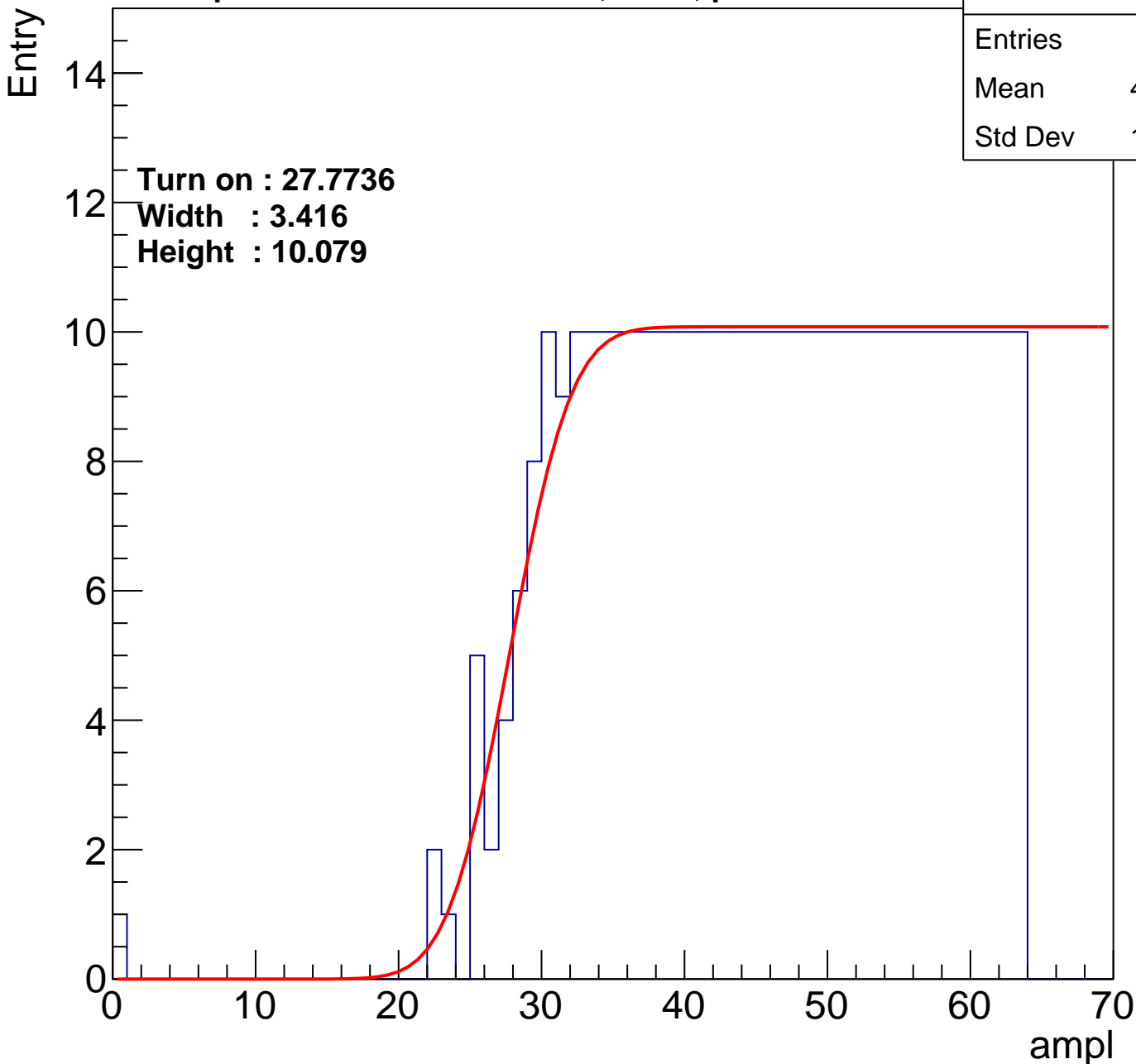
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.7736

Width : 3.416

Height : 10.079



B0L002S, U8-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.36
Std Dev	10.97

Turn on : 28.7795

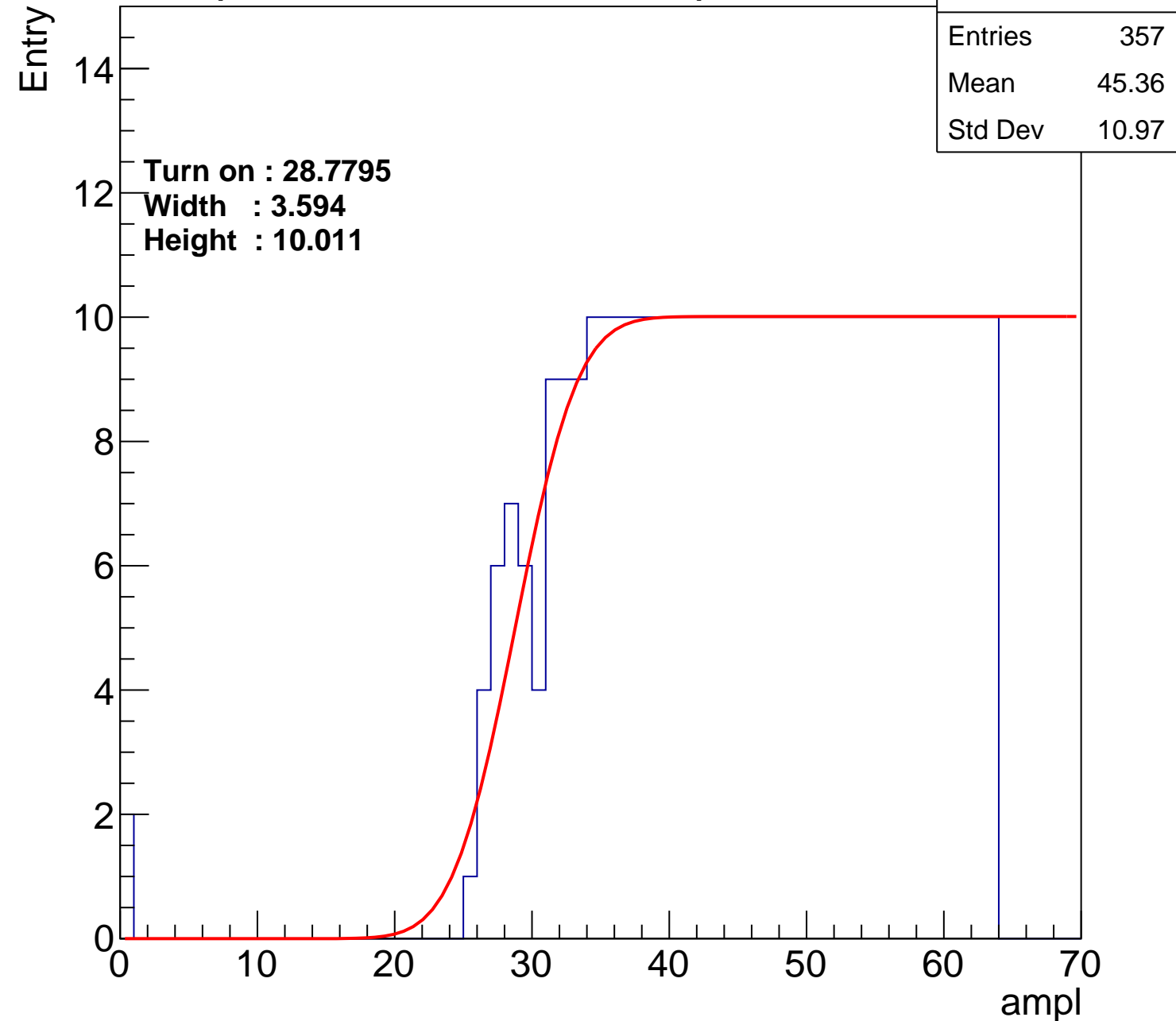
Width : 3.594

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 28.1107

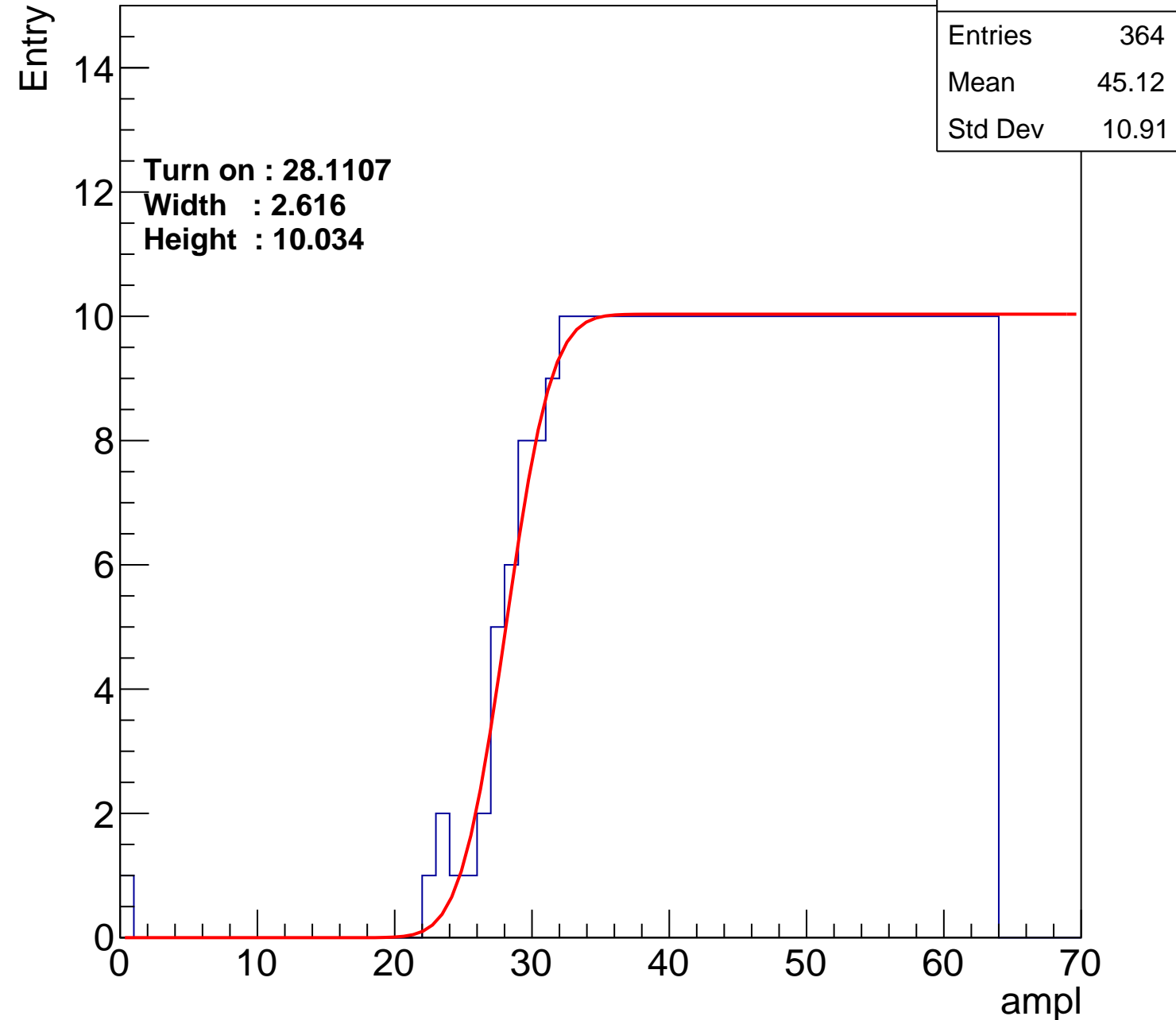
Width : 2.616

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.96
Std Dev	10.96

Turn on : 27.3651

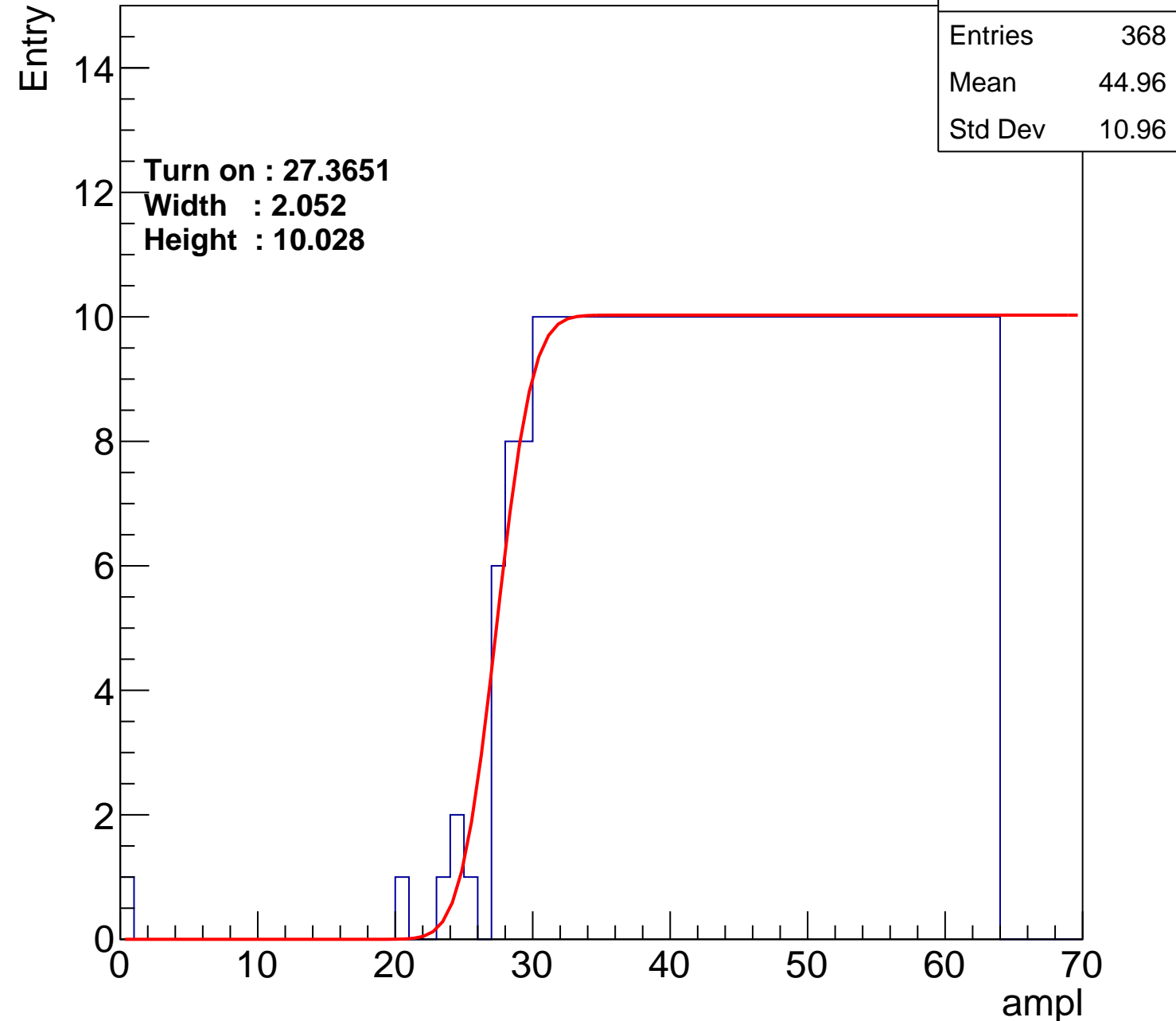
Width : 2.052

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.7840

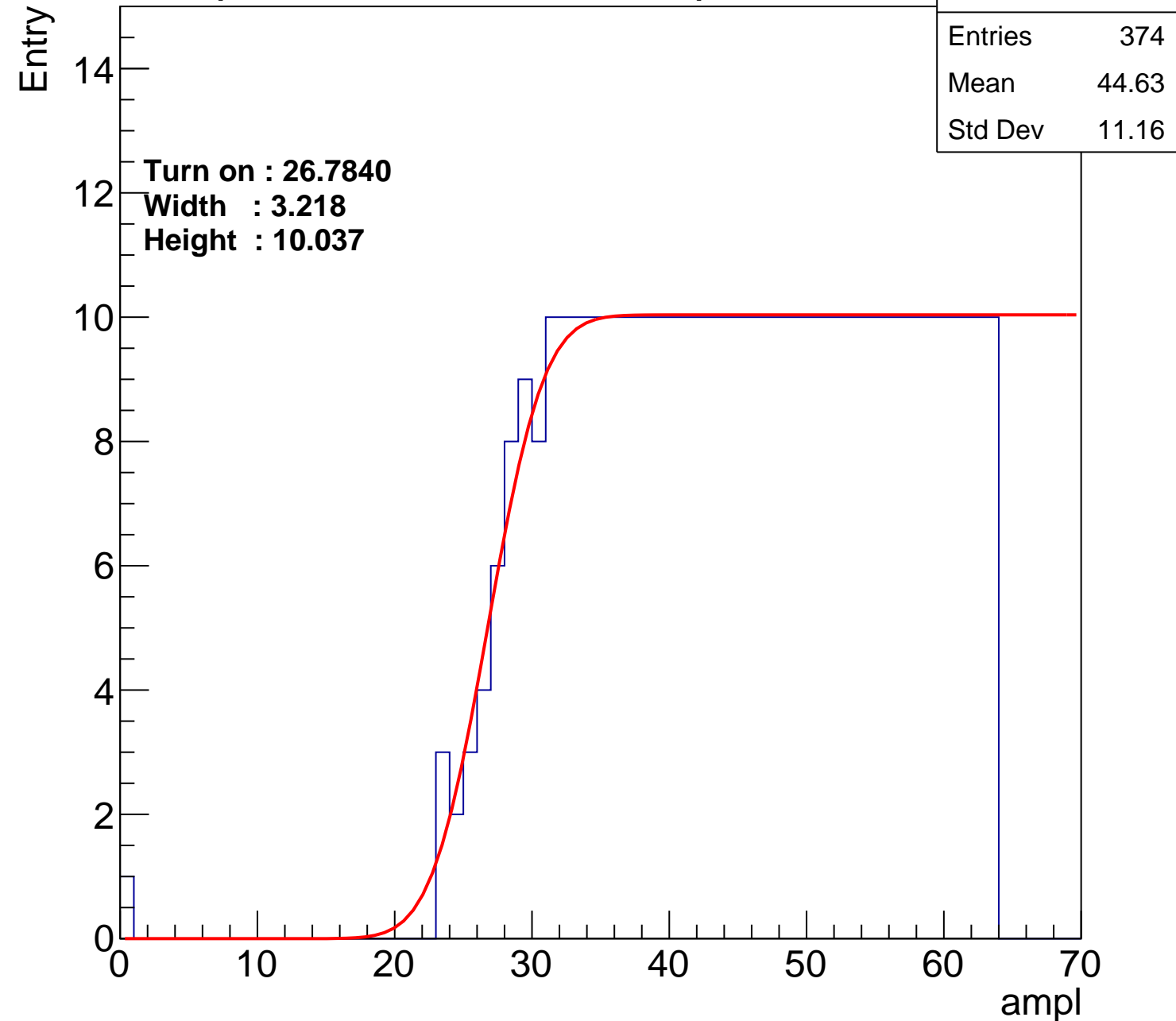
Width : 3.218

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch86

calib_packv5_042523_0143.root, FC#8, port C1

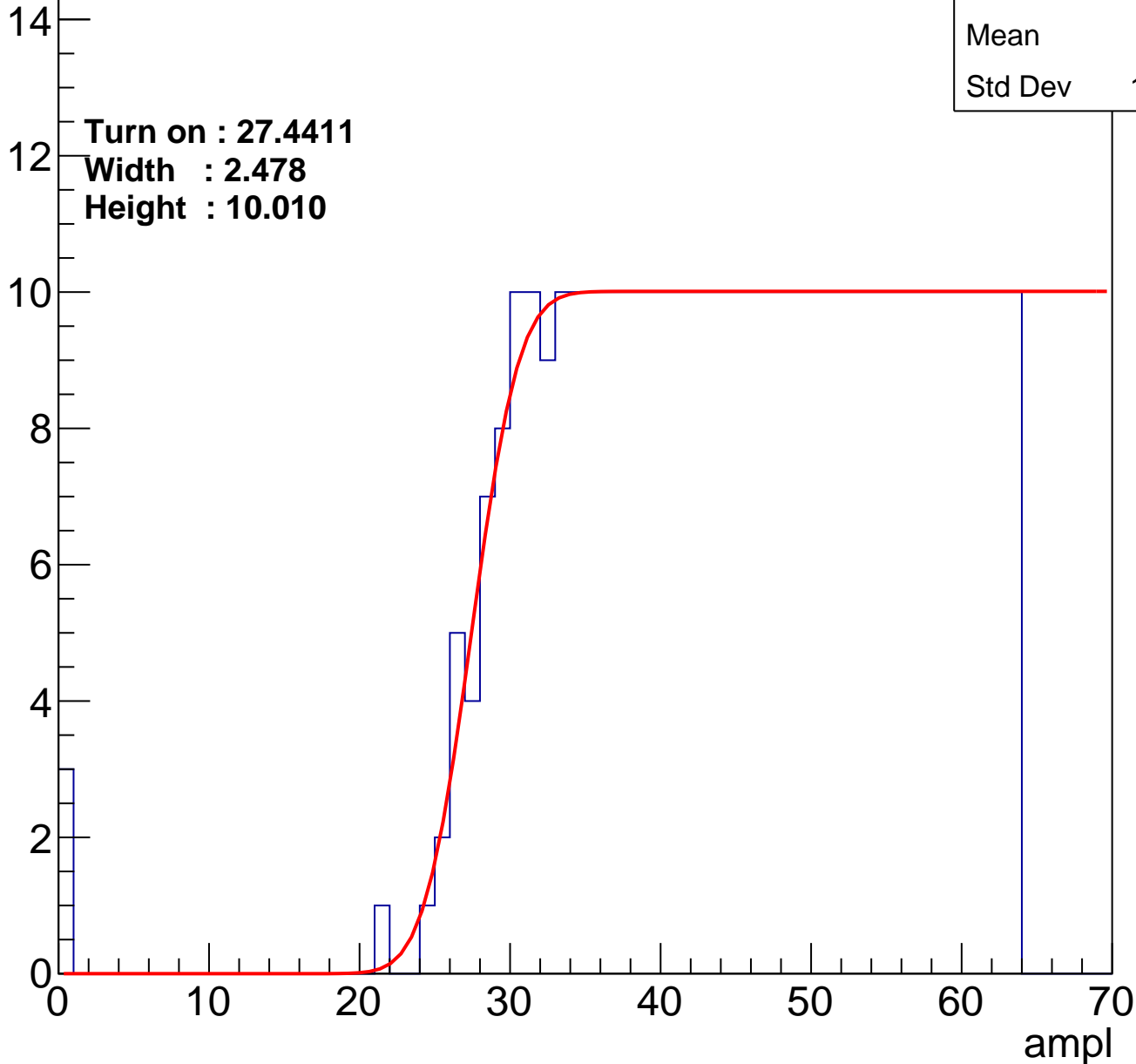
Entry

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.4411

Width : 2.478

Height : 10.010



B0L002S, U8-ch87

calib_packv5_042523_0143.root, FC#8, port C1

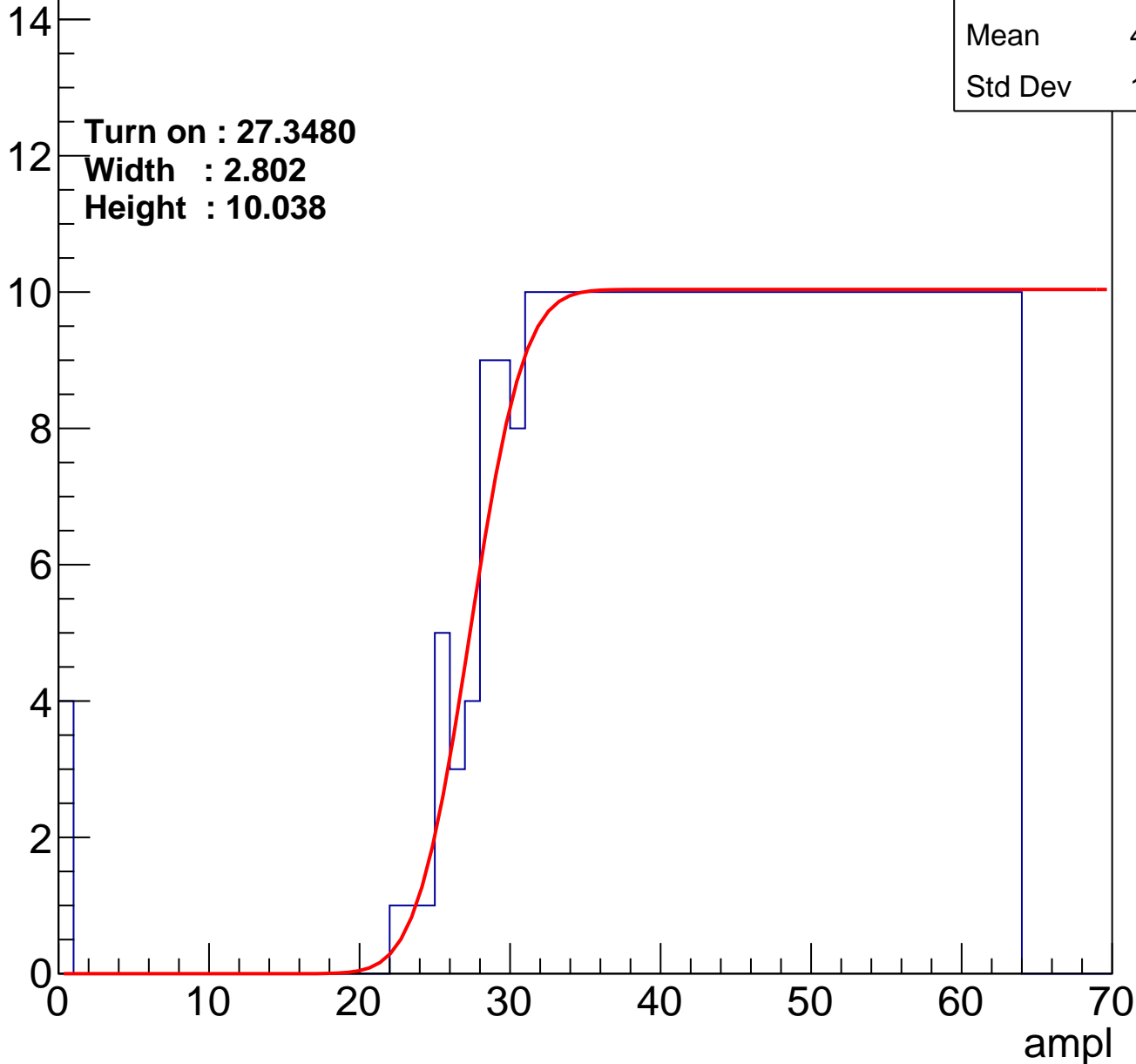
Entry

Entries	375
Mean	44.38
Std Dev	11.75

Turn on : 27.3480

Width : 2.802

Height : 10.038



B0L002S, U8-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 26.5013

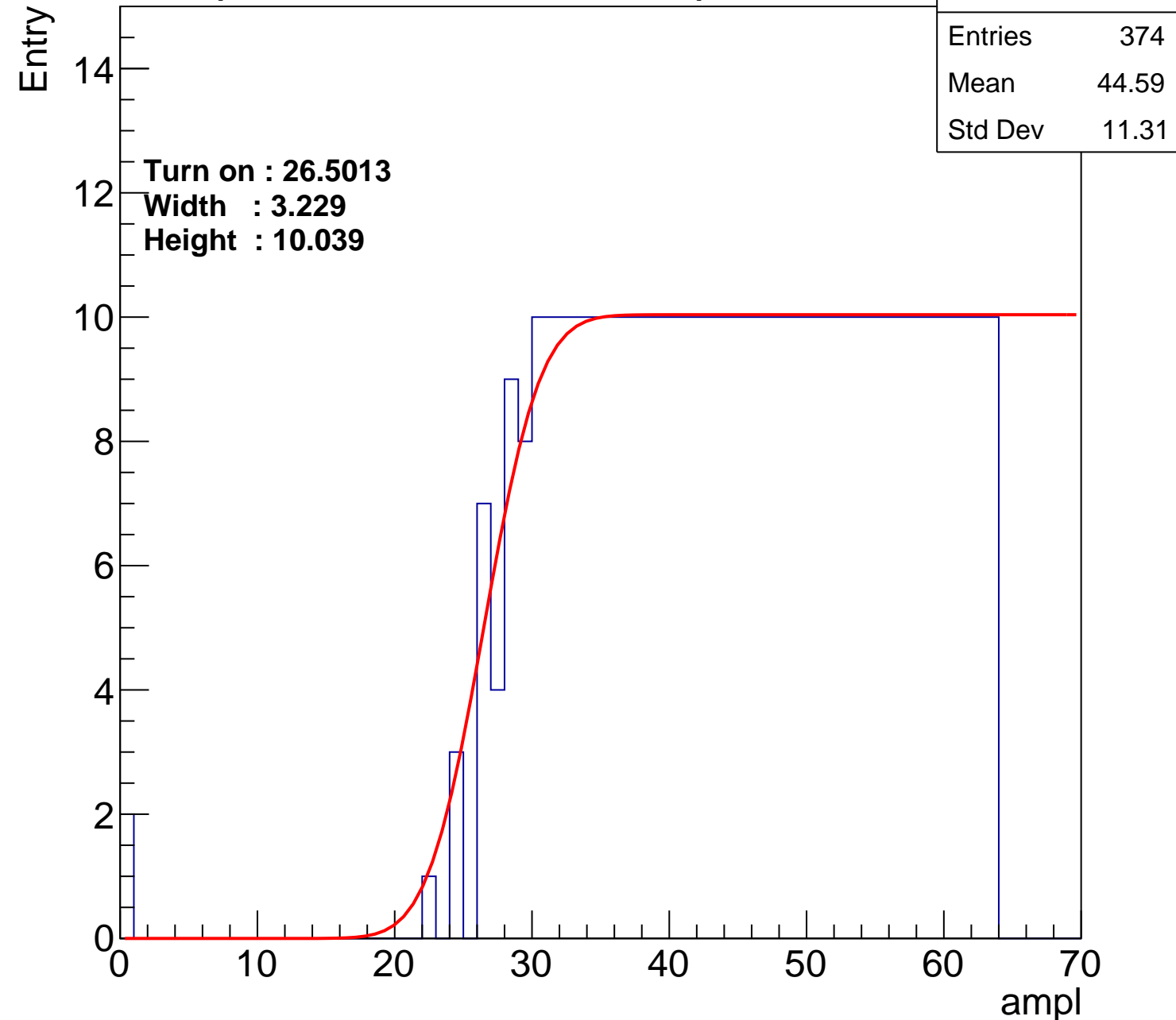
Width : 3.229

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.89
Std Dev	10.45

Turn on : 29.3687

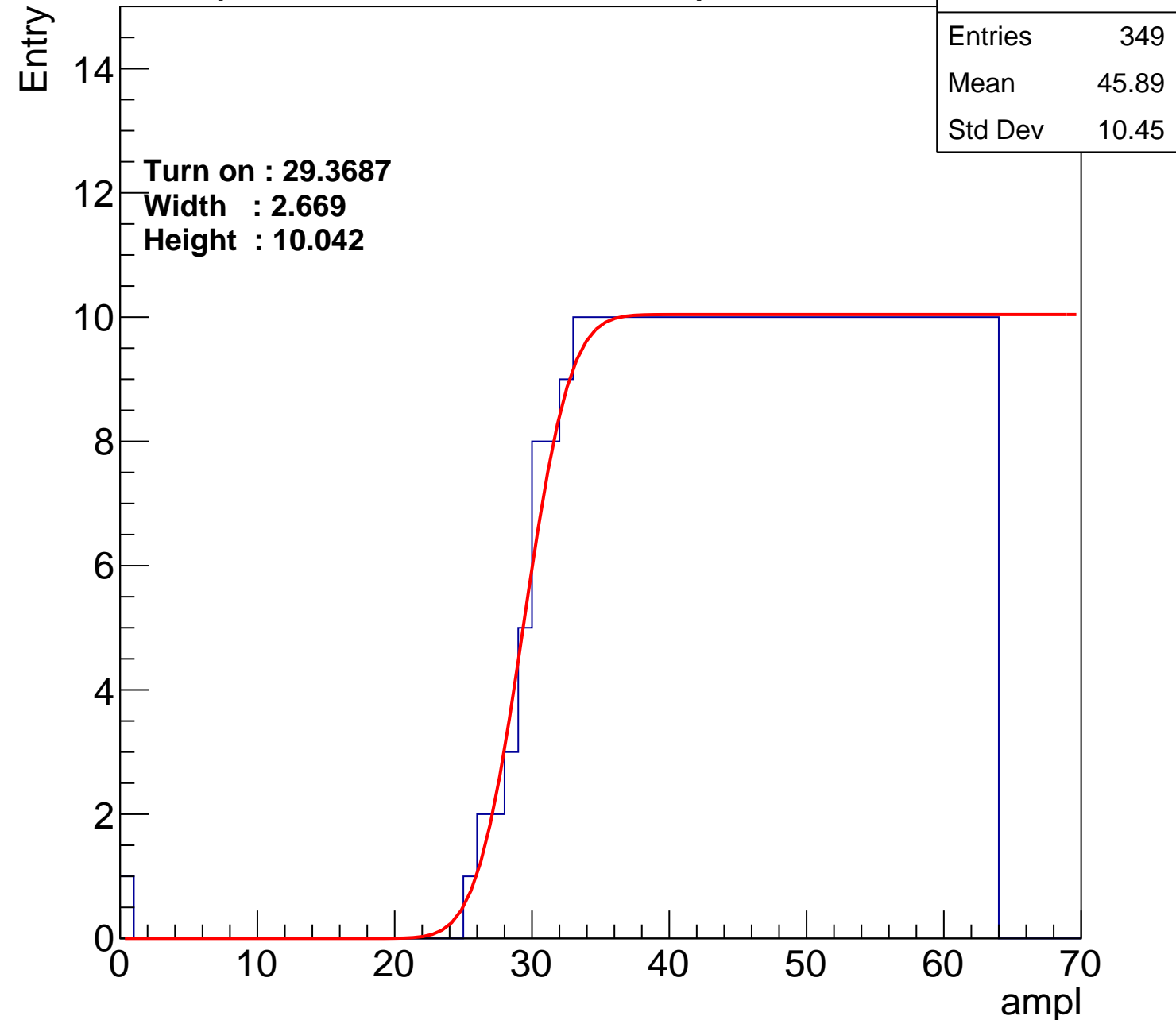
Width : 2.669

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch90

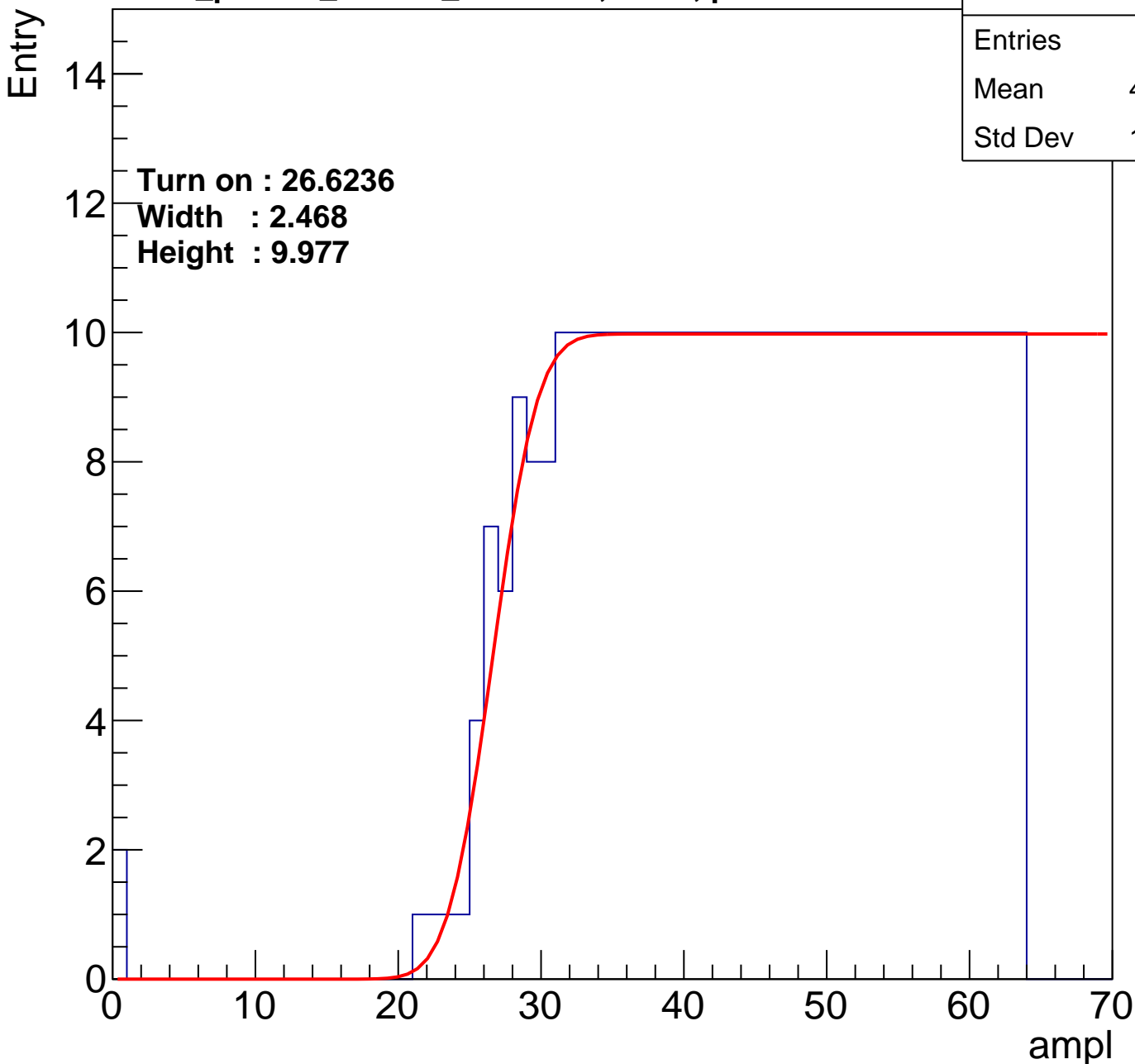
calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.6236

Width : 2.468

Height : 9.977



B0L002S, U8-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.24
Std Dev	11.03

Turn on : 28.7473

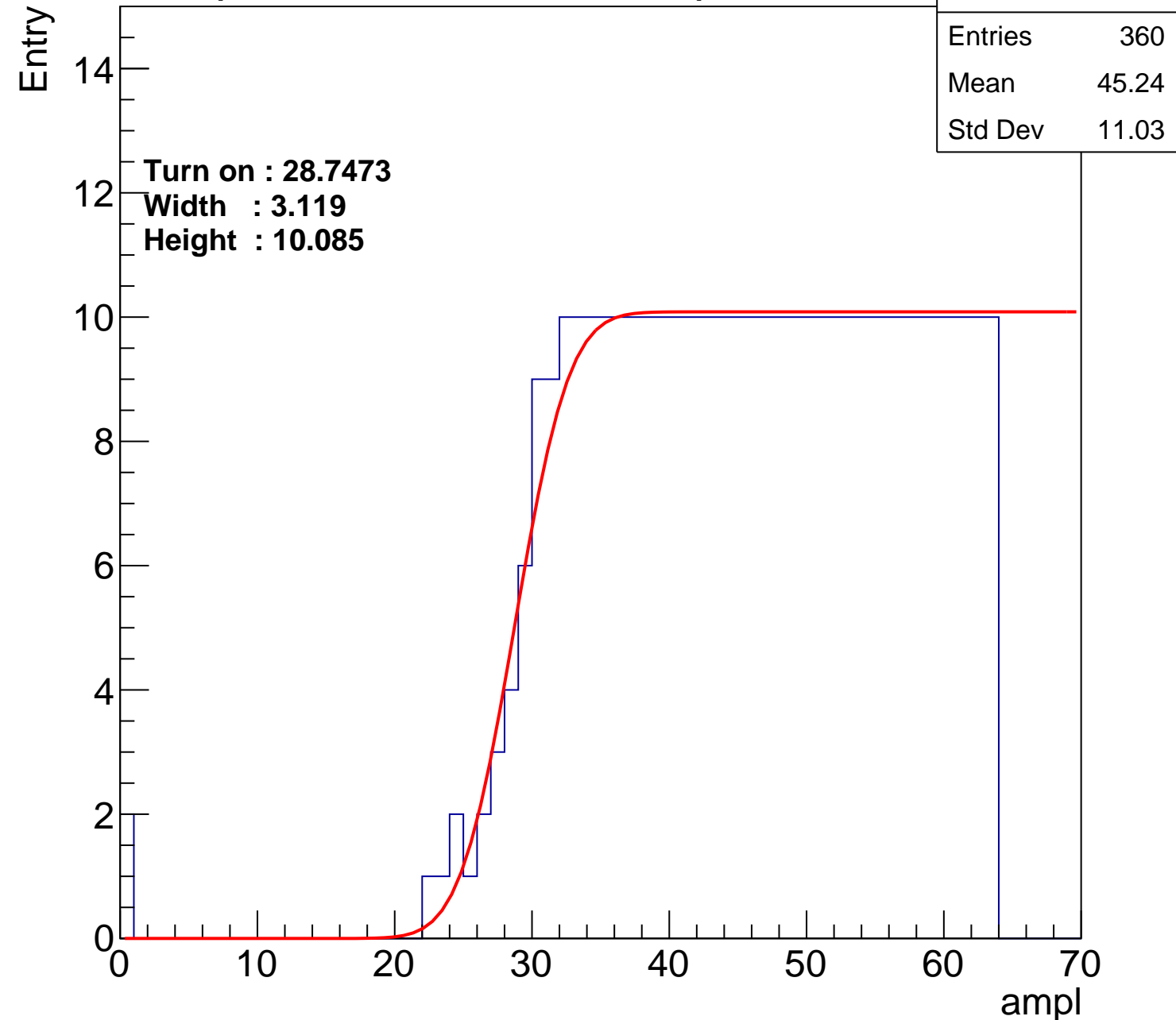
Width : 3.119

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.5
Std Dev	11.26

Turn on : 27.0210

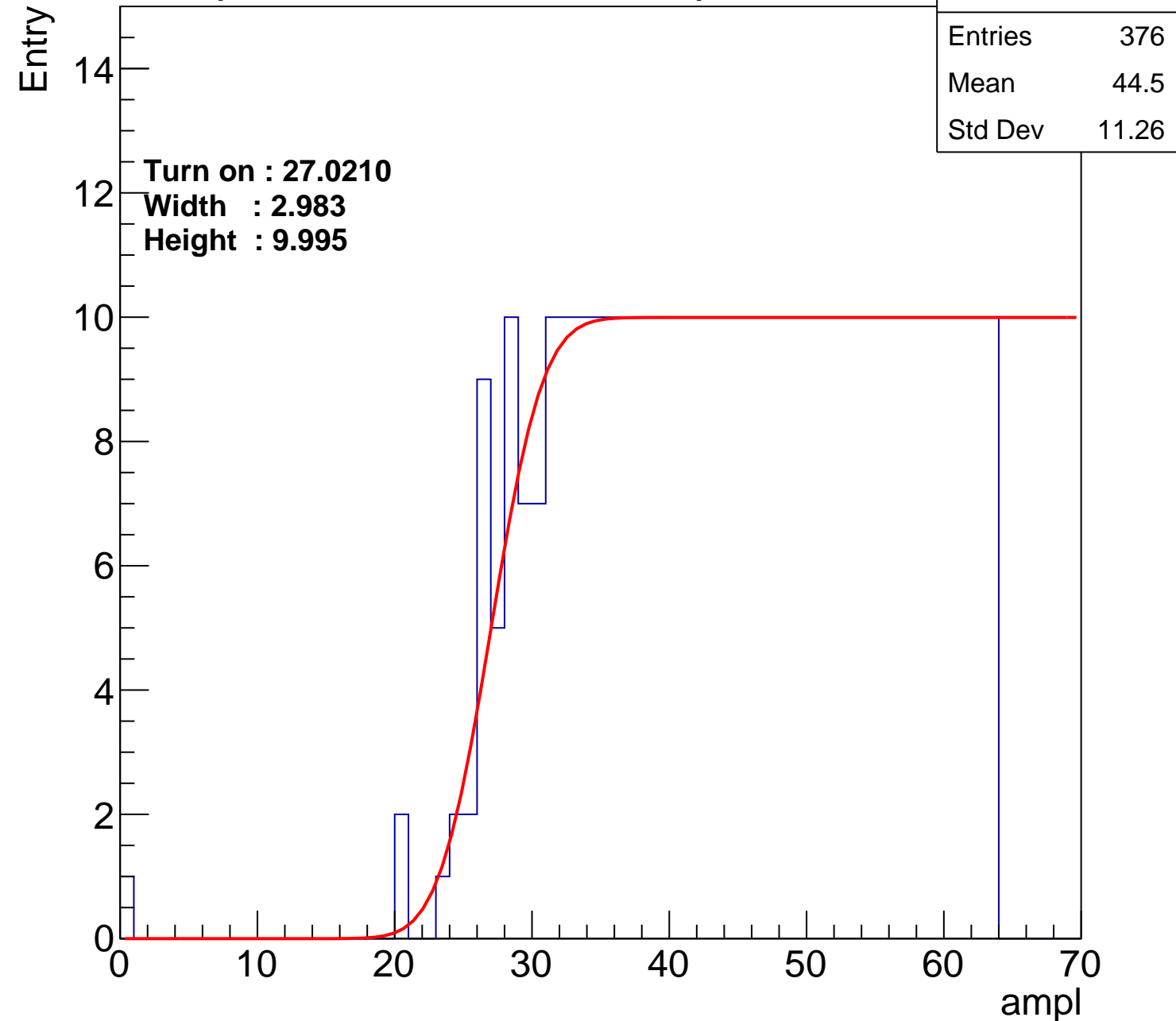
Width : 2.983

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.76
Std Dev	10.56

Turn on : 29.2114

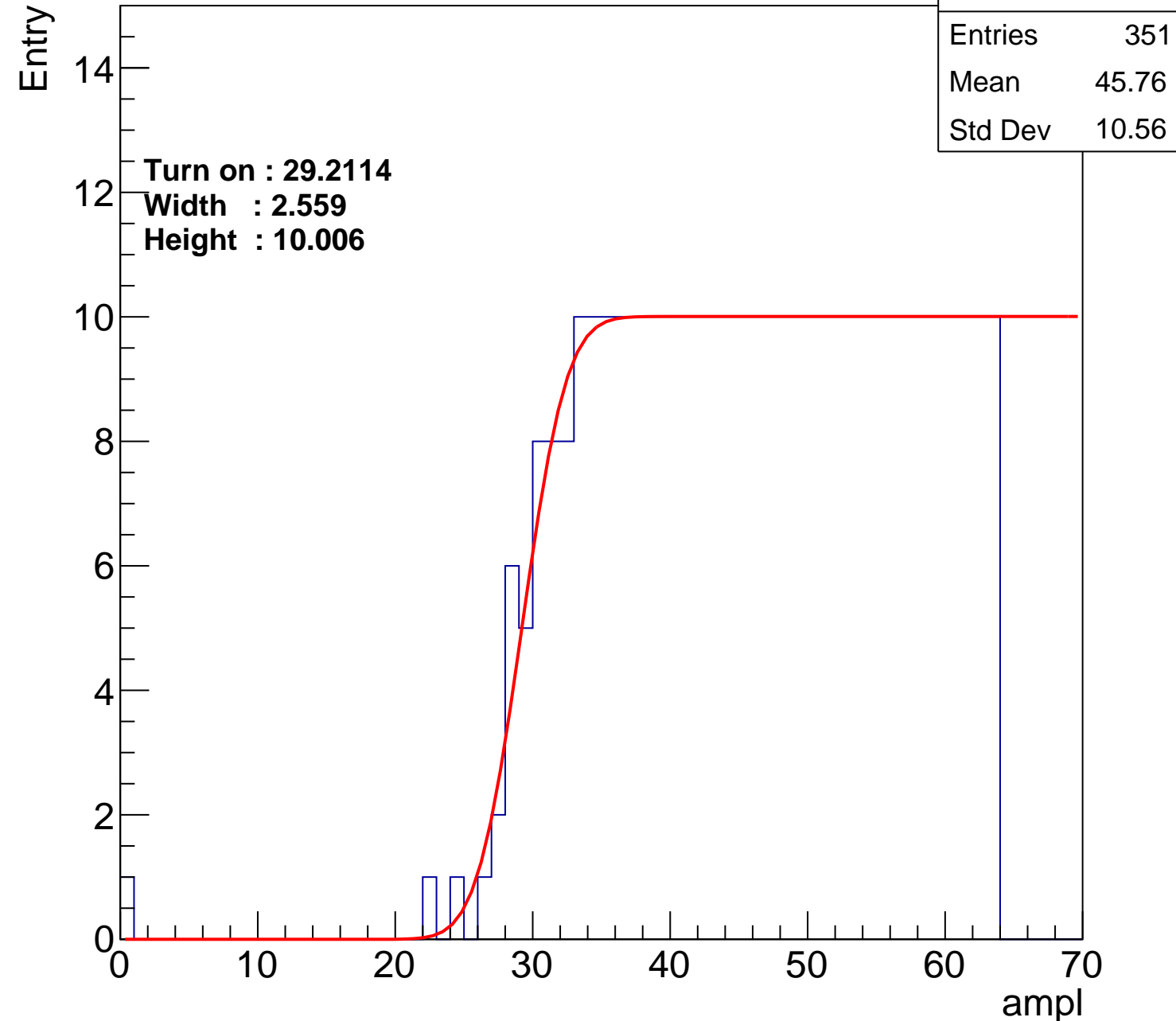
Width : 2.559

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 25.6117

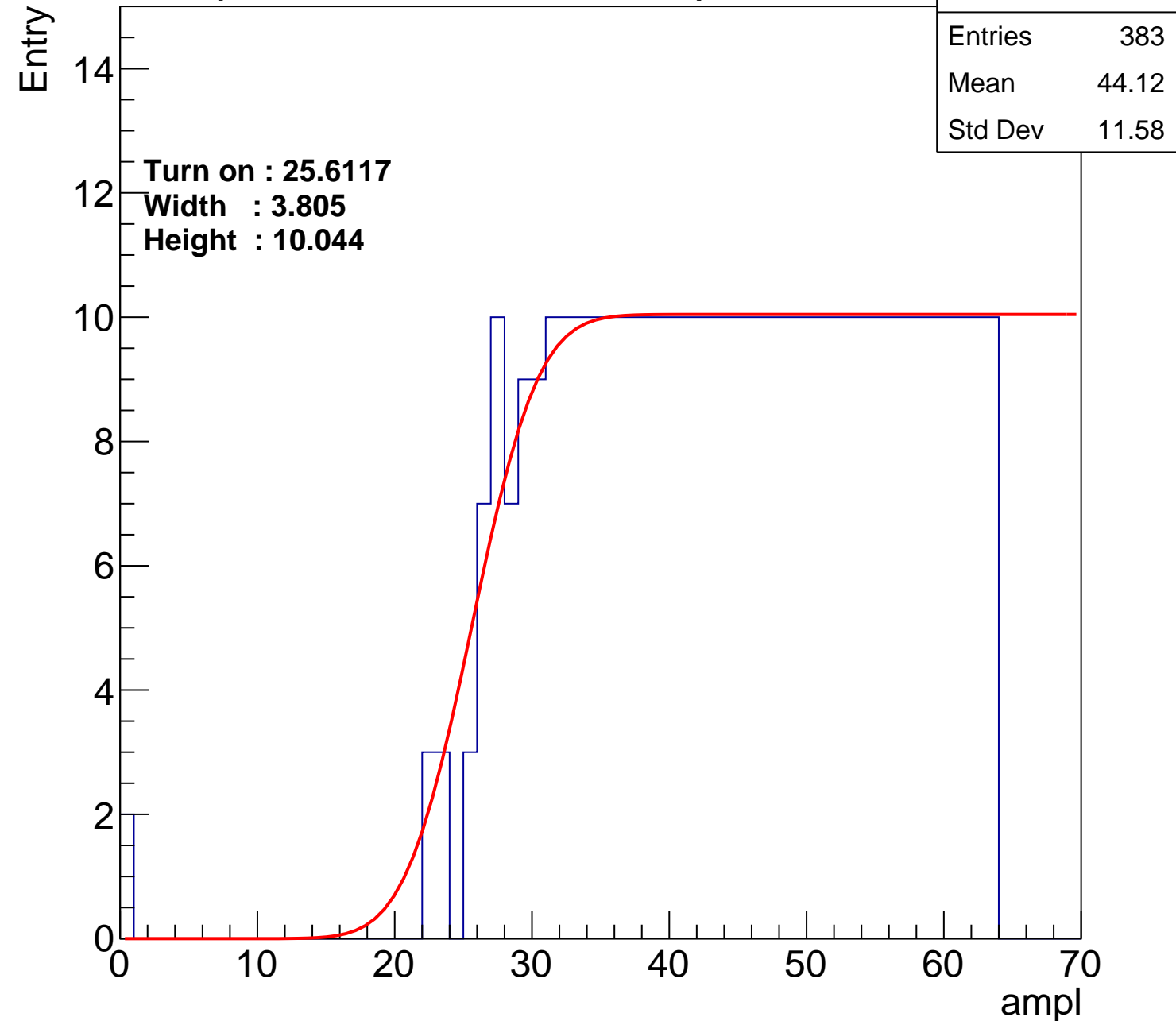
Width : 3.805

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.0926

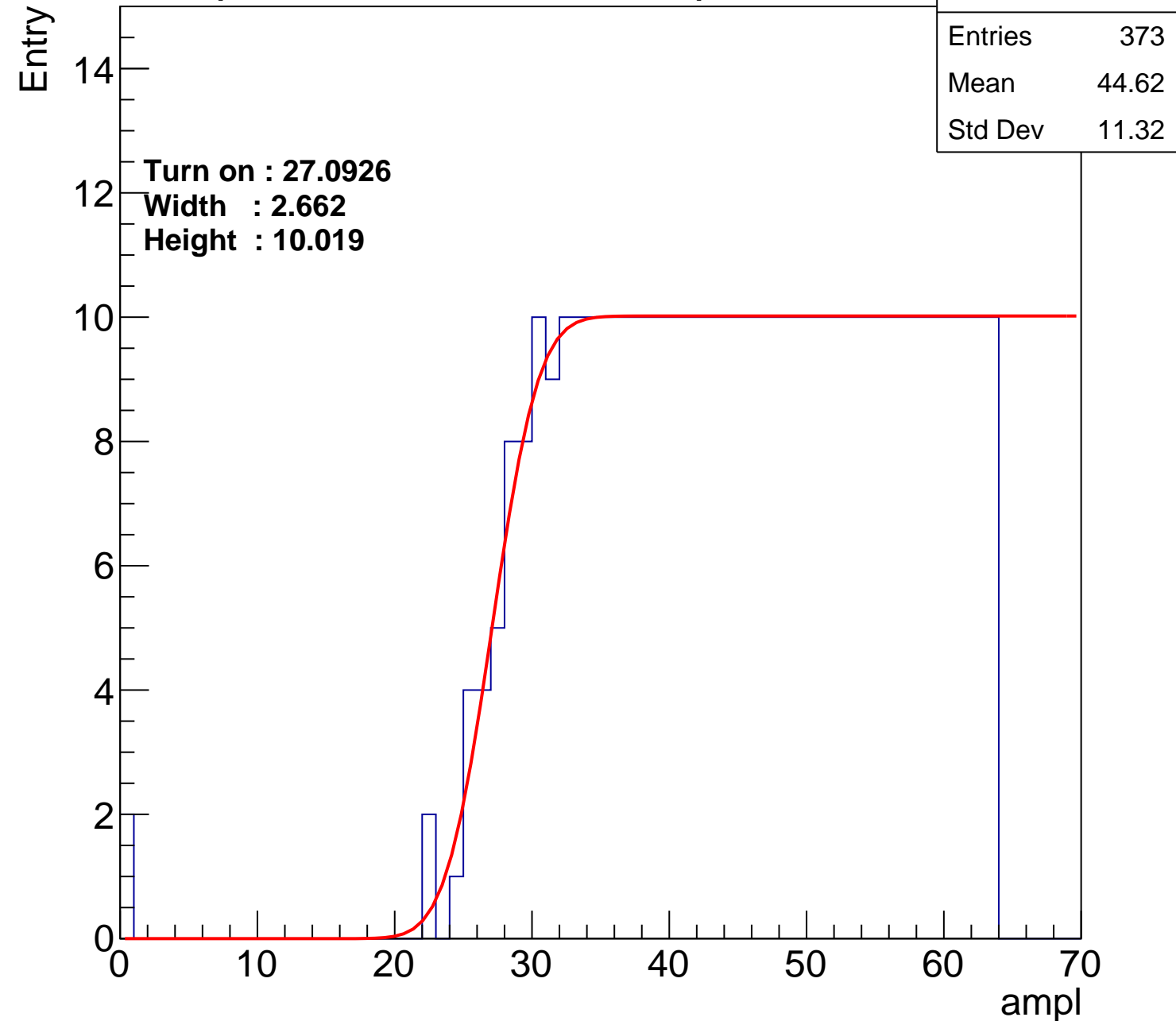
Width : 2.662

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch96

calib_packv5_042523_0143.root, FC#8, port C1

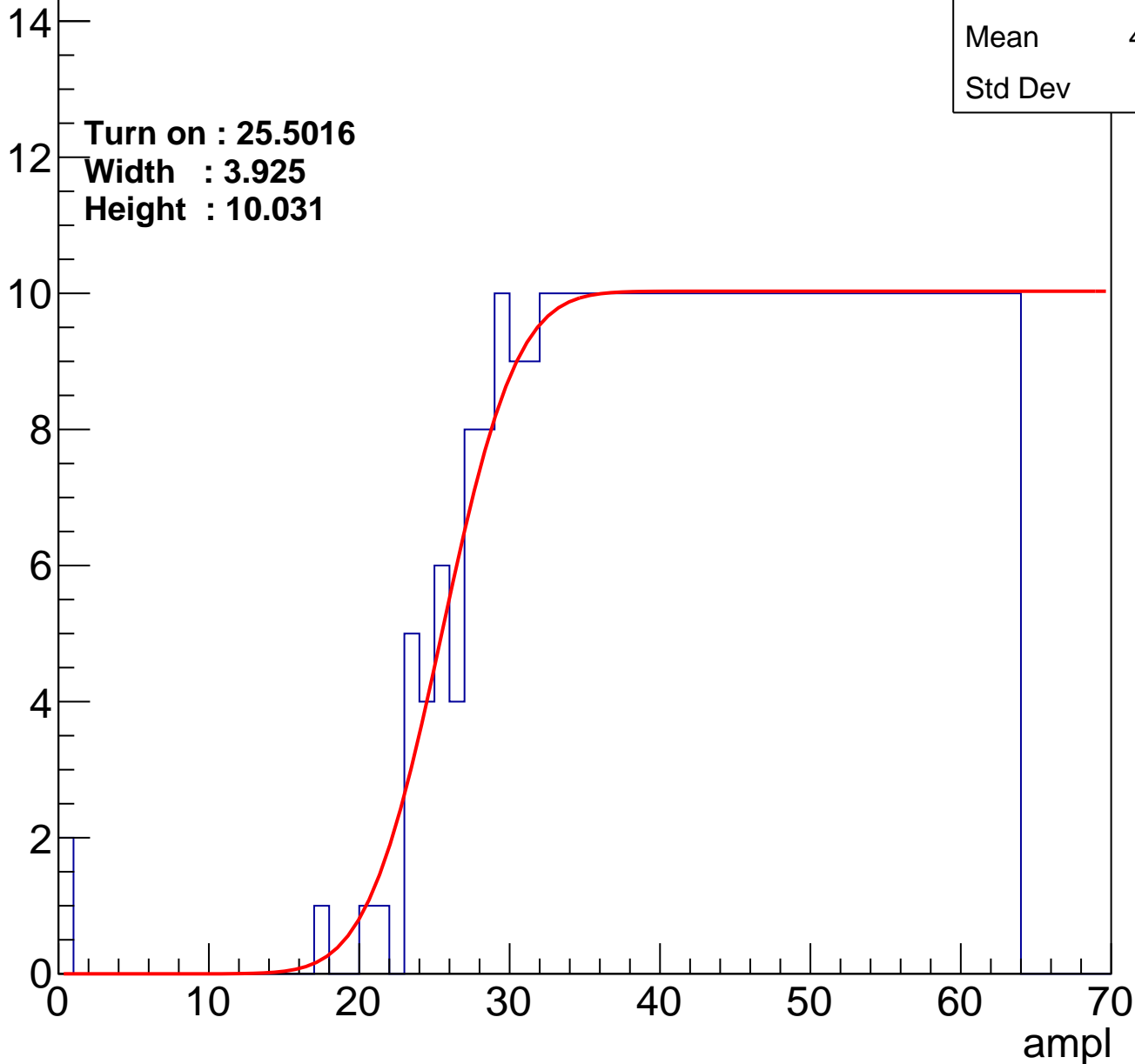
Entries	388
Mean	43.82
Std Dev	11.8

Turn on : 25.5016

Width : 3.925

Height : 10.031

Entry



B0L002S, U8-ch97

calib_packv5_042523_0143.root, FC#8, port C1

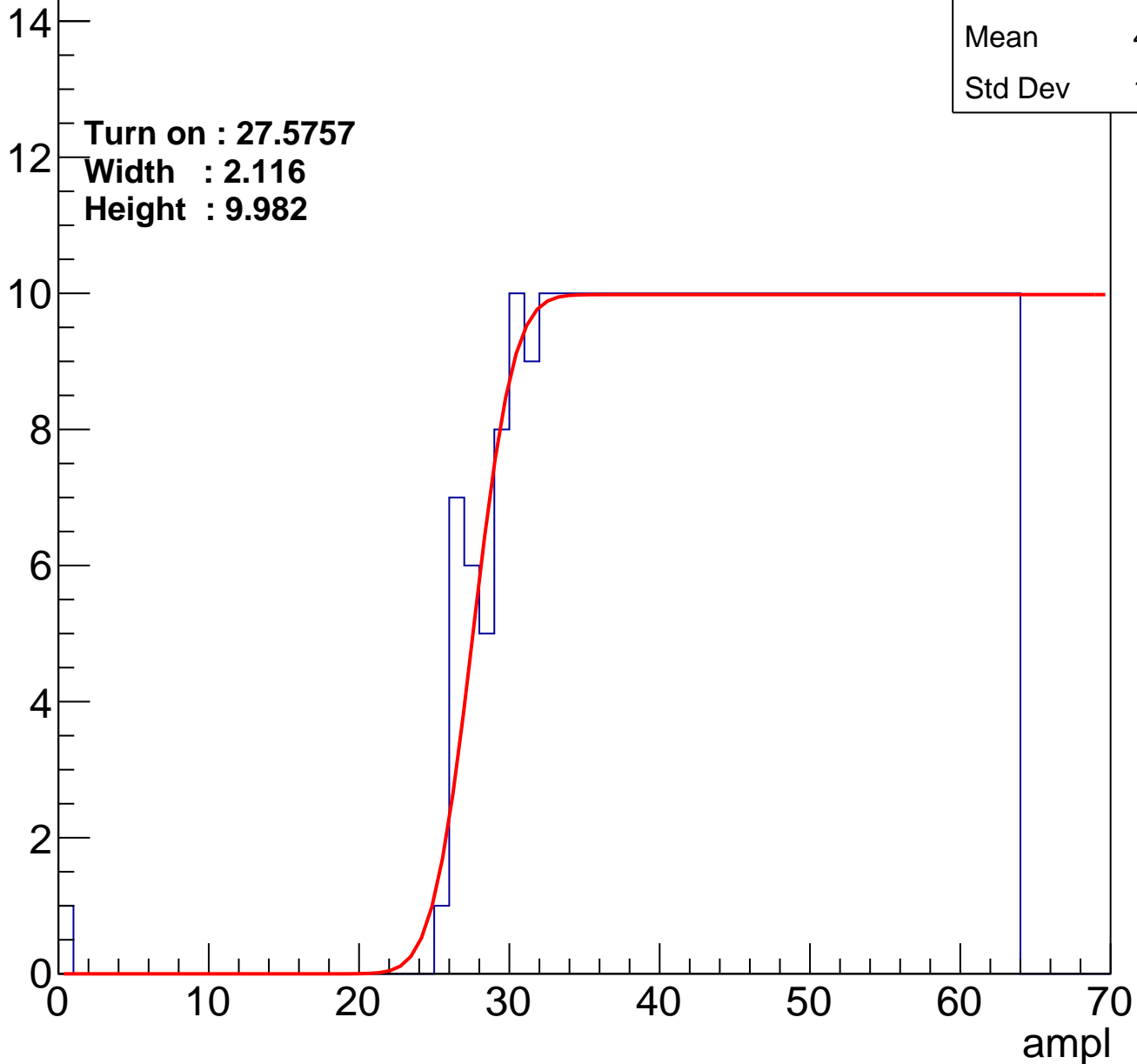
Entry

Entries	367
Mean	45.01
Std Dev	10.91

Turn on : 27.5757

Width : 2.116

Height : 9.982



B0L002S, U8-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.69
Std Dev	10.58

Turn on : 29.2513

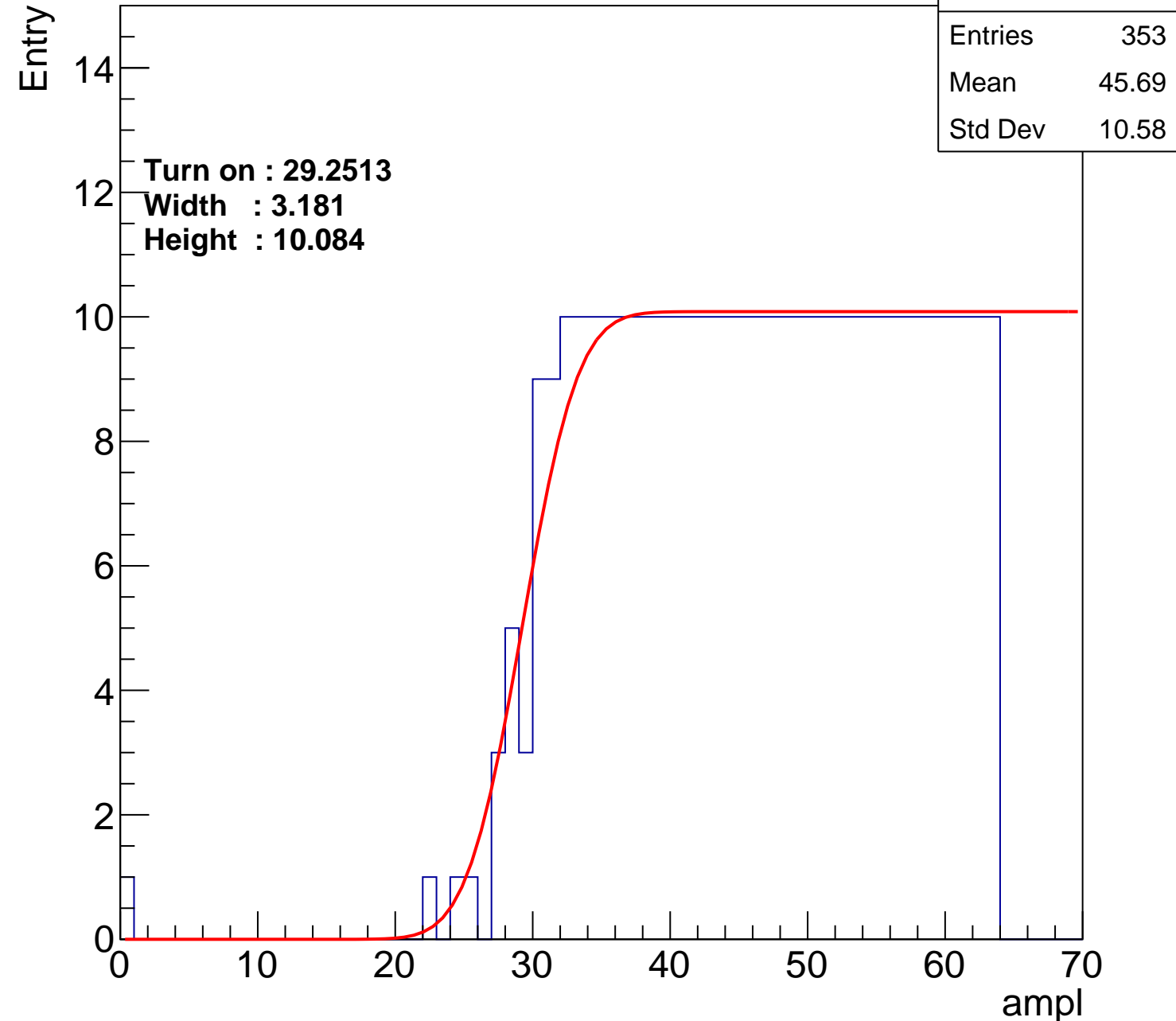
Width : 3.181

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
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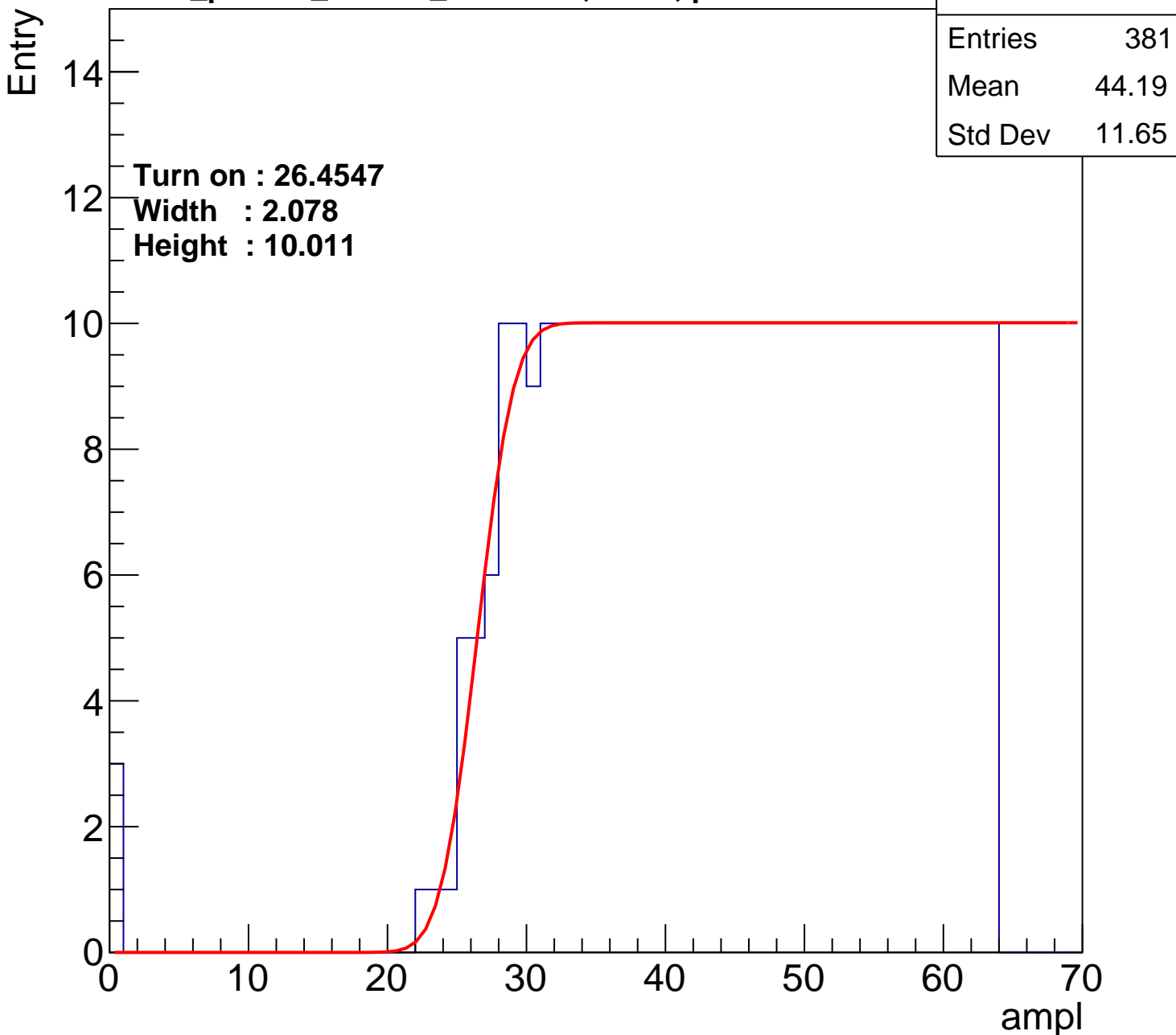
Mean	44.19
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Std Dev	11.65
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Turn on : 26.4547

Width : 2.078

Height : 10.011



B0L002S, U8-ch100

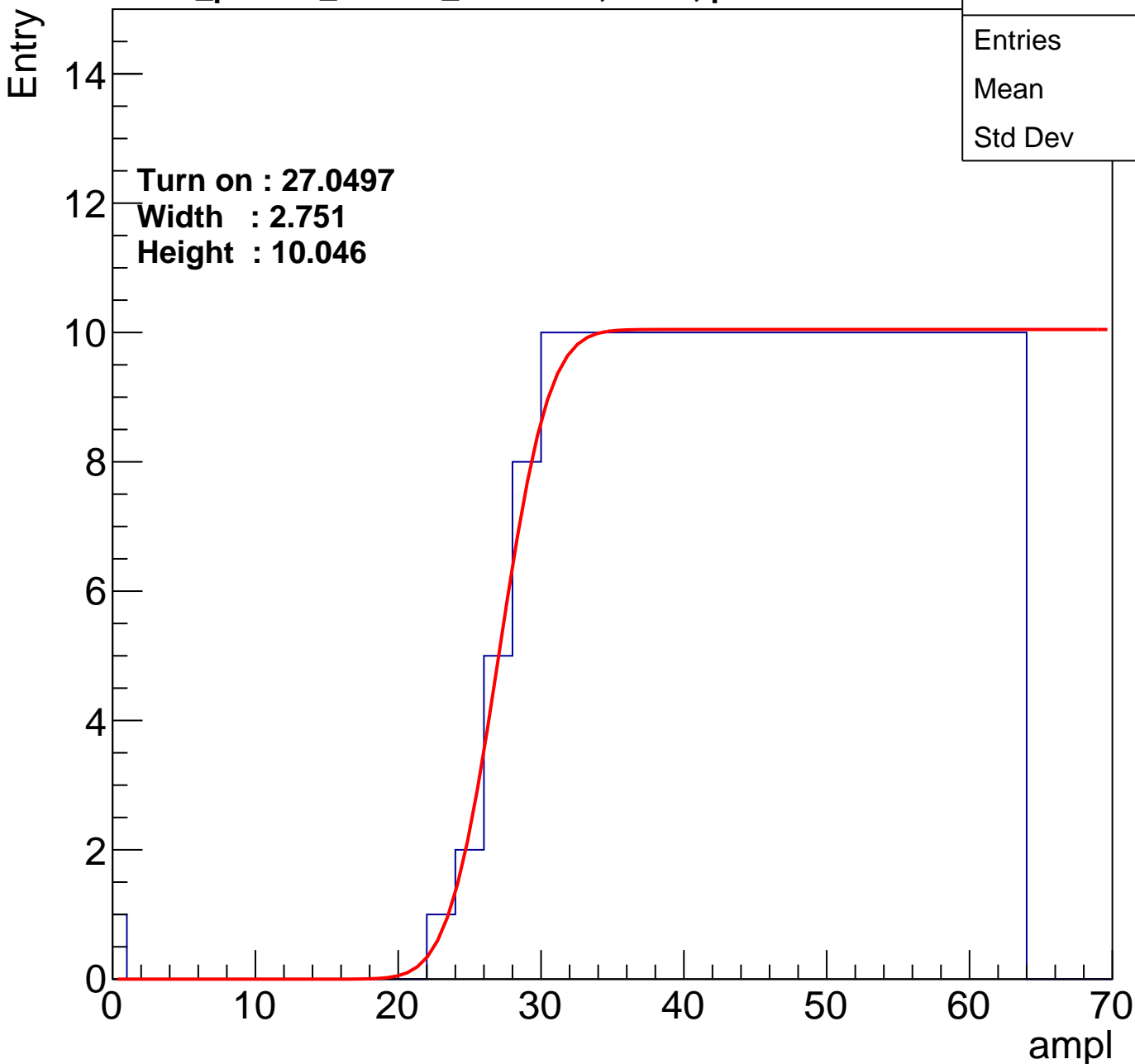
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 27.0497

Width : 2.751

Height : 10.046



B0L002S, U8-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.2626

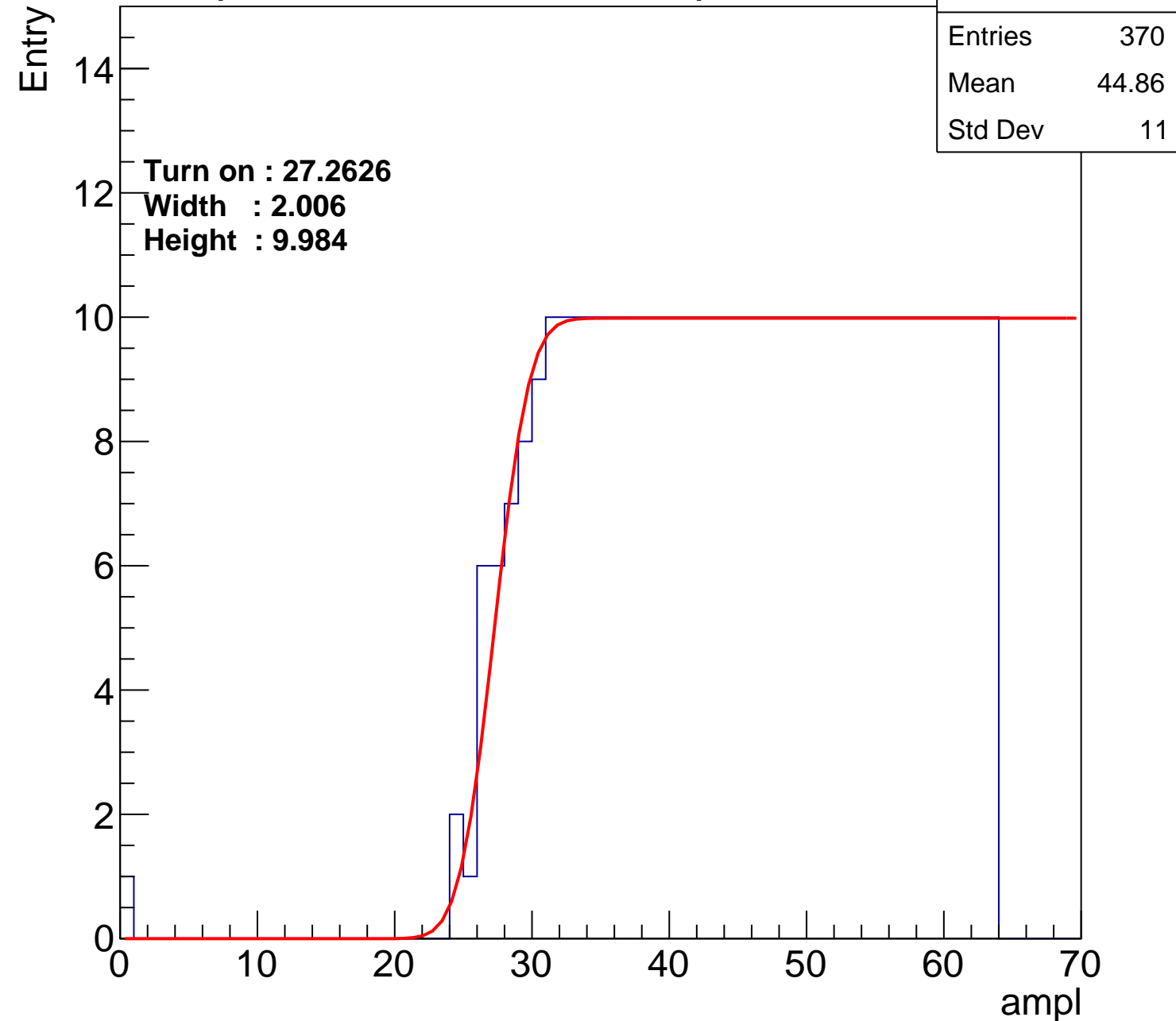
Width : 2.006

Height : 9.984

Entry

14
12
10
8
6
4
2
0

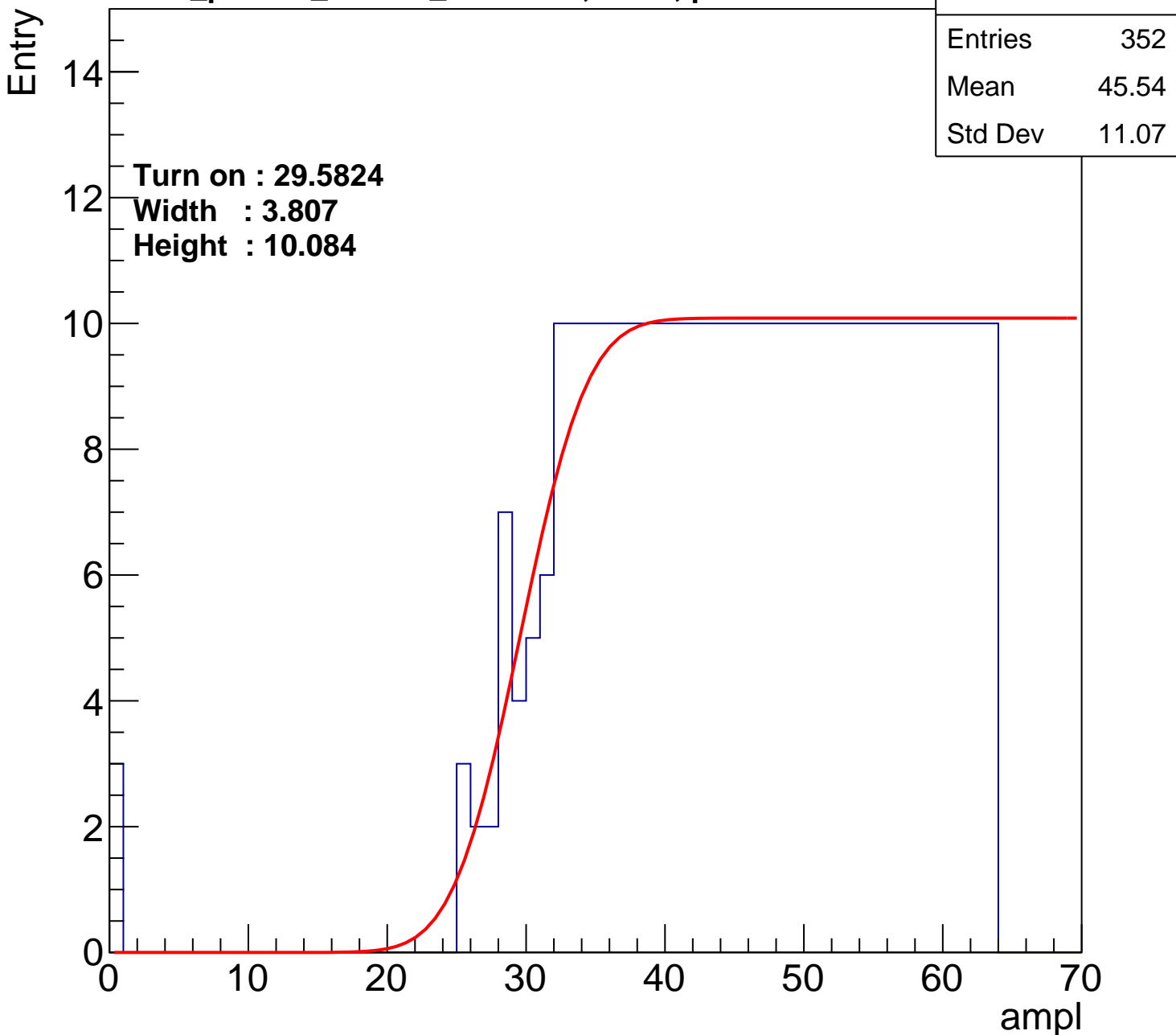
ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.5824
Width : 3.807
Height : 10.084



calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.44
Std Dev	10.71

Mean	45.44
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Std Dev	10.71
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Width : 3.125

Height : 10.086



B0L002S, U8-ch104

calib_packv5_042523_0143.root, FC#8, port C1

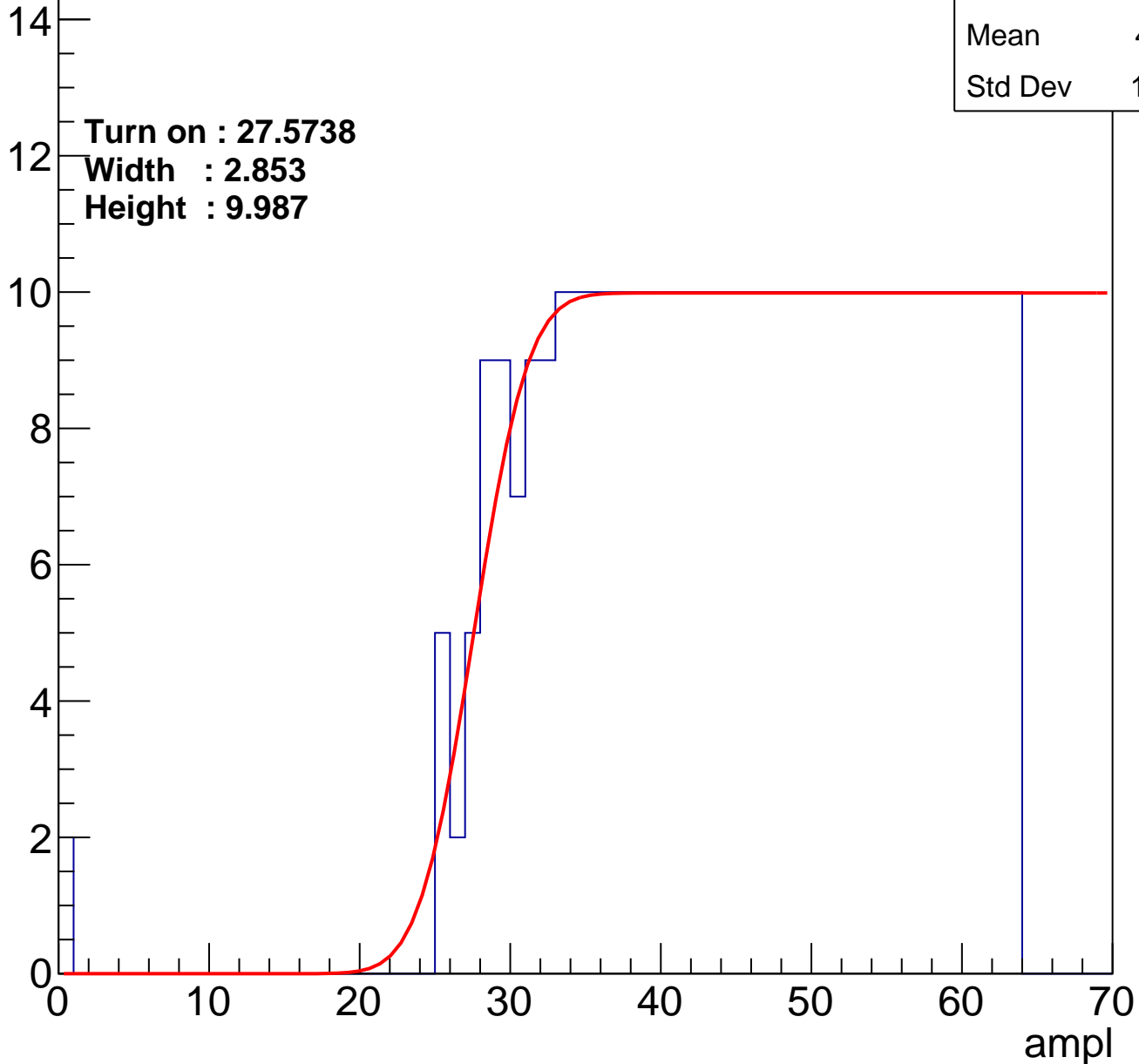
Entries	367
Mean	44.91
Std Dev	11.16

Turn on : 27.5738

Width : 2.853

Height : 9.987

Entry



B0L002S, U8-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.13
Std Dev	10.94

Turn on : 28.0259

Width : 3.472

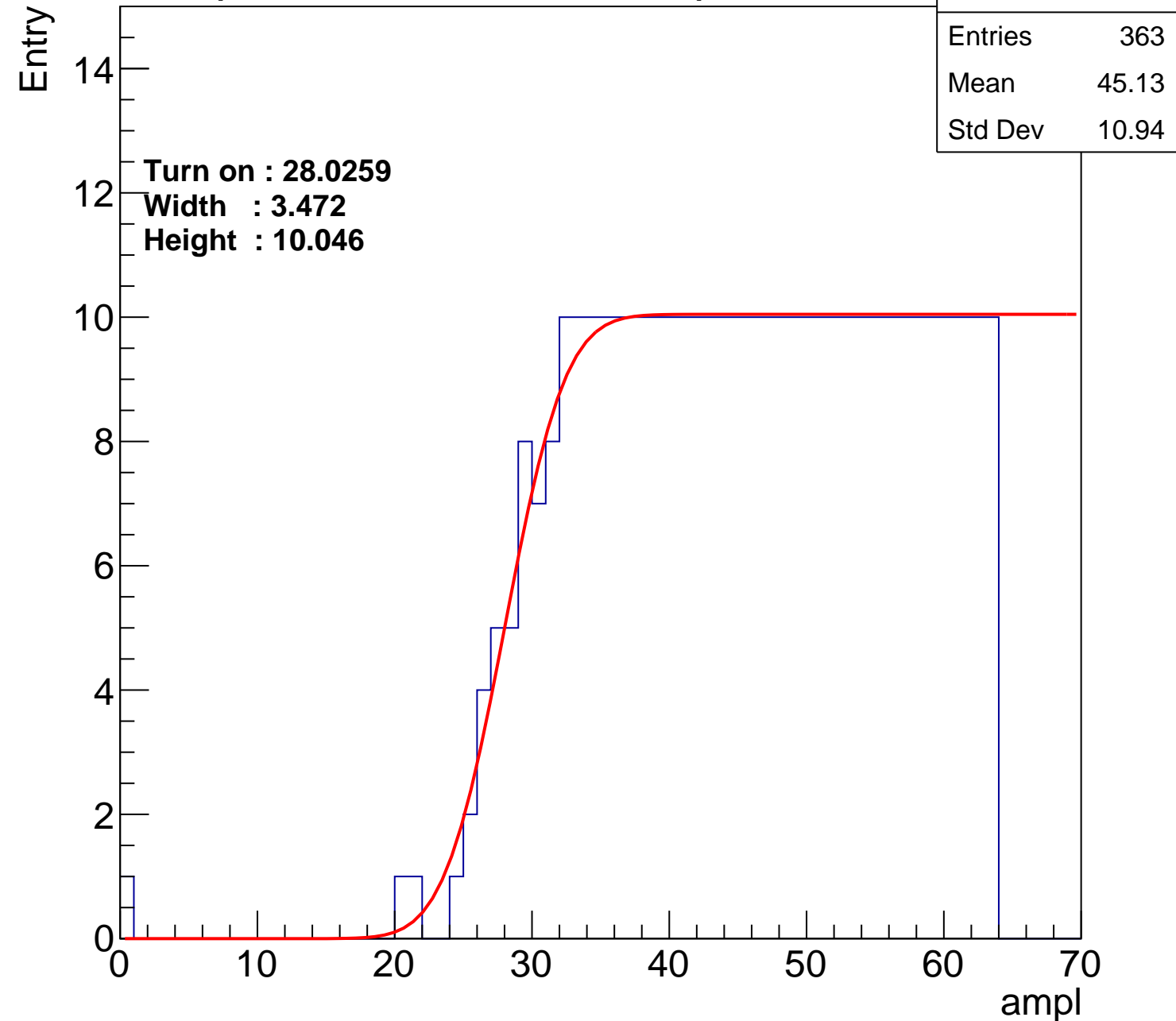
Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U8-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.85
Std Dev	11.23

Turn on : 27.4692

Width : 3.703

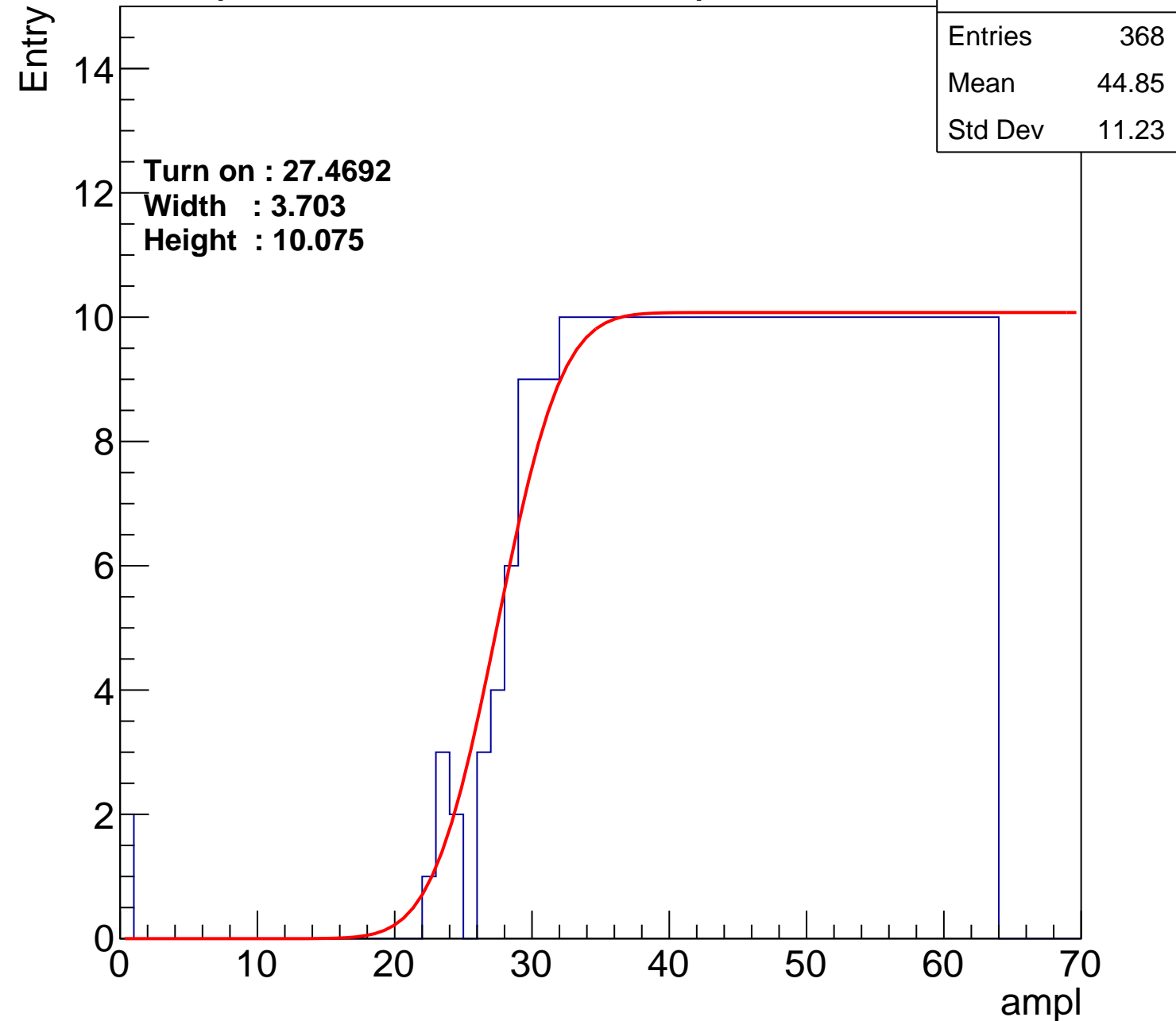
Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U8-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.24
Std Dev	11.25

Turn on : 28.2517

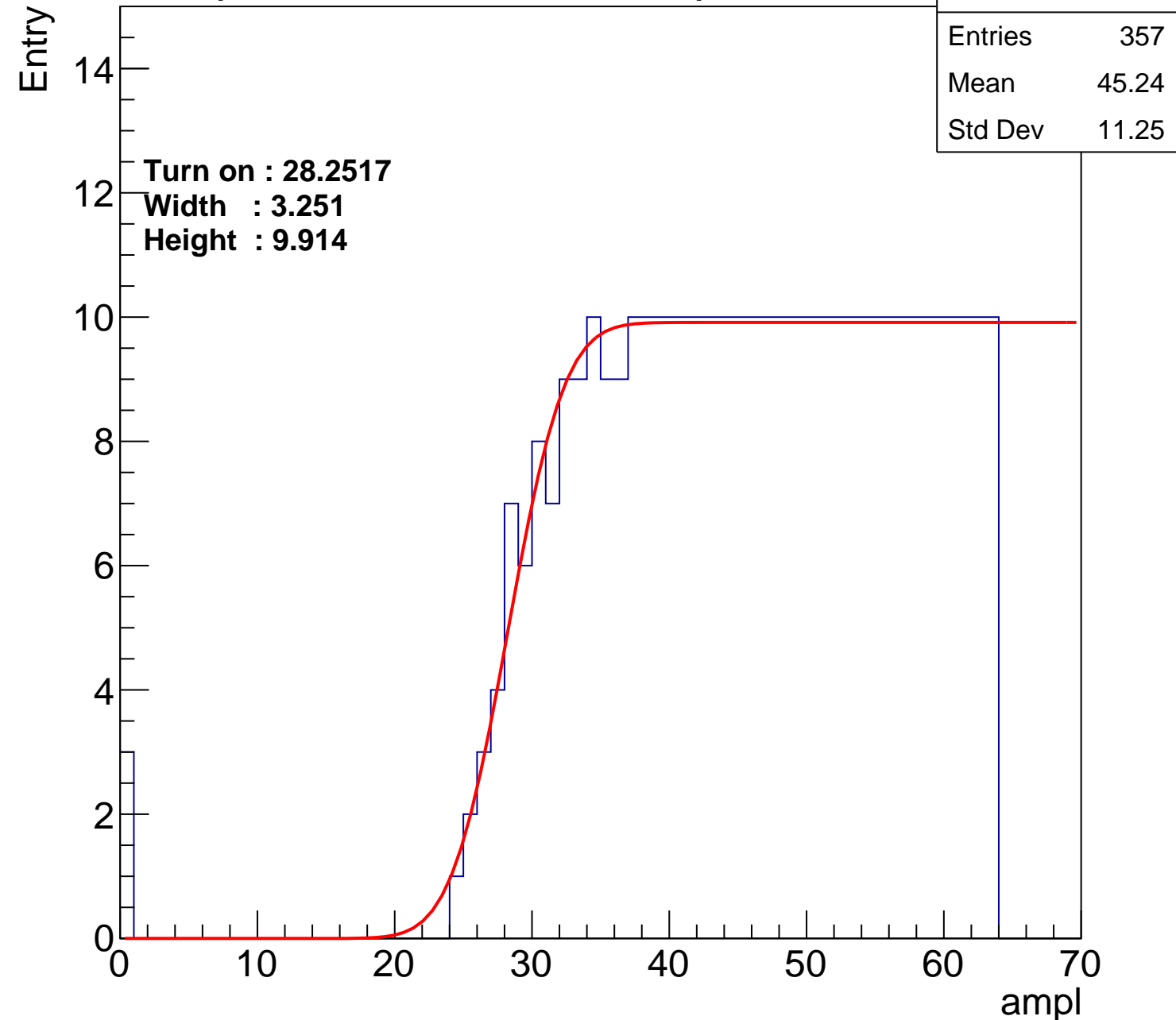
Width : 3.251

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch108

calib_packv5_042523_0143.root, FC#8, port C1

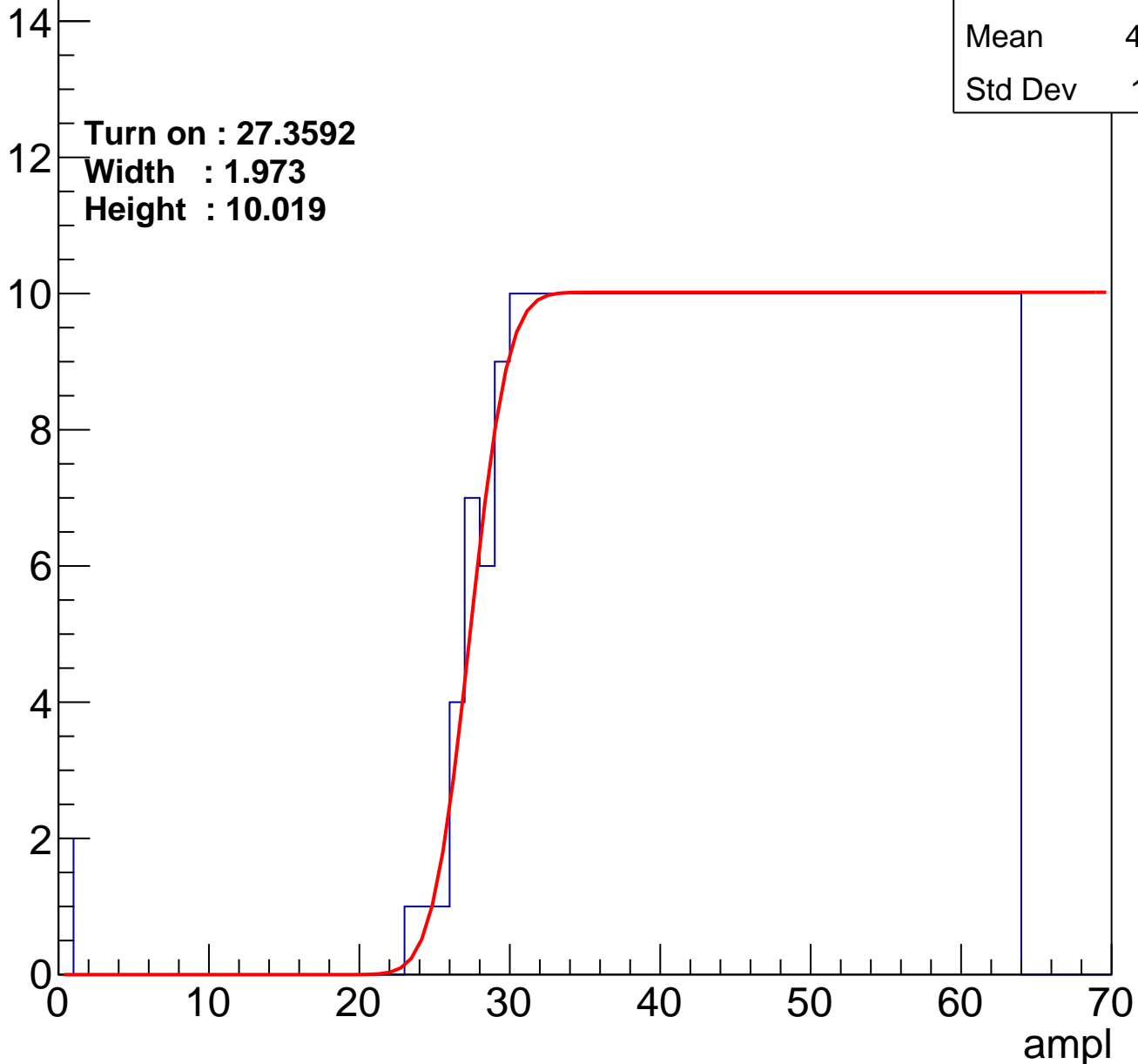
Entries	371
Mean	44.75
Std Dev	11.21

Turn on : 27.3592

Width : 1.973

Height : 10.019

Entry



B0L002S, U8-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.09
Std Dev	10.9

Turn on : 27.8925

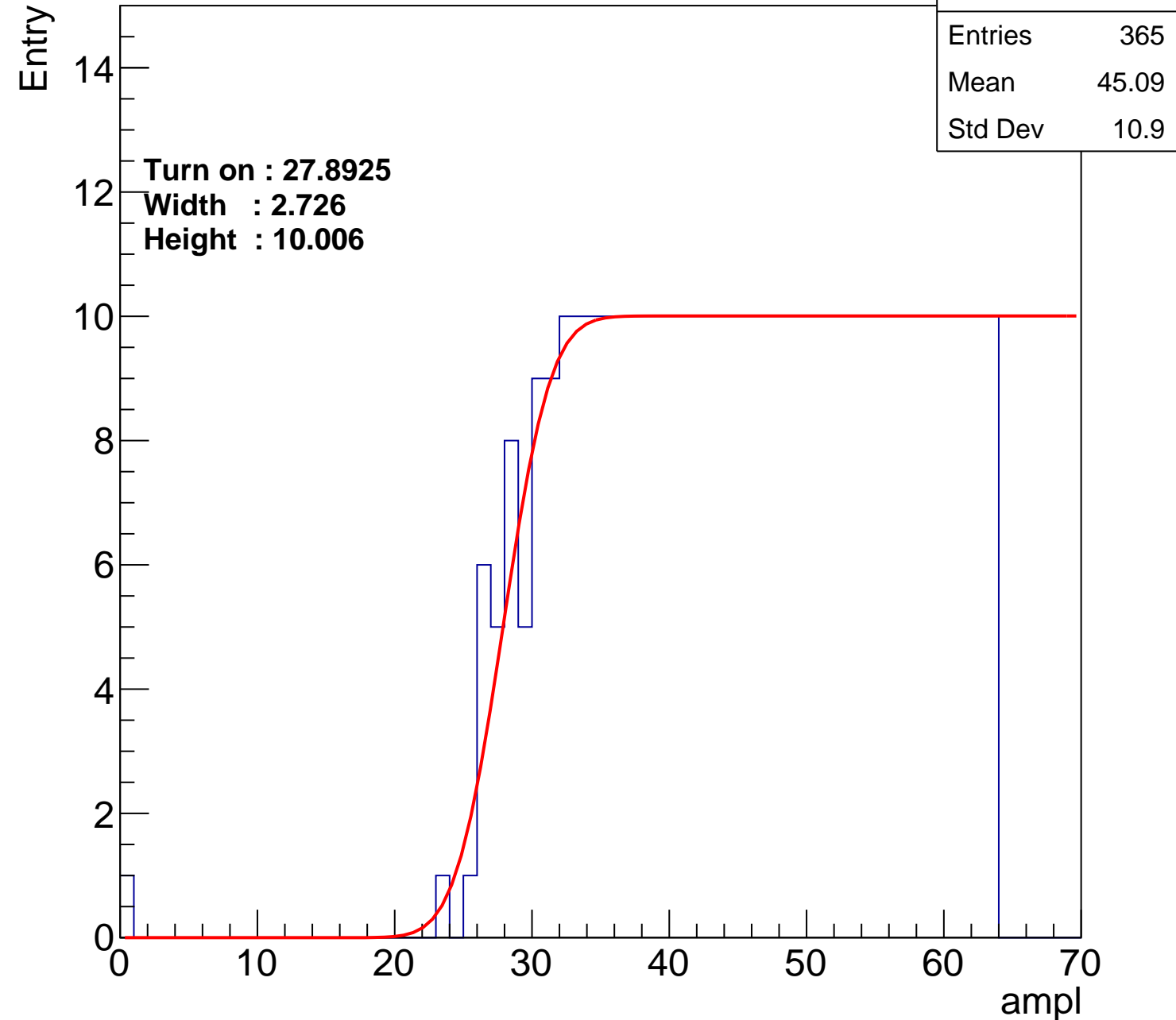
Width : 2.726

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.9852
Width : 3.124
Height : 9.999



B0L002S, U8-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.98
Std Dev	11.77

Turn on : 25.4763

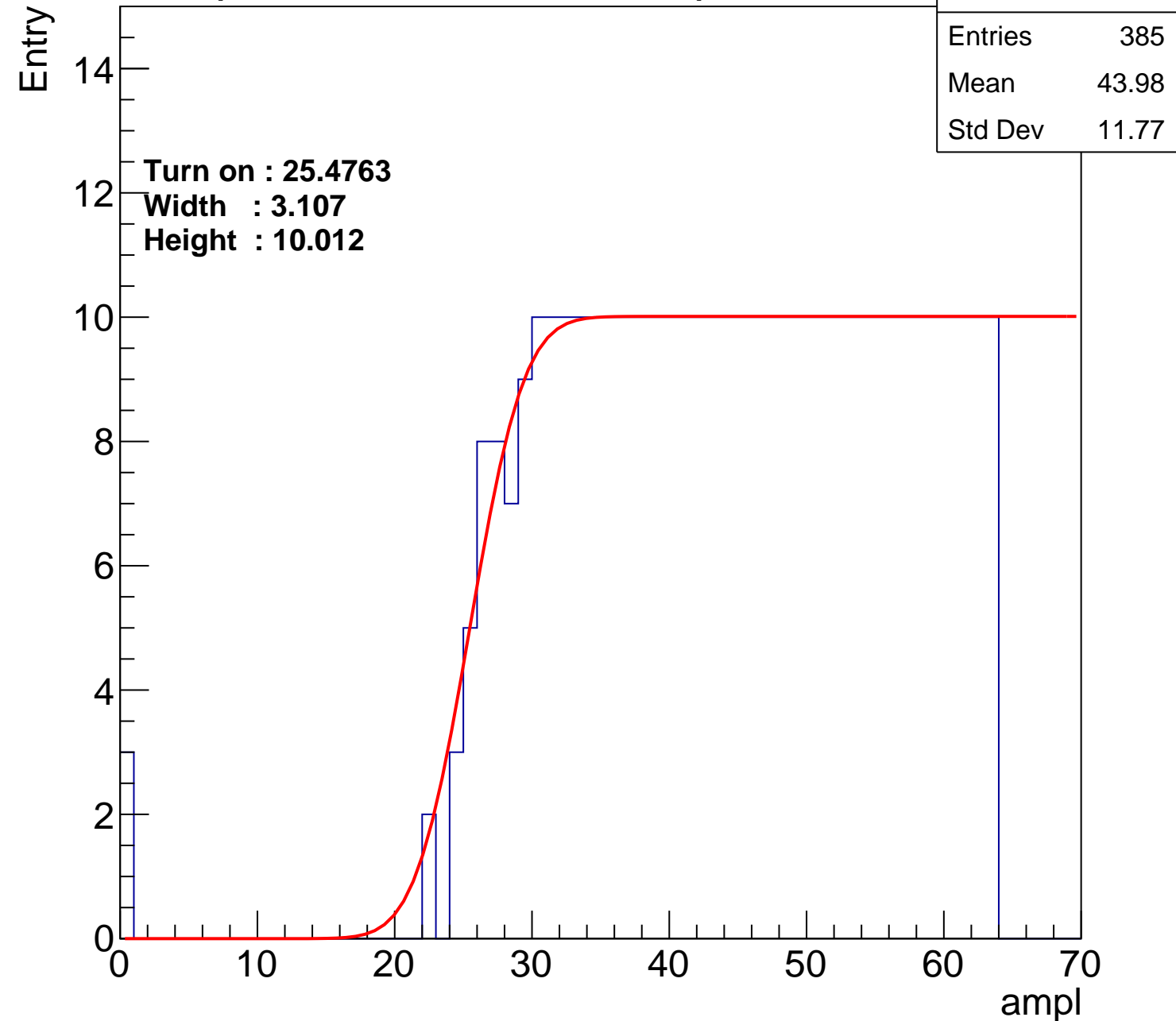
Width : 3.107

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.81
Std Dev	11.75

Turn on : 29.0571

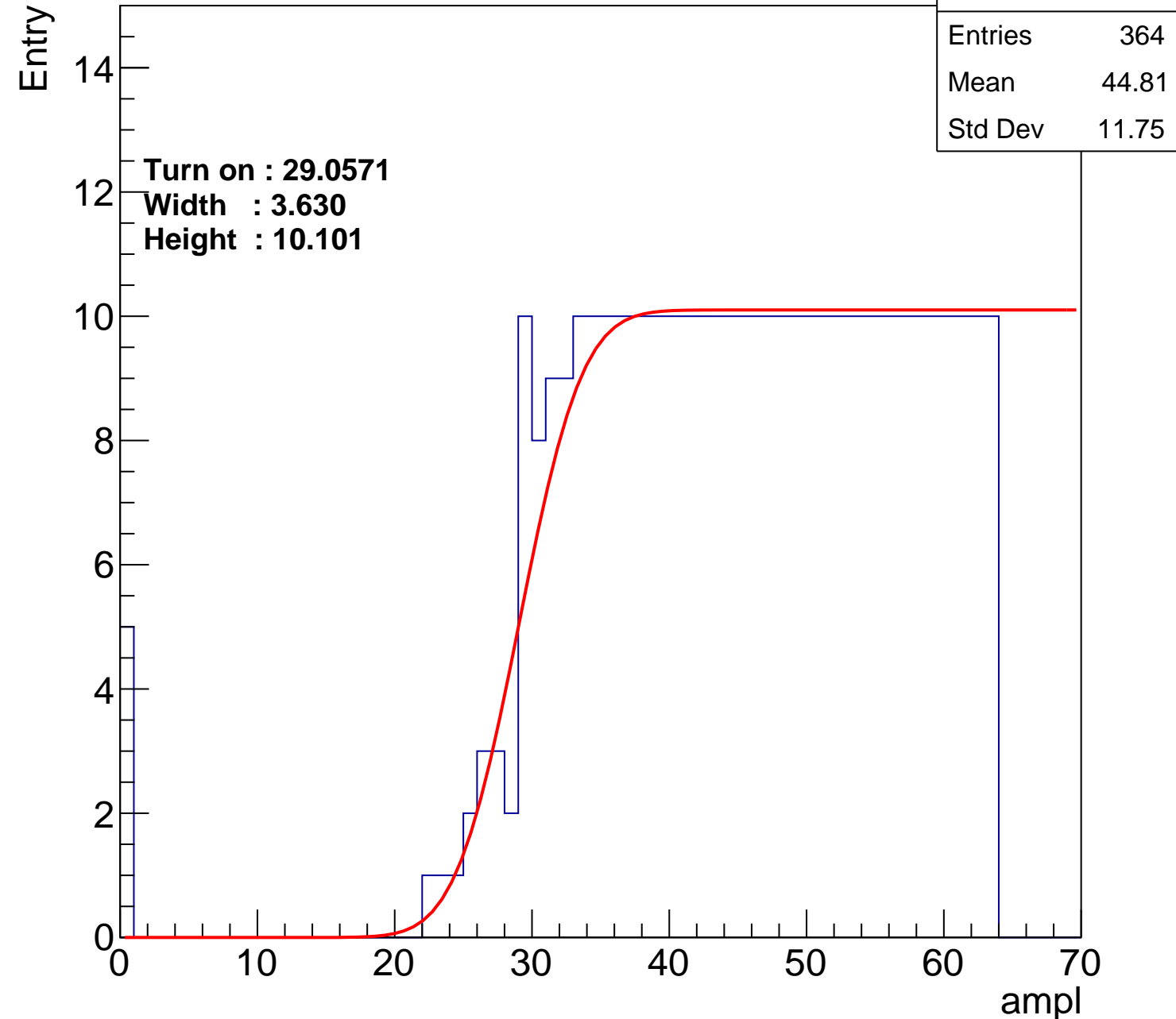
Width : 3.630

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.45
Std Dev	11.74

Turn on : 27.2903

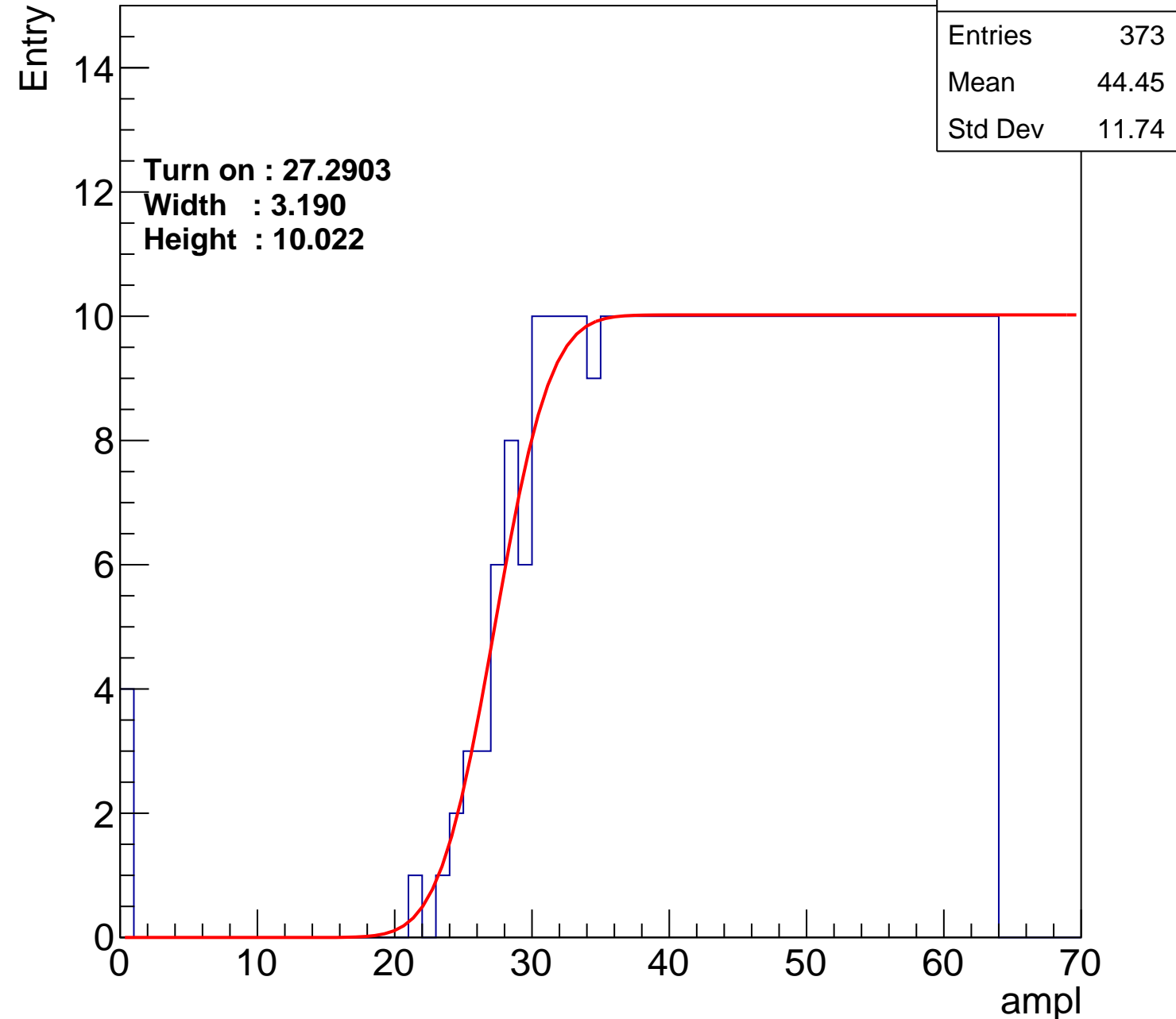
Width : 3.190

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.4834

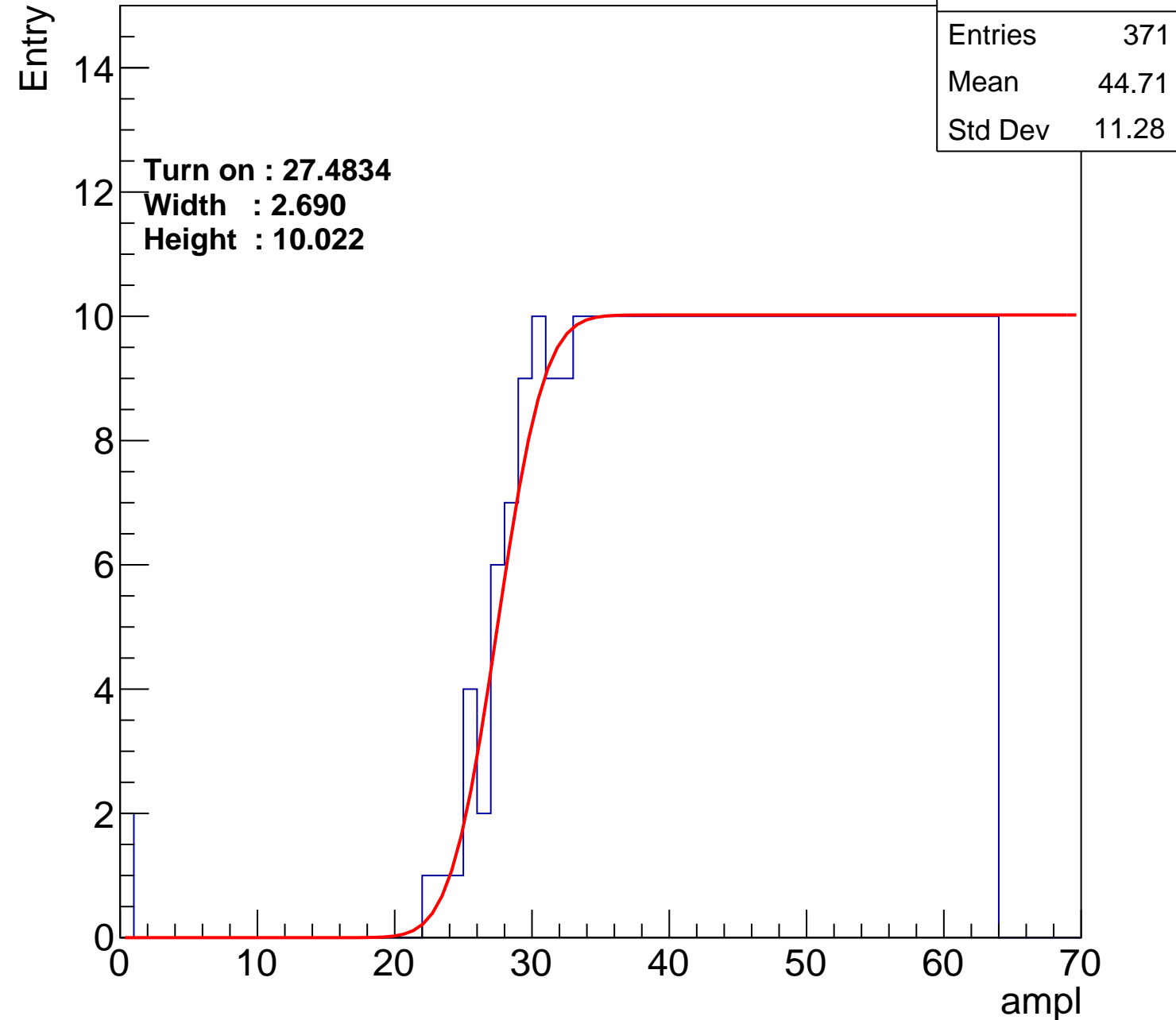
Width : 2.690

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.11
Std Dev	10.76

Turn on : 30.2396

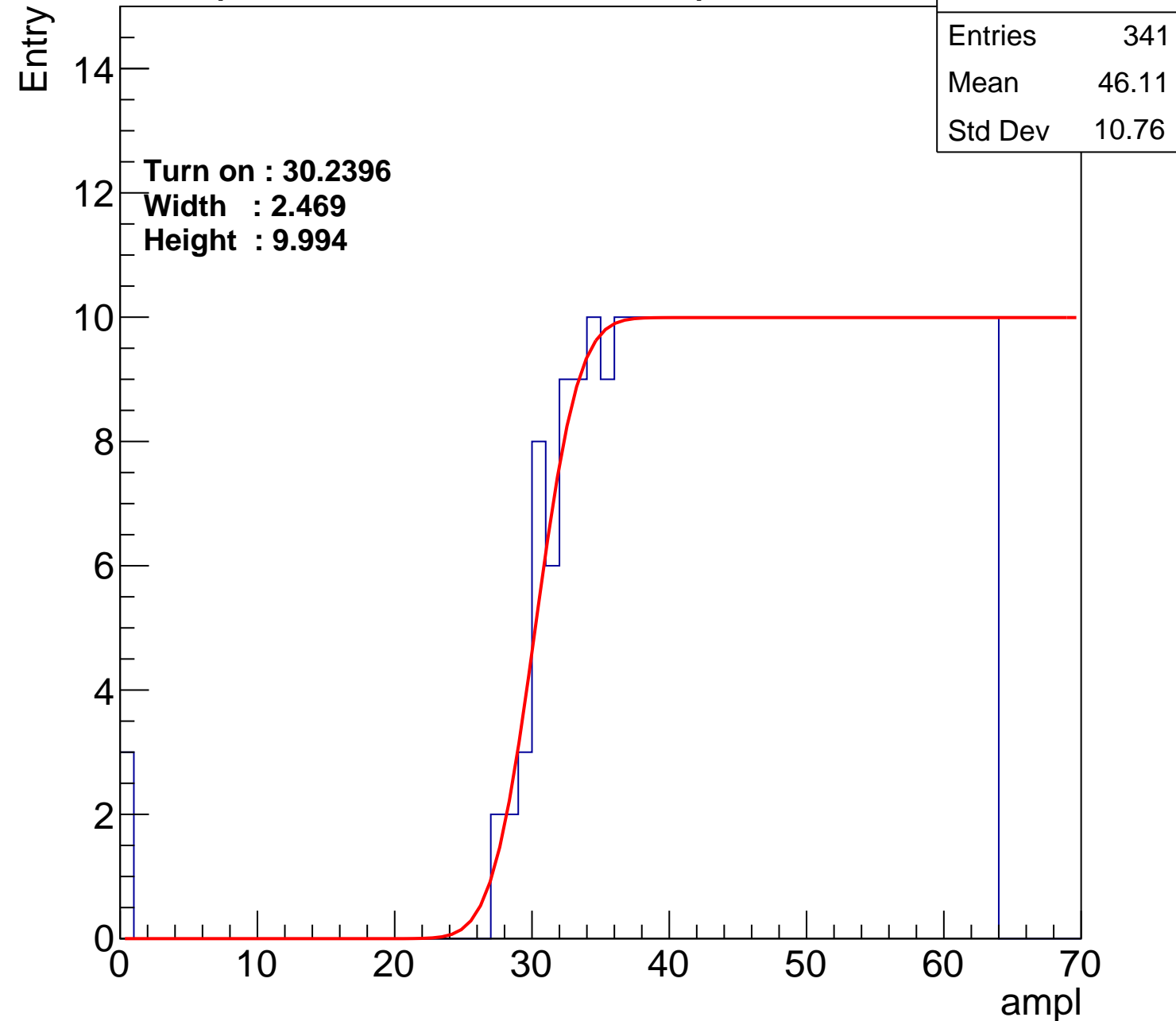
Width : 2.469

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.87
Std Dev	10.98

Turn on : 27.2525

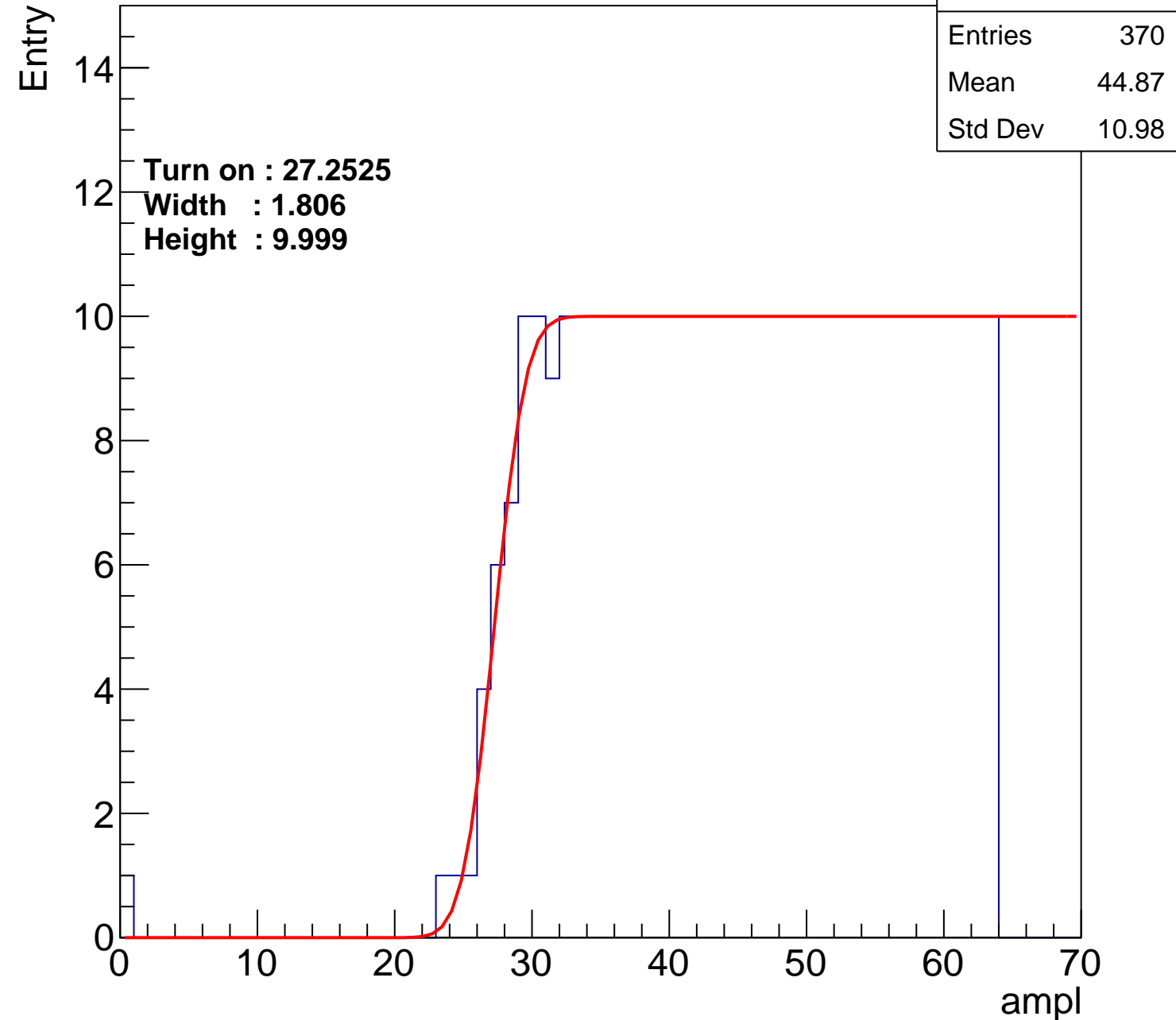
Width : 1.806

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.14
Std Dev	11.59

Turn on : 25.7176

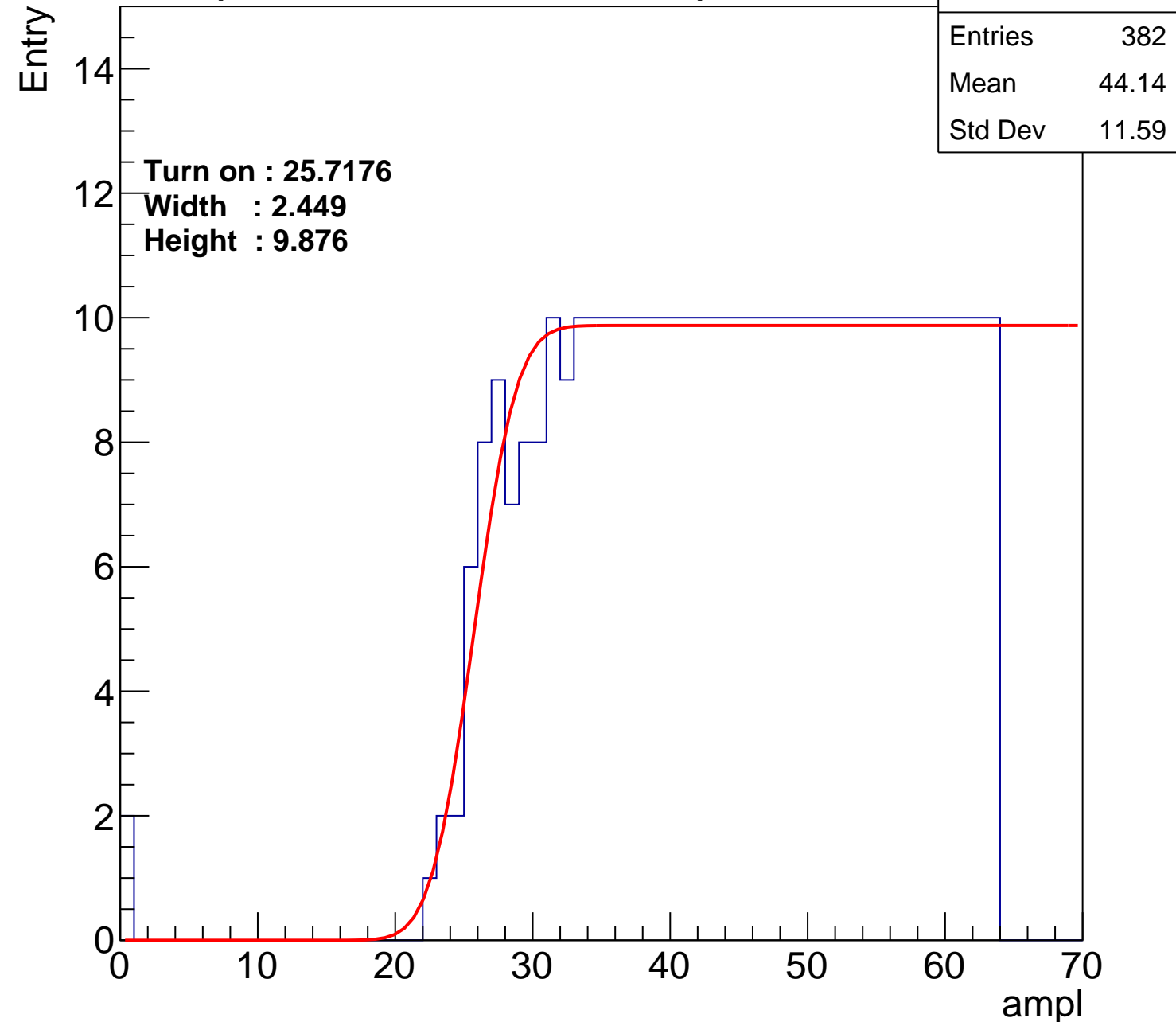
Width : 2.449

Height : 9.876

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch118

calib_packv5_042523_0143.root, FC#8, port C1

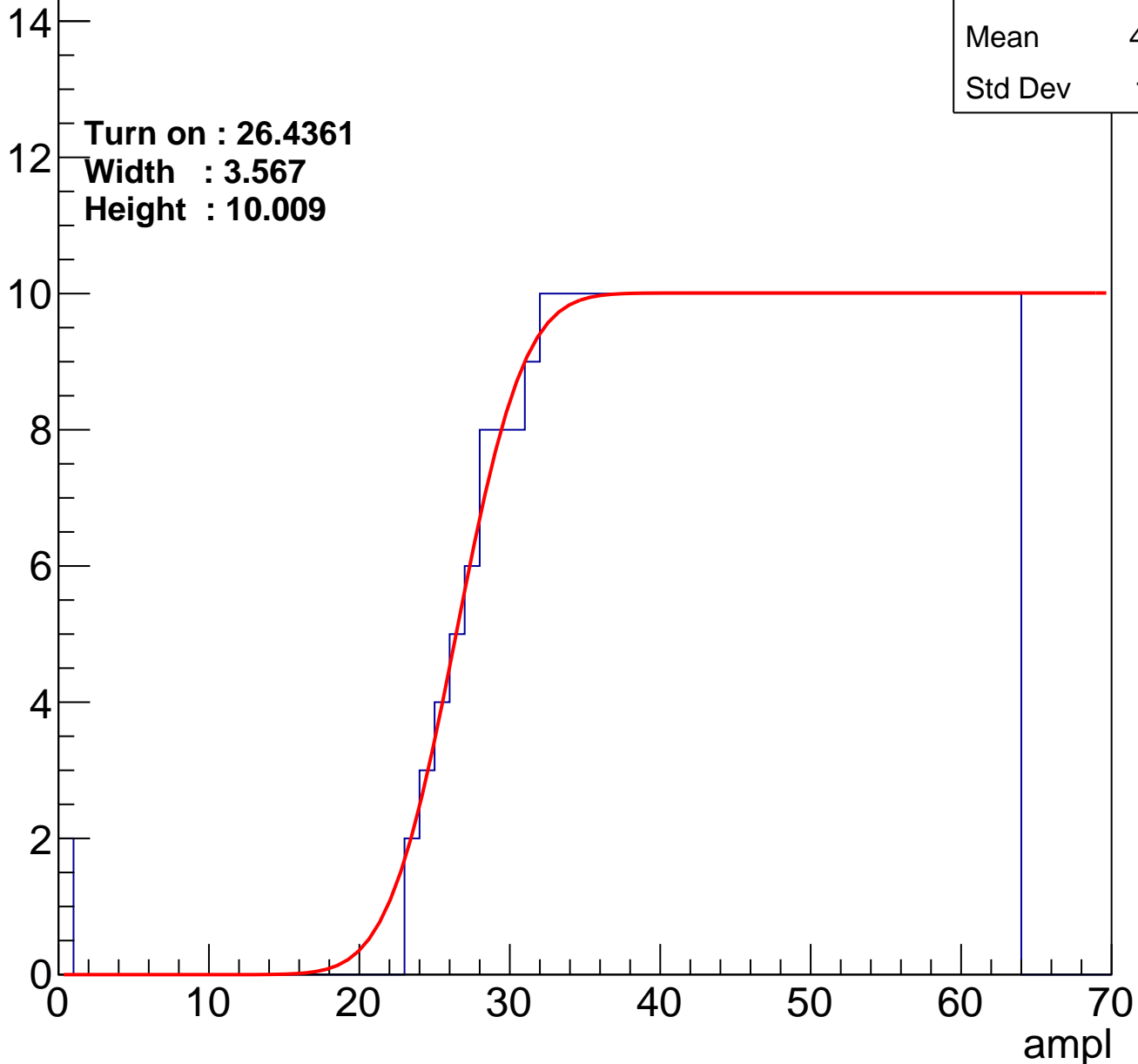
Entries	375
Mean	44.49
Std Dev	11.41

Turn on : 26.4361

Width : 3.567

Height : 10.009

Entry



B0L002S, U8-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 27.5068

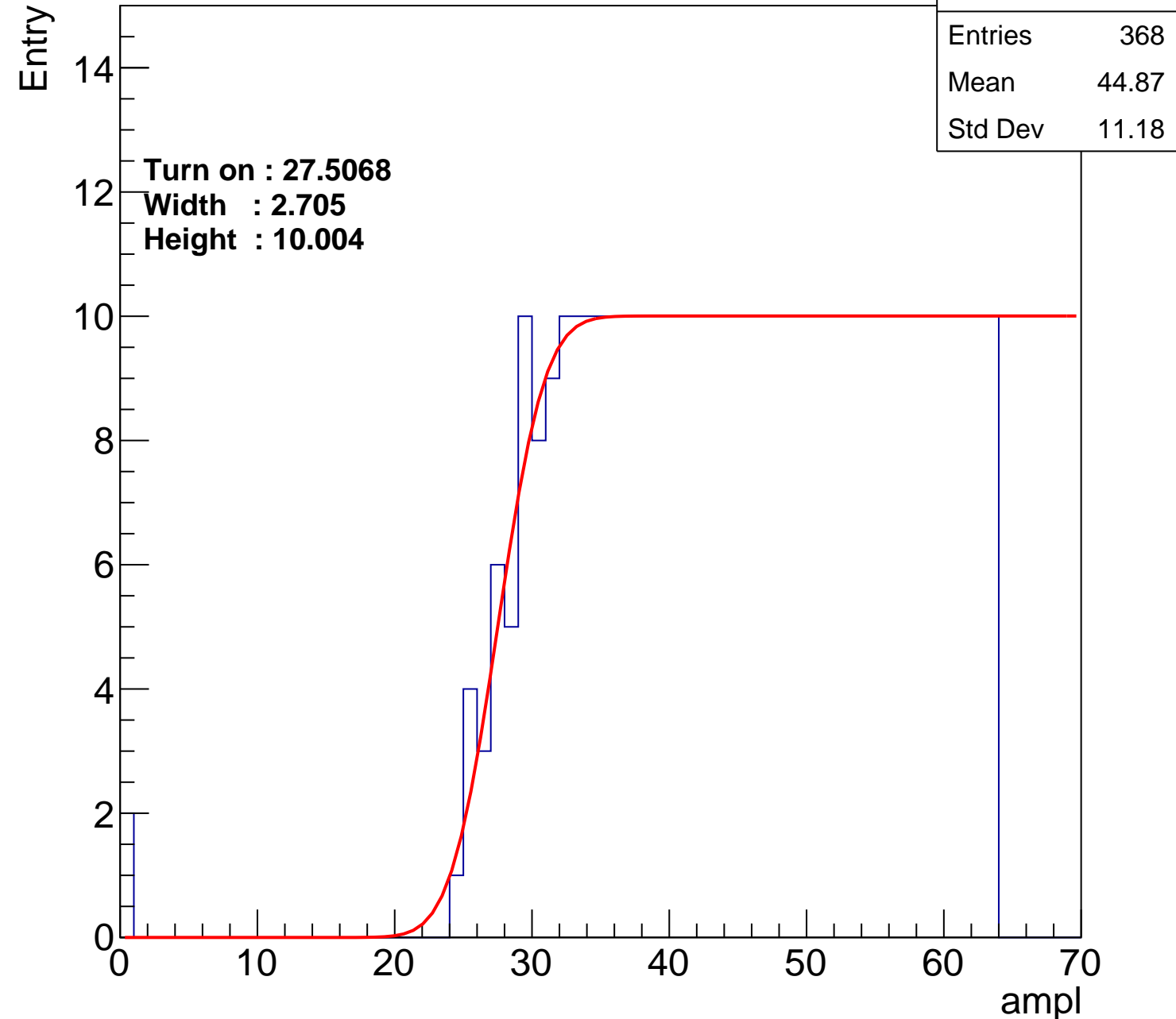
Width : 2.705

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.16
Std Dev	11.71

Turn on : 26.6445

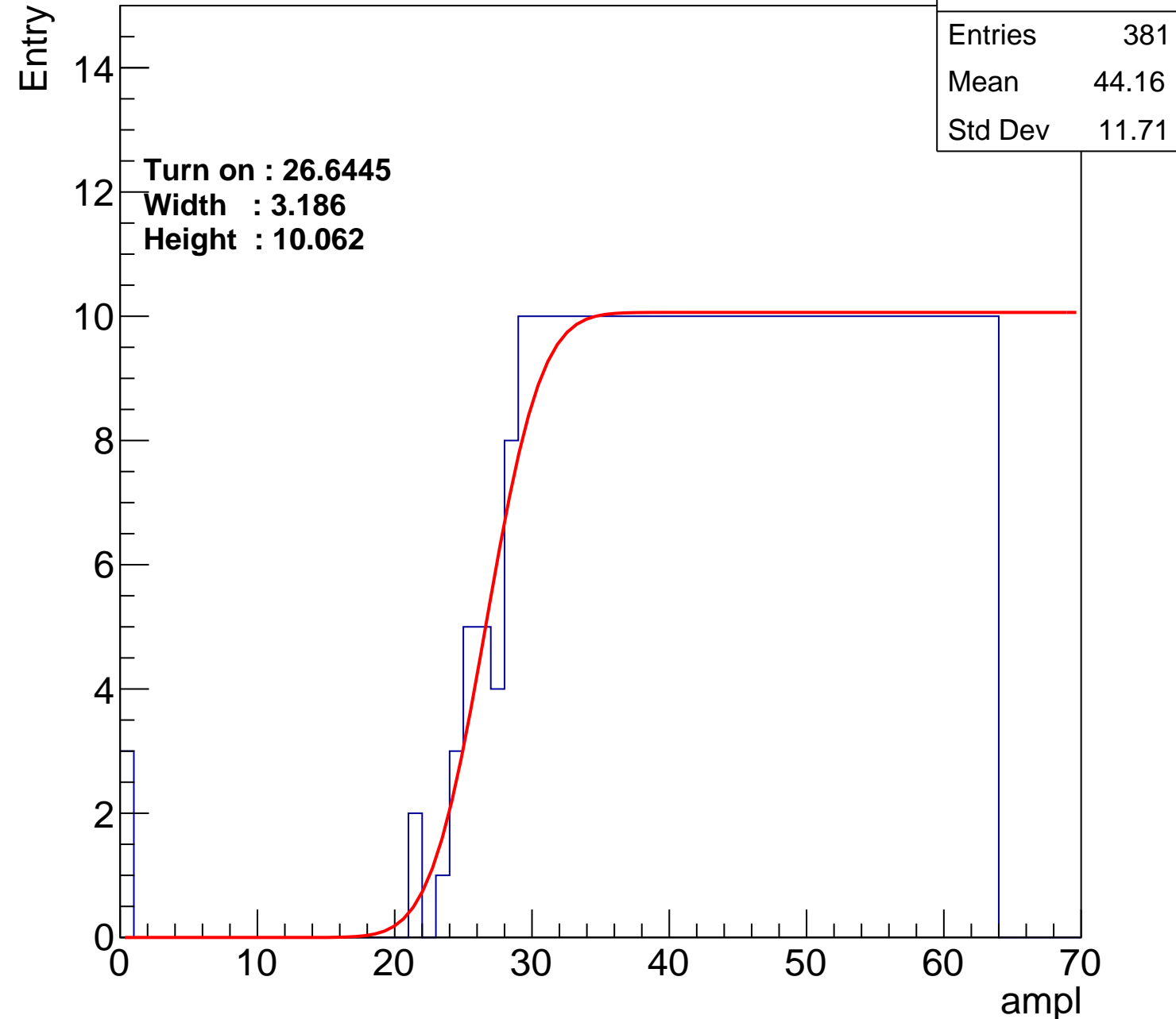
Width : 3.186

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch121

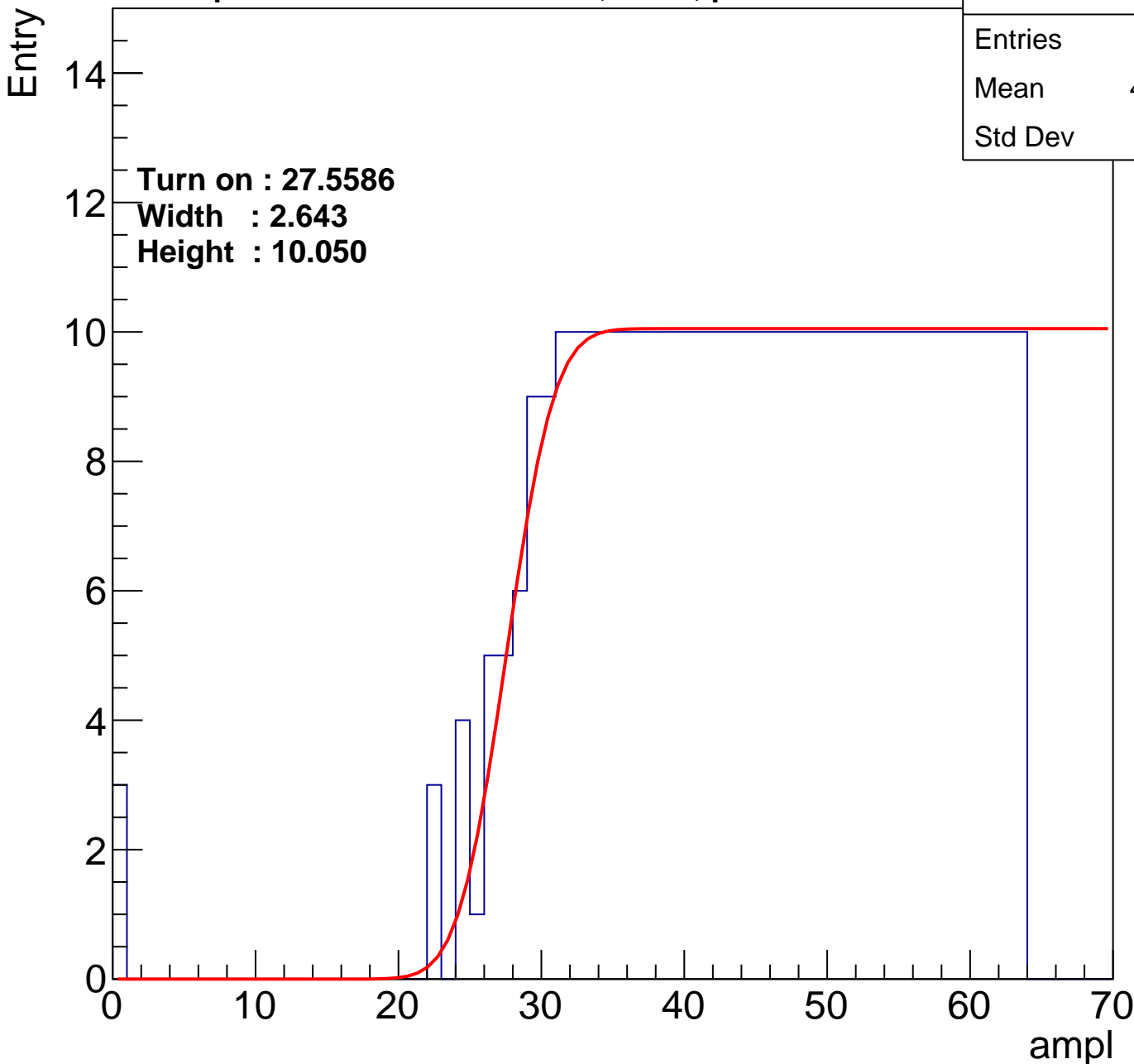
calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.43
Std Dev	11.6

Turn on : 27.5586

Width : 2.643

Height : 10.050



B0L002S, U8-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.5658

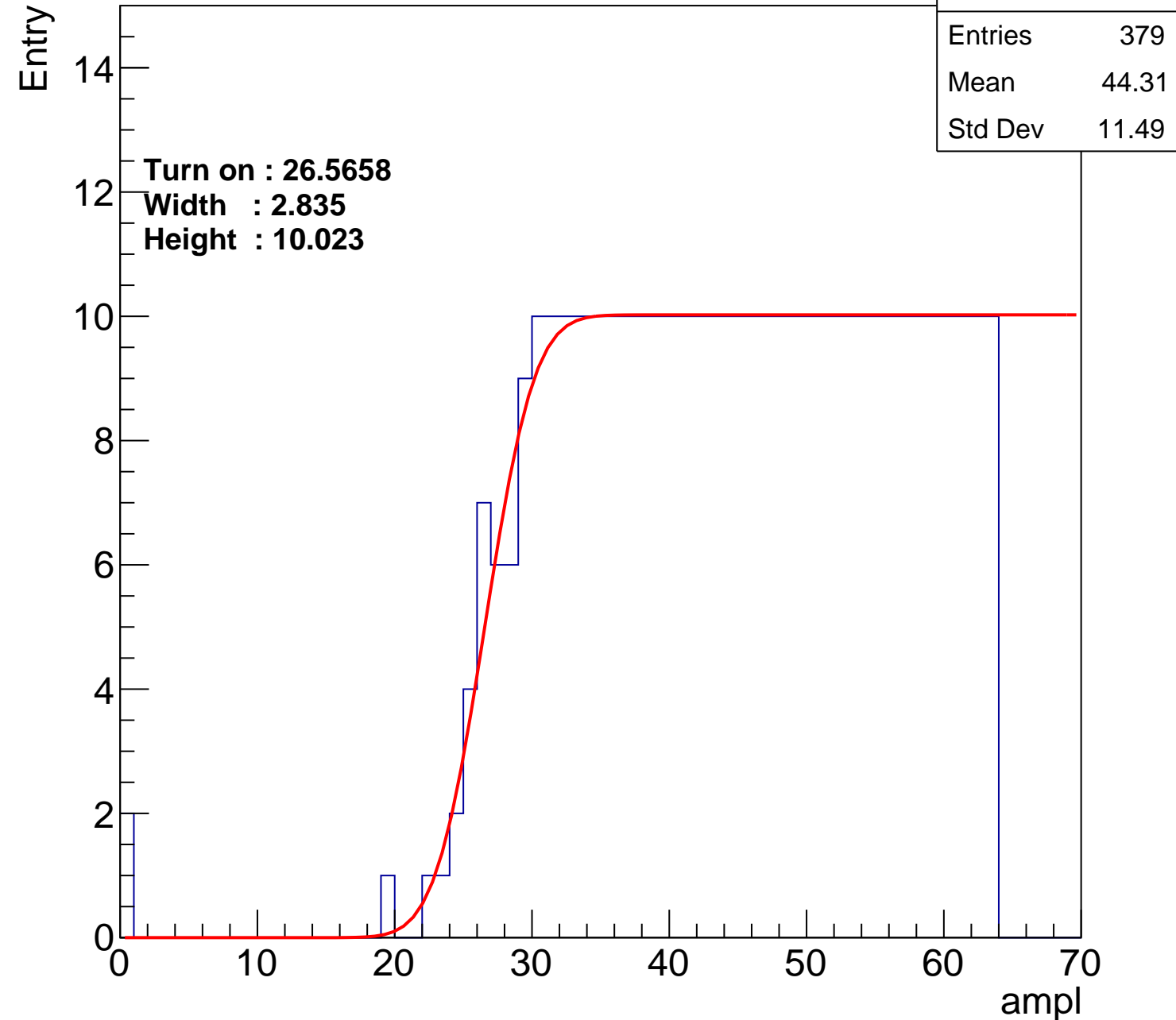
Width : 2.835

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.8215
Width : 3.171
Height : 10.060



B0L002S, U8-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.6479

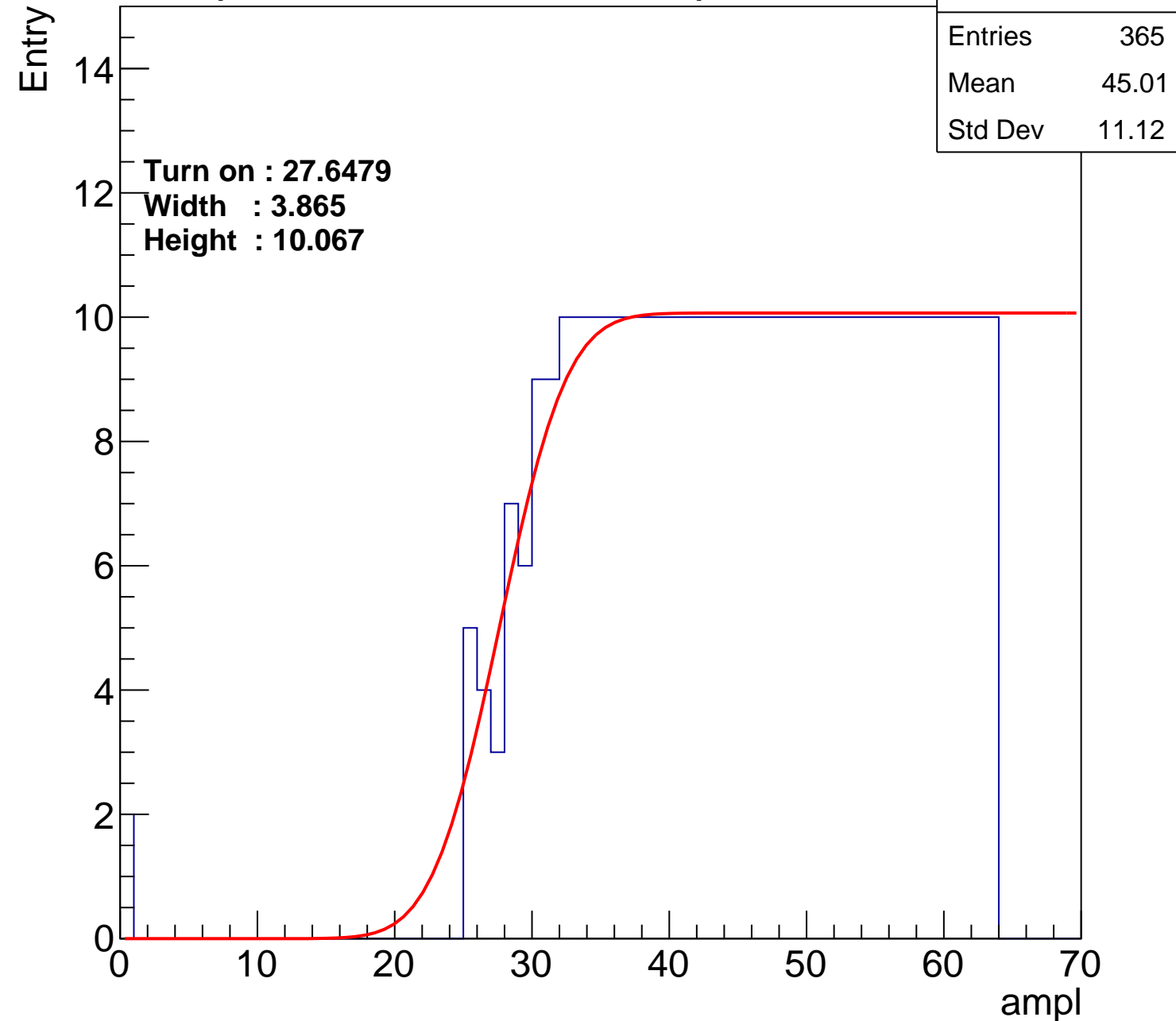
Width : 3.865

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.75
Std Dev	11.07

Turn on : 26.4078

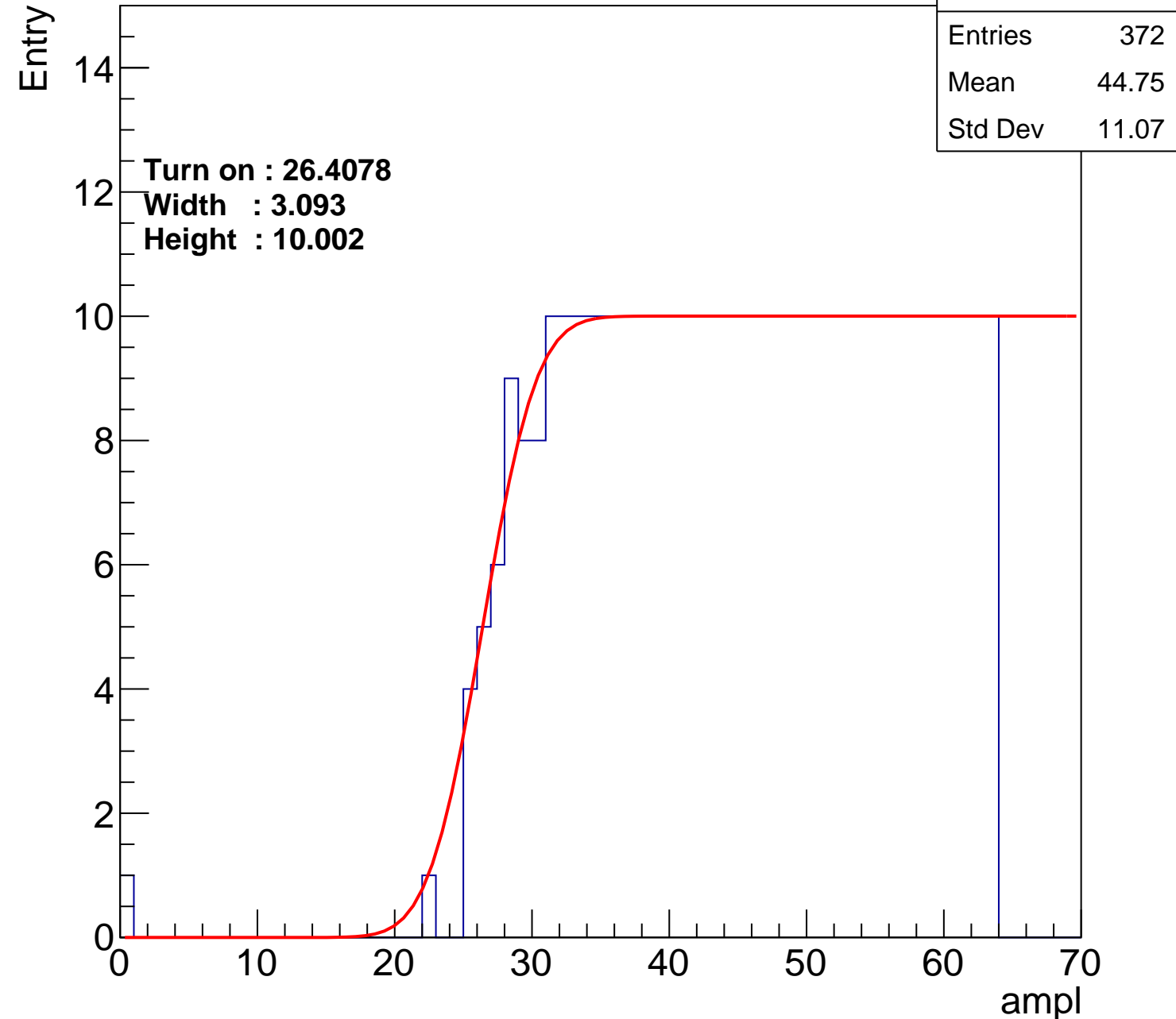
Width : 3.093

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch126

calib_packv5_042523_0143.root, FC#8, port C1

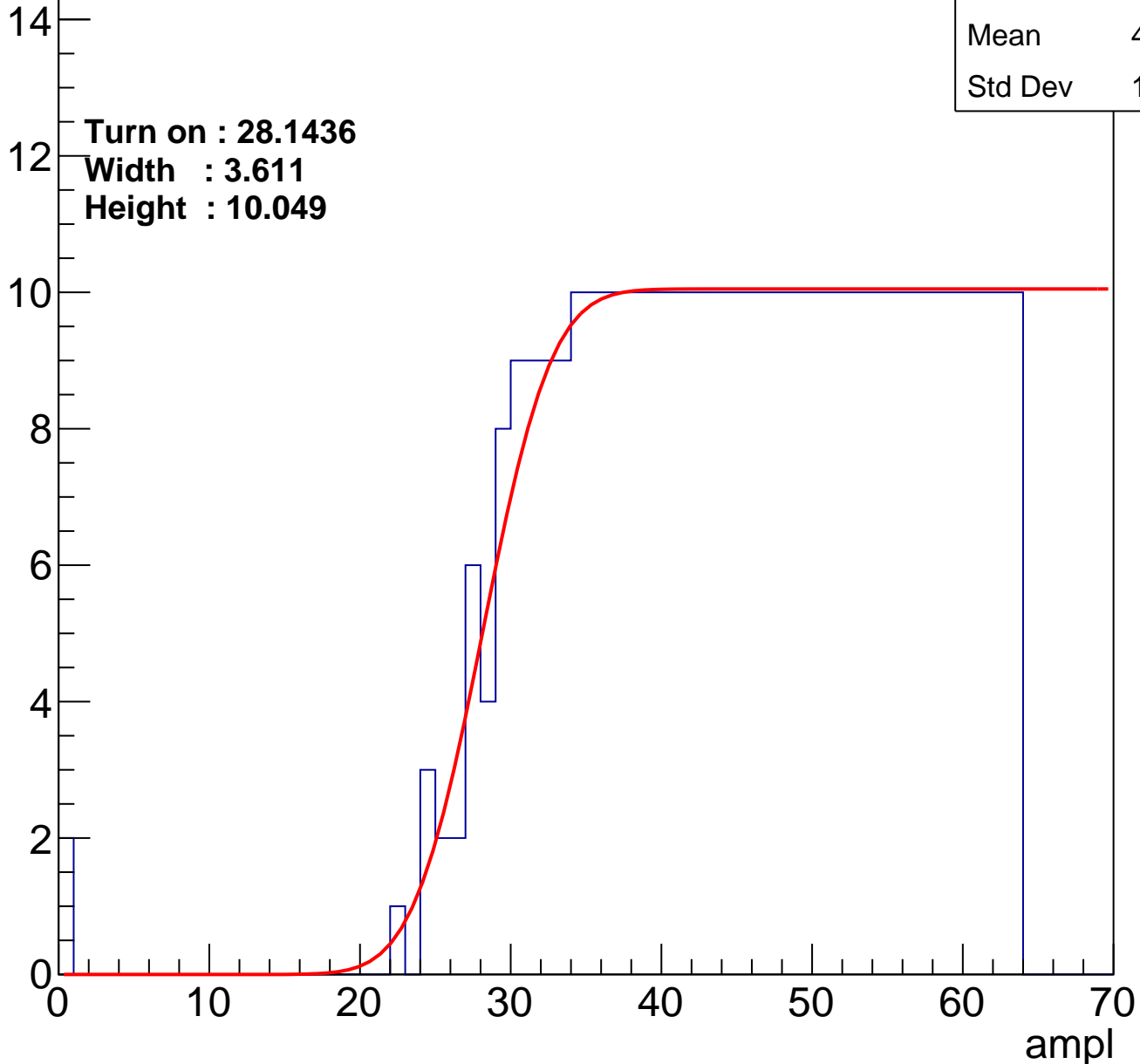
Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 28.1436

Width : 3.611

Height : 10.049

Entry



B0L002S, U8-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.4697

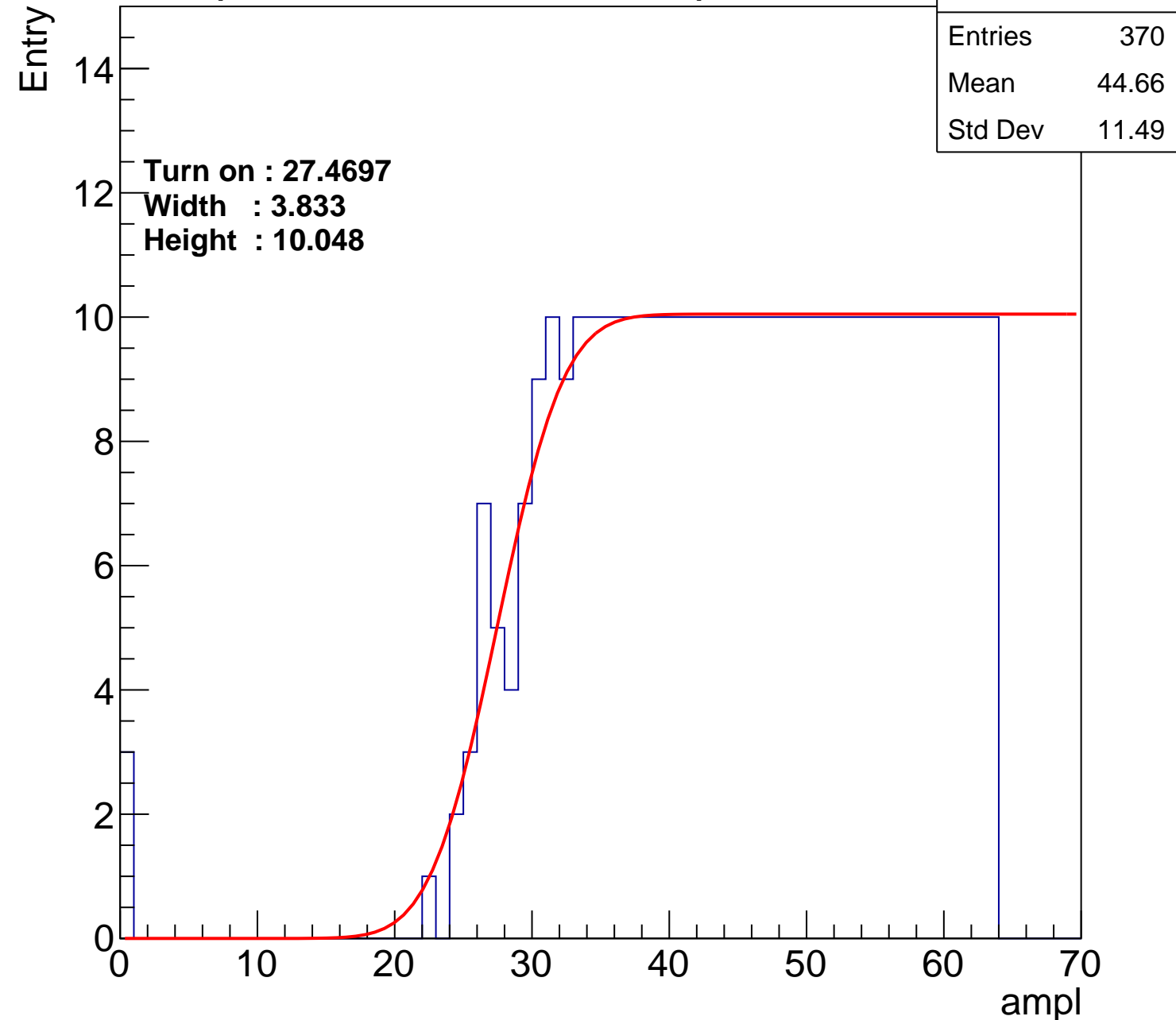
Width : 3.833

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U8-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.4697

Width : 3.833

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl

