

B1L103S, U6-ch0

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 420 |
| Mean | 41.87 |
| Std Dev | 13.49 |

Turn on : 22.7657

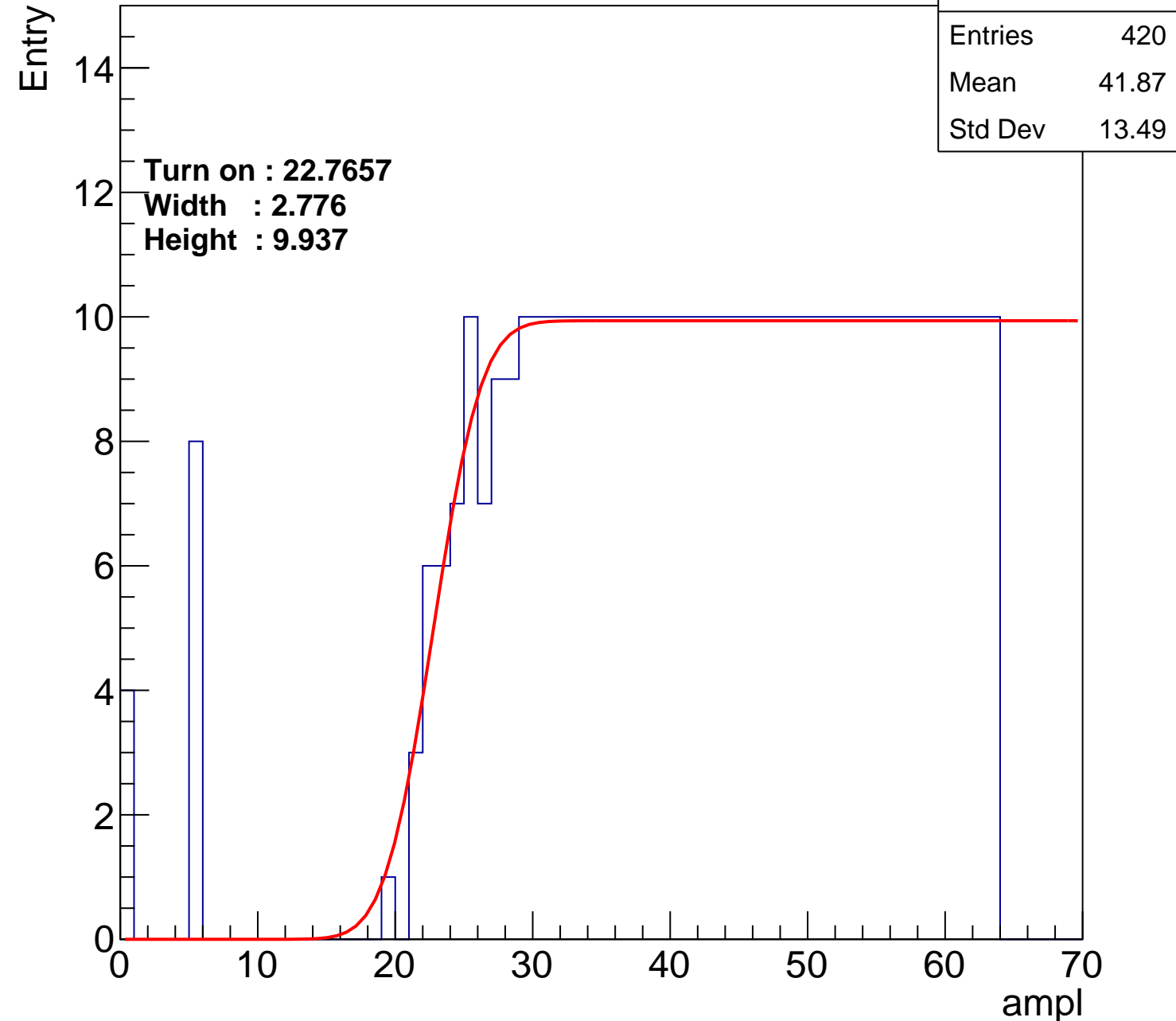
Width : 2.776

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch1

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 410 |
| Mean | 42.69 |
| Std Dev | 12.56 |

Turn on : 24.9307

Width : 2.979

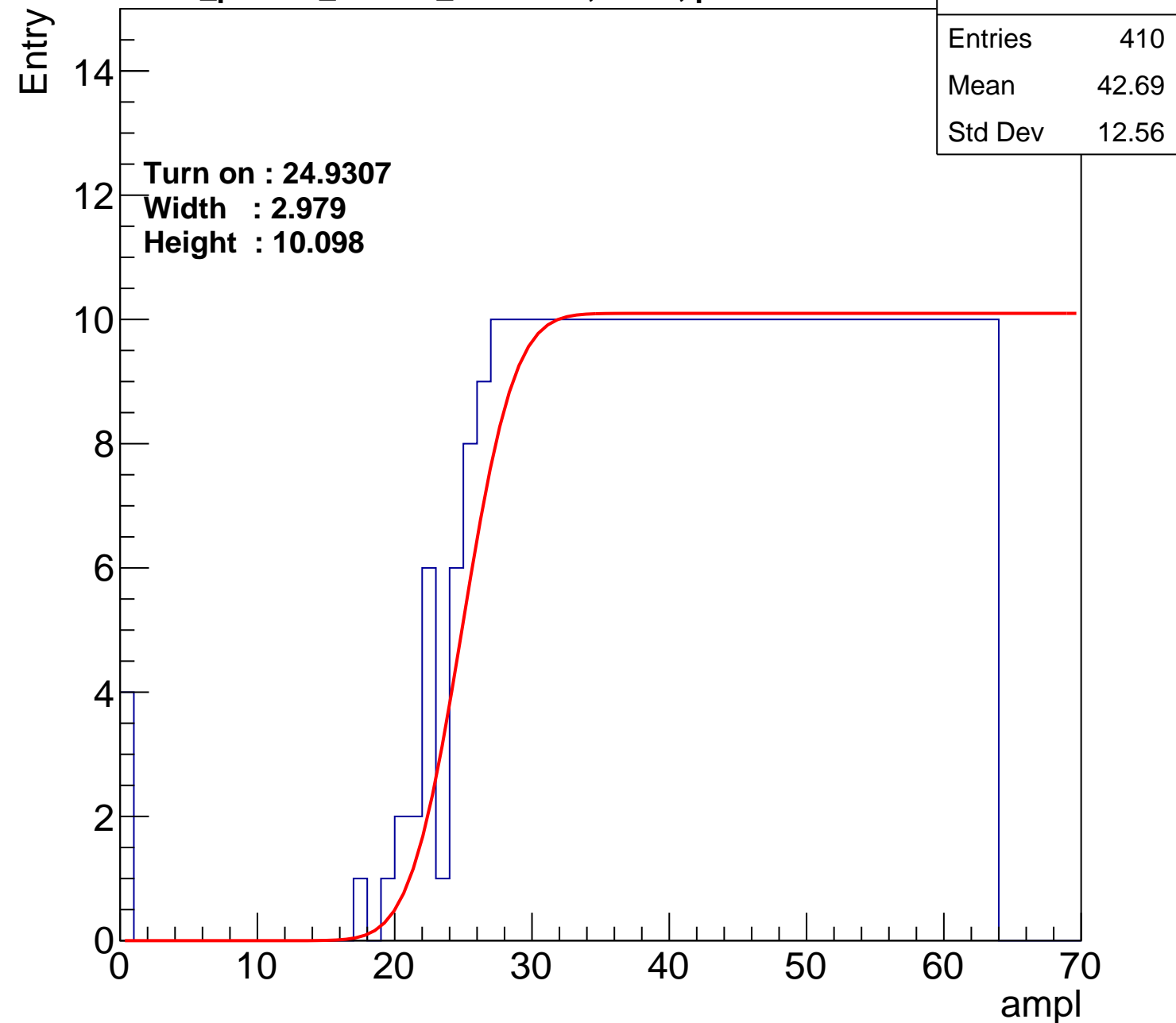
Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U6-ch2

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.61 |
| Std Dev | 11.21 |

Turn on : 27.2821

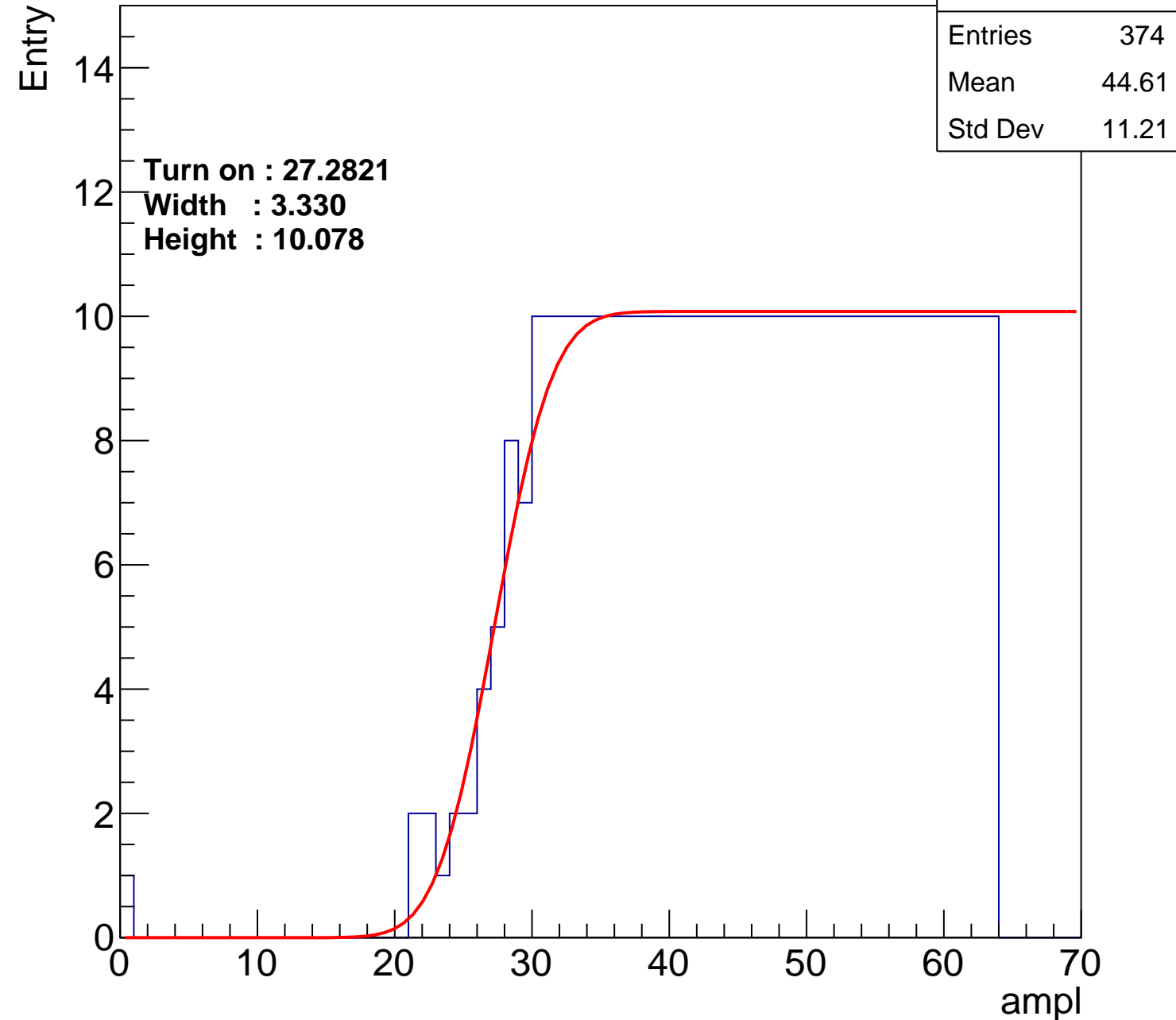
Width : 3.330

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch3

calib_packv5_042523_0143.root, FC#7, port C2

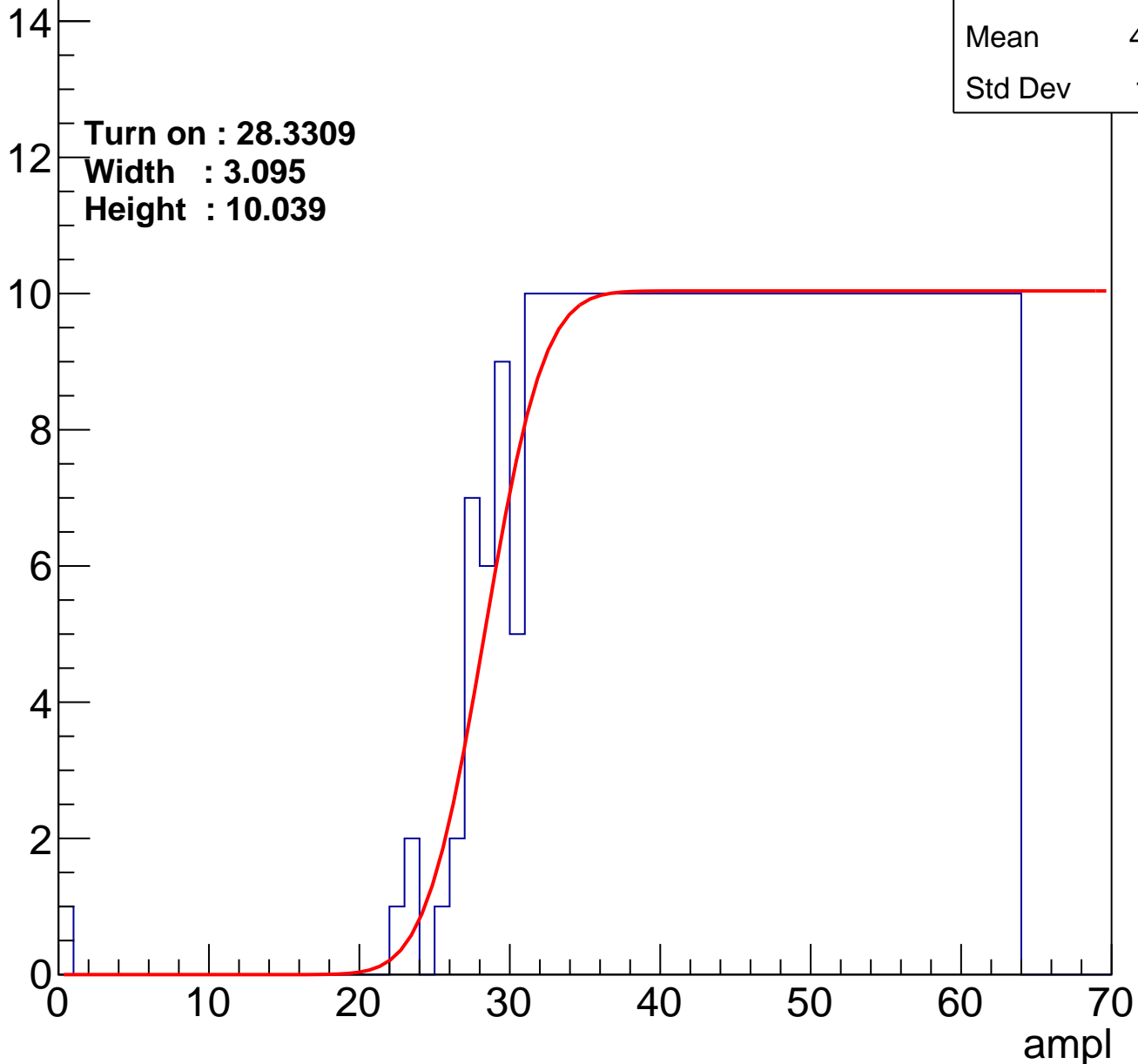
| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.12 |
| Std Dev | 10.91 |

Turn on : 28.3309

Width : 3.095

Height : 10.039

Entry



B1L103S, U6-ch4

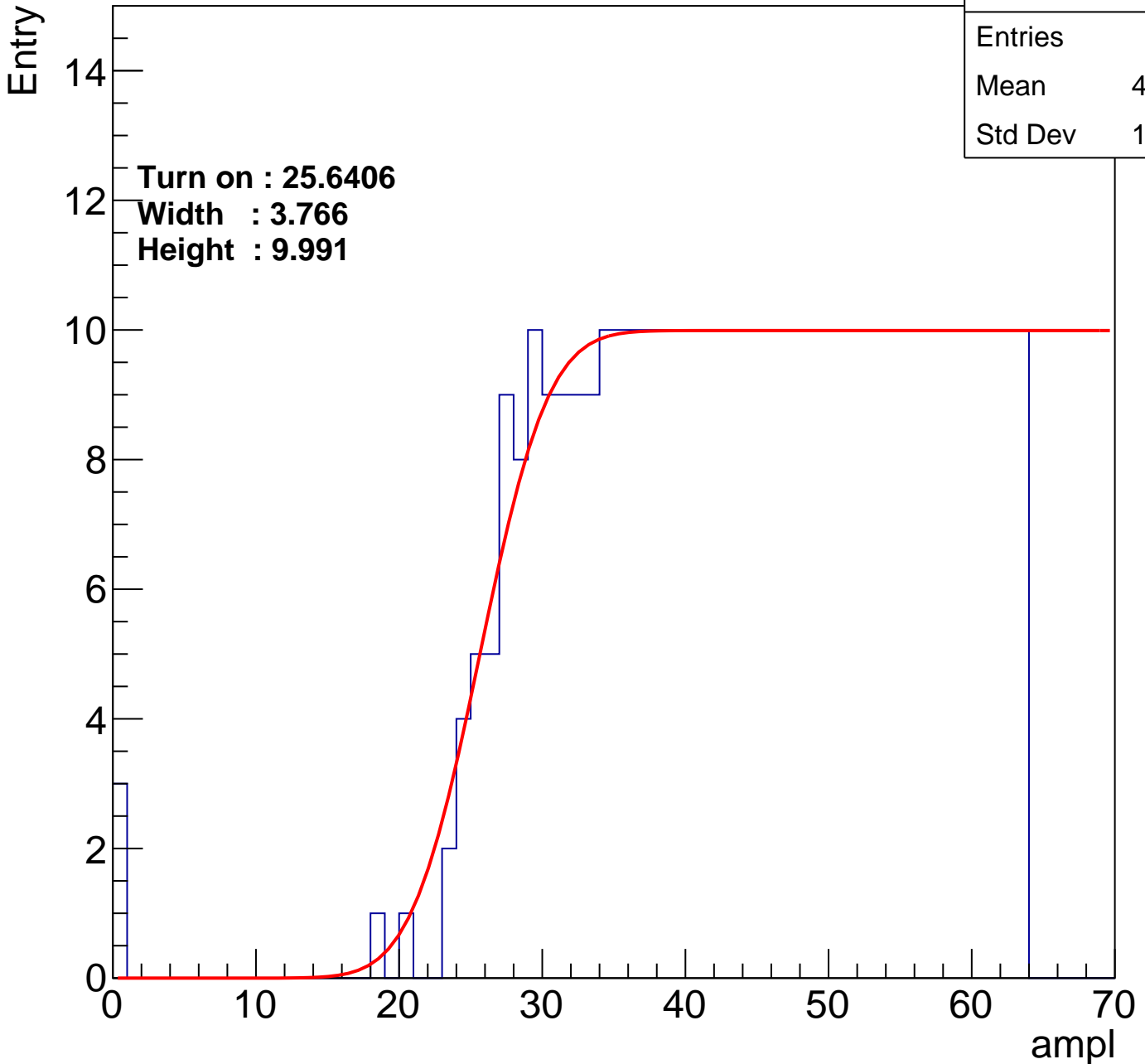
calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.95 |
| Std Dev | 11.87 |

Turn on : 25.6406

Width : 3.766

Height : 9.991



B1L103S, U6-ch5

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.32 |
| Std Dev | 11.14 |

Turn on : 28.6315

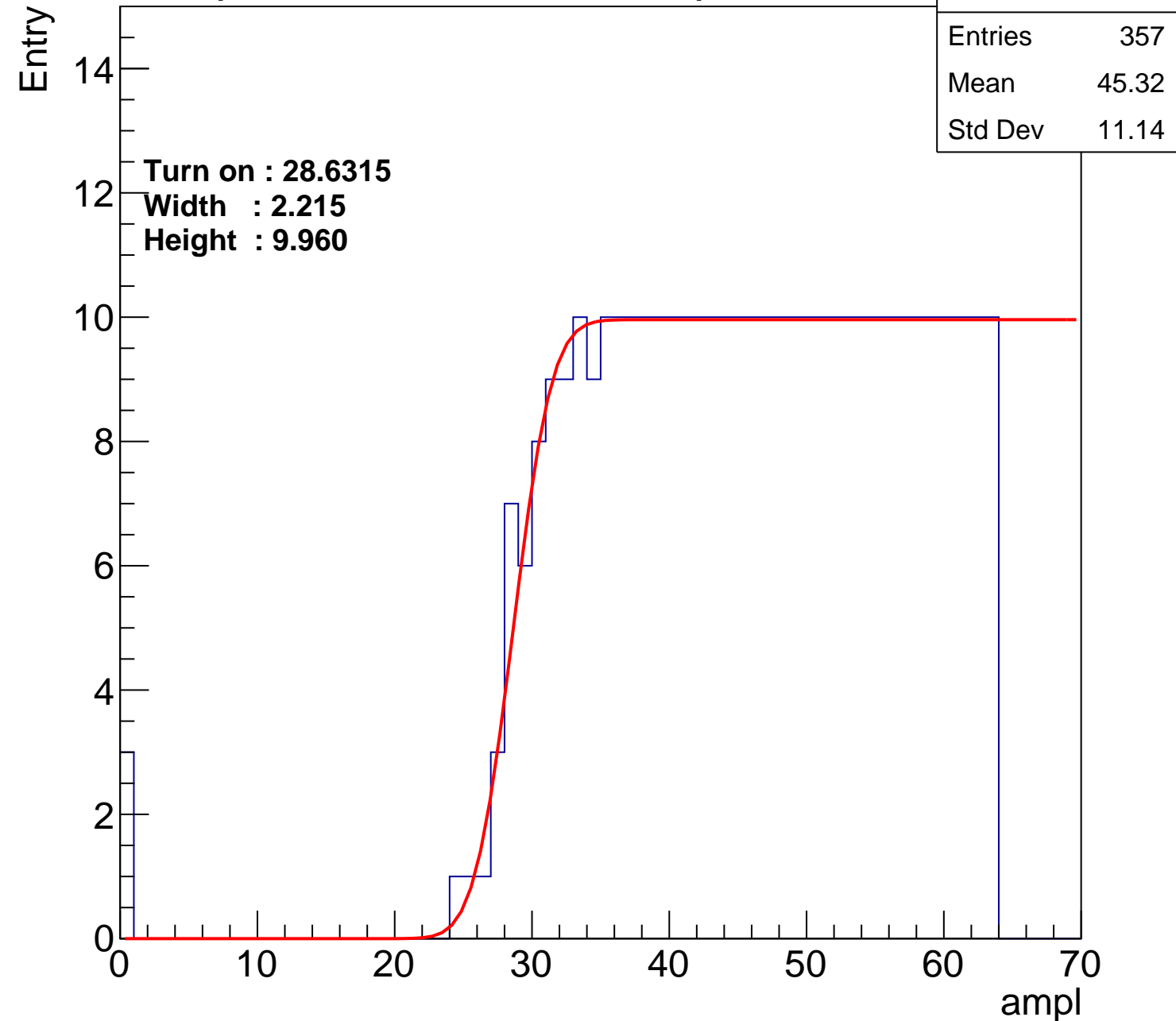
Width : 2.215

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch6

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.54 |
| Std Dev | 12.05 |

Turn on : 25.3861

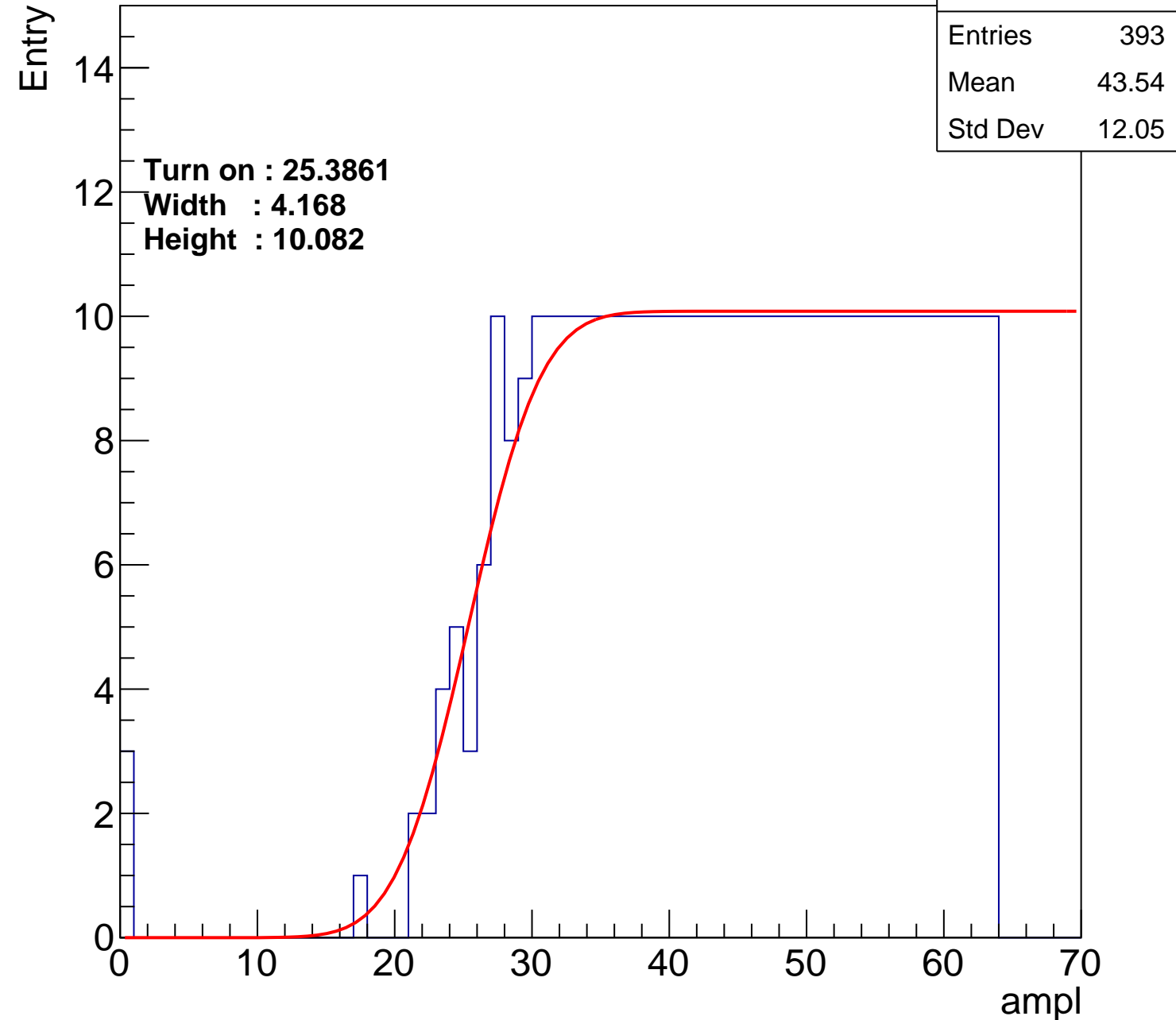
Width : 4.168

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch7

calib_packv5_042523_0143.root, FC#7, port C2

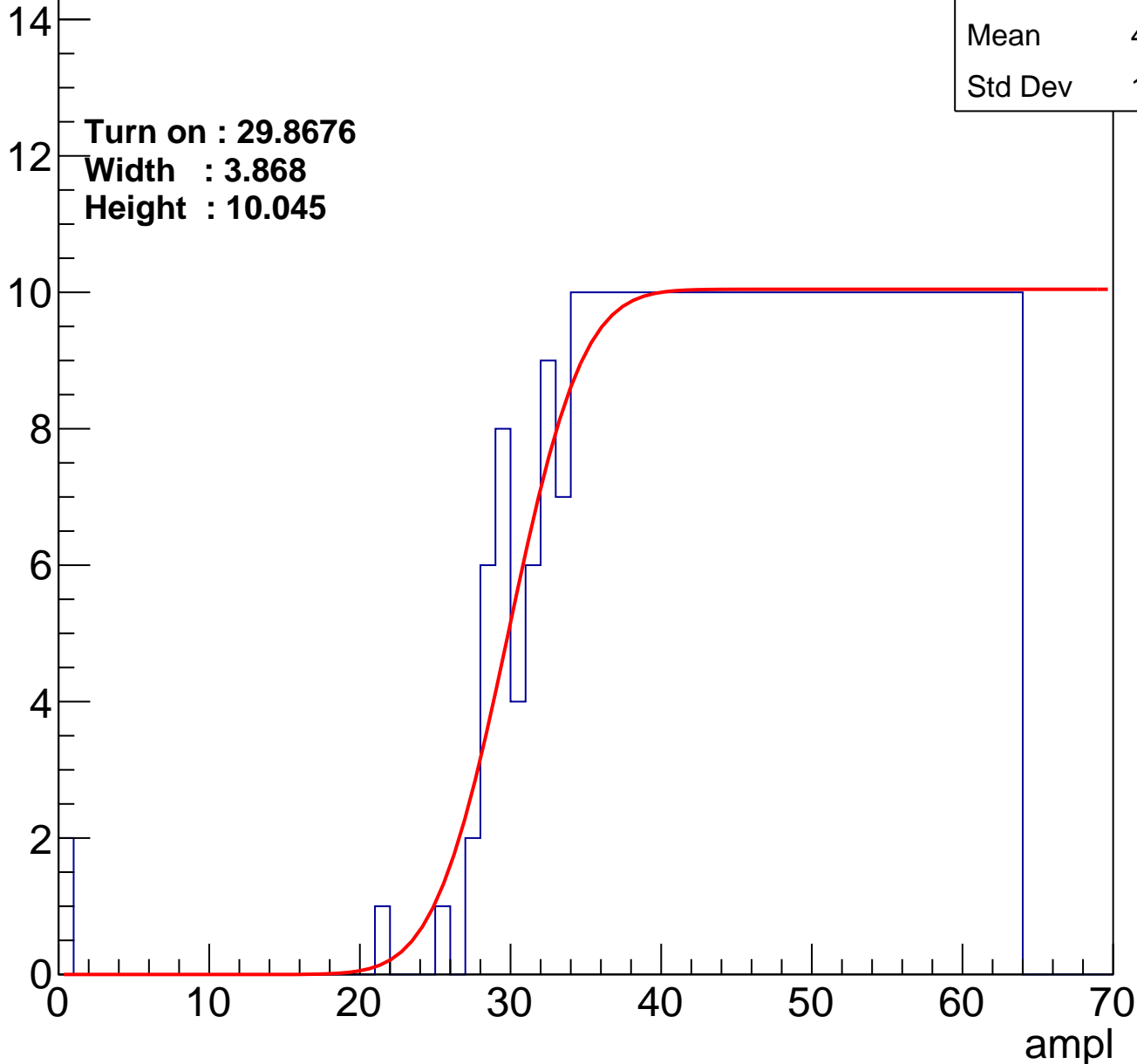
| | |
|---------|-------|
| Entries | 346 |
| Mean | 45.88 |
| Std Dev | 10.74 |

Turn on : 29.8676

Width : 3.868

Height : 10.045

Entry



B1L103S, U6-ch8

calib_packv5_042523_0143.root, FC#7, port C2

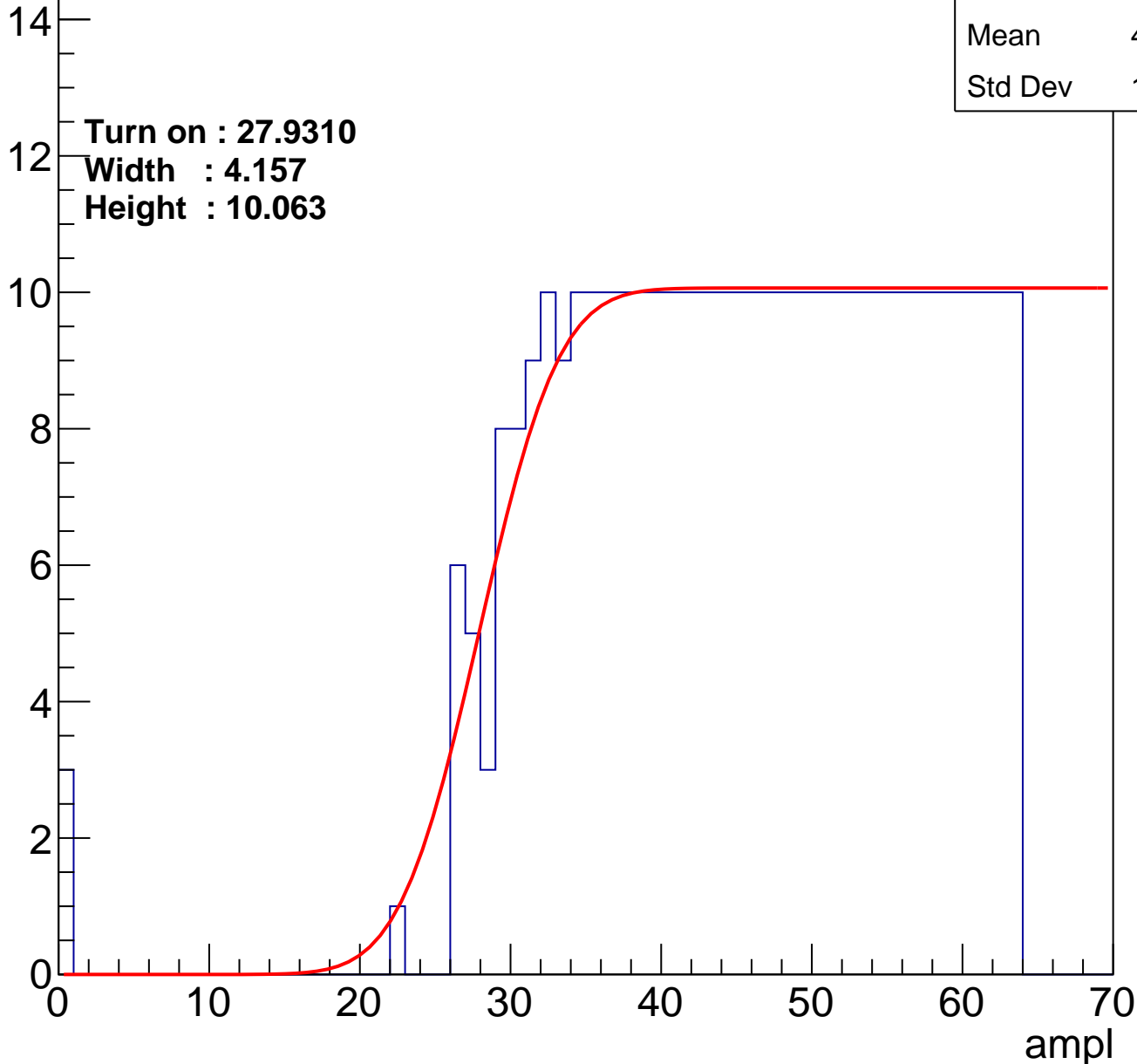
| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.07 |
| Std Dev | 11.27 |

Turn on : 27.9310

Width : 4.157

Height : 10.063

Entry



B1L103S, U6-ch9

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.02 |
| Std Dev | 11.74 |

Turn on : 25.5673

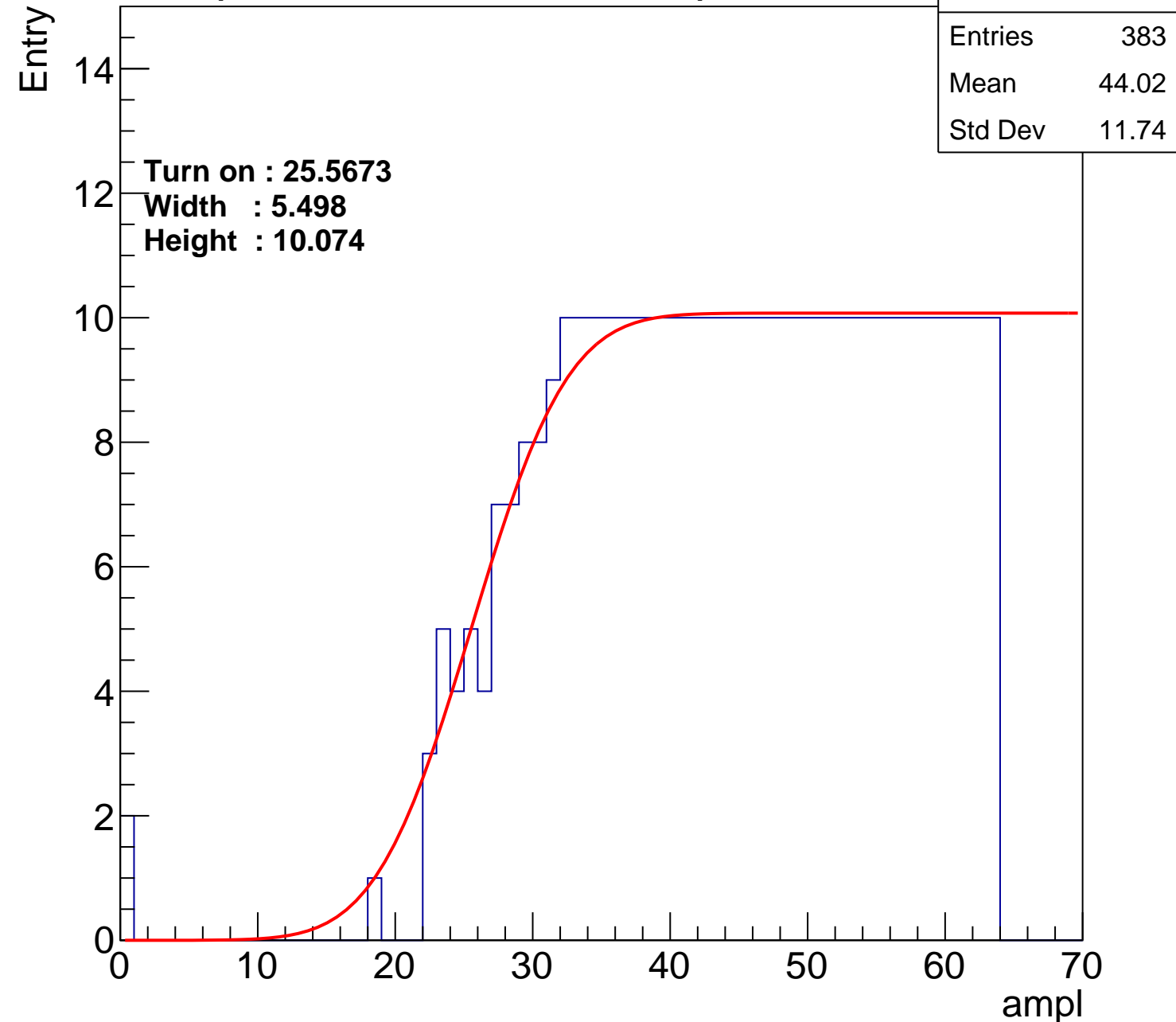
Width : 5.498

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch10

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.19 |
| Std Dev | 11.52 |

Turn on : 25.9564

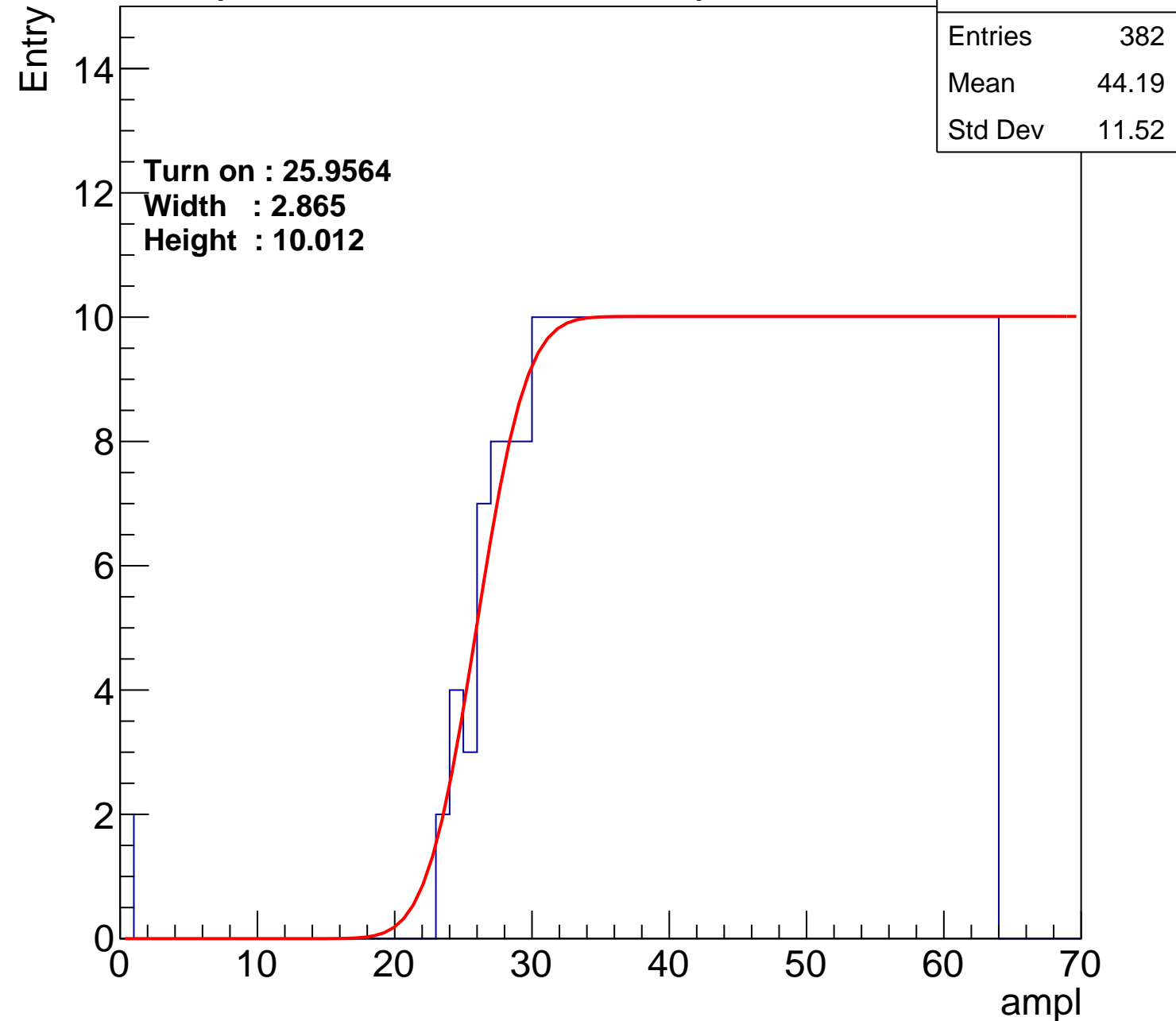
Width : 2.865

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch11

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.19 |
| Std Dev | 11.96 |

Turn on : 26.5925

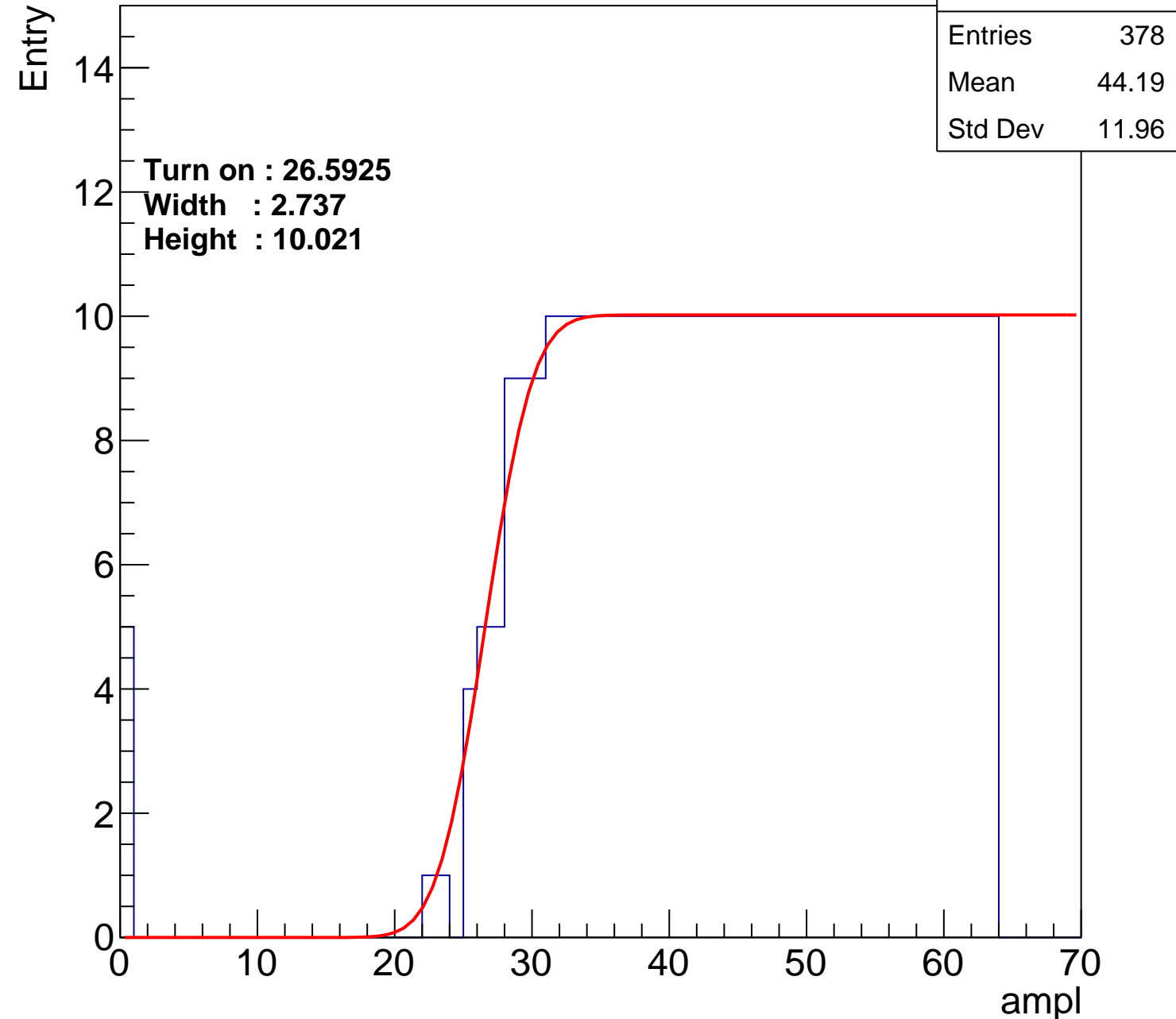
Width : 2.737

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch12

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.54 |
| Std Dev | 12.04 |

Turn on : 24.8586

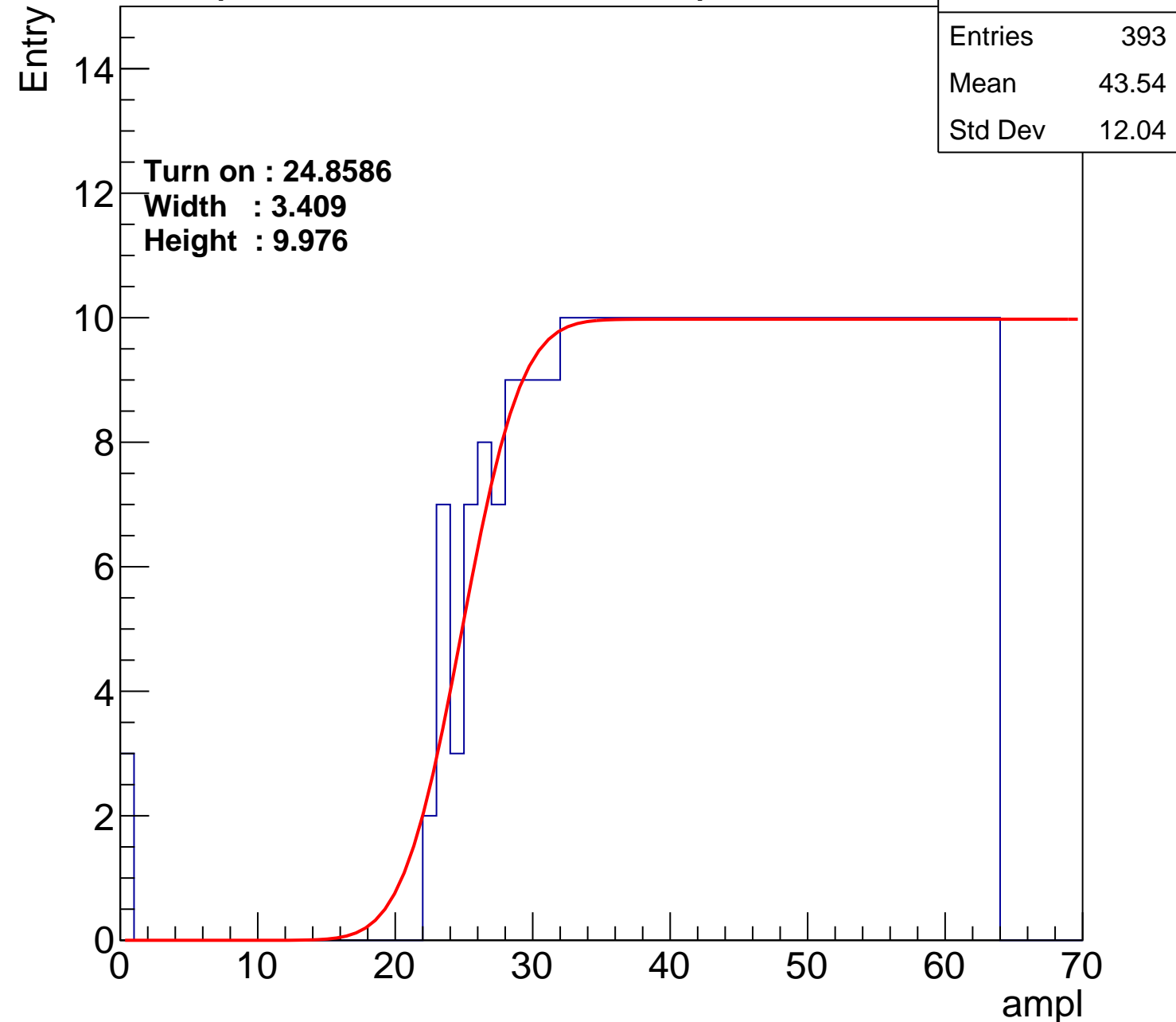
Width : 3.409

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch13

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.99 |
| Std Dev | 11.52 |

Turn on : 25.7498

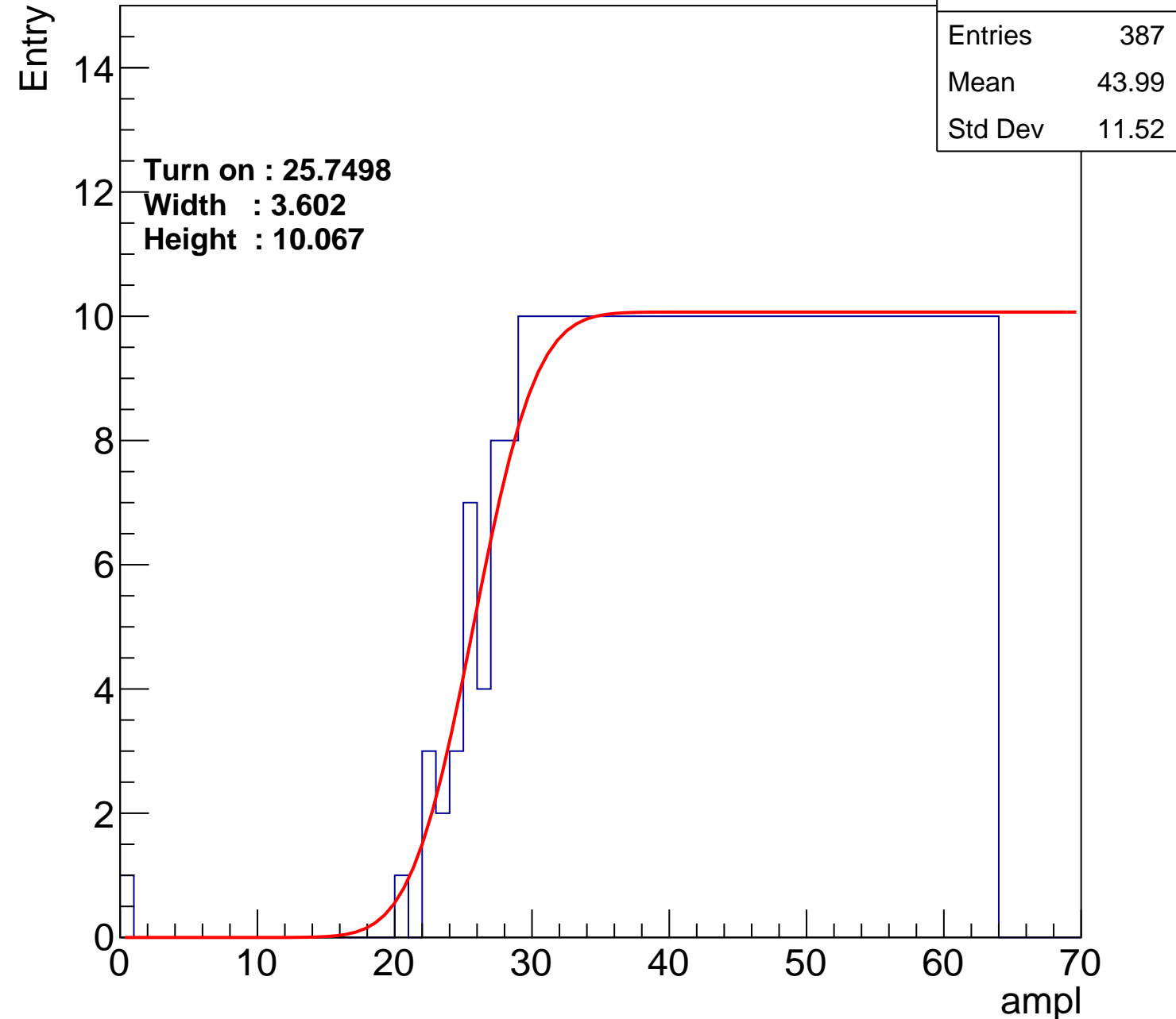
Width : 3.602

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch14

calib_packv5_042523_0143.root, FC#7, port C2

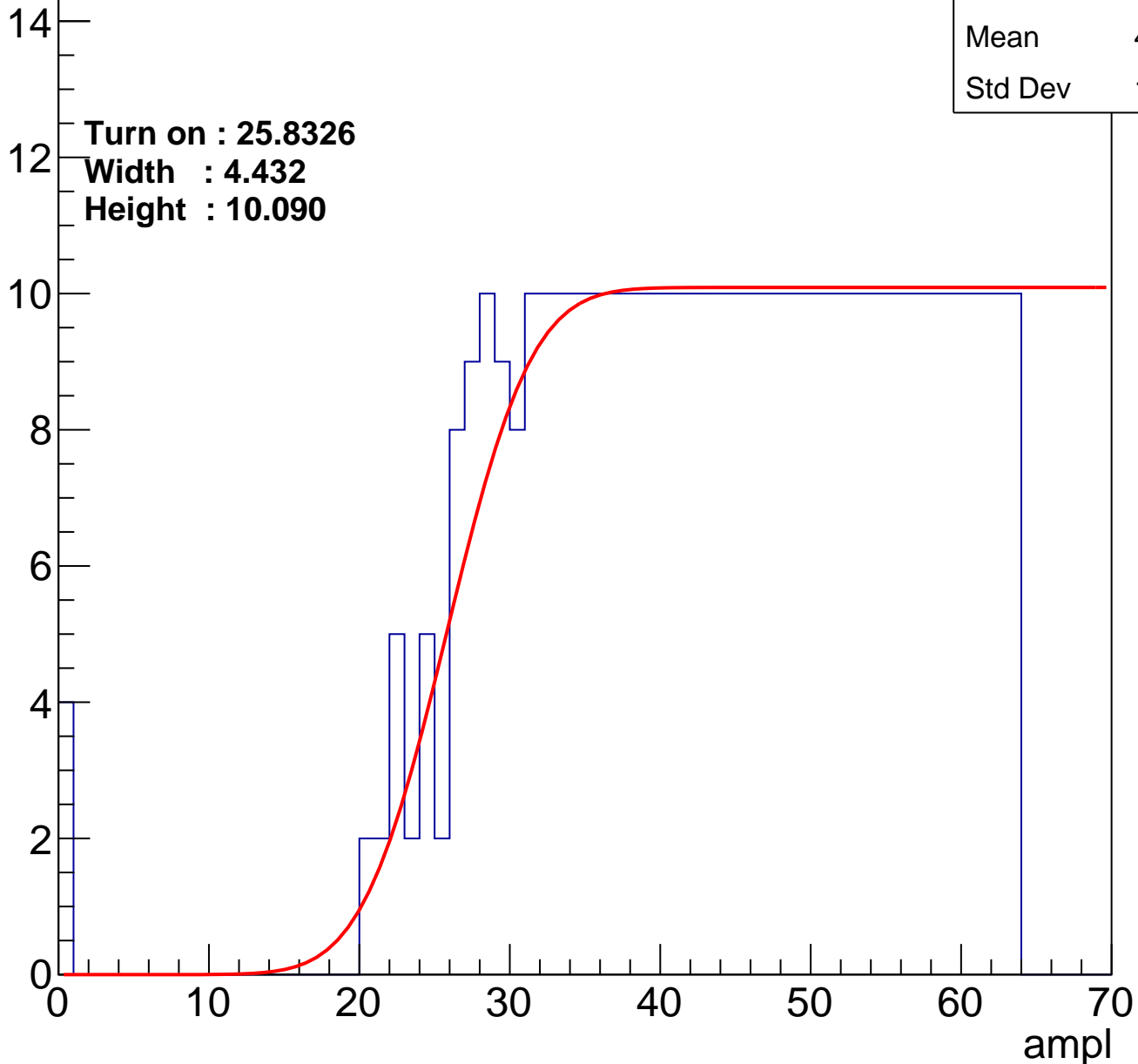
| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.31 |
| Std Dev | 12.31 |

Turn on : 25.8326

Width : 4.432

Height : 10.090

Entry



B1L103S, U6-ch15

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.7 |
| Std Dev | 11.11 |

Turn on : 27.2345

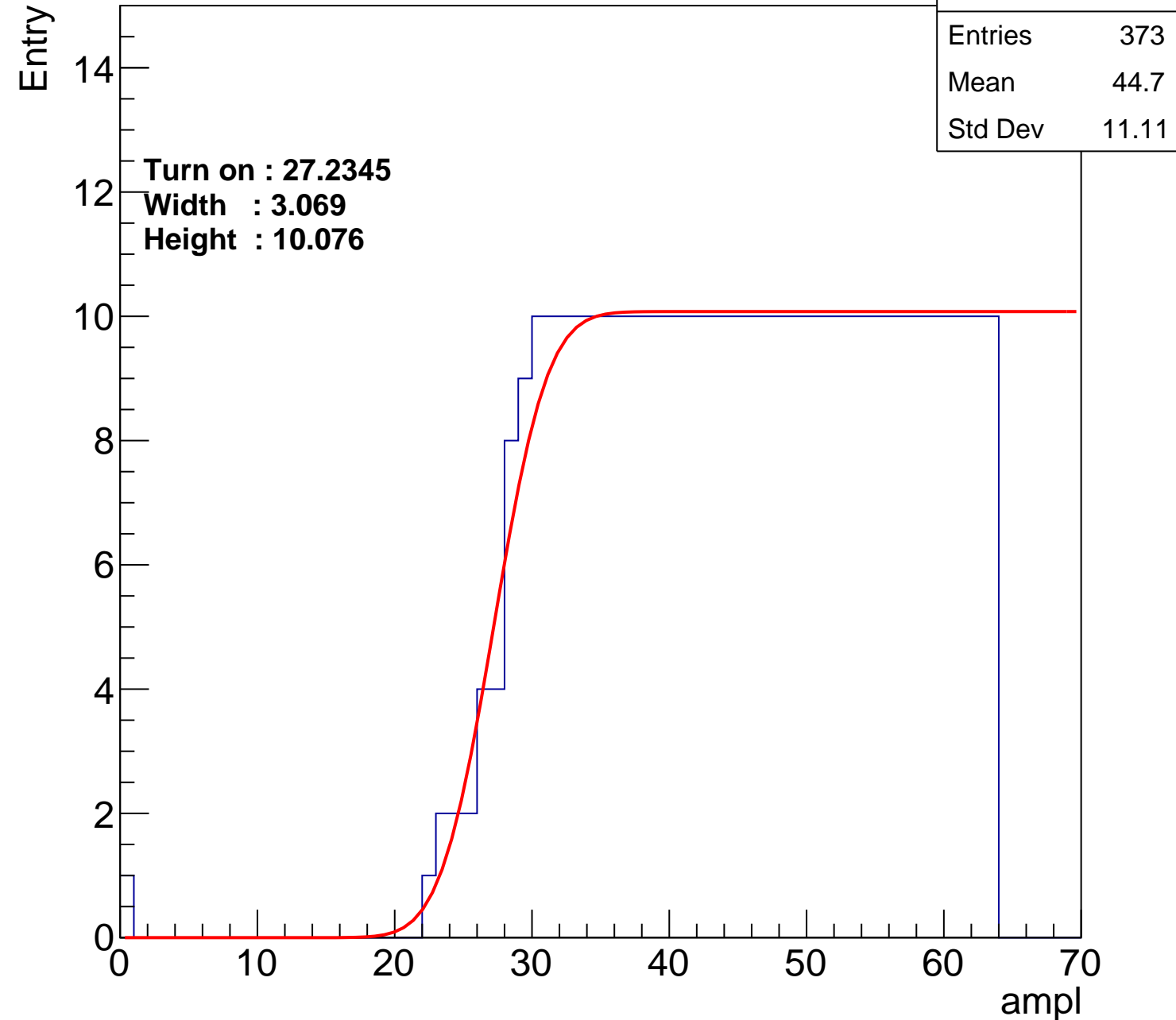
Width : 3.069

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch16

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.79 |
| Std Dev | 12.28 |

Turn on : 26.1589

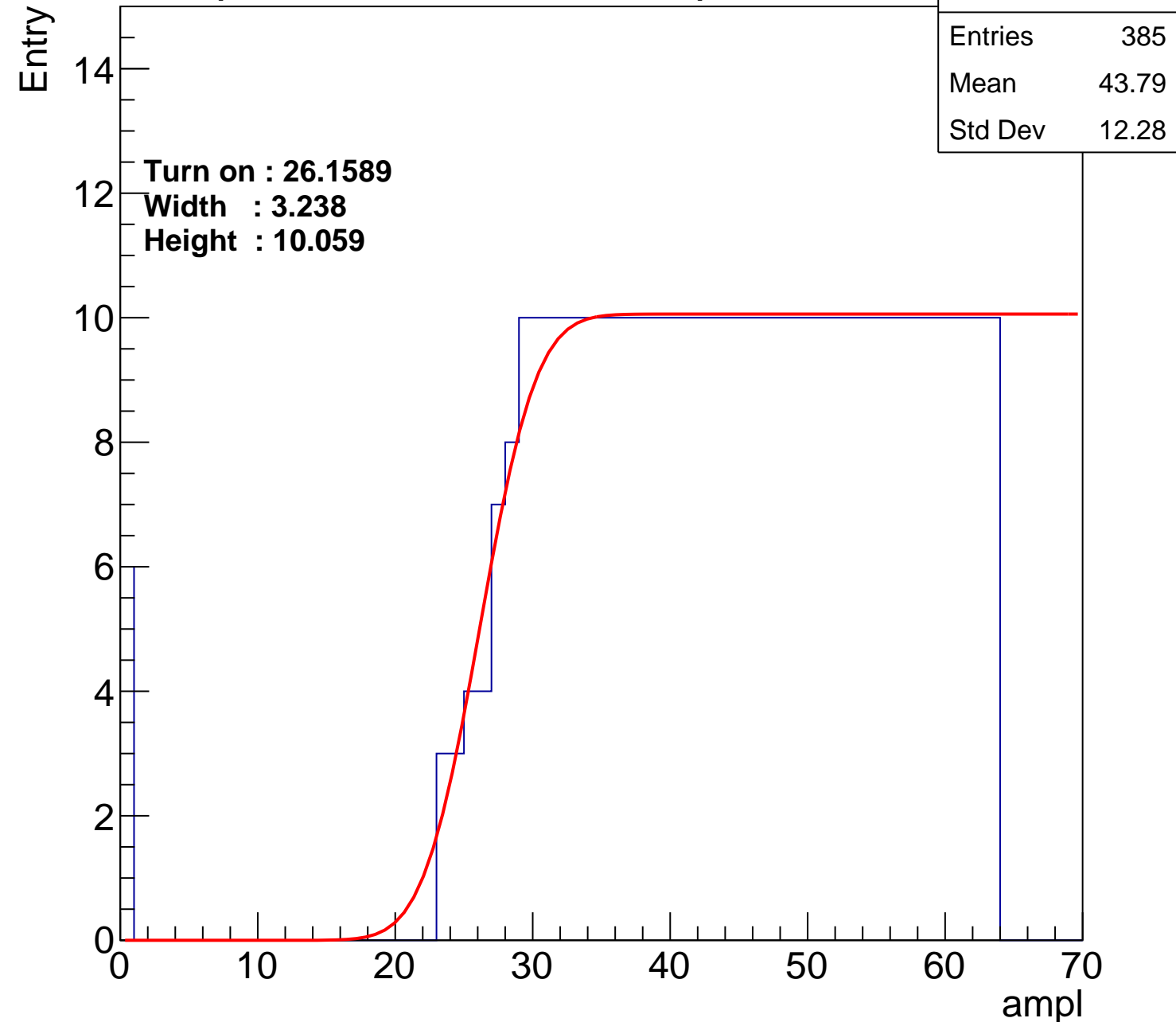
Width : 3.238

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch17

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.12 |
| Std Dev | 11.64 |

Turn on : 26.5175

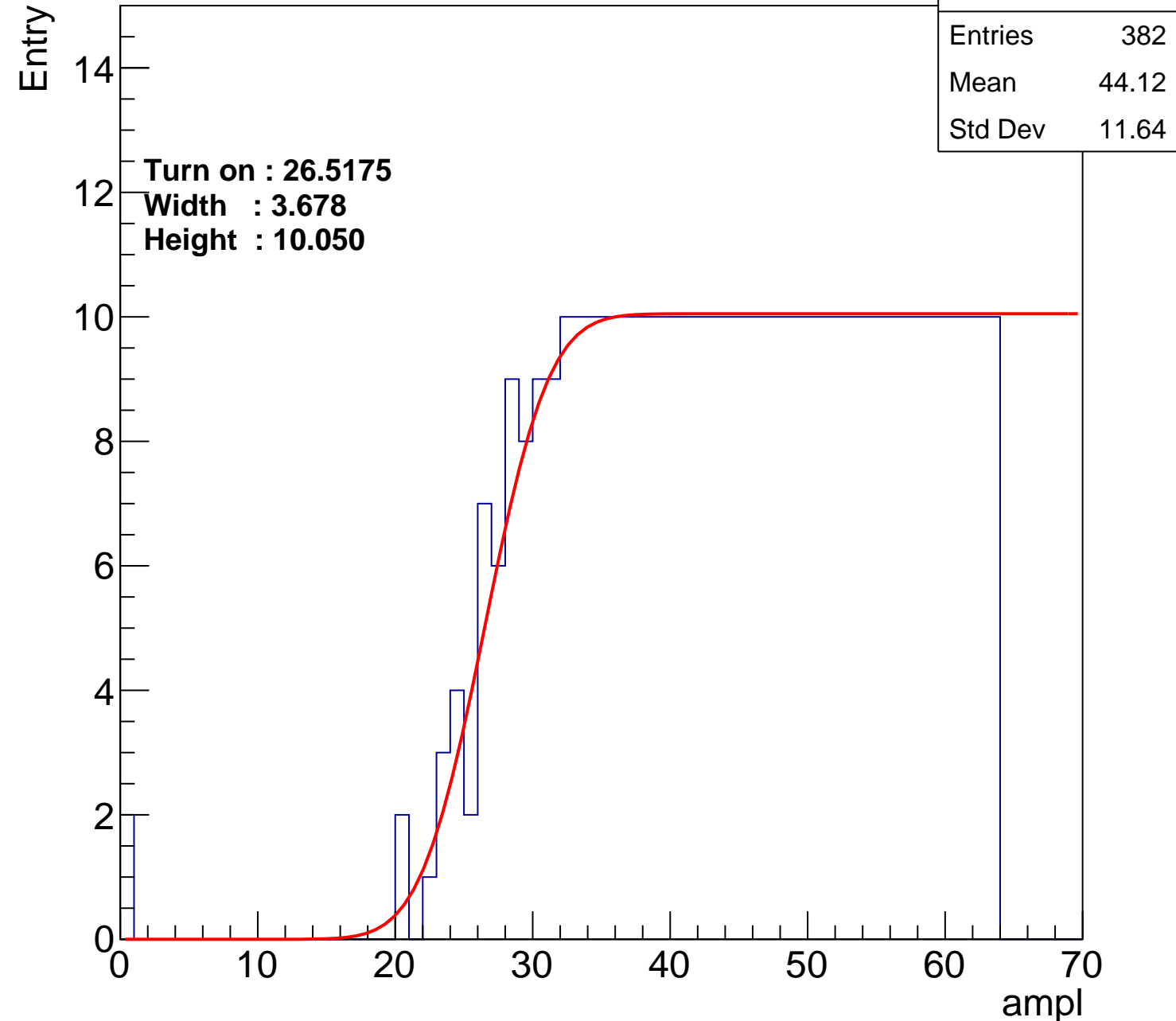
Width : 3.678

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch18

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.11 |
| Std Dev | 12.03 |

Turn on : 26.7001

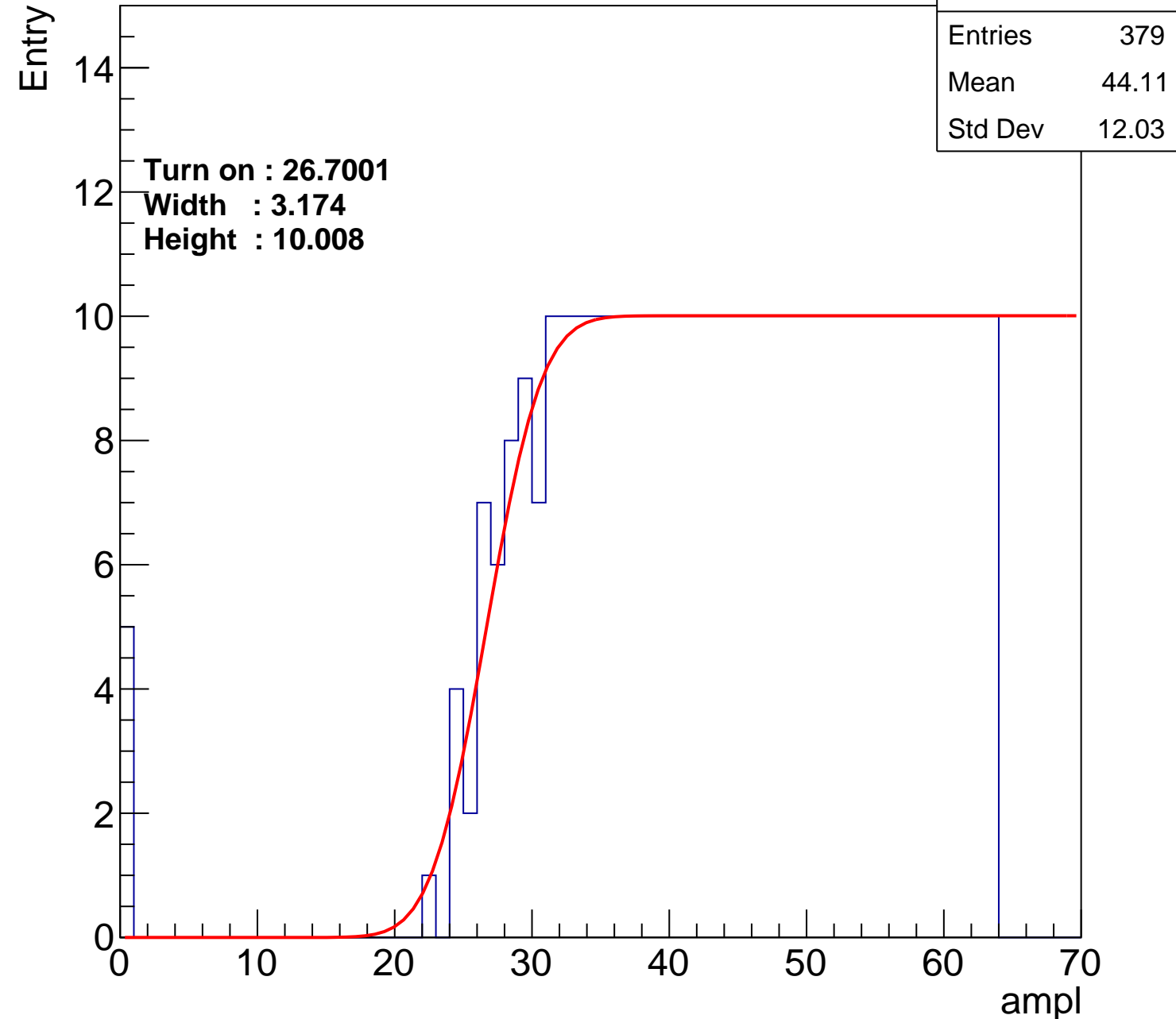
Width : 3.174

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch19

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.92 |
| Std Dev | 11.05 |

Turn on : 27.3596

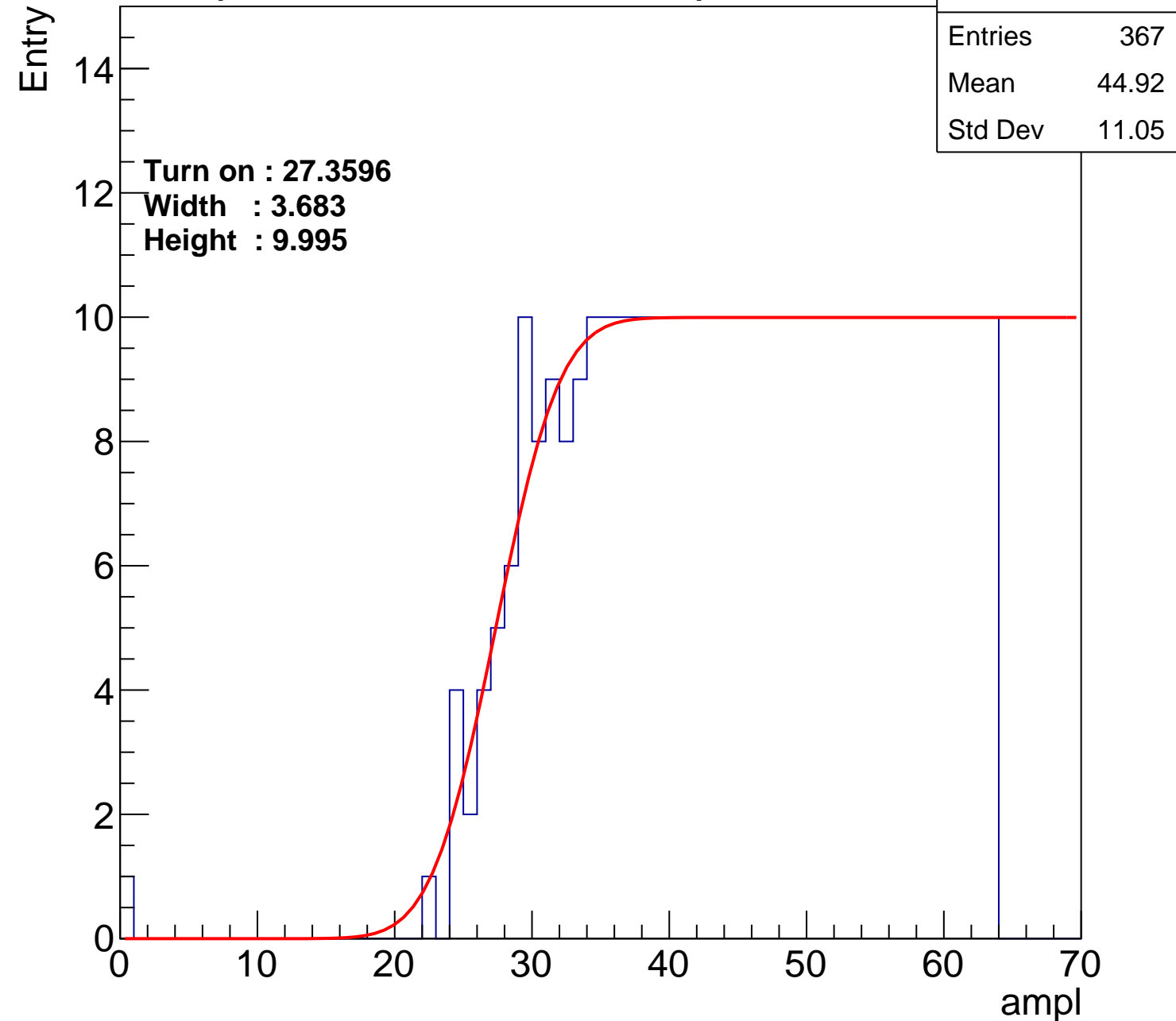
Width : 3.683

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch20

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.29 |
| Std Dev | 10.99 |

Turn on : 28.5397

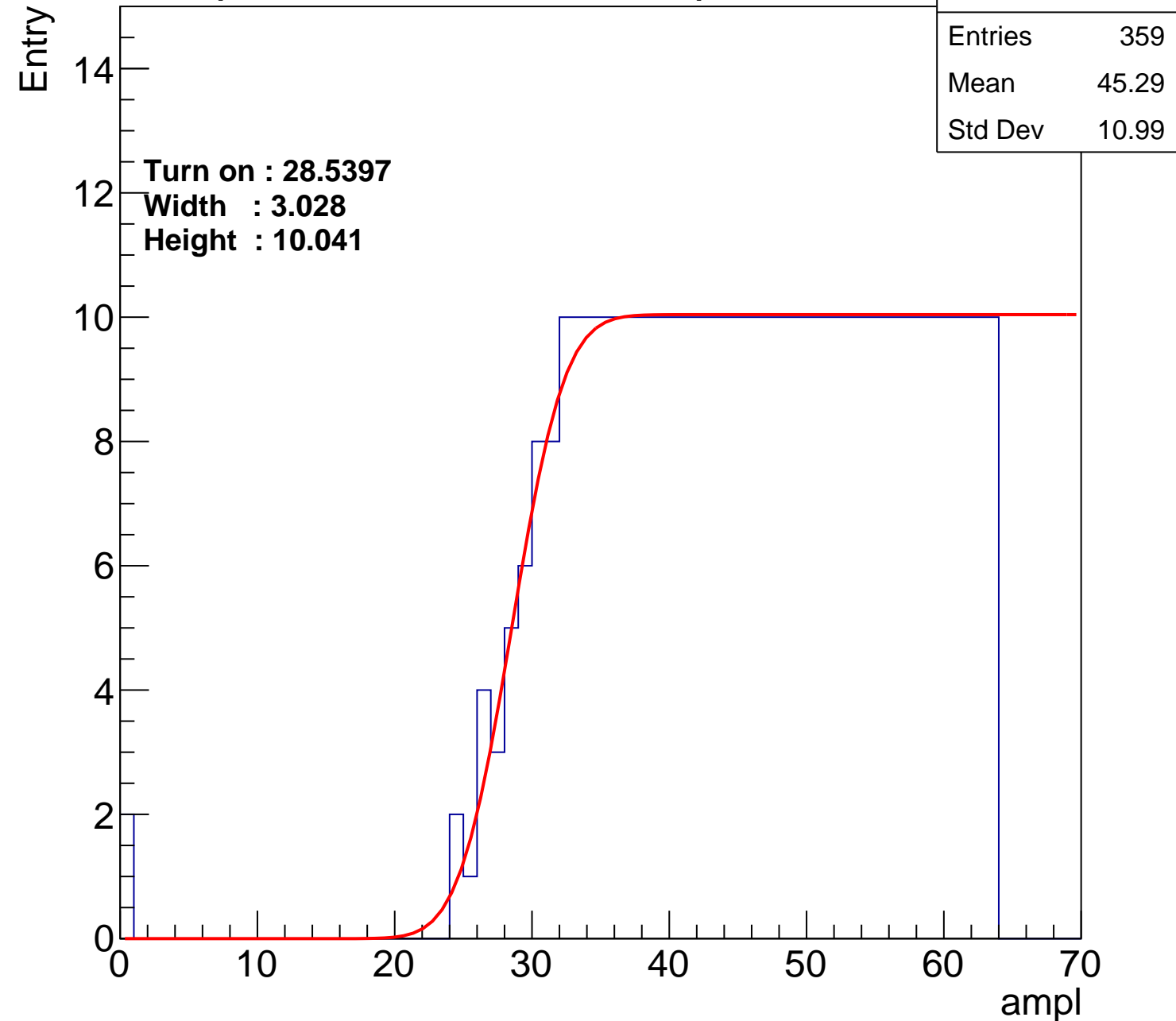
Width : 3.028

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch21

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.04 |
| Std Dev | 10.93 |

Turn on : 27.6595

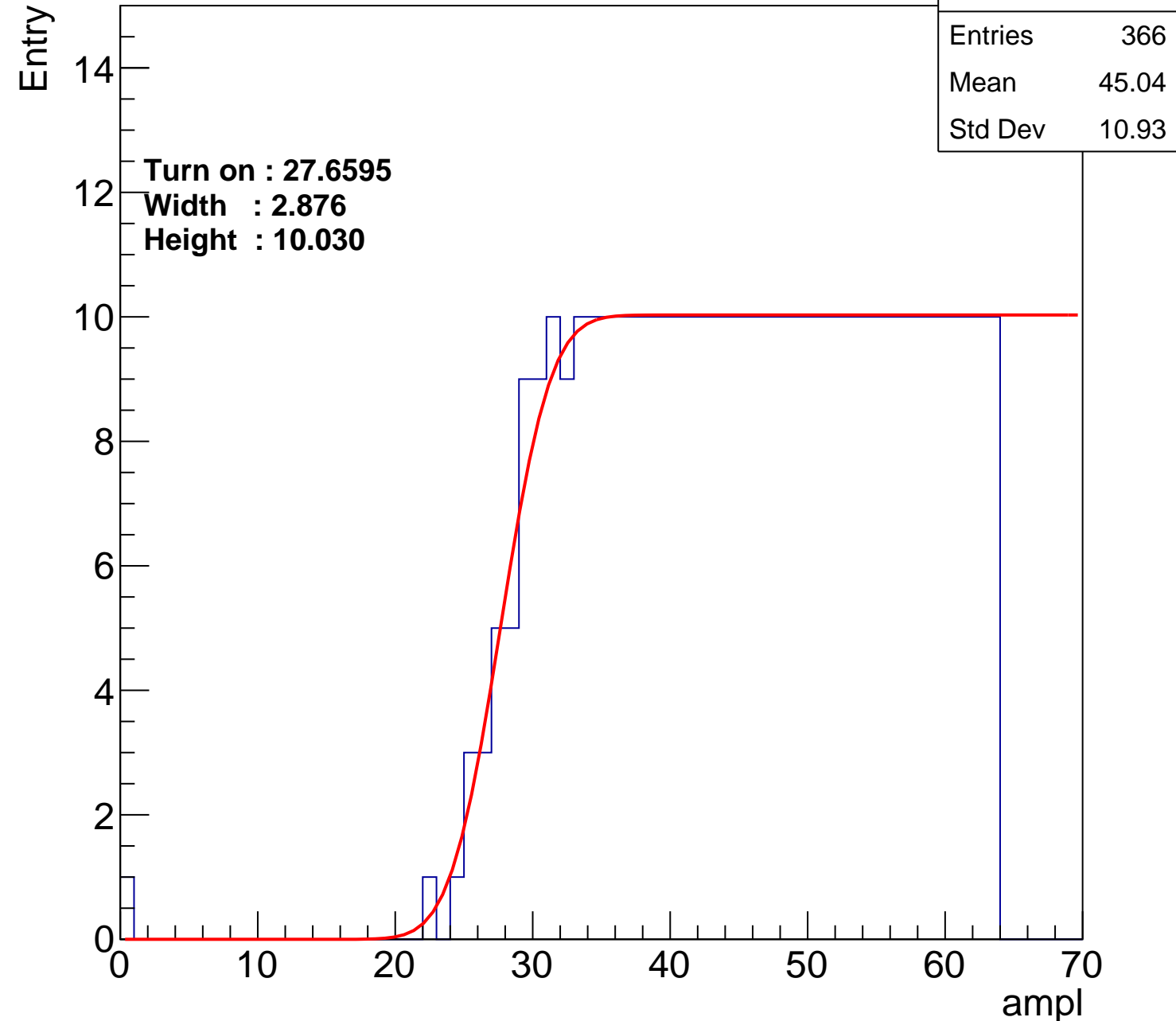
Width : 2.876

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch22

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.72 |
| Std Dev | 11.87 |

Turn on : 25.2657

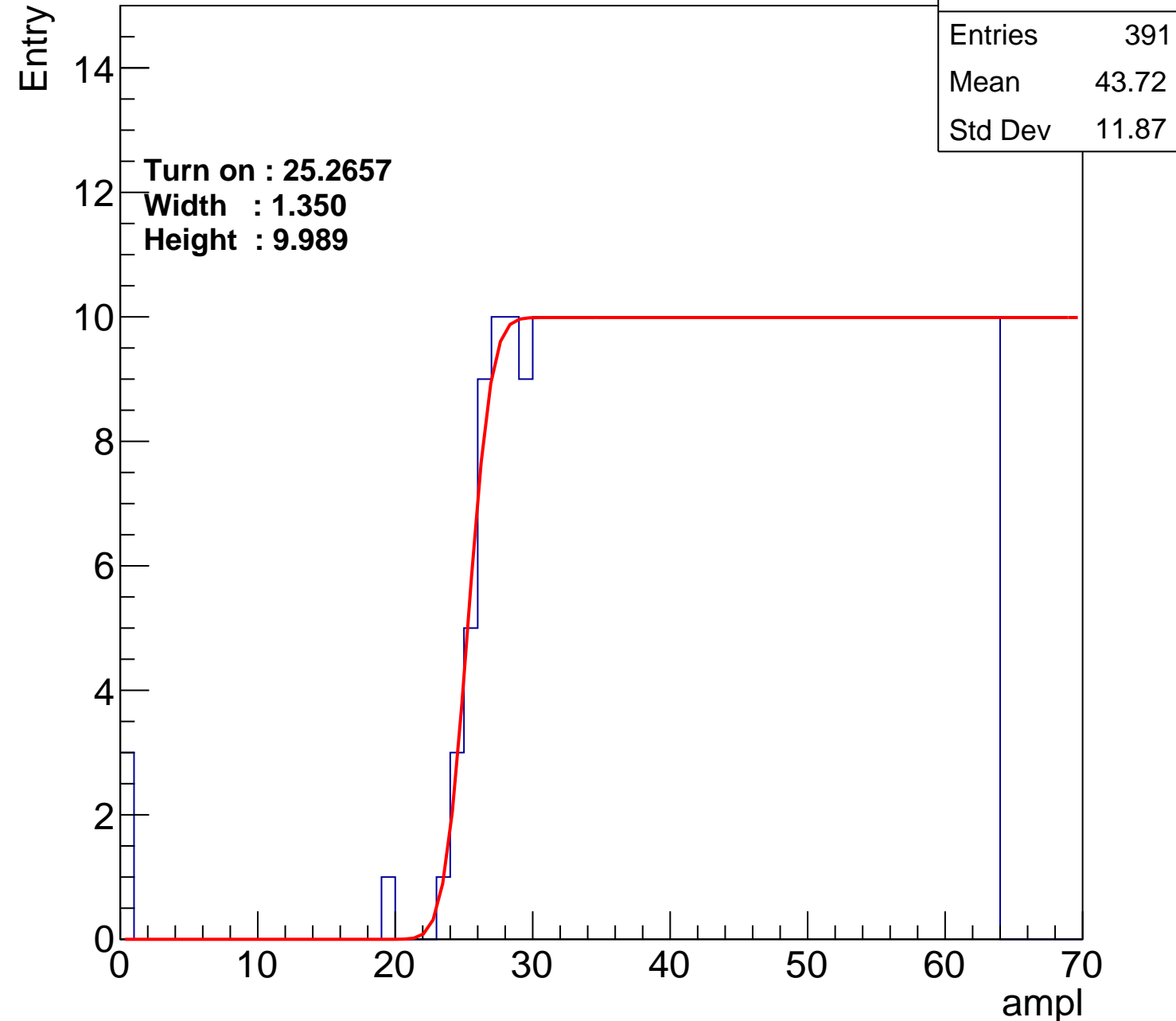
Width : 1.350

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch23

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.18 |
| Std Dev | 11.54 |

Turn on : 26.3247

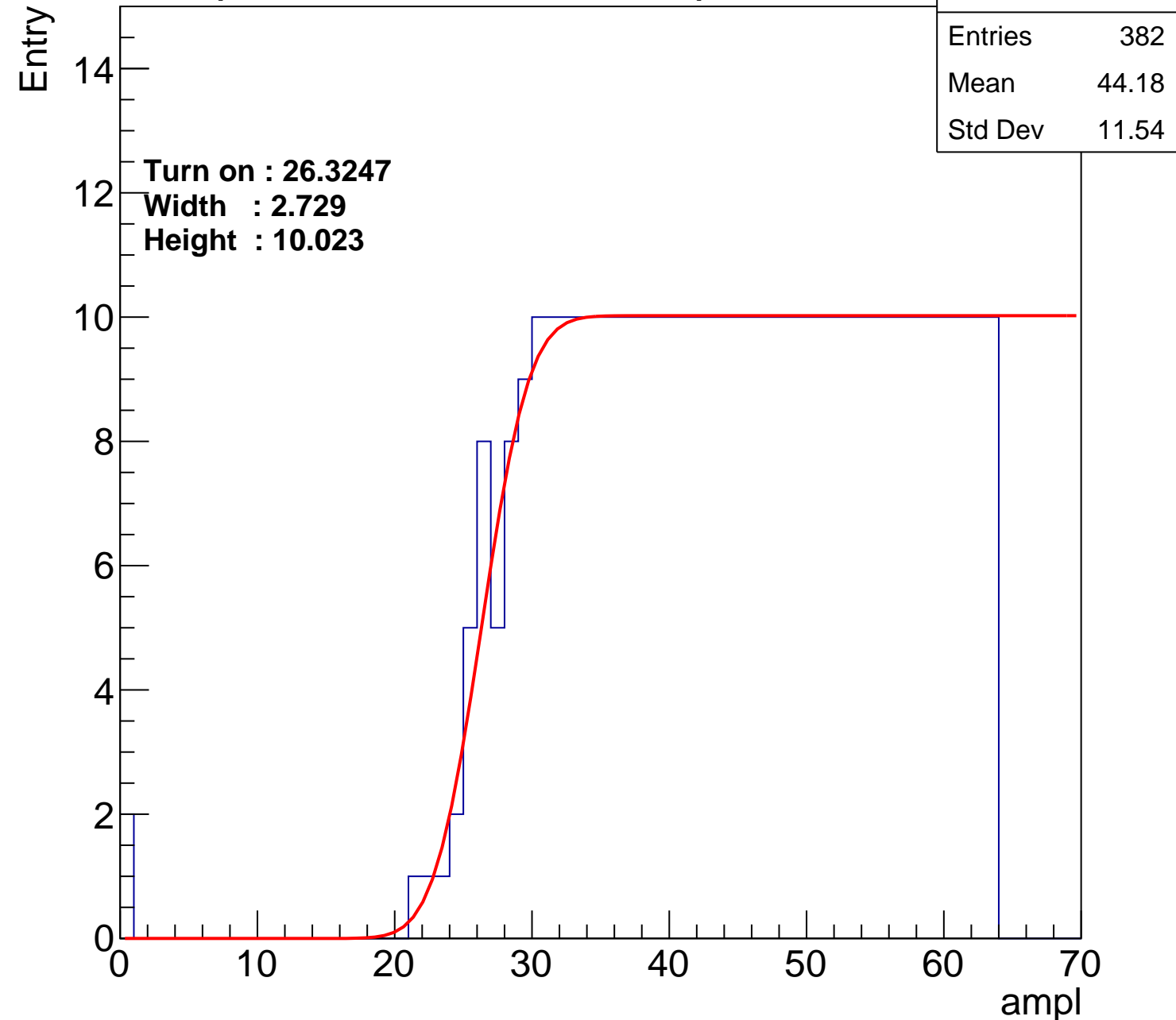
Width : 2.729

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch24

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.09 |
| Std Dev | 11.5 |

Turn on : 25.8800

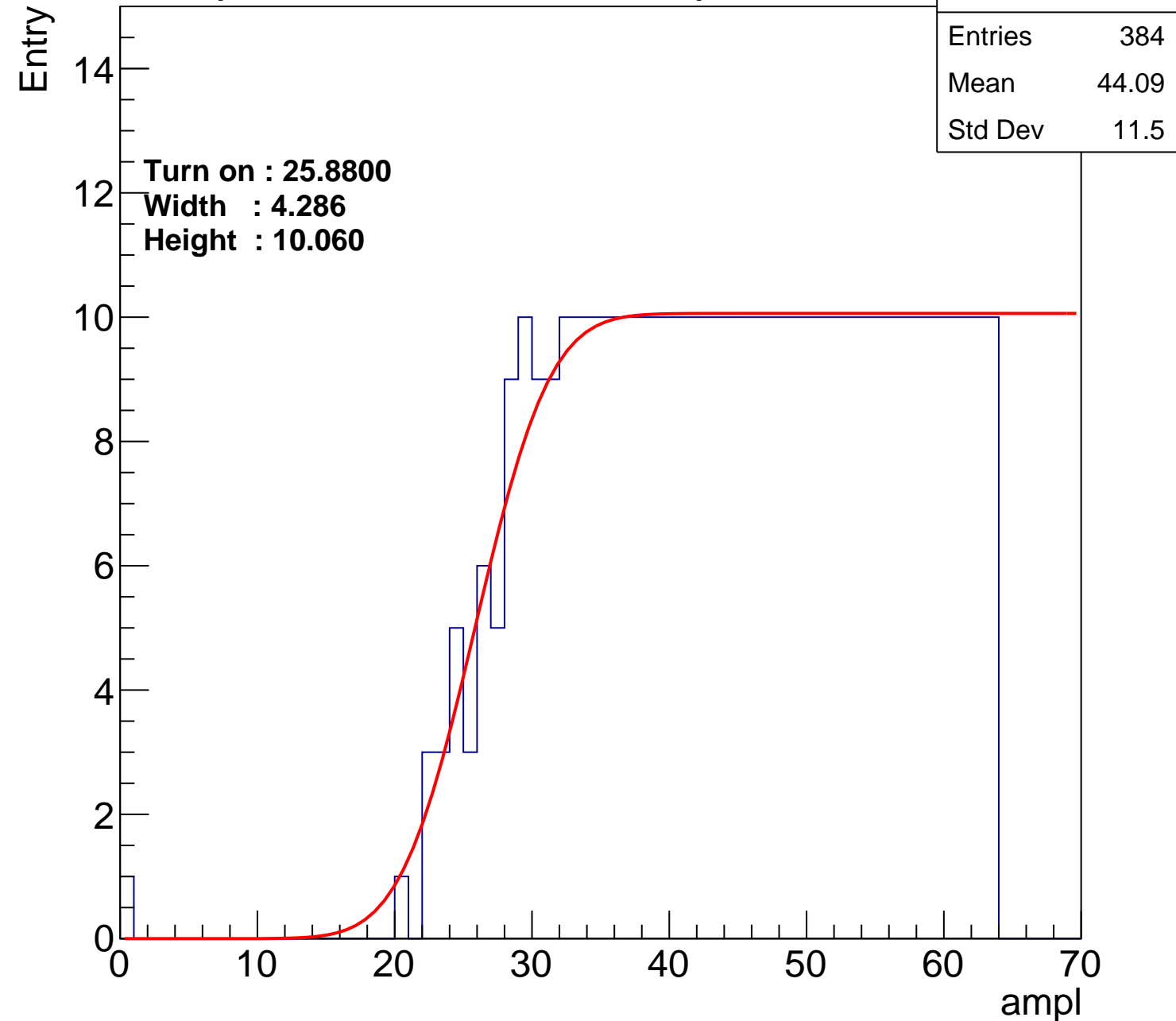
Width : 4.286

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch25

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.74 |
| Std Dev | 11.76 |

Turn on : 25.1419

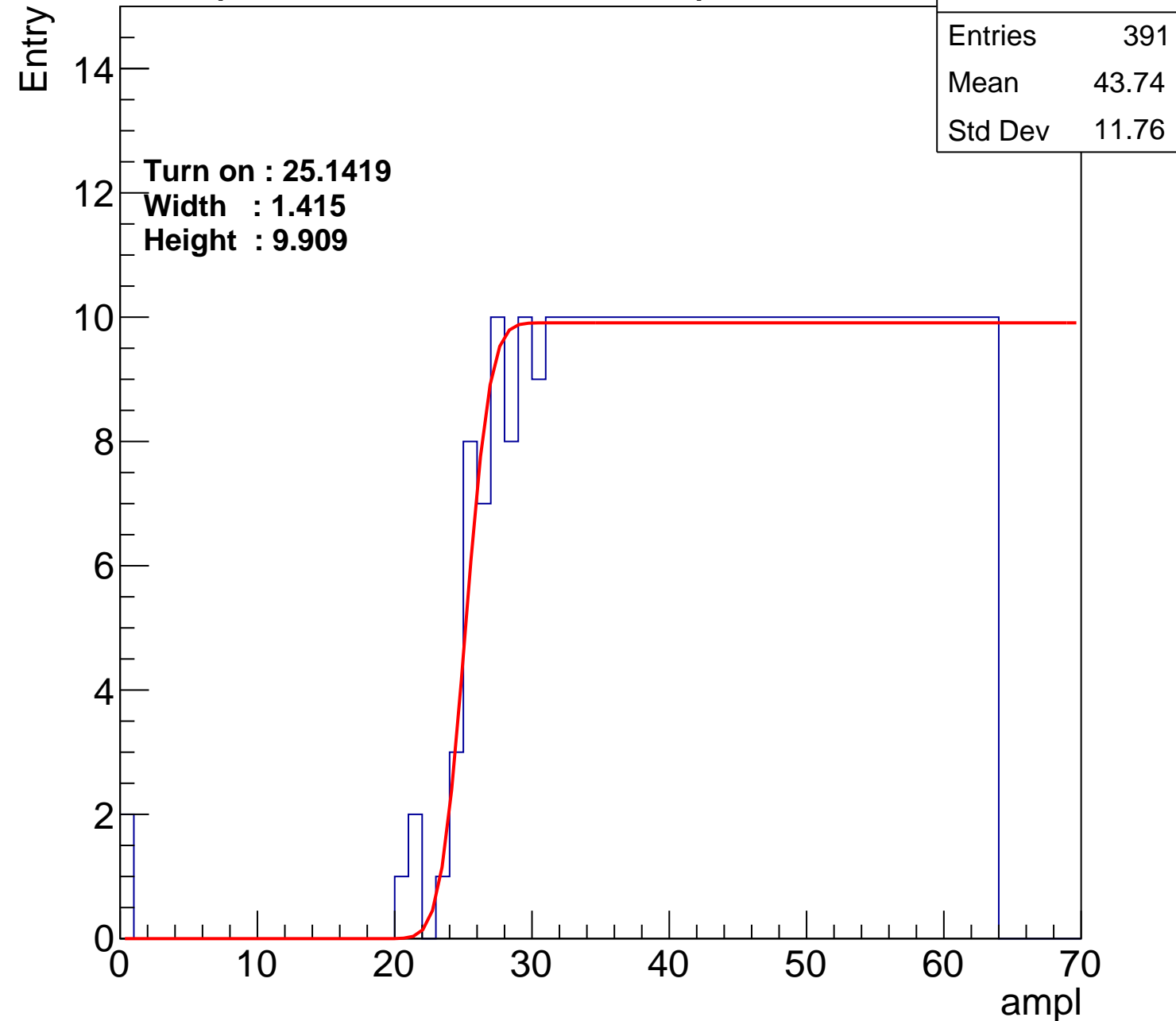
Width : 1.415

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch26

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.03 |
| Std Dev | 10.99 |

Turn on : 28.0083

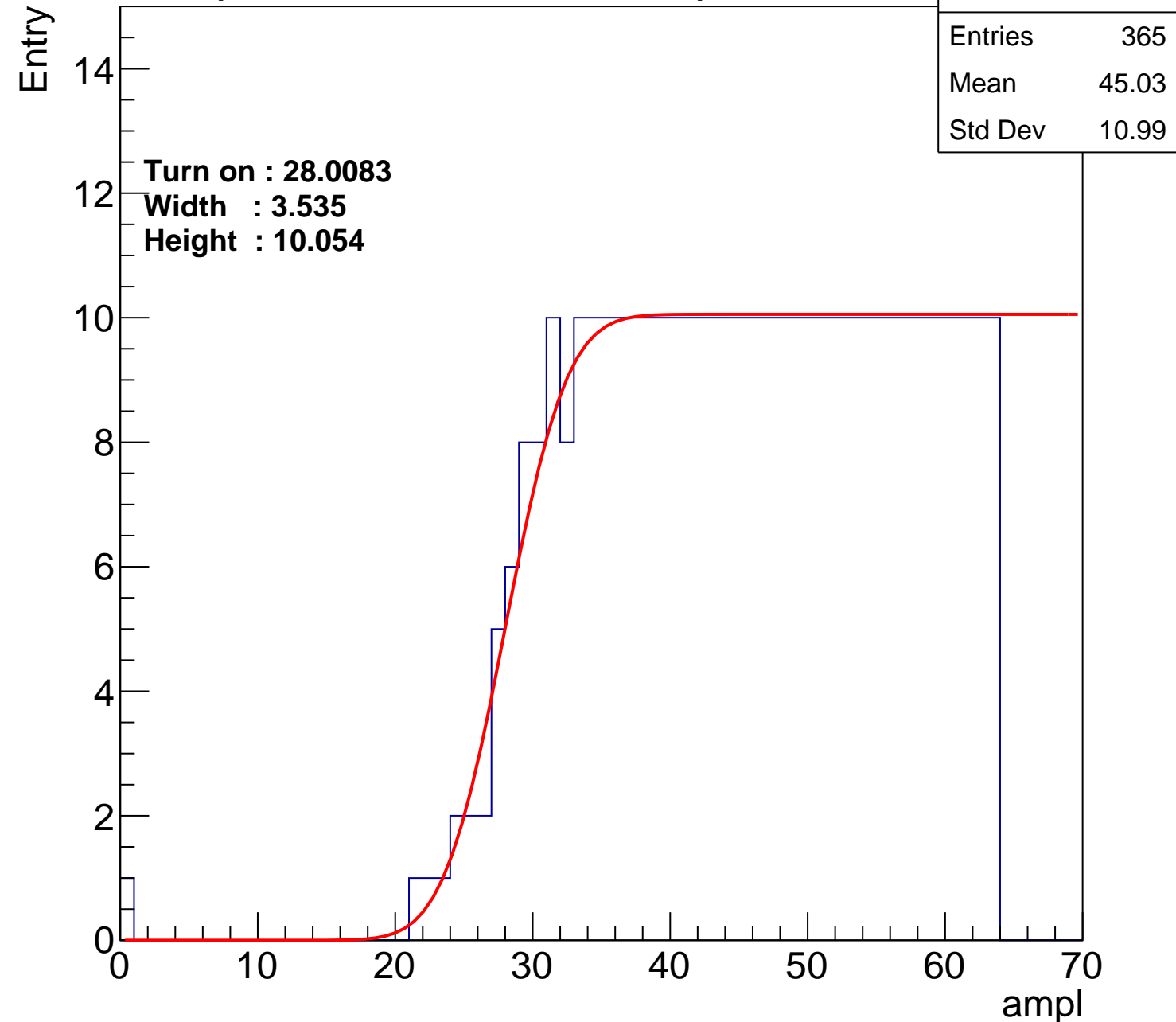
Width : 3.535

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch27

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.56 |
| Std Dev | 11.69 |

Turn on : 27.7427

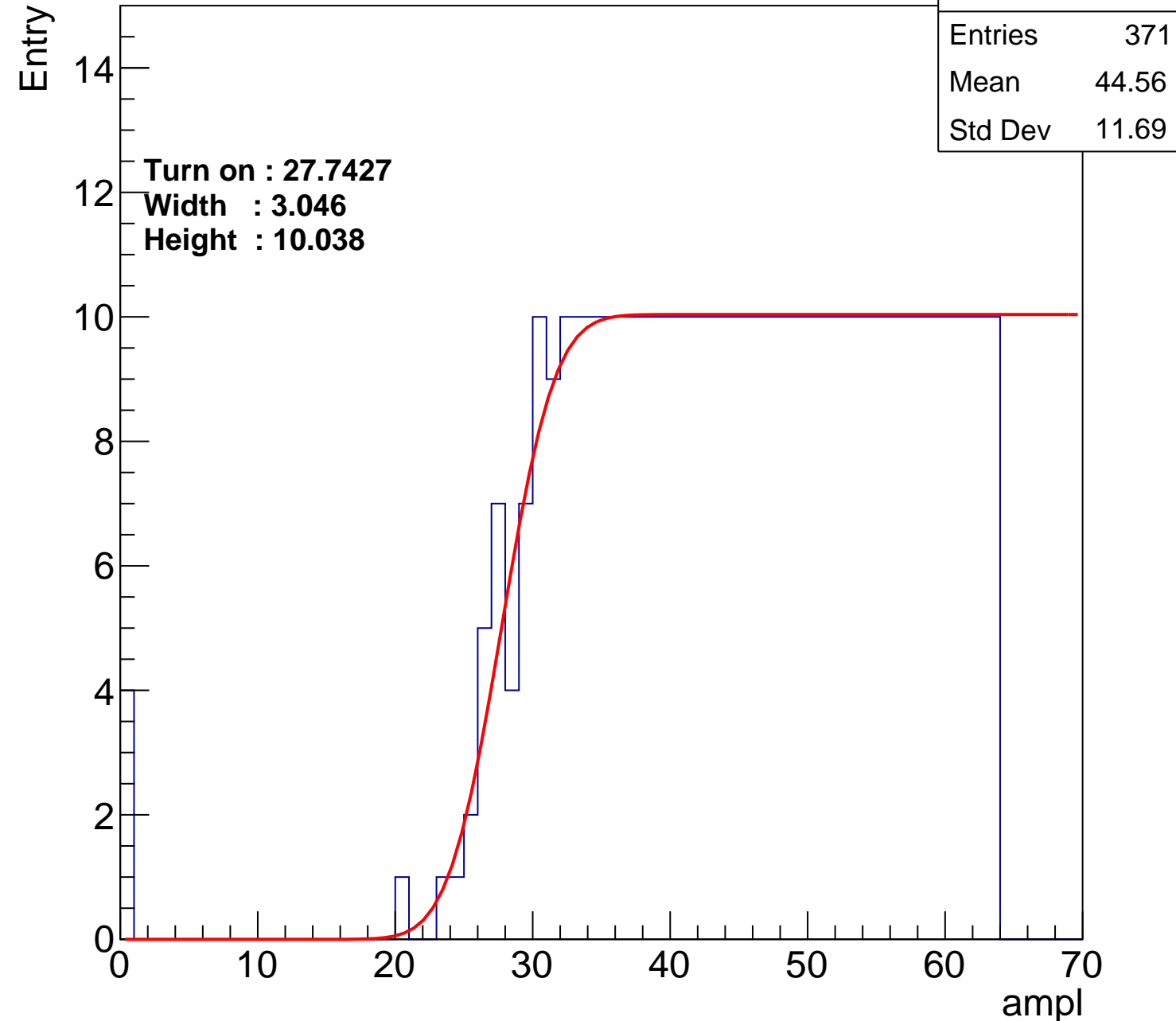
Width : 3.046

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch28

calib_packv5_042523_0143.root, FC#7, port C2

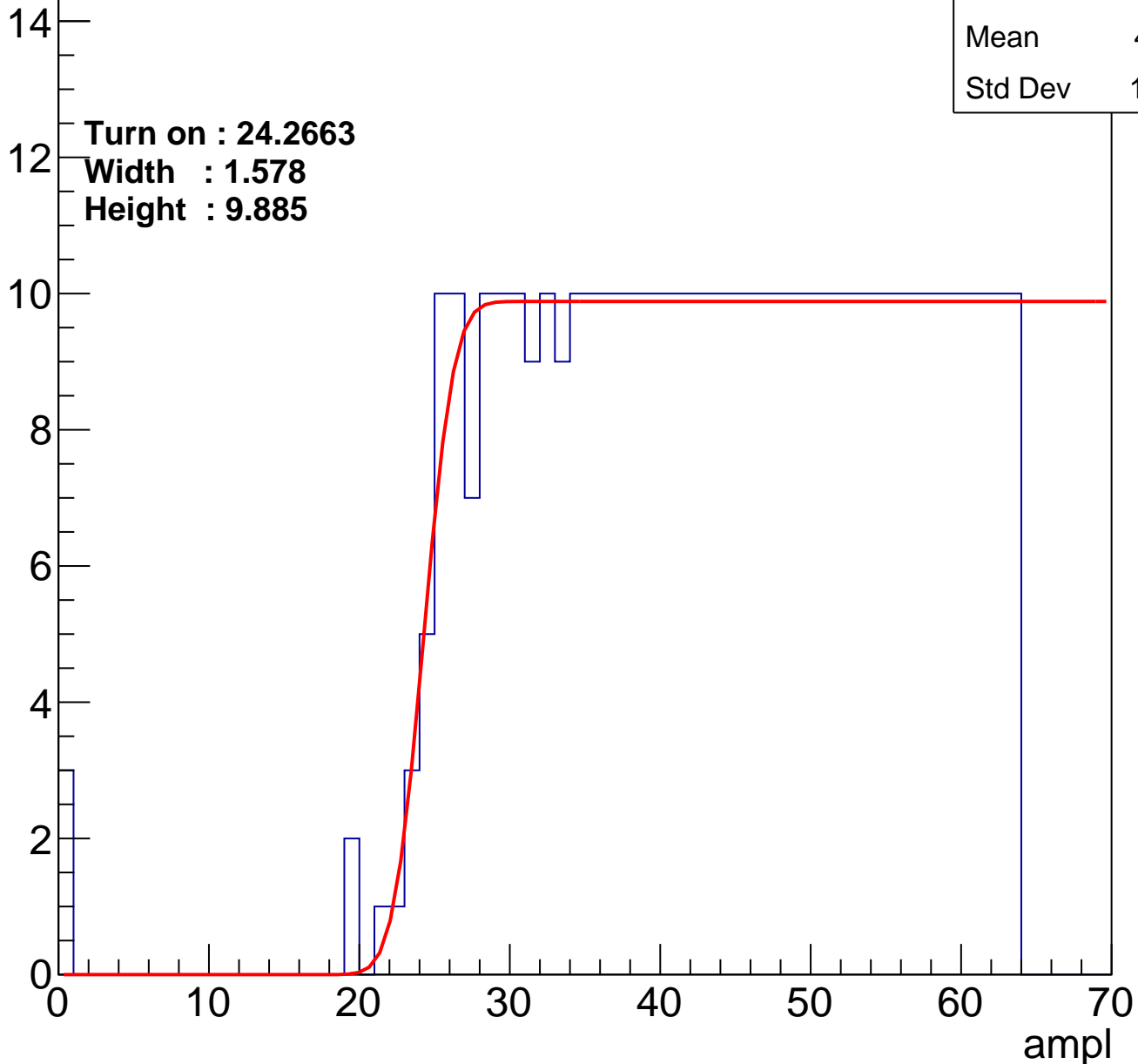
| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.21 |
| Std Dev | 12.18 |

Turn on : 24.2663

Width : 1.578

Height : 9.885

Entry



B1L103S, U6-ch29

calib_packv5_042523_0143.root, FC#7, port C2

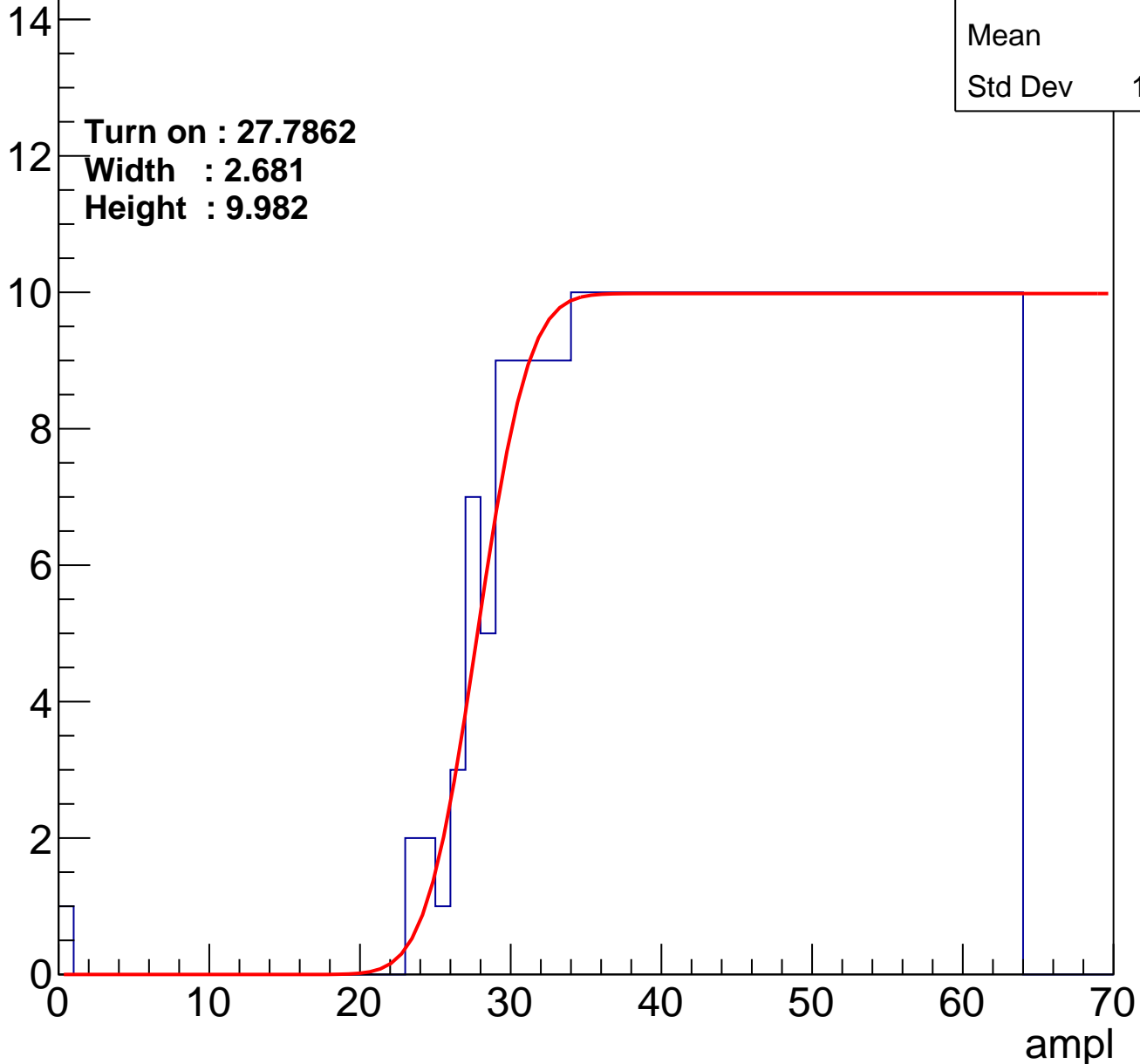
| | |
|---------|-------|
| Entries | 366 |
| Mean | 45 |
| Std Dev | 10.98 |

Turn on : 27.7862

Width : 2.681

Height : 9.982

Entry



B1L103S, U6-ch30

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.46 |
| Std Dev | 11.54 |

Turn on : 27.1109

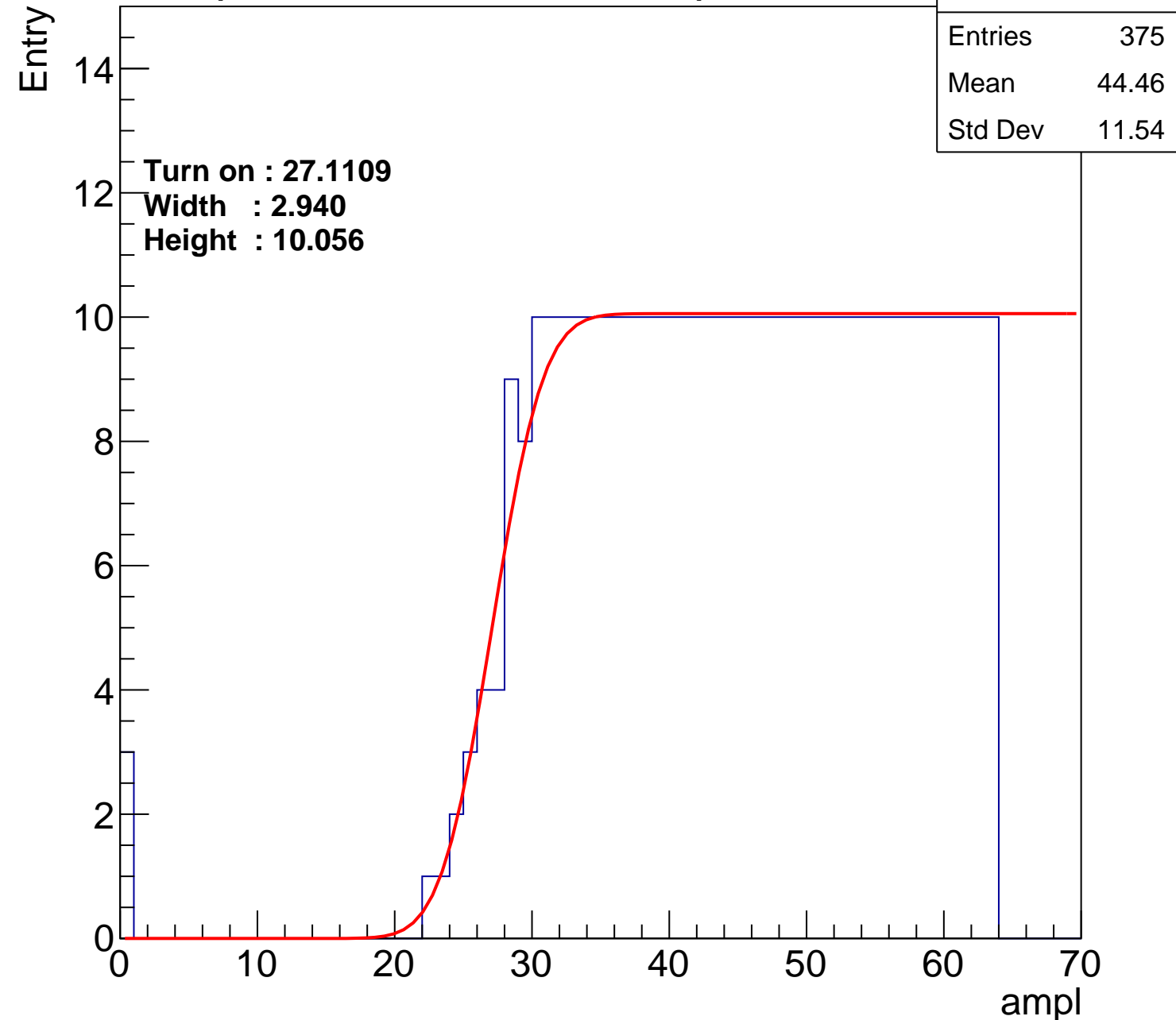
Width : 2.940

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch31

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.38 |
| Std Dev | 10.95 |

Turn on : 28.8302

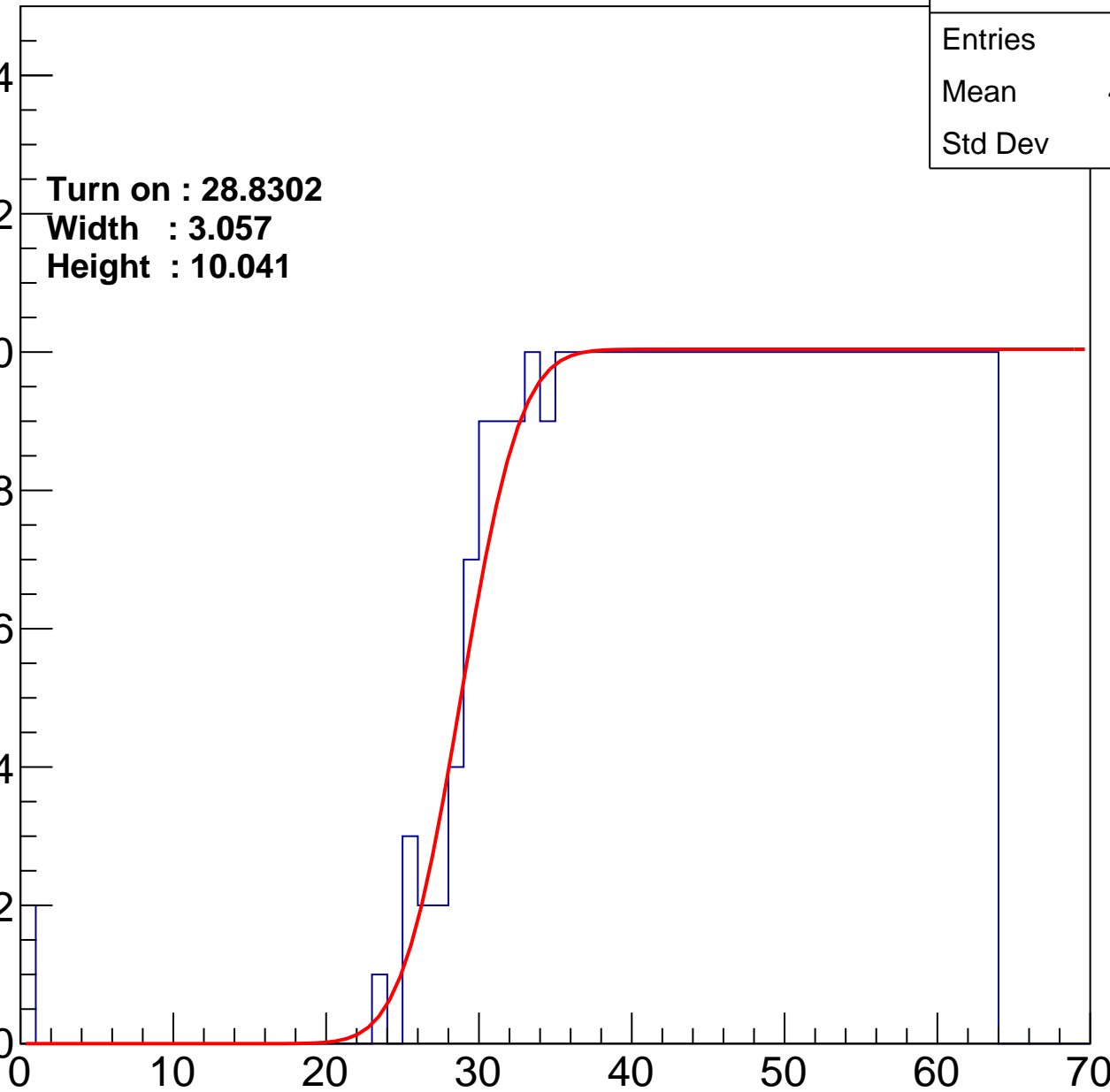
Width : 3.057

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch32

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.58 |
| Std Dev | 12.04 |

Turn on : 25.7111

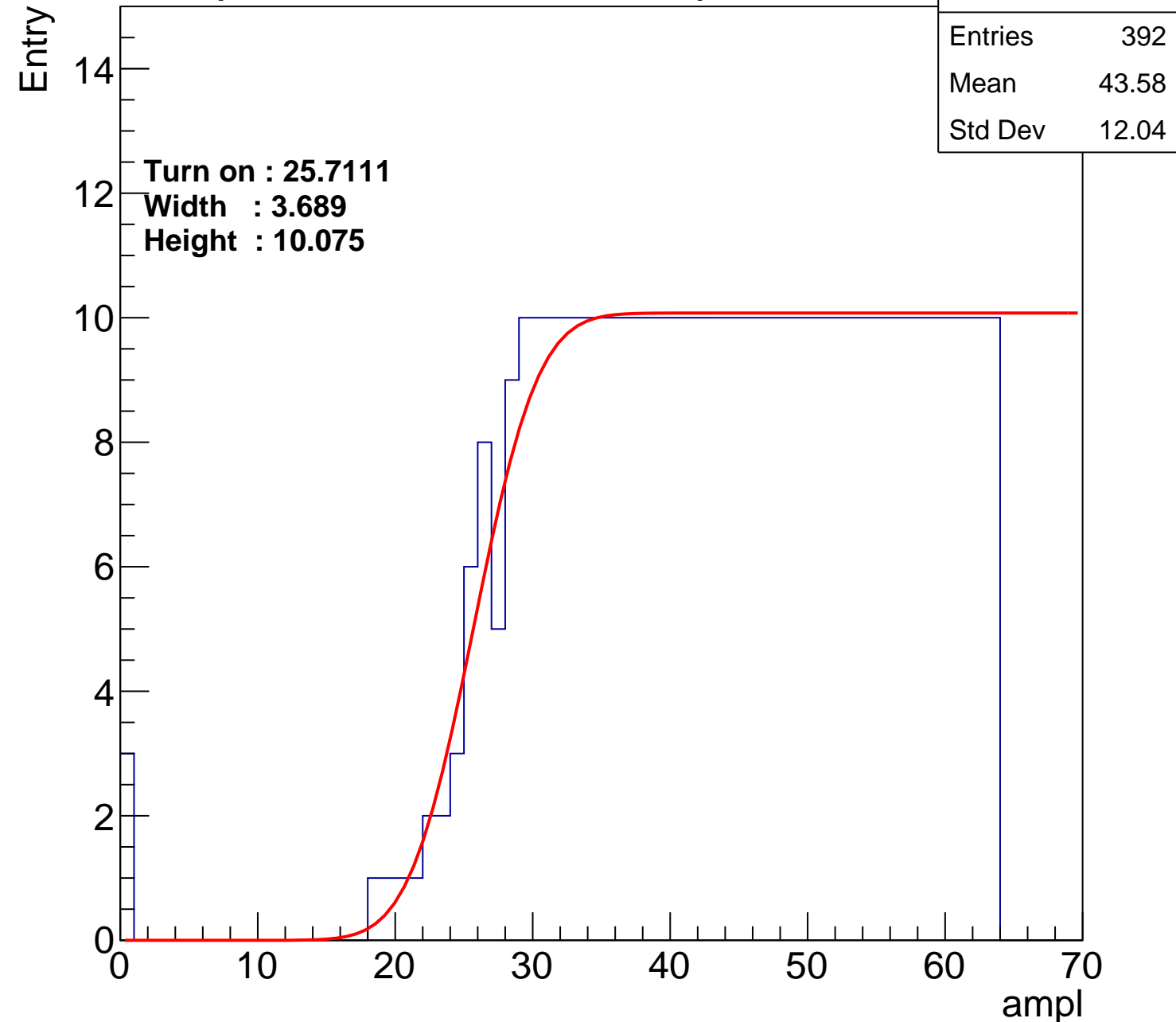
Width : 3.689

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch33

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.71 |
| Std Dev | 11.94 |

Turn on : 25.5619

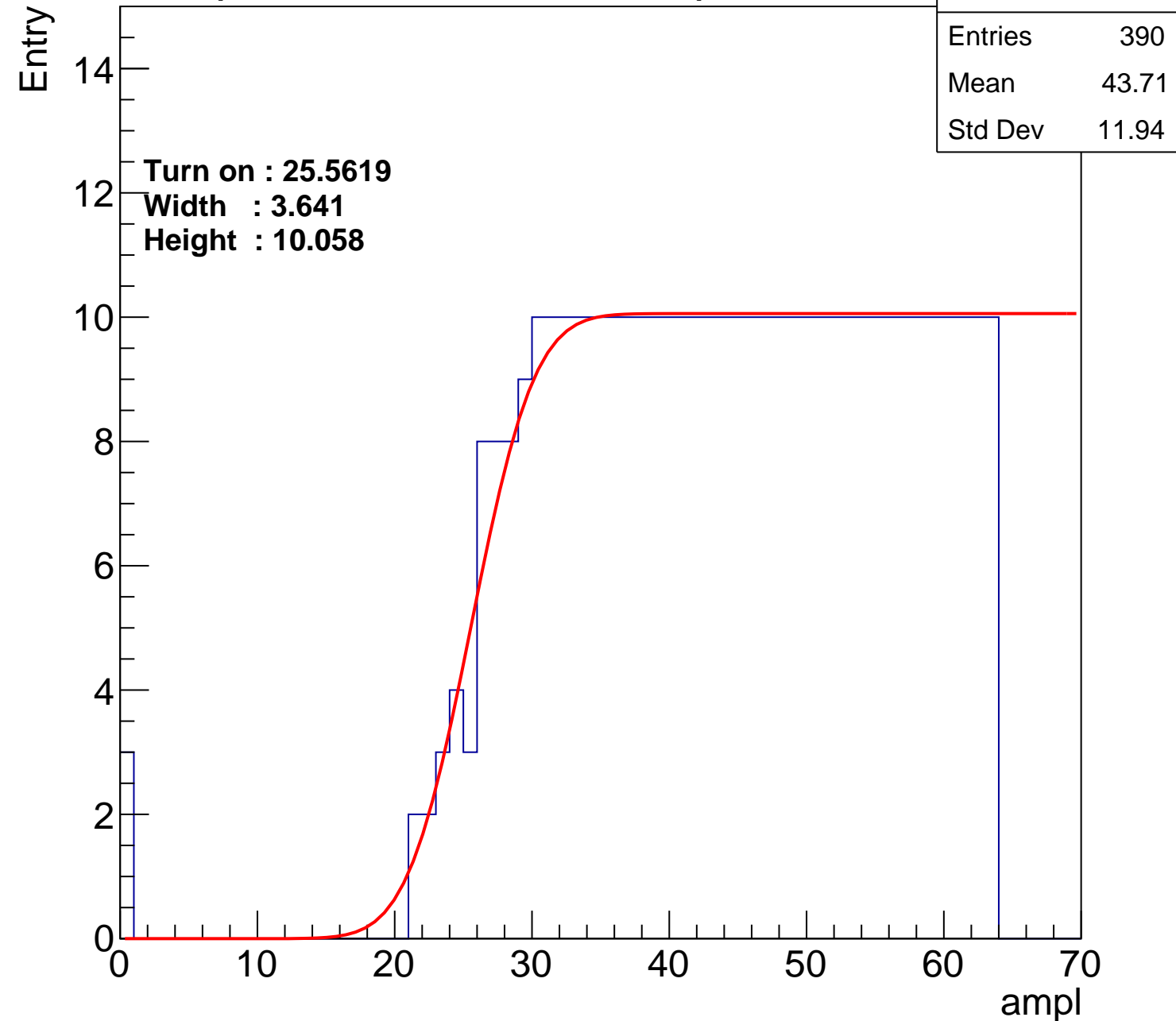
Width : 3.641

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch34

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.08 |
| Std Dev | 11.47 |

Turn on : 25.6262

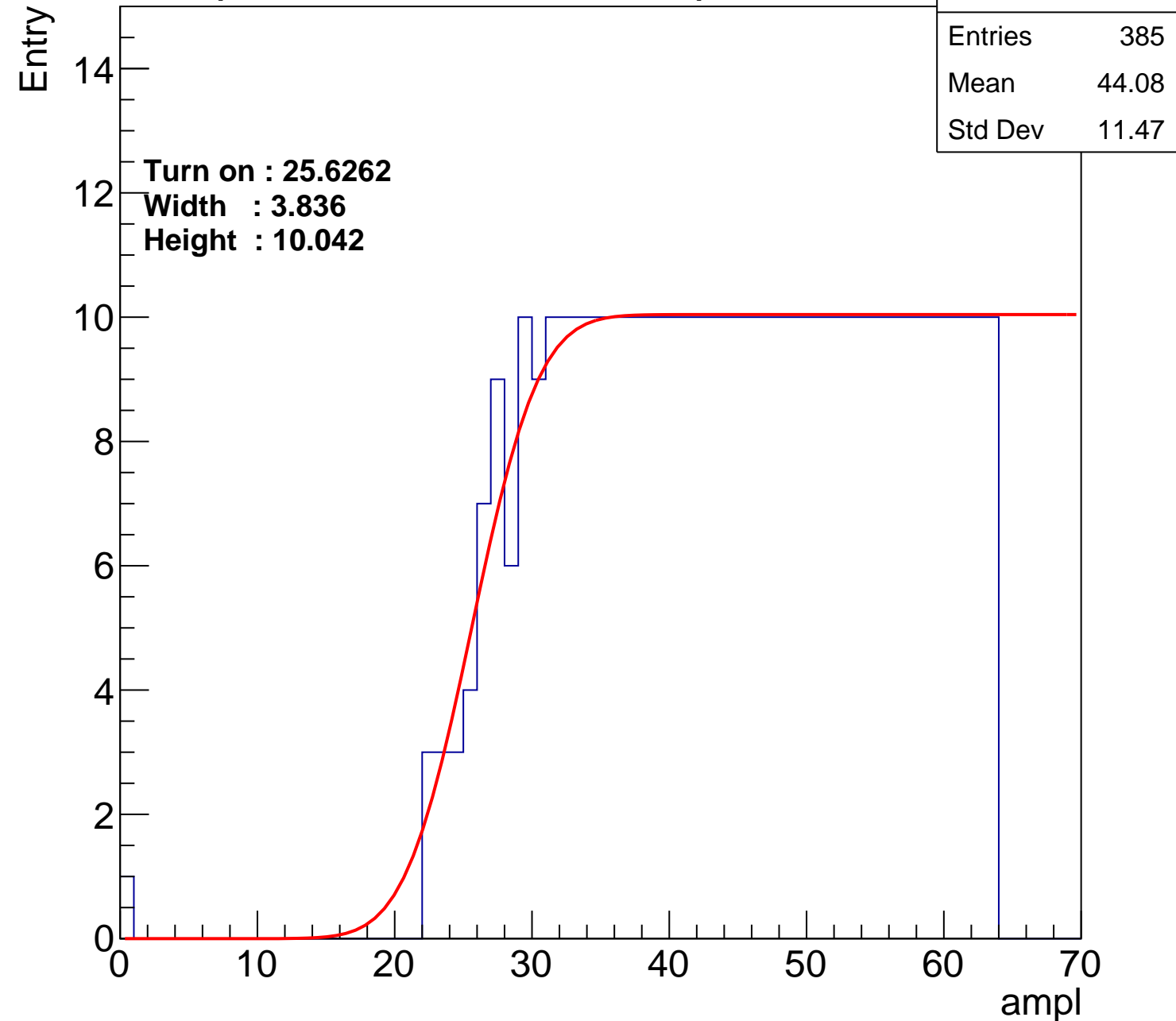
Width : 3.836

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch35

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.62 |
| Std Dev | 11.18 |

Turn on : 26.5889

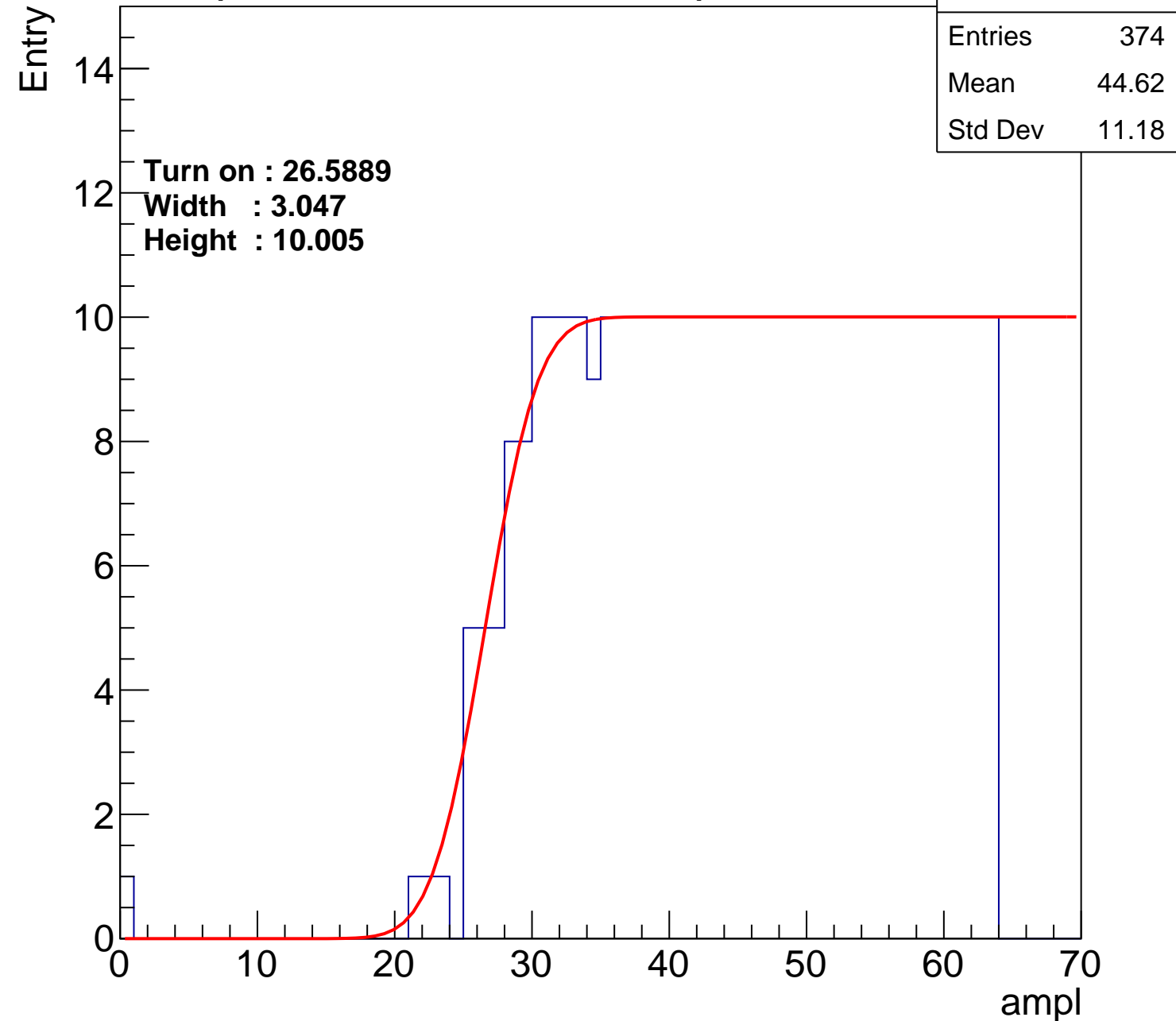
Width : 3.047

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch36

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.51 |
| Std Dev | 11.91 |

Turn on : 24.5581

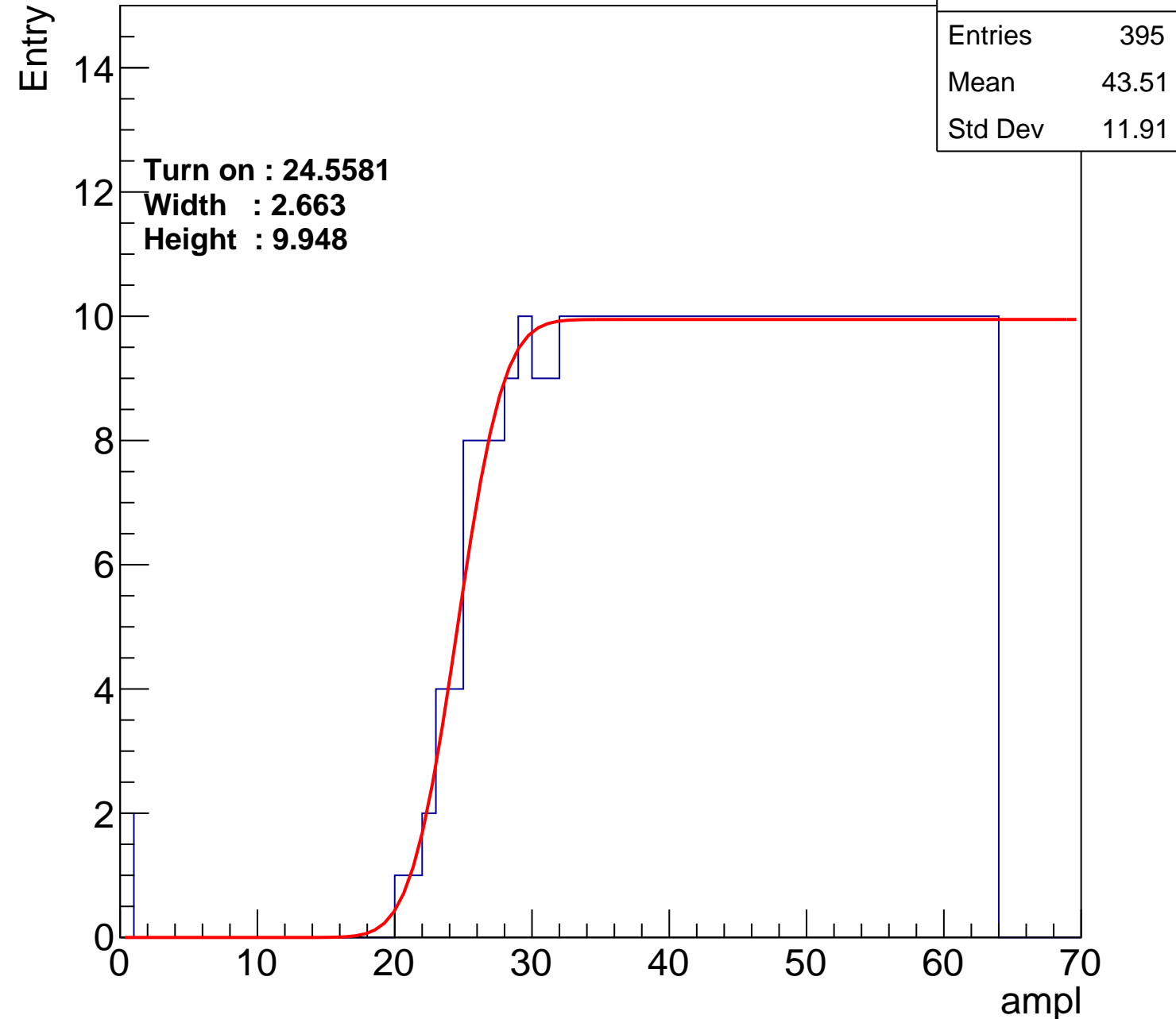
Width : 2.663

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch37

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.52 |
| Std Dev | 11.37 |

Turn on : 26.7451

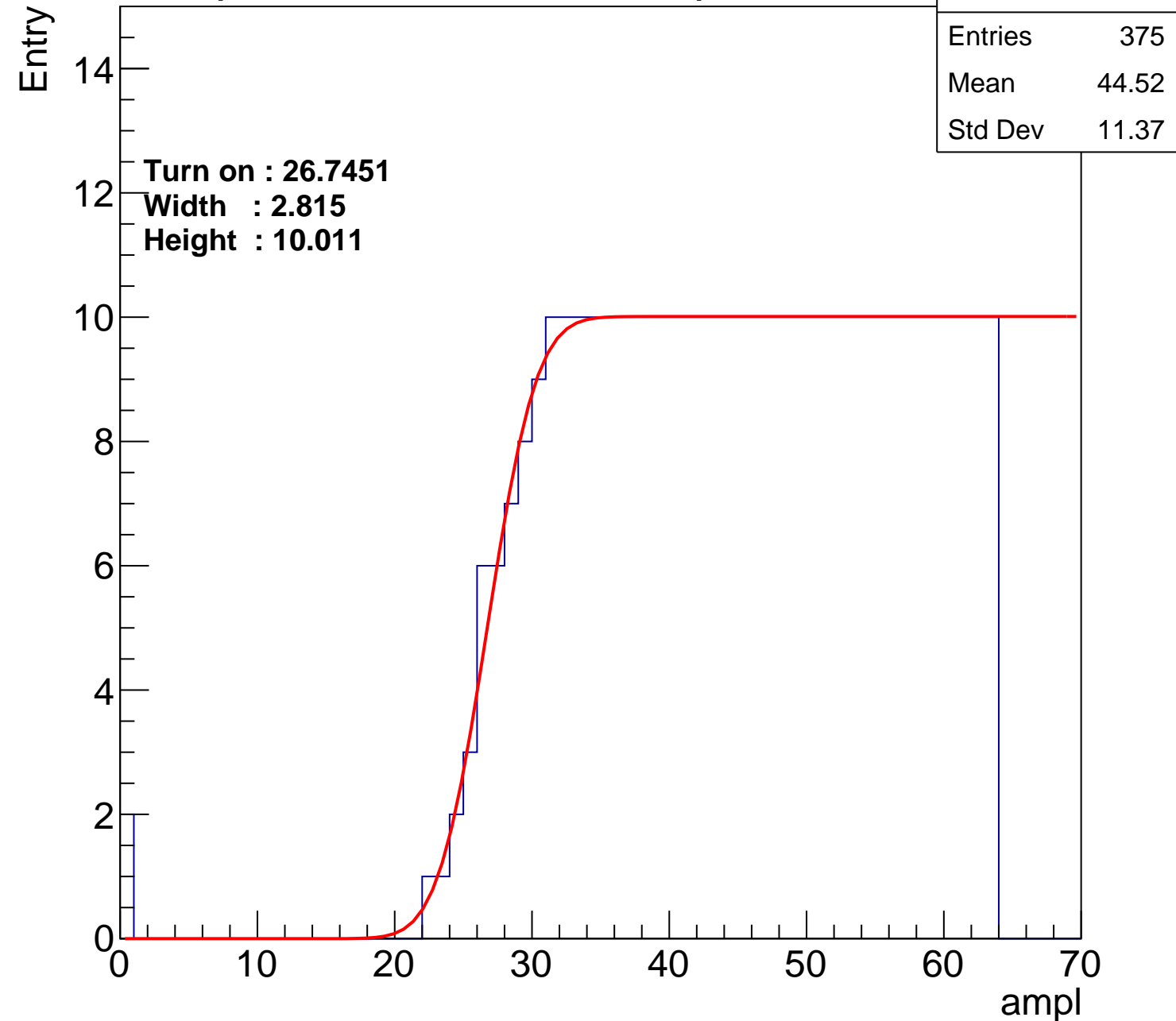
Width : 2.815

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch38

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.49 |
| Std Dev | 11.84 |

Turn on : 25.0563

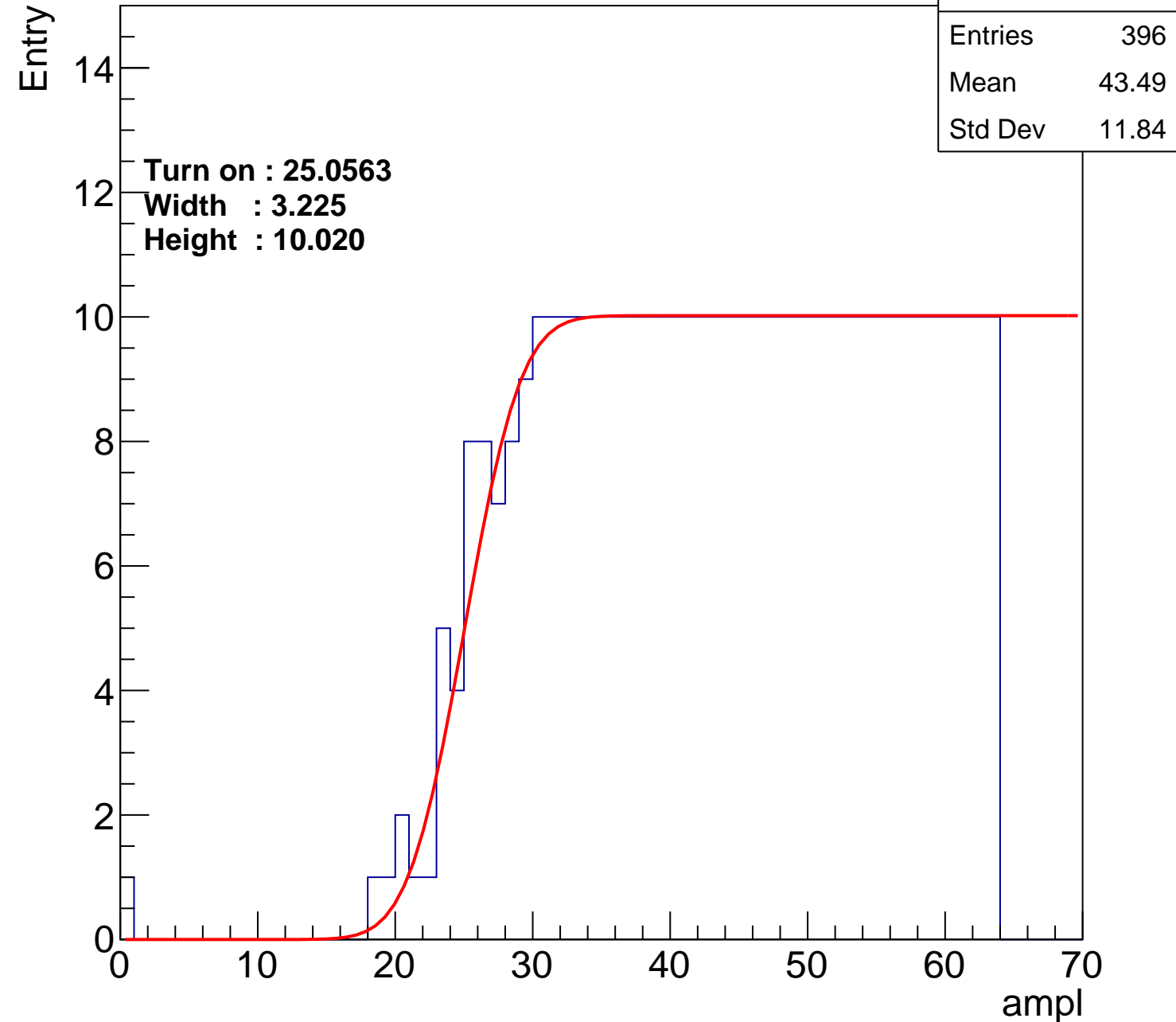
Width : 3.225

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch39

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.5 |
| Std Dev | 11.21 |

Turn on : 26.0327

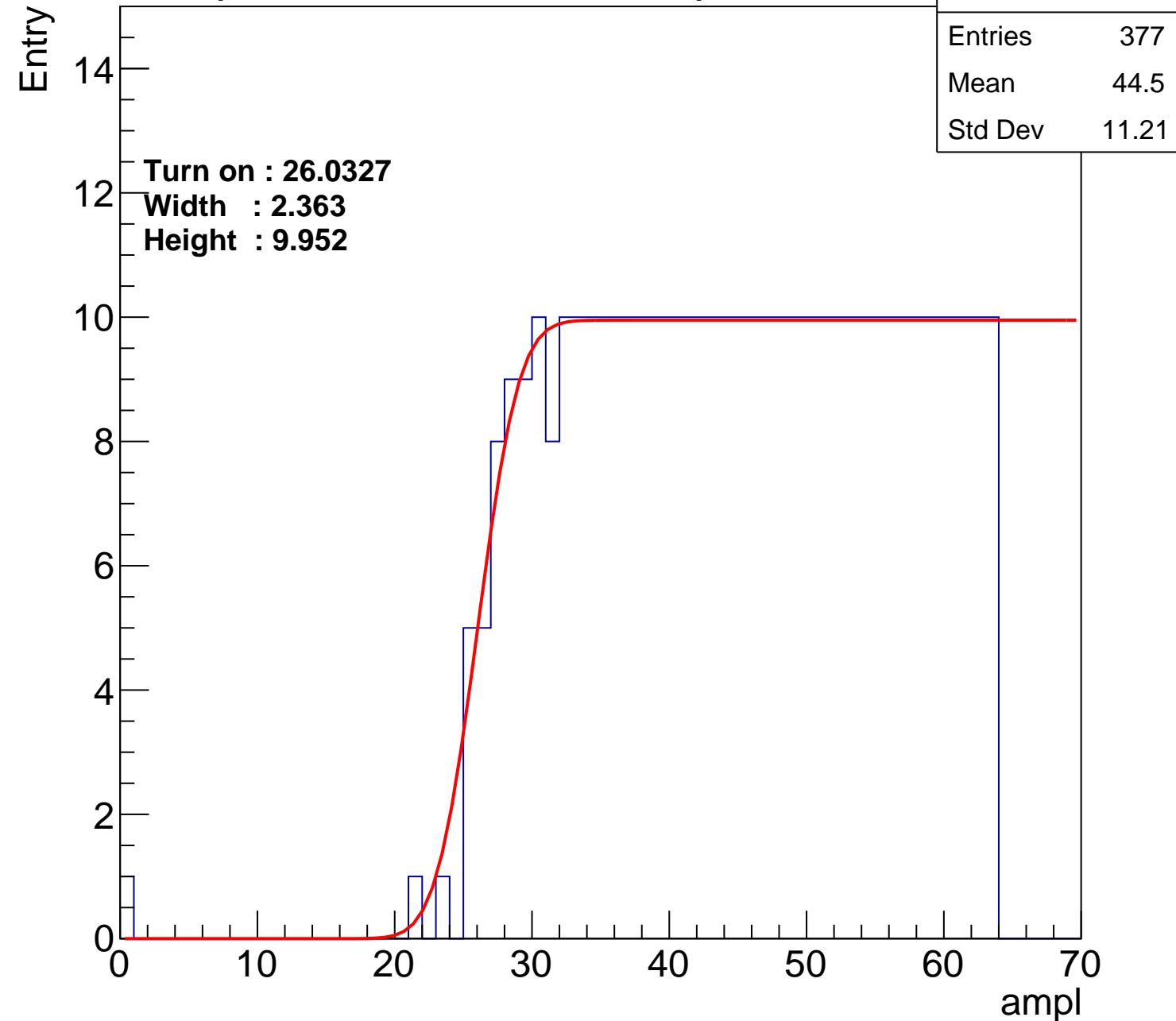
Width : 2.363

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch40

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.43 |
| Std Dev | 12.23 |

Turn on : 25.4618

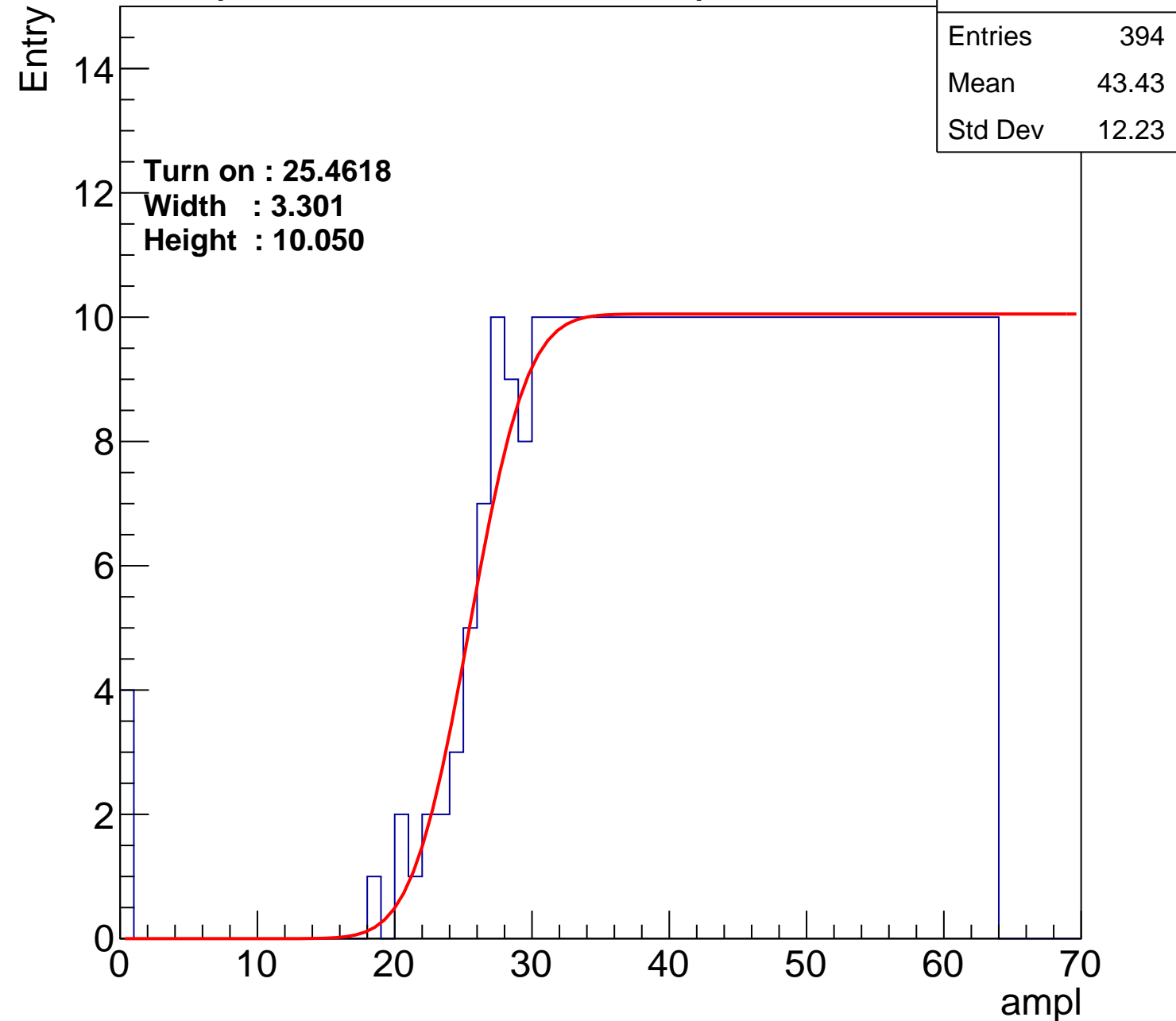
Width : 3.301

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch41

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.35 |
| Std Dev | 11.43 |

Turn on : 26.4415

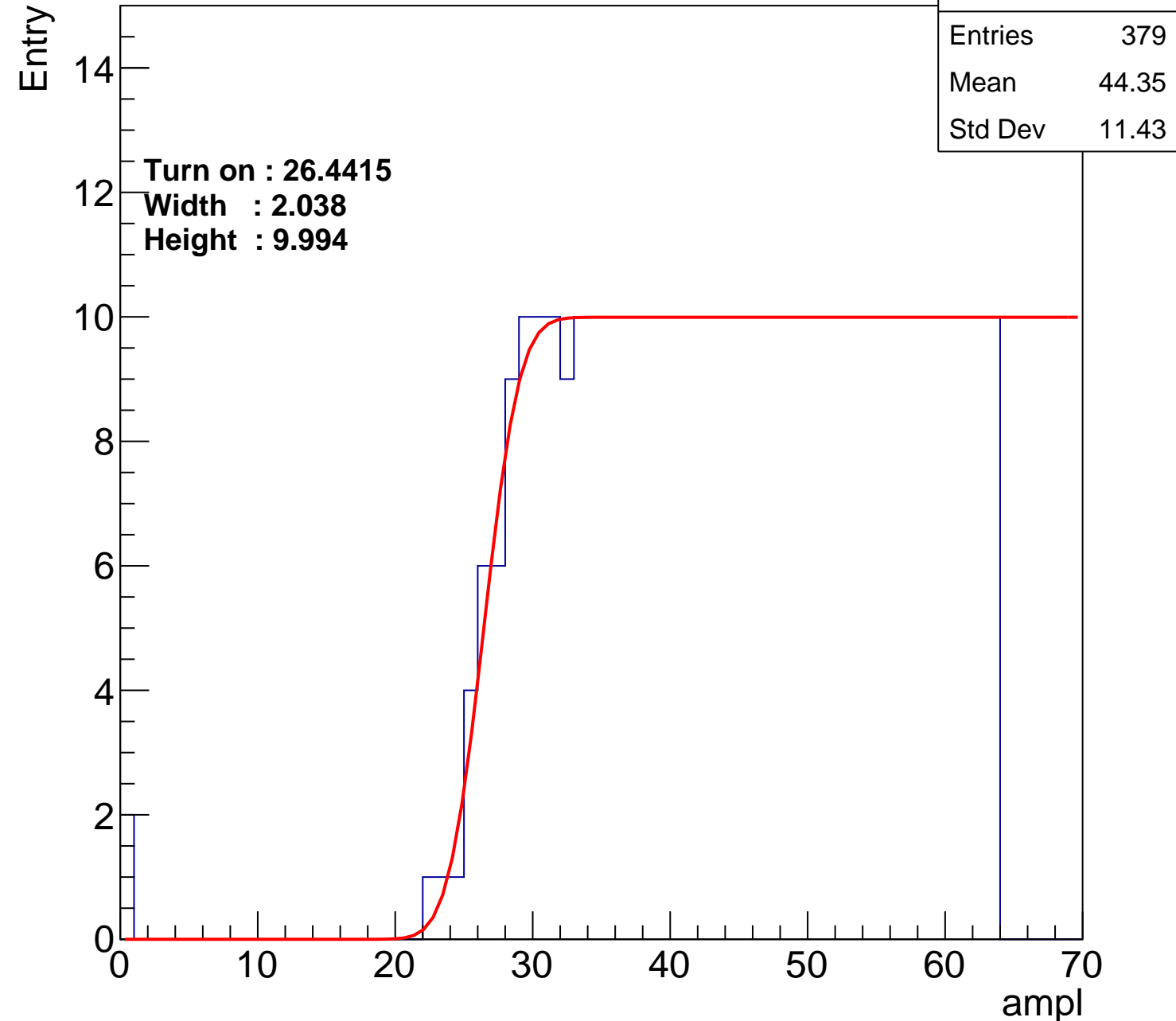
Width : 2.038

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch42

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.44 |
| Std Dev | 11.44 |

Turn on : 26.9132

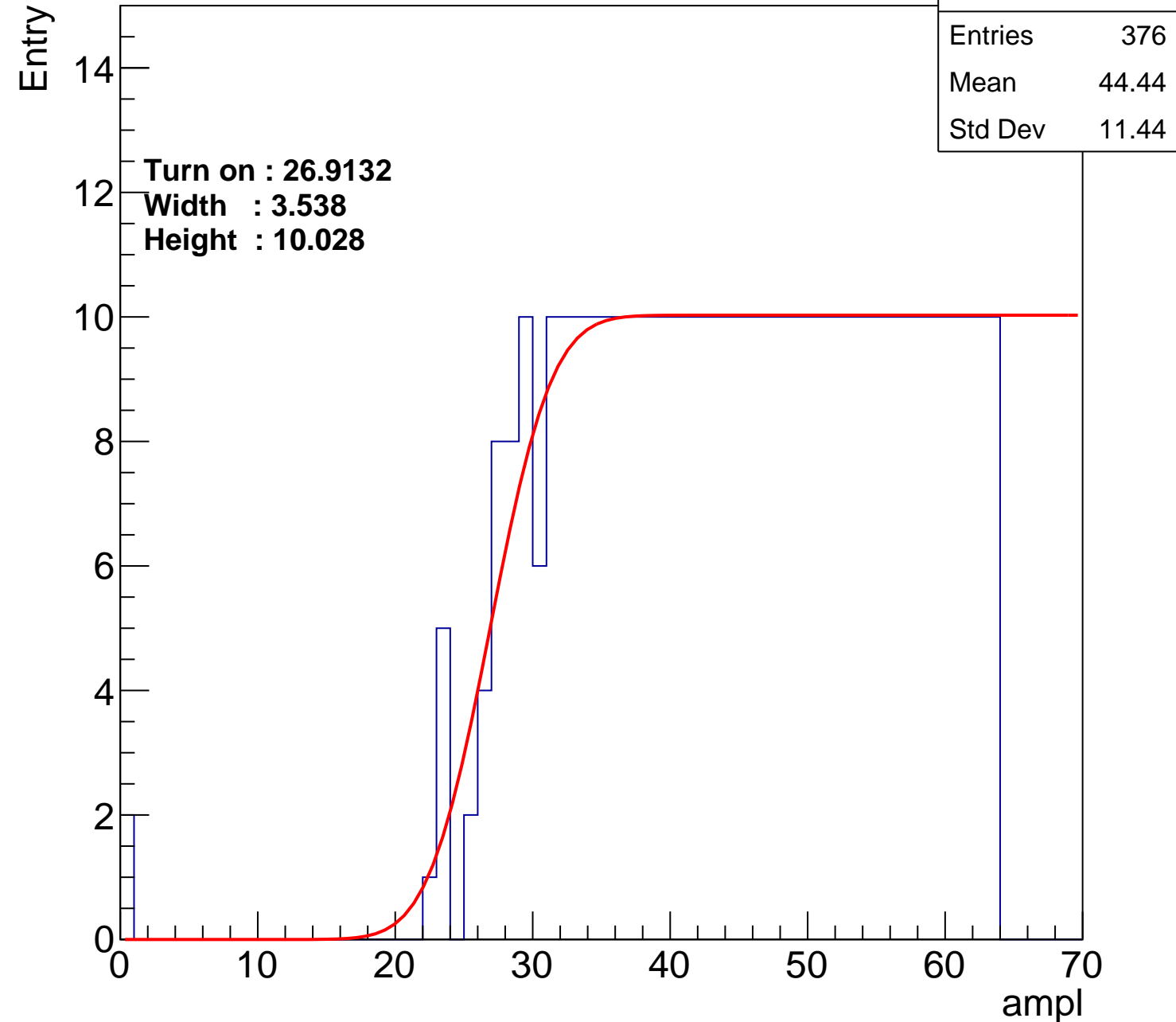
Width : 3.538

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch43

calib_packv5_042523_0143.root, FC#7, port C2

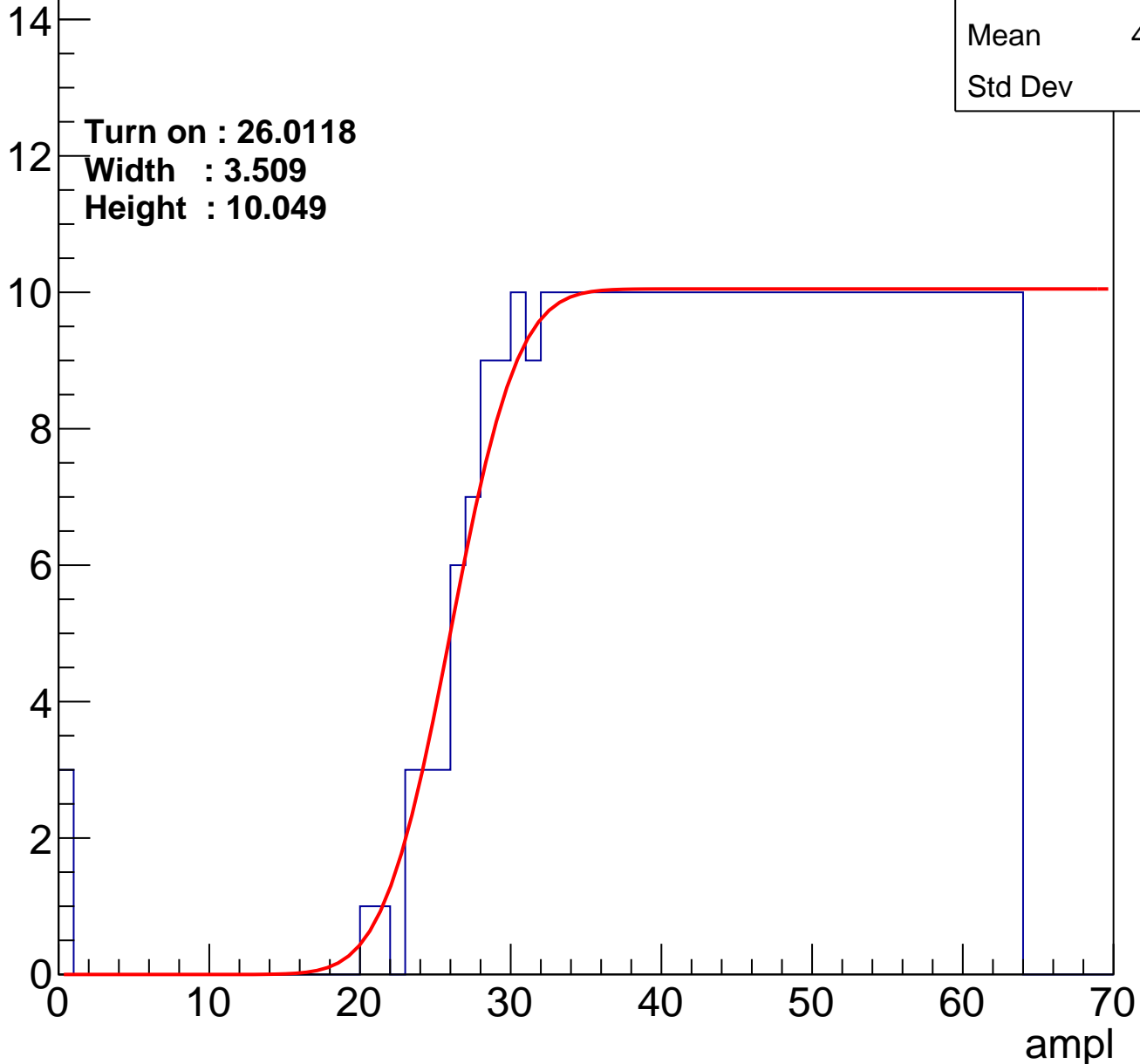
| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.99 |
| Std Dev | 11.8 |

Turn on : 26.0118

Width : 3.509

Height : 10.049

Entry



B1L103S, U6-ch44

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.87 |
| Std Dev | 11.61 |

Turn on : 25.5343

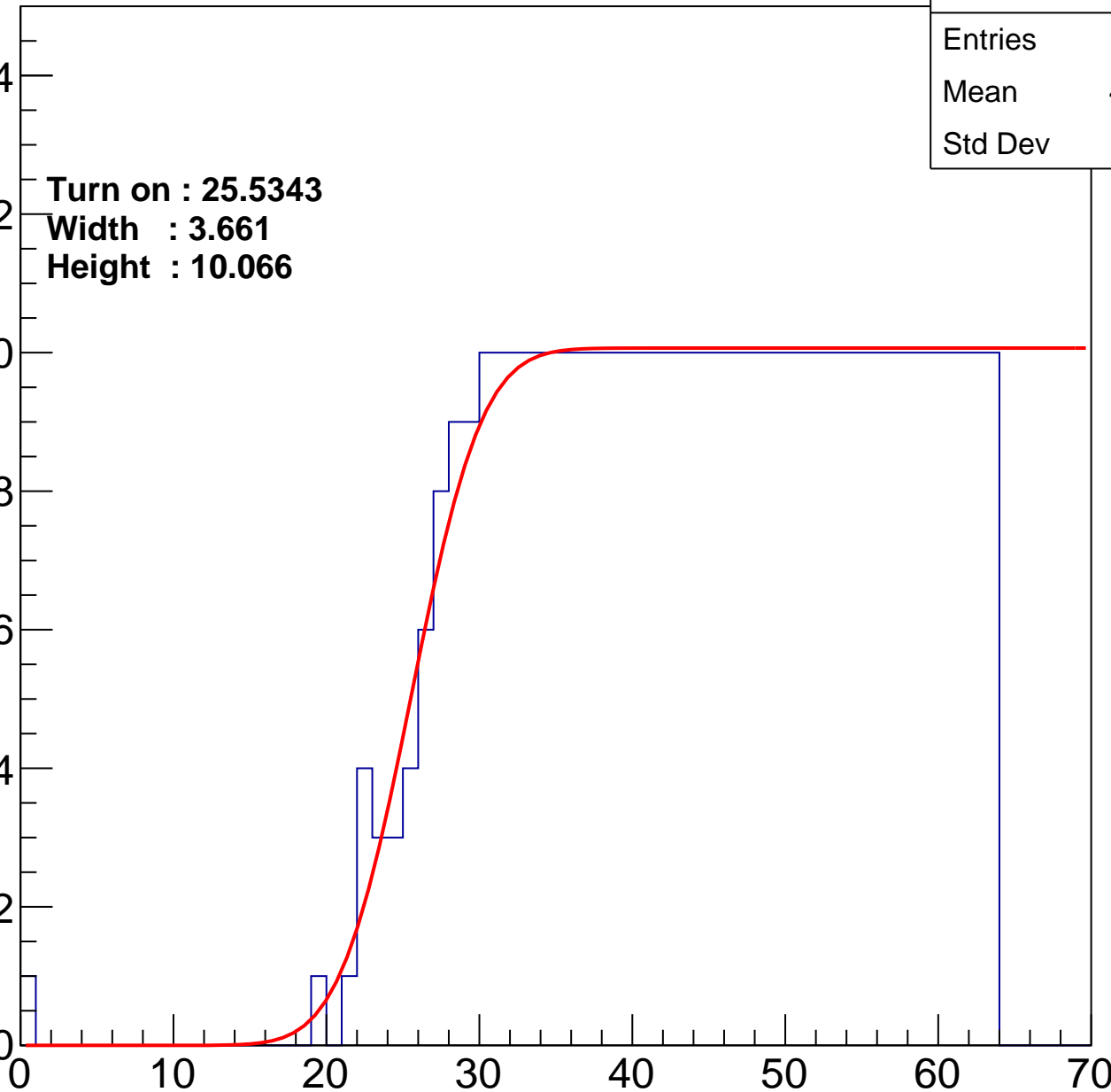
Width : 3.661

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch45

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.76 |
| Std Dev | 11.25 |

Turn on : 28.0931

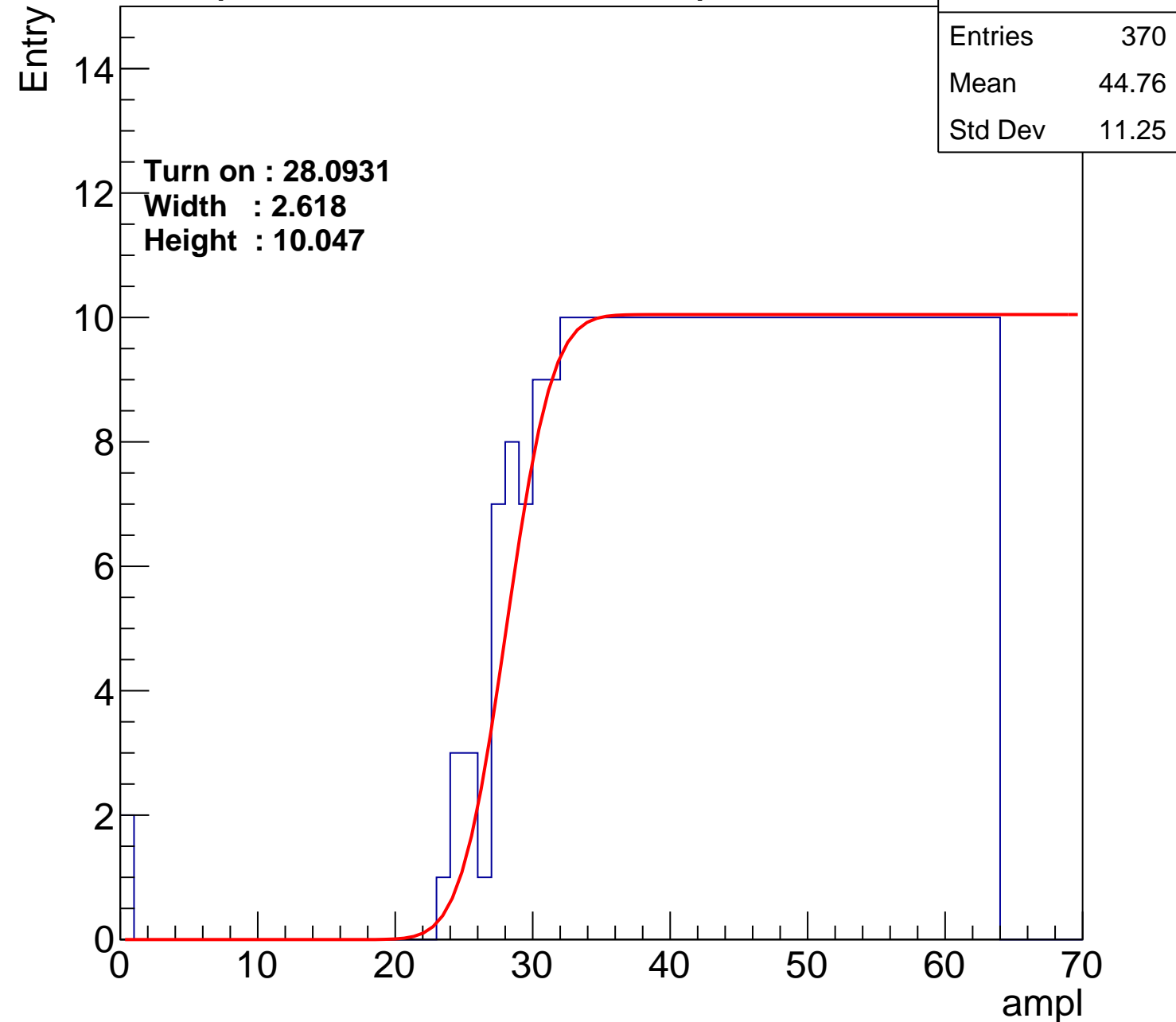
Width : 2.618

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch46

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.42 |
| Std Dev | 12.13 |

Turn on : 24.9784

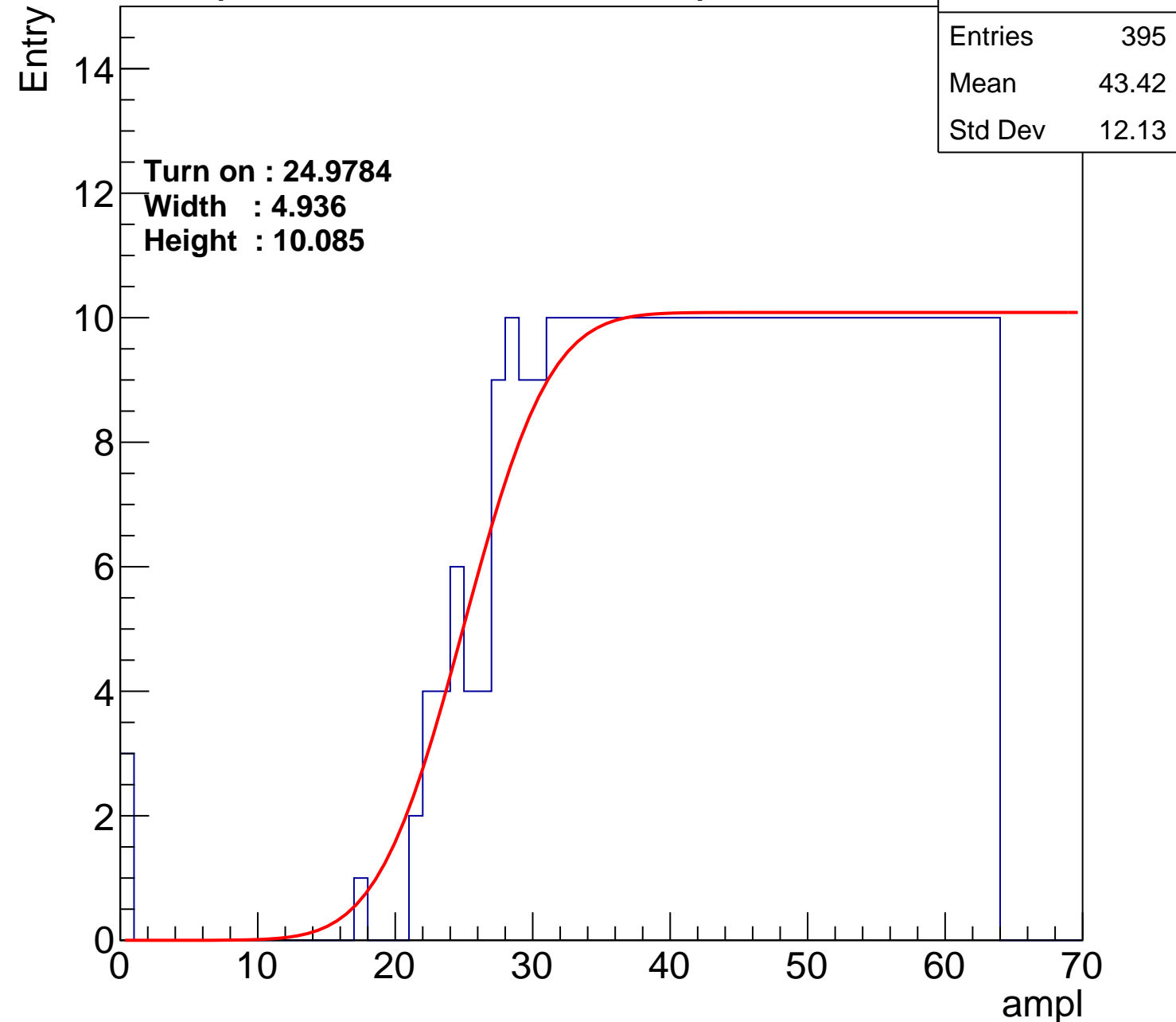
Width : 4.936

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch47

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.86 |
| Std Dev | 11.86 |

Turn on : 26.1800

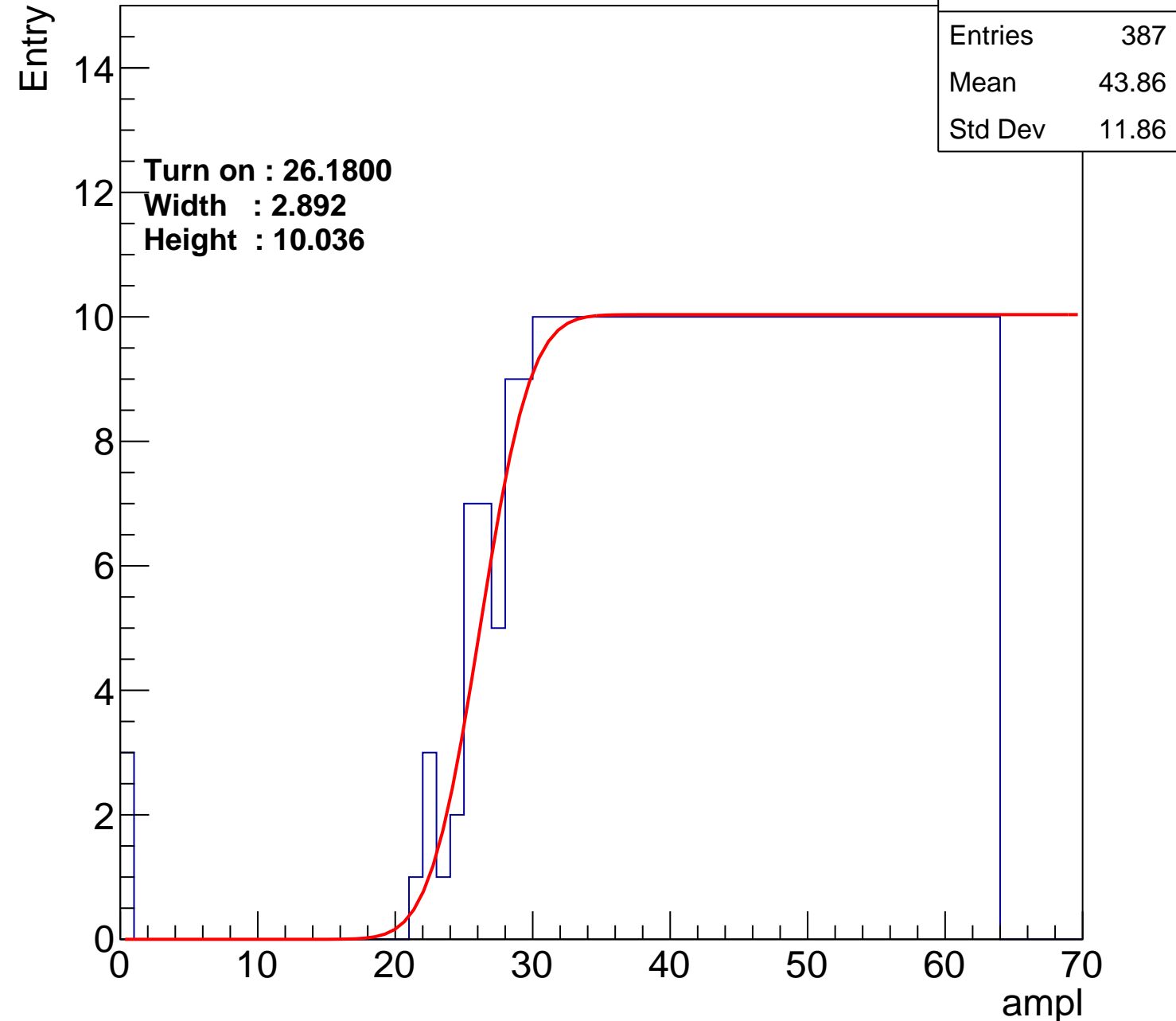
Width : 2.892

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch48

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.22 |
| Std Dev | 11.53 |

Turn on : 26.3196

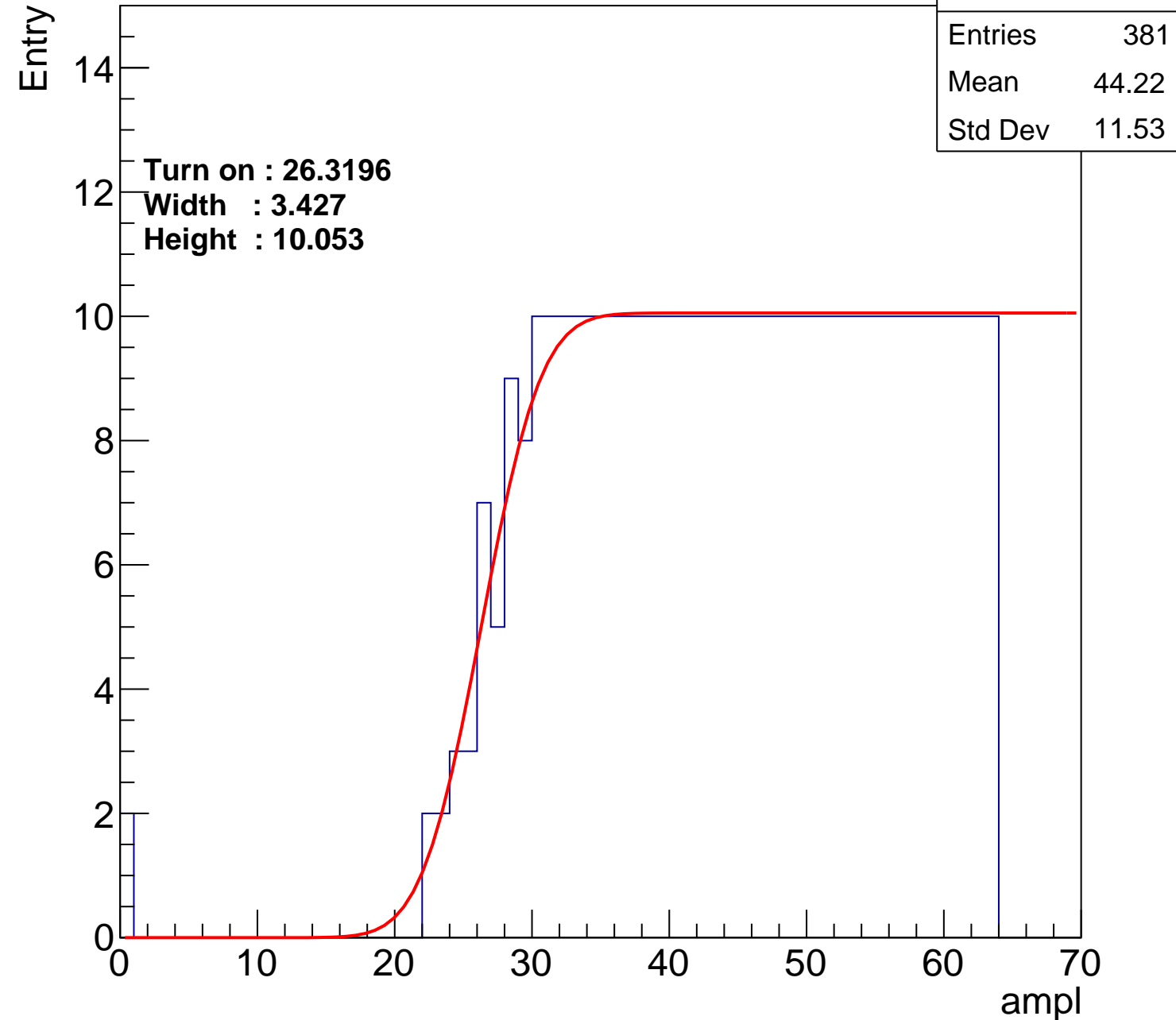
Width : 3.427

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch49

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.25 |
| Std Dev | 11.49 |

Turn on : 26.7568

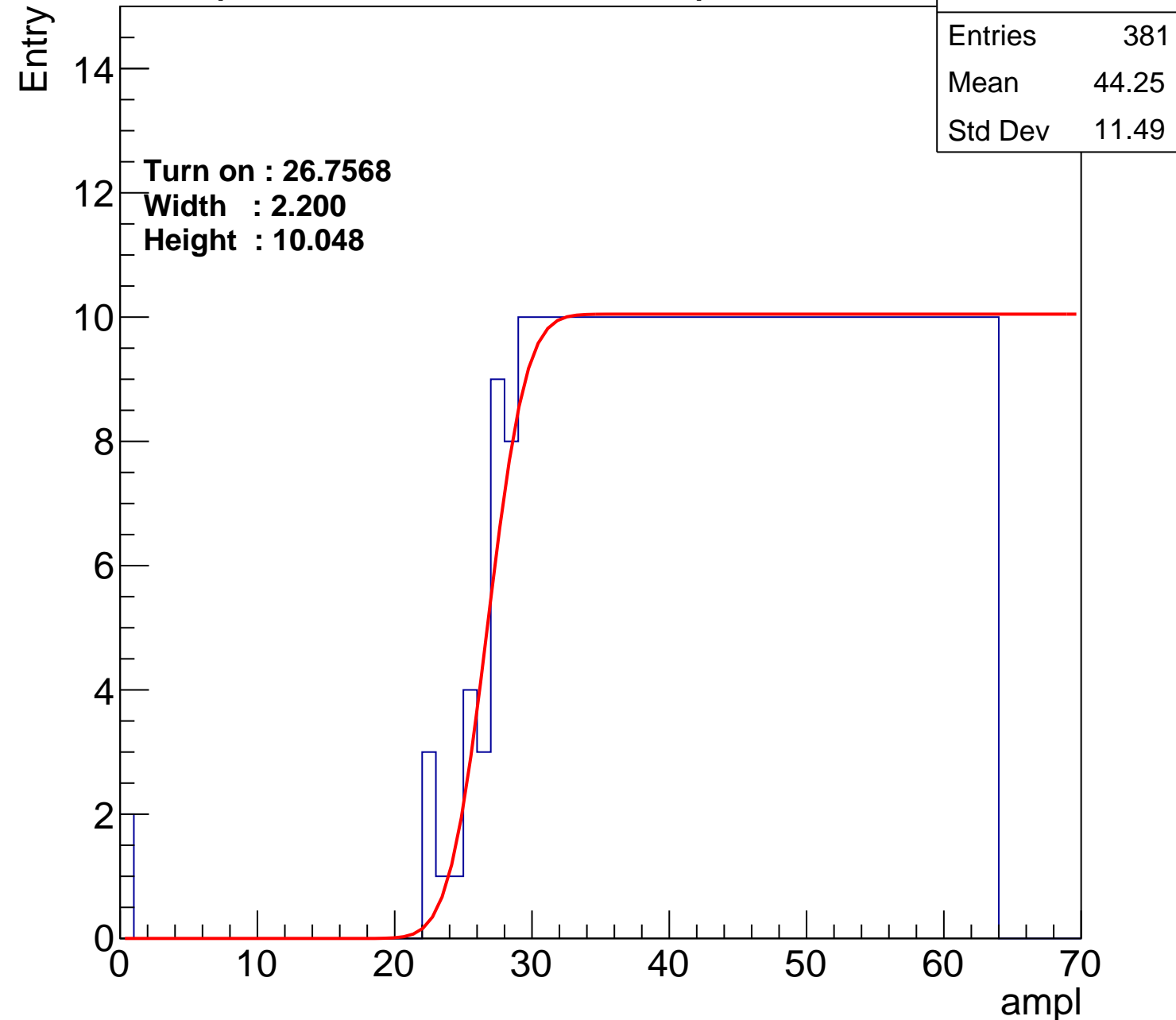
Width : 2.200

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch50

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.69 |
| Std Dev | 11.69 |

Turn on : 24.8094

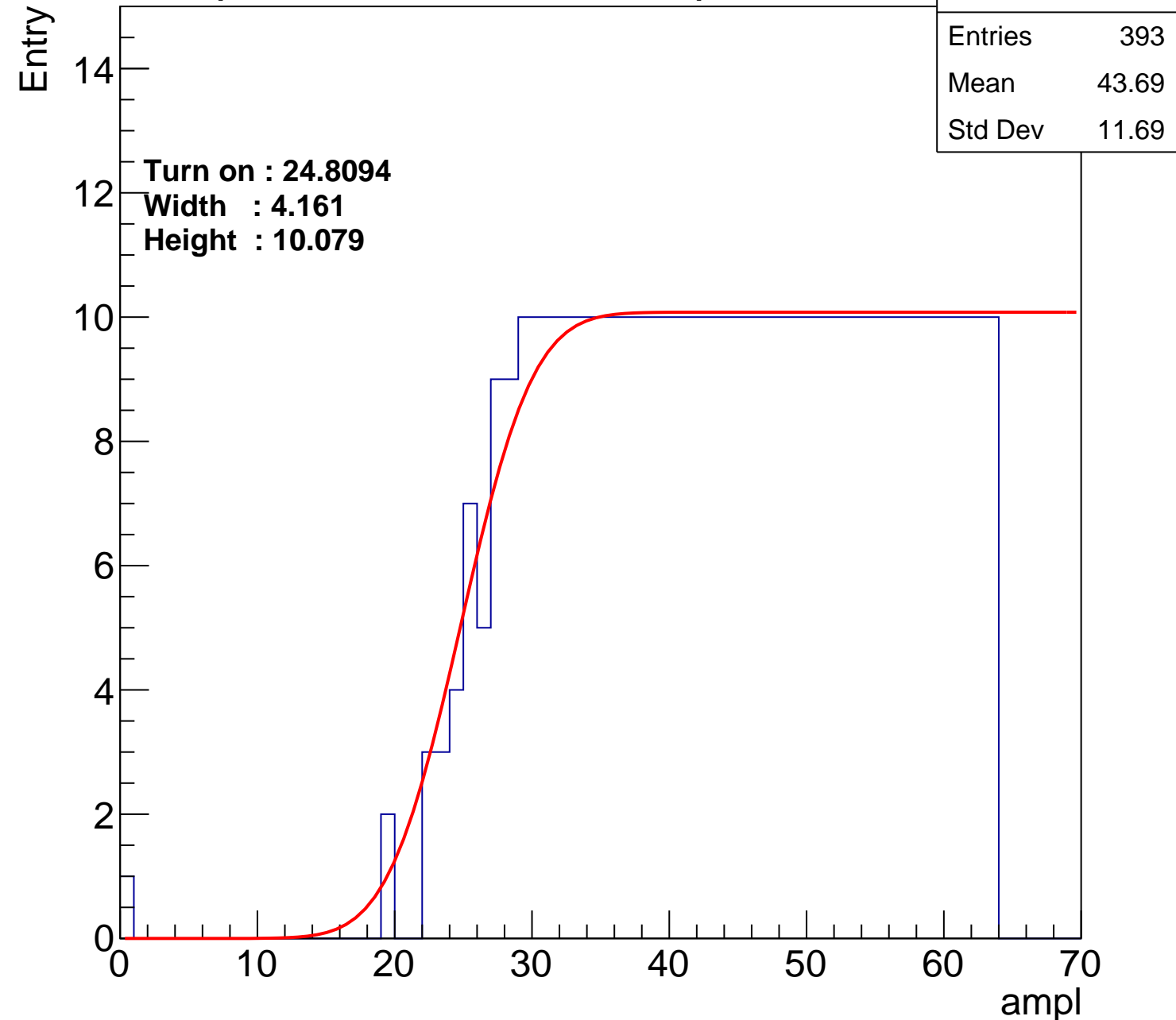
Width : 4.161

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch51

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.85 |
| Std Dev | 12.04 |

Turn on : 25.7497

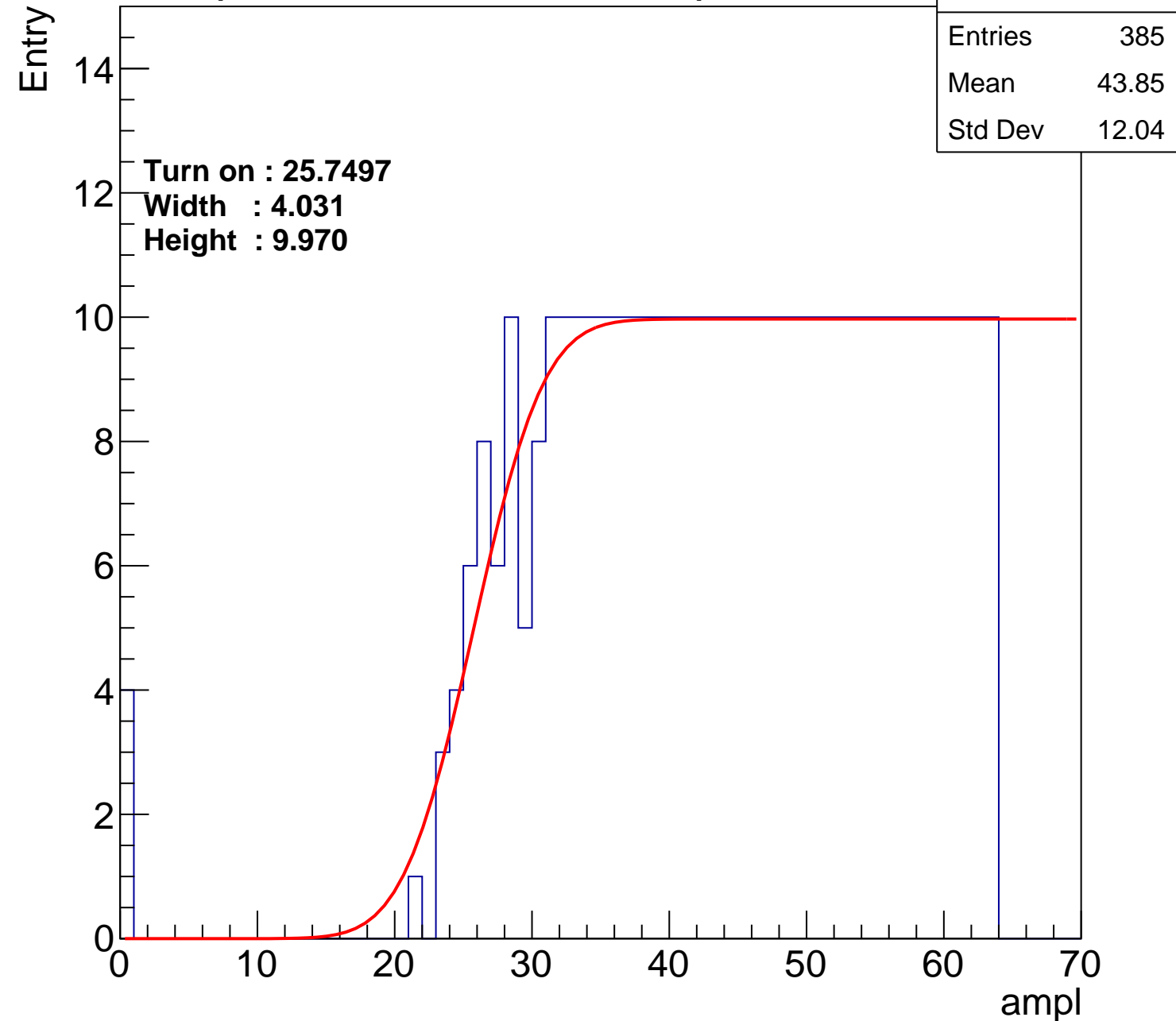
Width : 4.031

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch52

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.66 |
| Std Dev | 12.14 |

Turn on : 26.0229

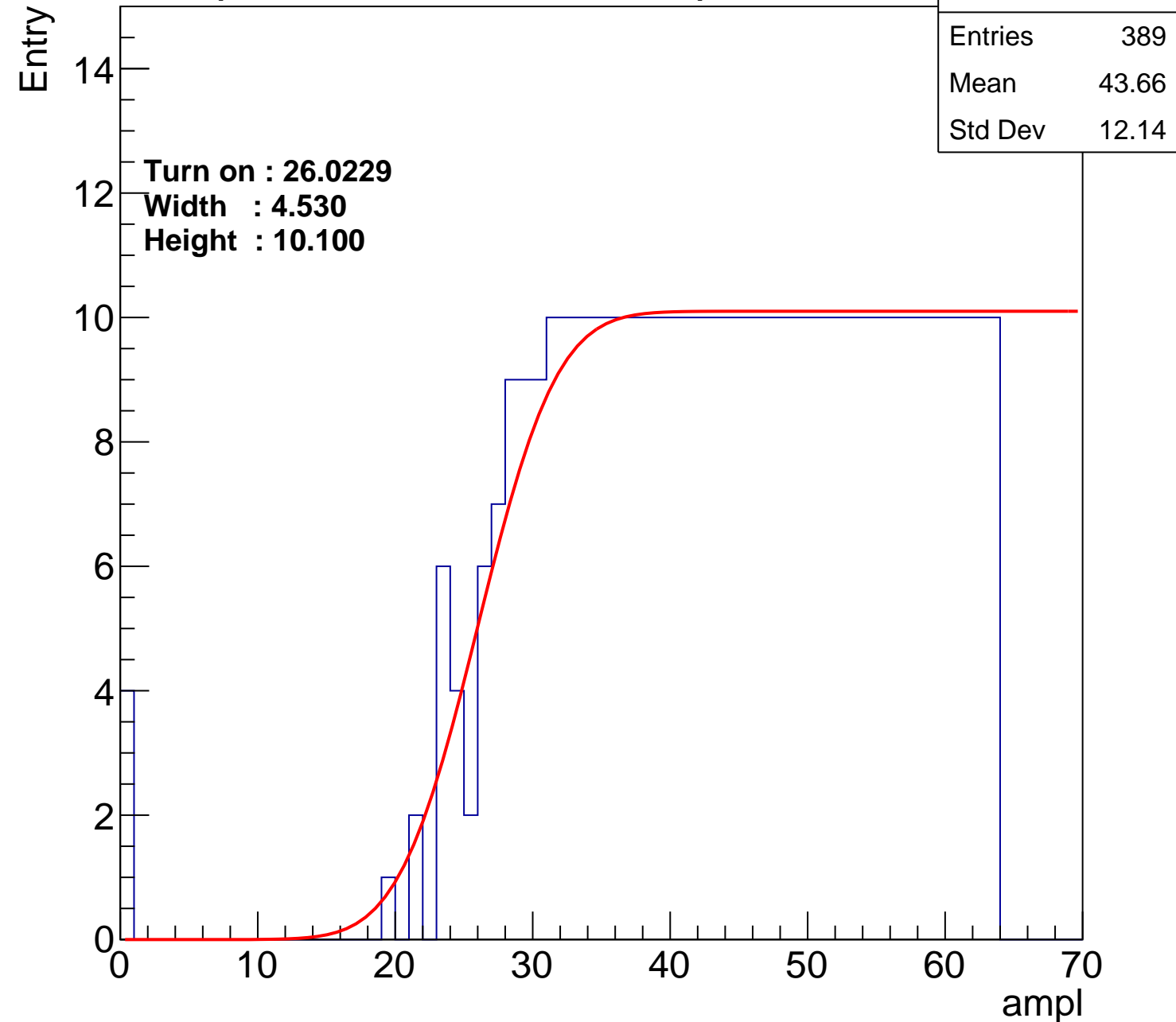
Width : 4.530

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch53

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.29 |
| Std Dev | 11.14 |

Turn on : 28.6600

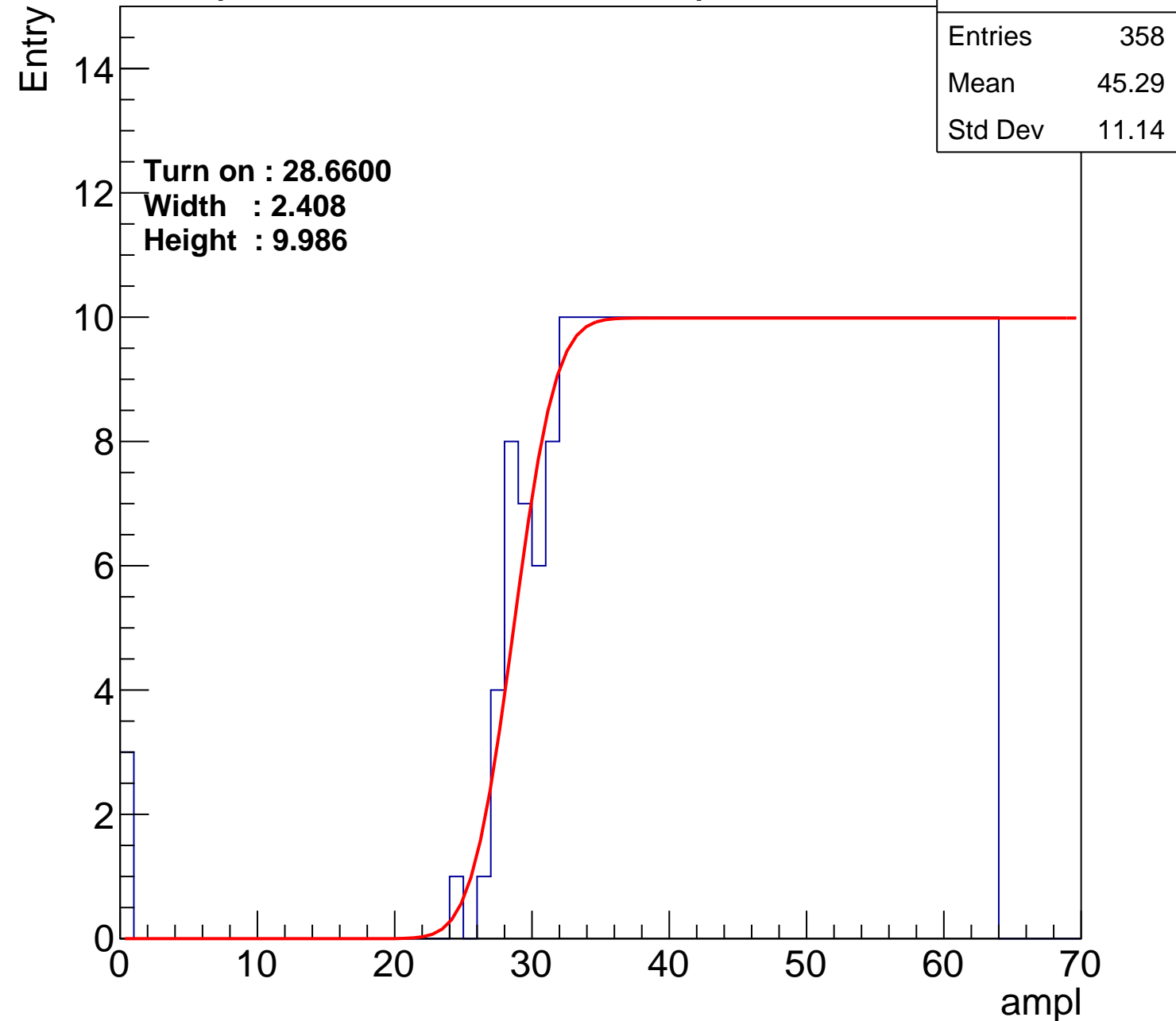
Width : 2.408

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch54

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.32 |
| Std Dev | 11.71 |

Turn on : 27.0443

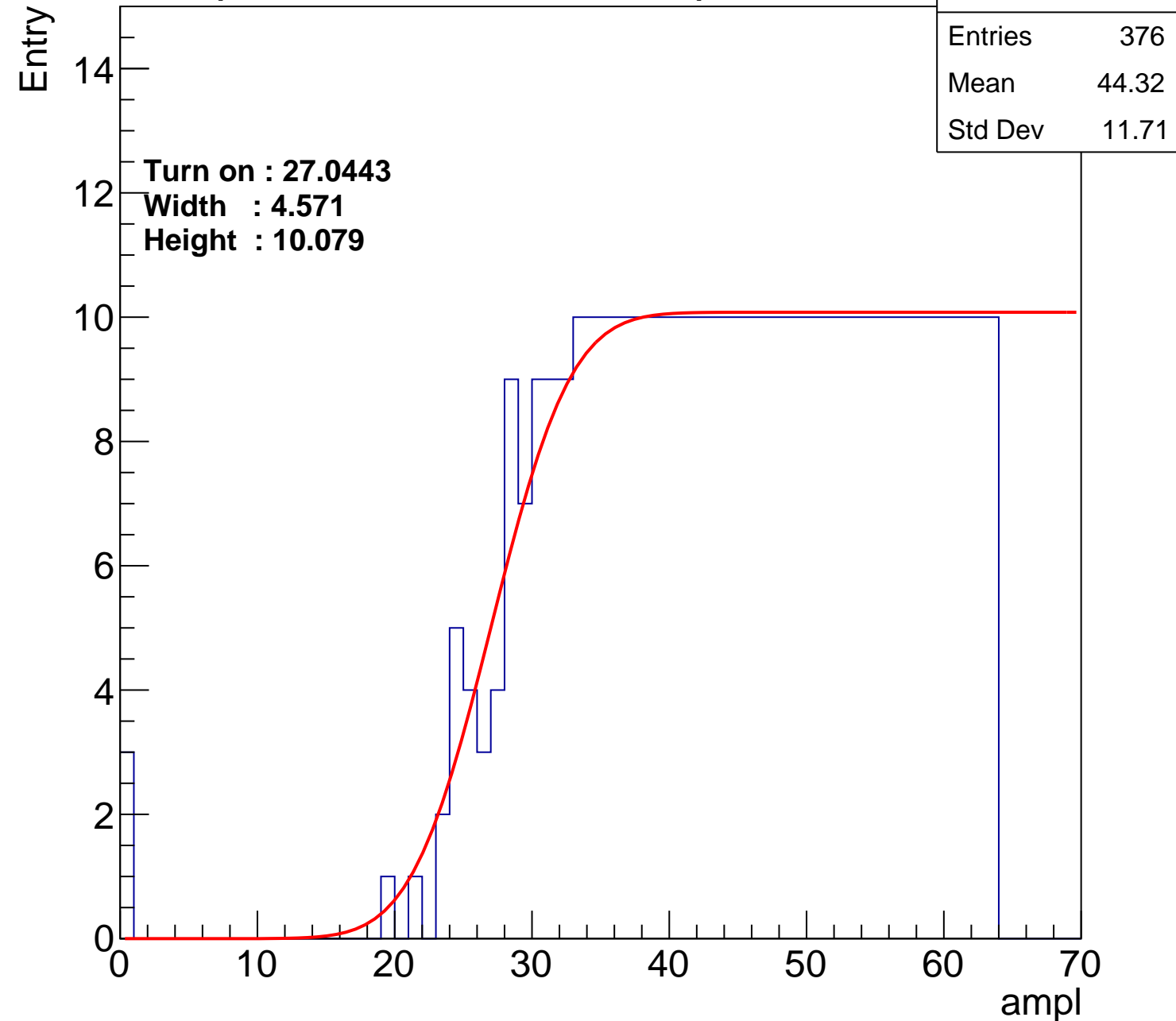
Width : 4.571

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch55

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.4 |
| Std Dev | 11.58 |

Turn on : 26.5326

Width : 2.467

Height : 9.952

Entry

14

12

10

8

6

4

2

0

0

10

20

30

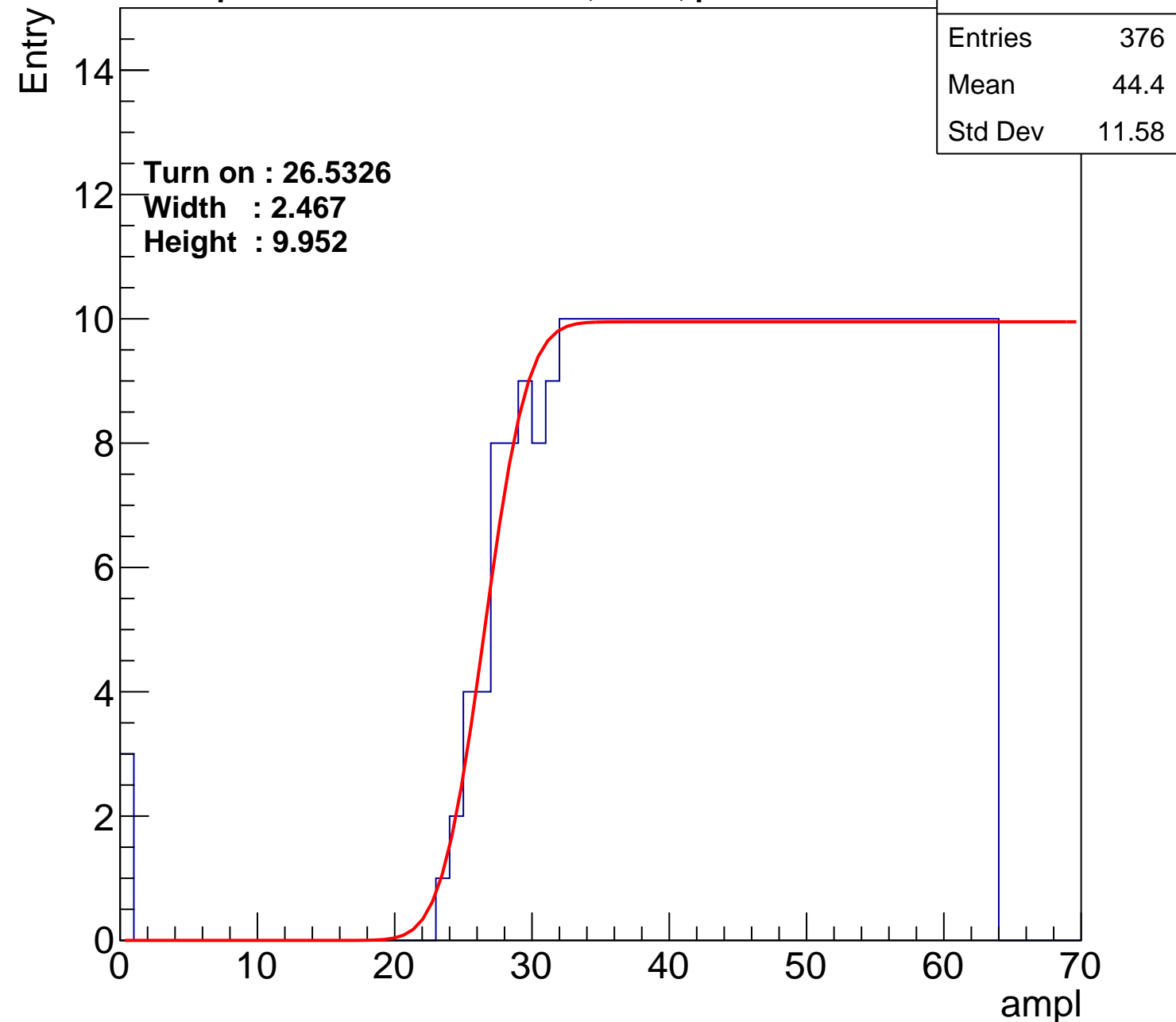
40

50

60

70

ampl



B1L103S, U6-ch56

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 43.95 |
| Std Dev | 12.01 |

Turn on : 26.9413

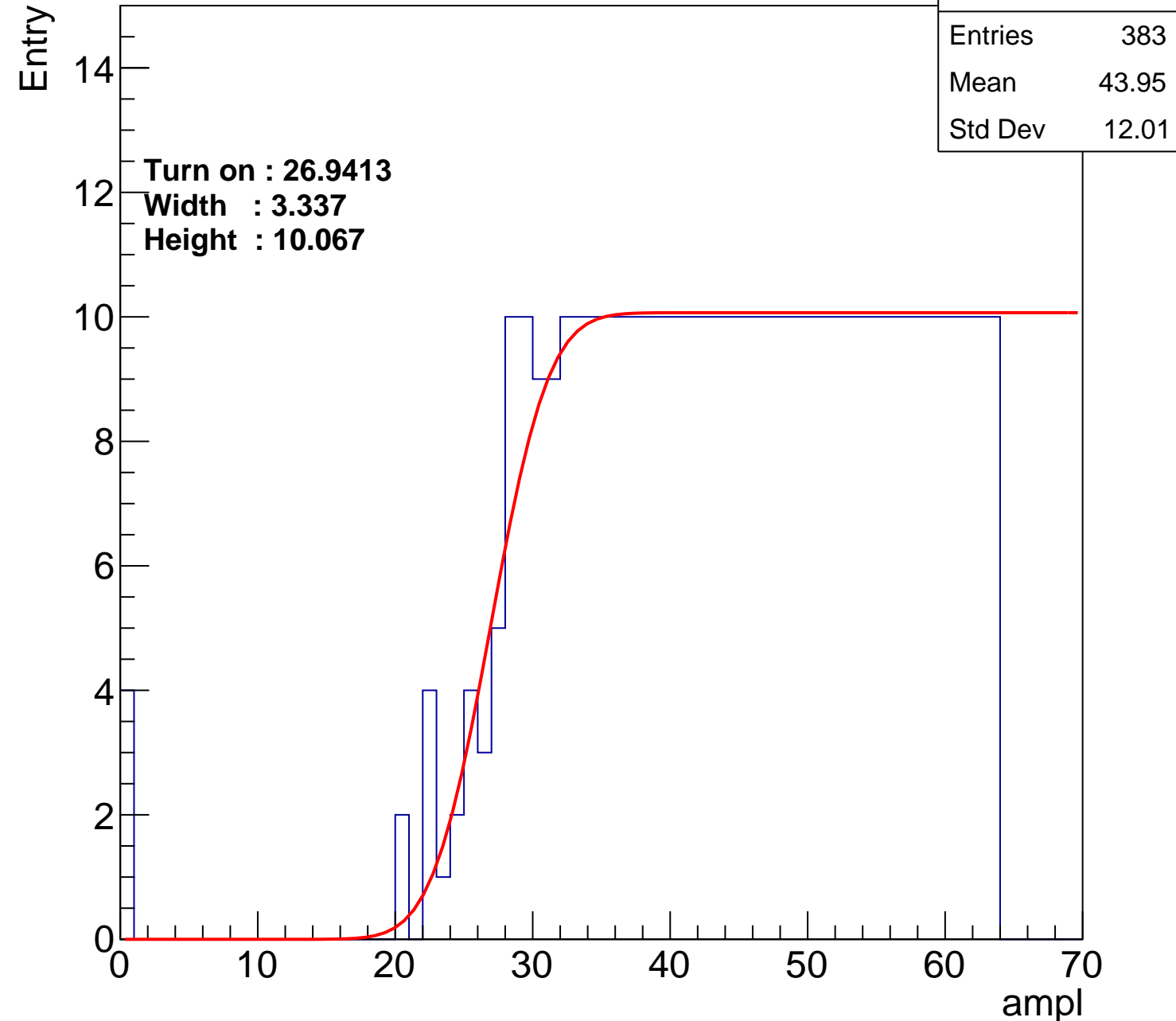
Width : 3.337

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch57

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.24 |
| Std Dev | 11.36 |

Turn on : 27.7916

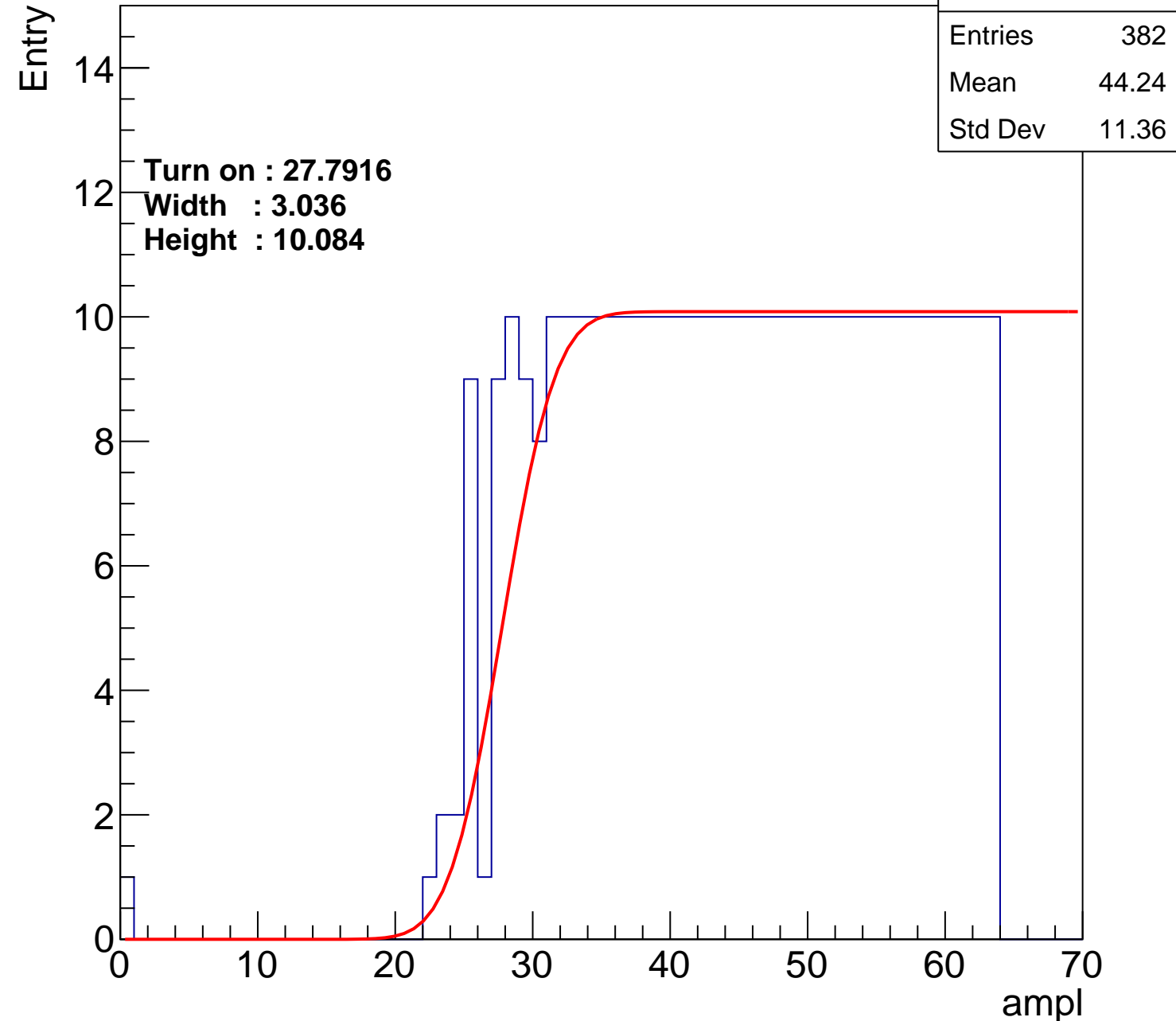
Width : 3.036

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch58

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.51 |
| Std Dev | 11.25 |

Turn on : 26.9684

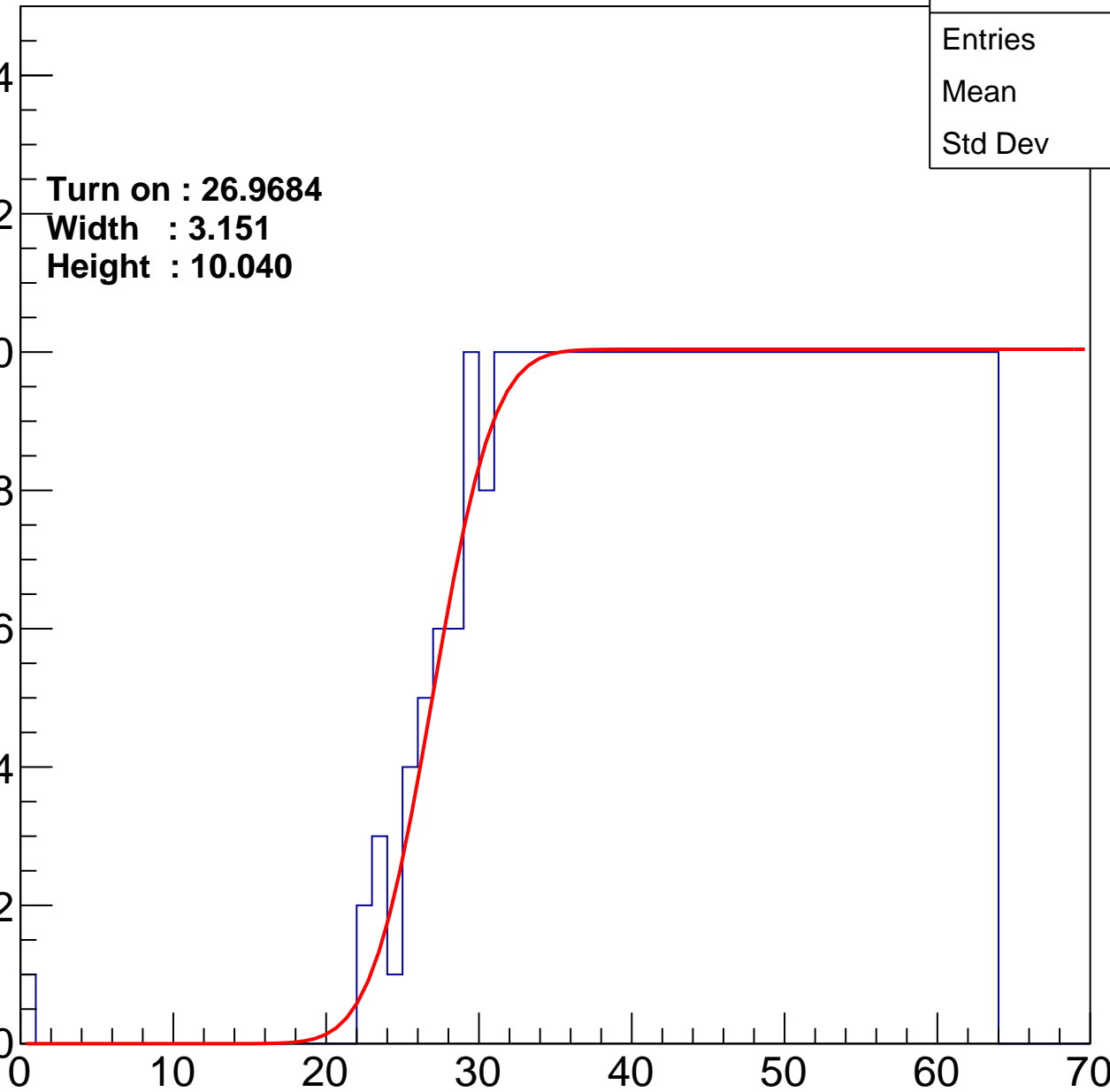
Width : 3.151

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch59

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.98 |
| Std Dev | 11.52 |

Turn on : 26.4710

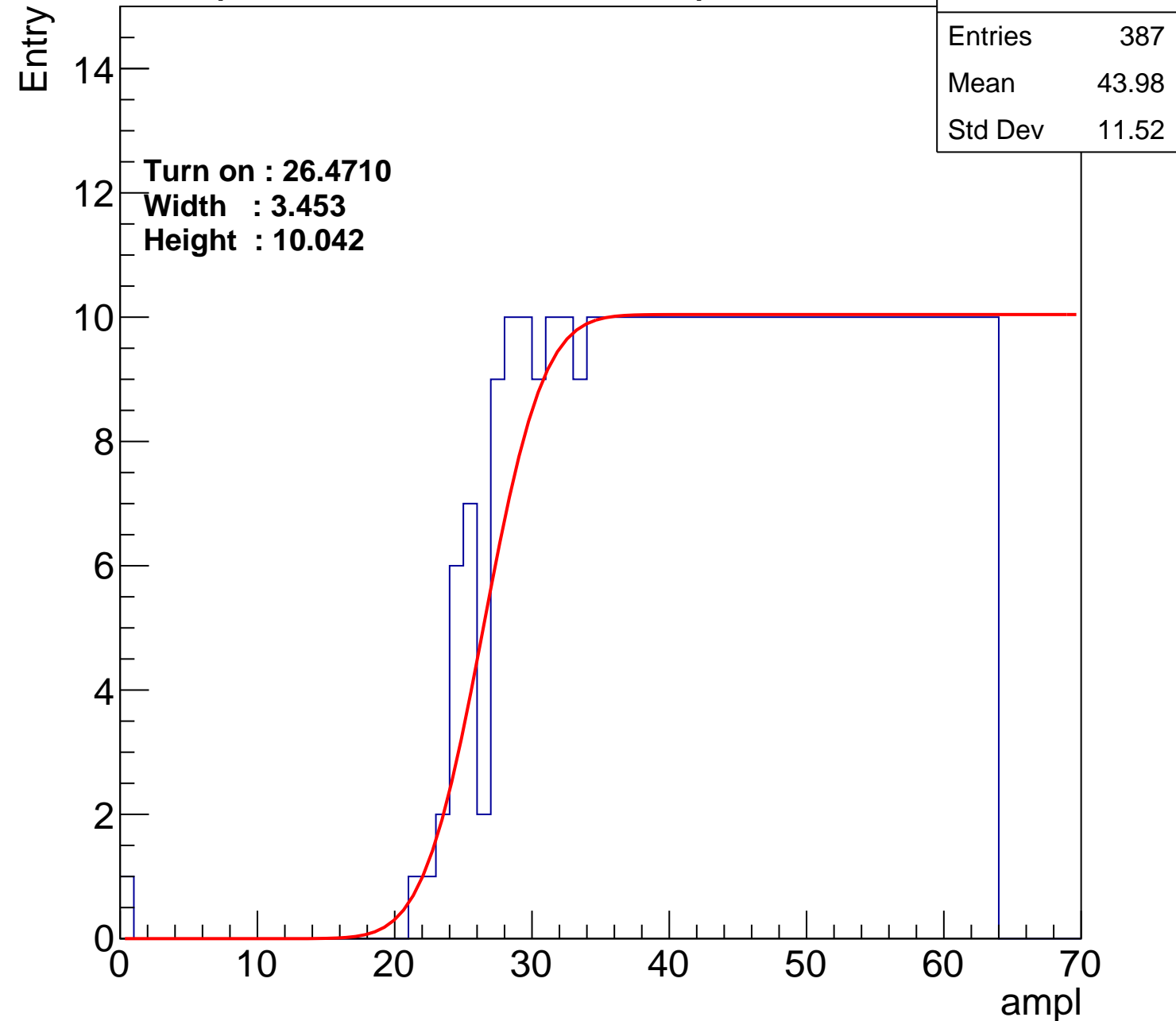
Width : 3.453

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch60

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.92 |
| Std Dev | 11.69 |

Turn on : 25.5081

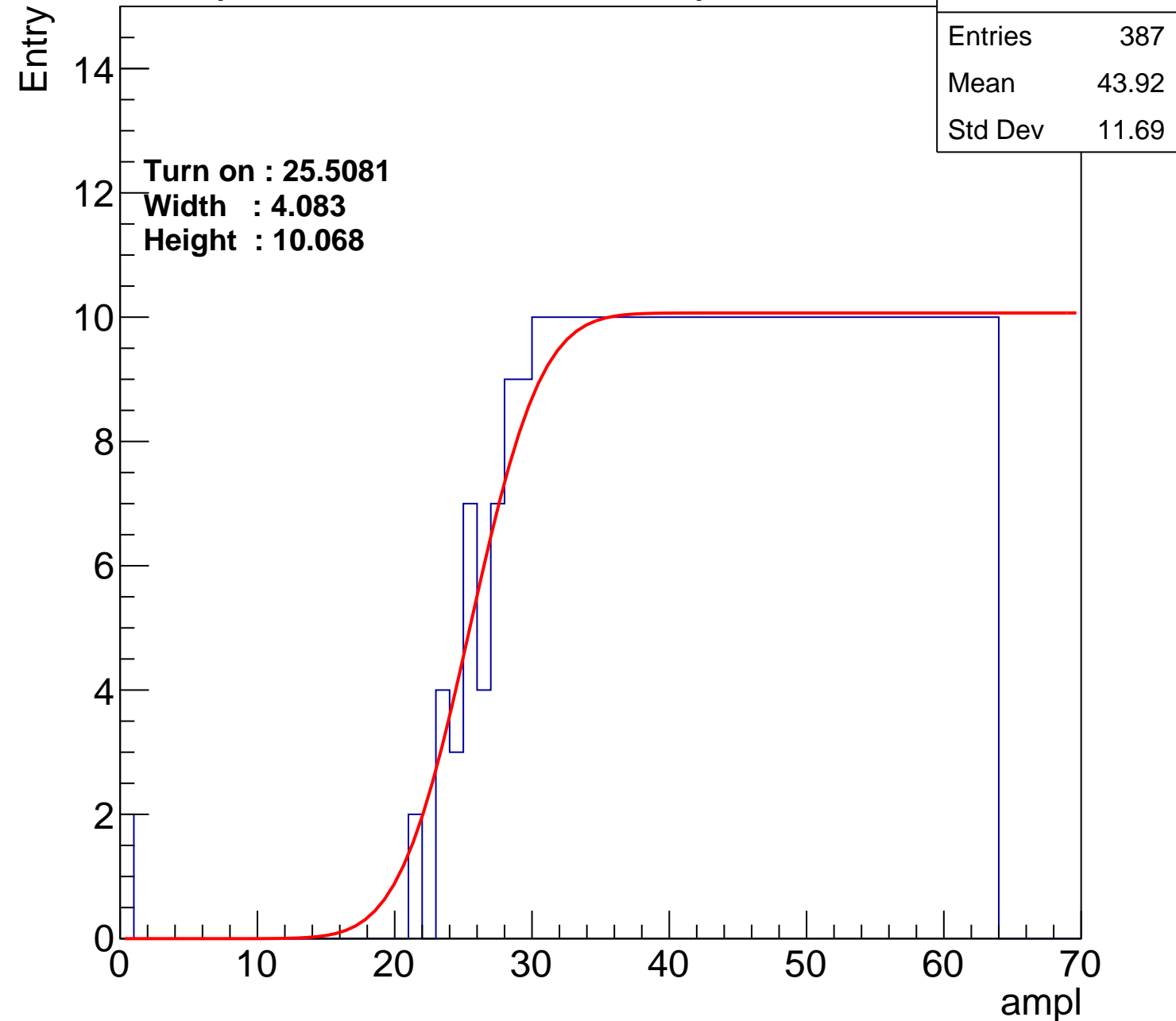
Width : 4.083

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch61

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.72 |
| Std Dev | 11.59 |

Turn on : 27.8319

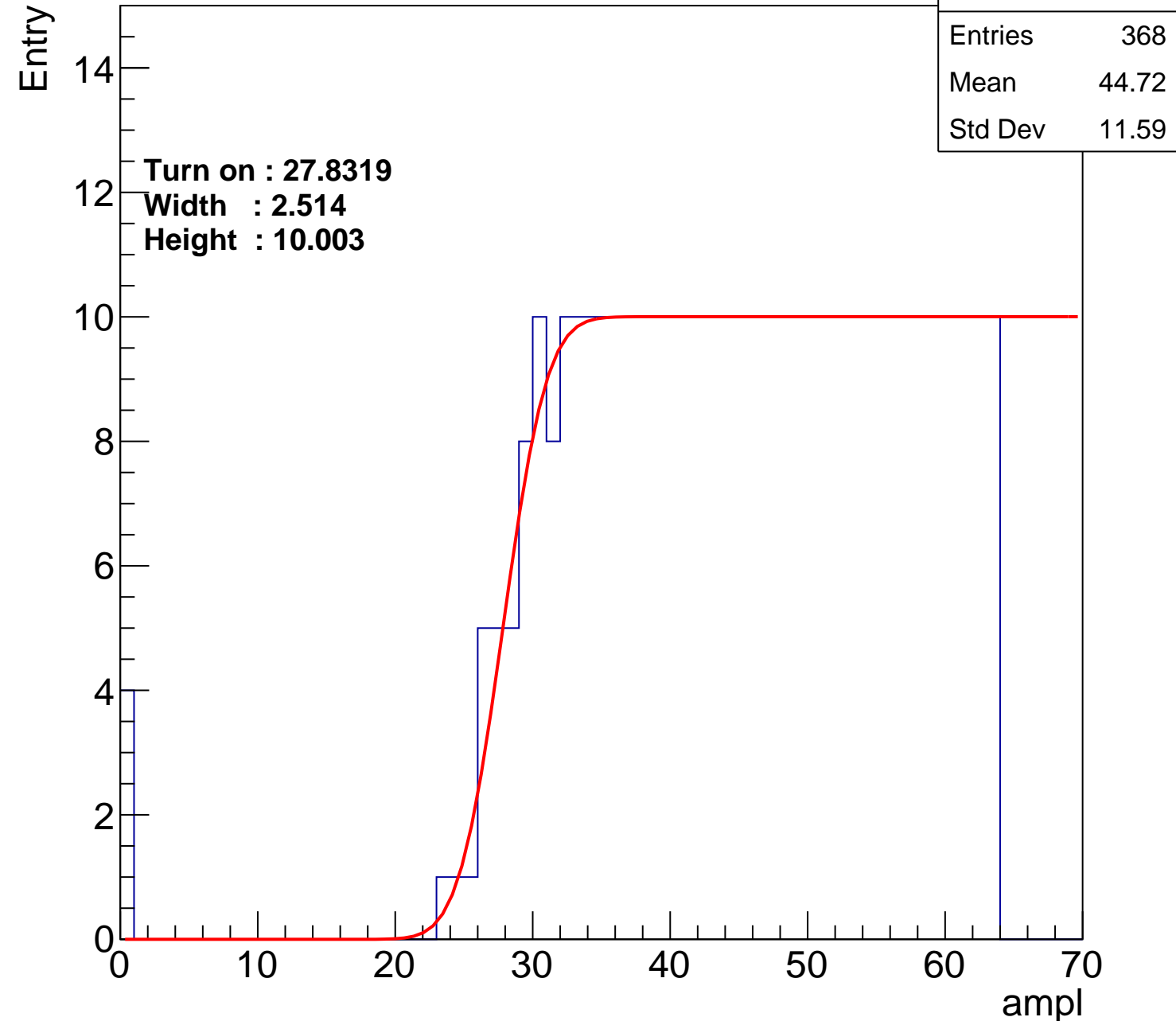
Width : 2.514

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch62

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.07 |
| Std Dev | 12.37 |

Turn on : 24.7731

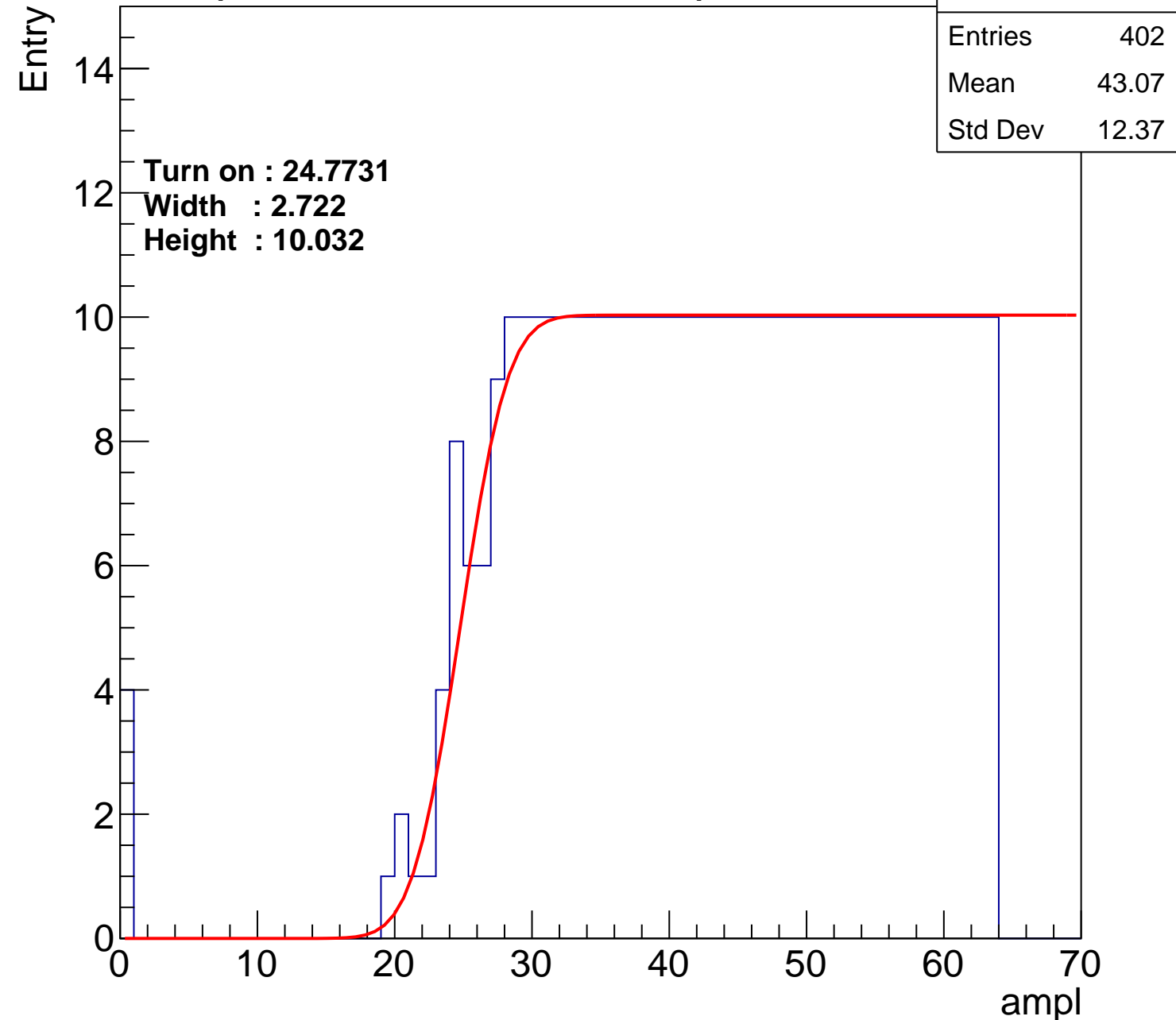
Width : 2.722

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch63

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.5 |
| Std Dev | 11.58 |

Turn on : 27.1177

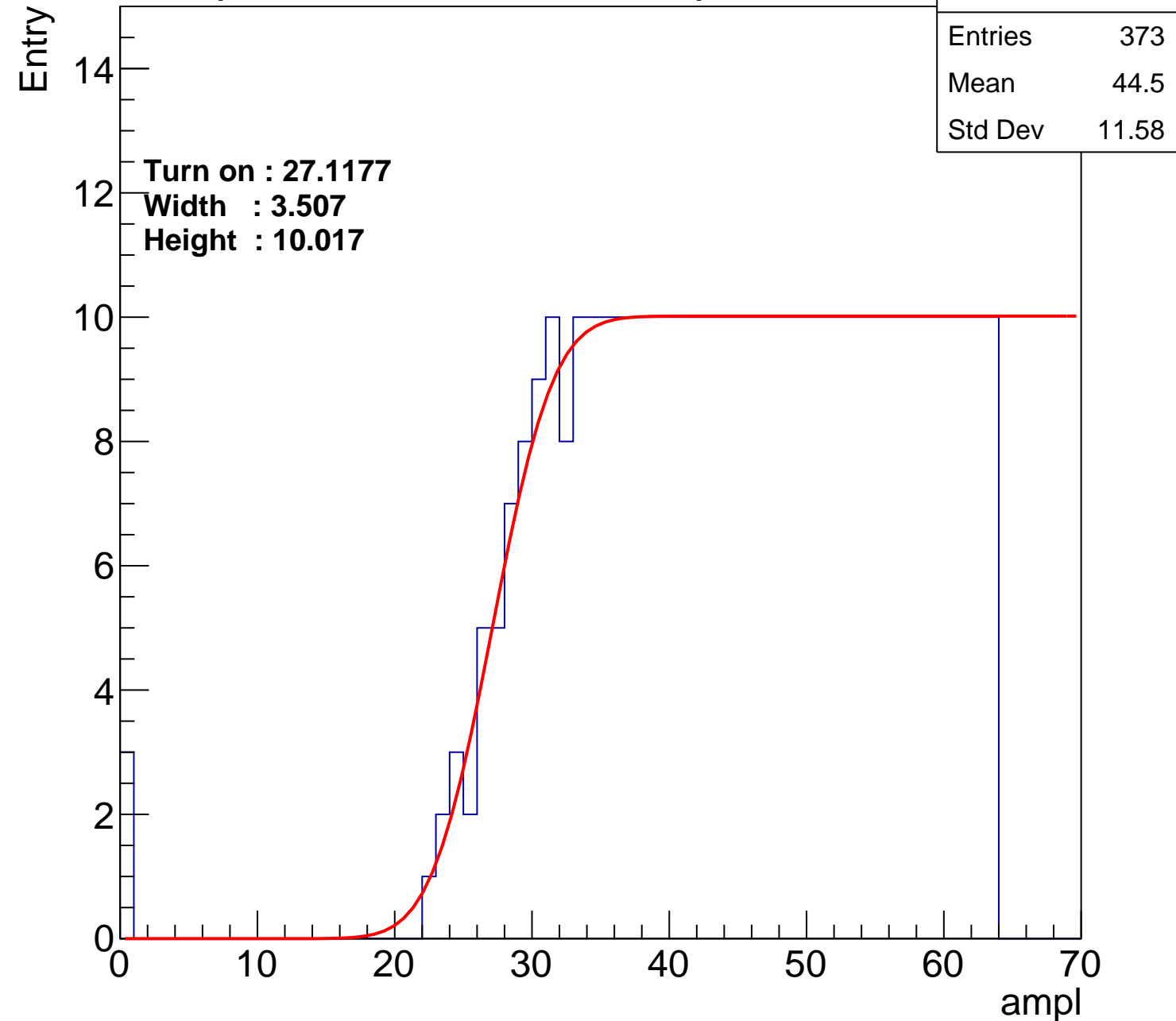
Width : 3.507

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch64

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.15 |
| Std Dev | 12.02 |

Turn on : 26.4805

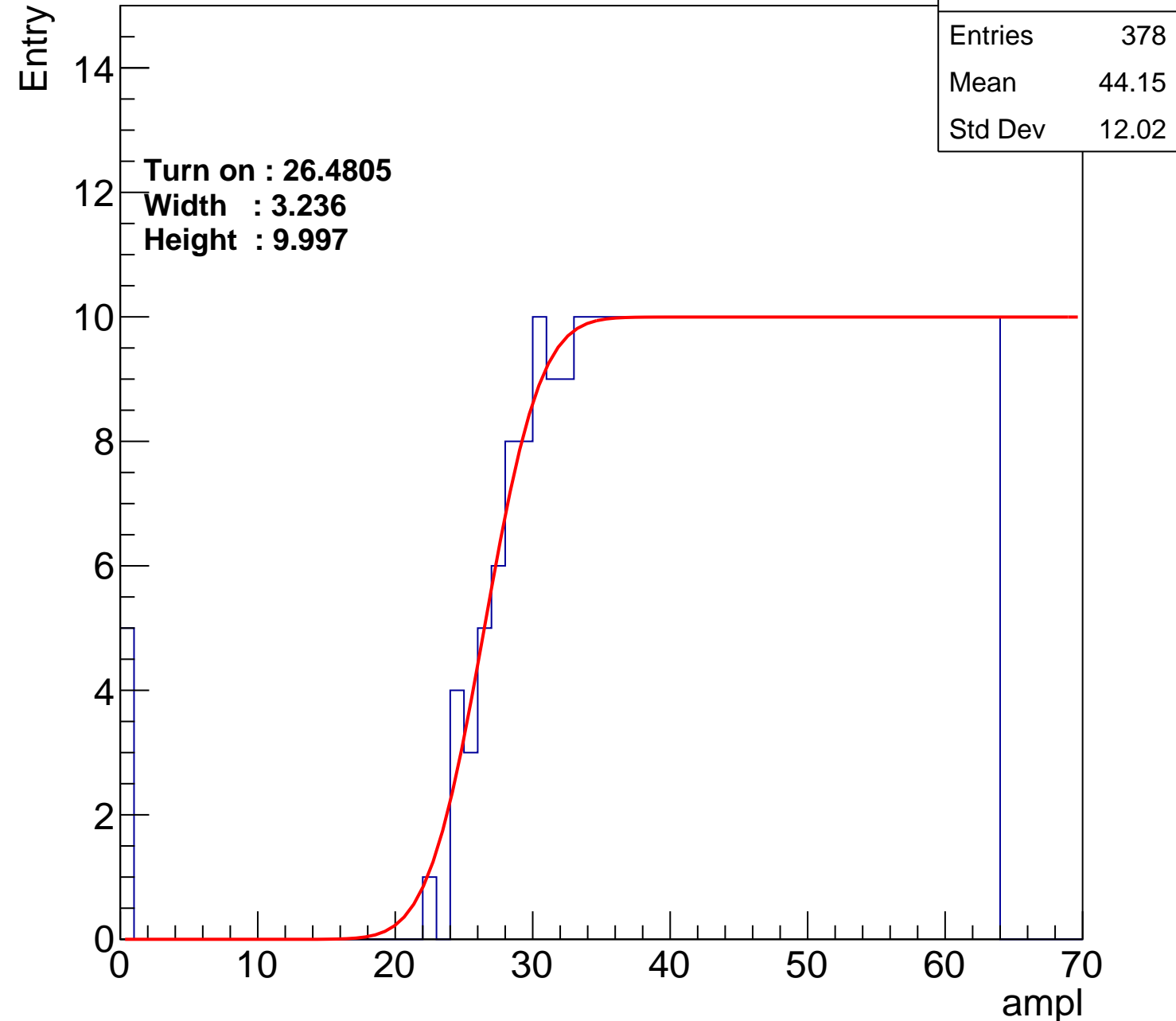
Width : 3.236

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch65

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.83 |
| Std Dev | 11.4 |

Turn on : 28.3765

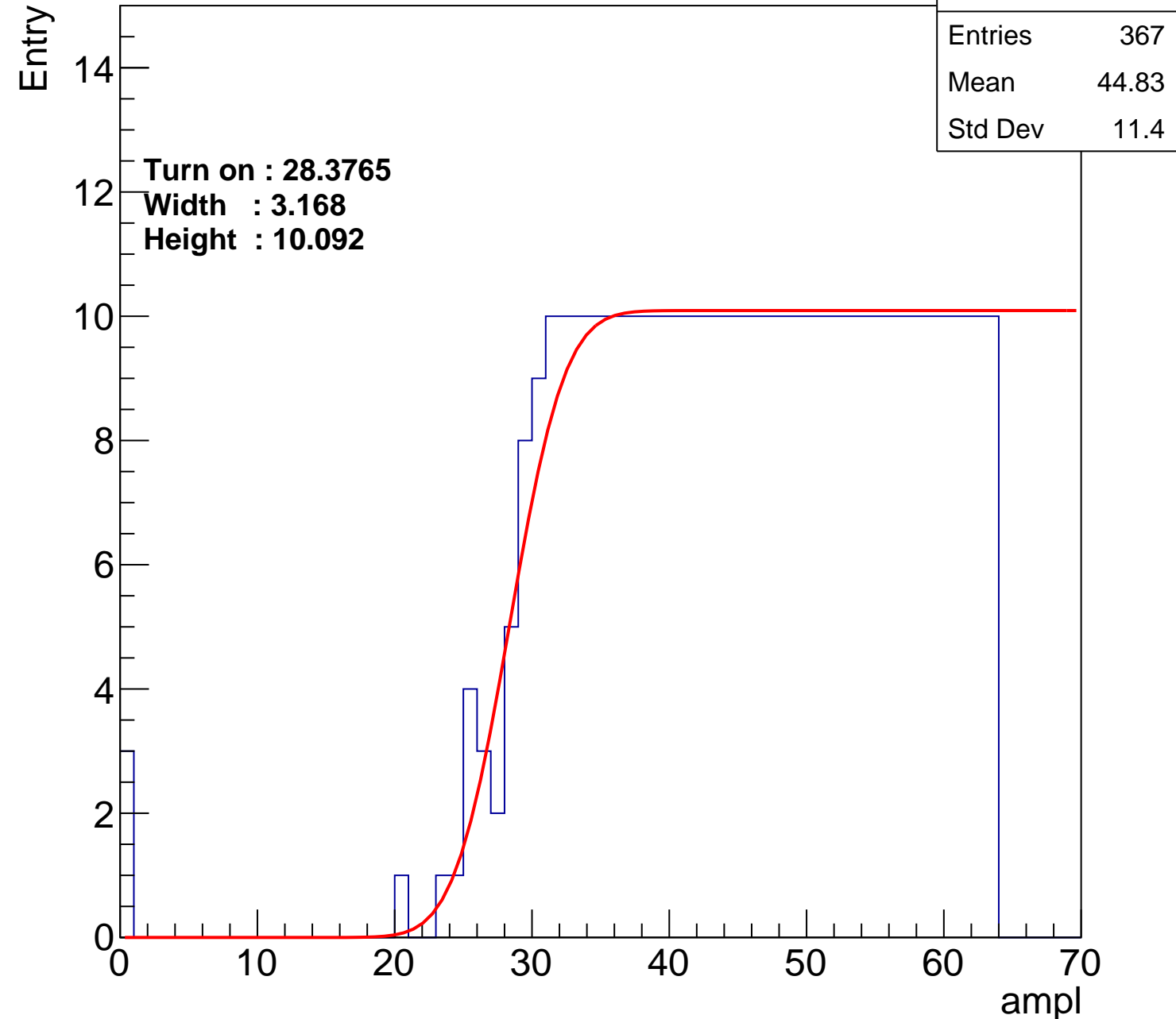
Width : 3.168

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch66

calib_packv5_042523_0143.root, FC#7, port C2

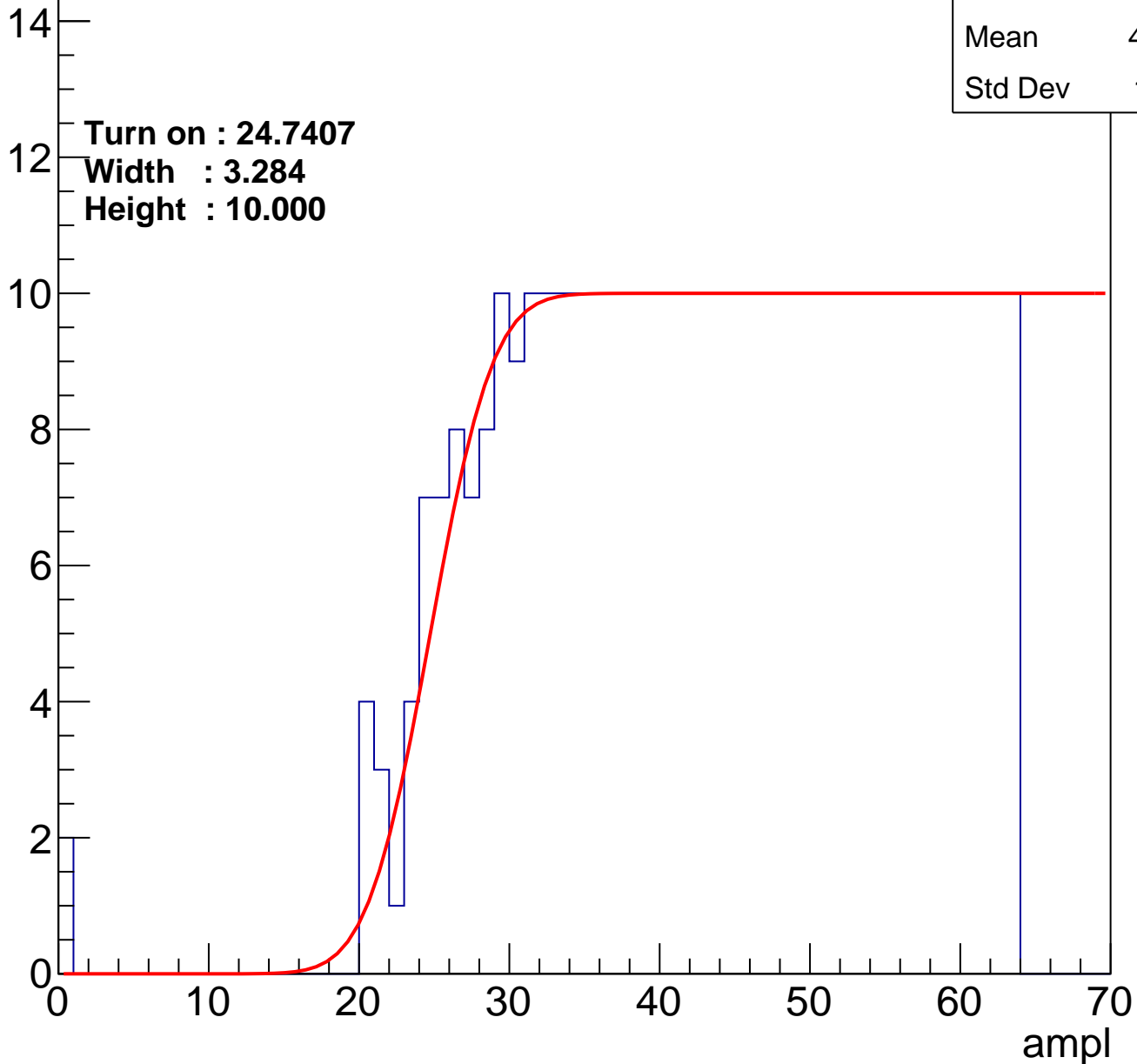
| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.23 |
| Std Dev | 12.11 |

Turn on : 24.7407

Width : 3.284

Height : 10.000

Entry



B1L103S, U6-ch67

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.12 |
| Std Dev | 11.64 |

Turn on : 26.2905

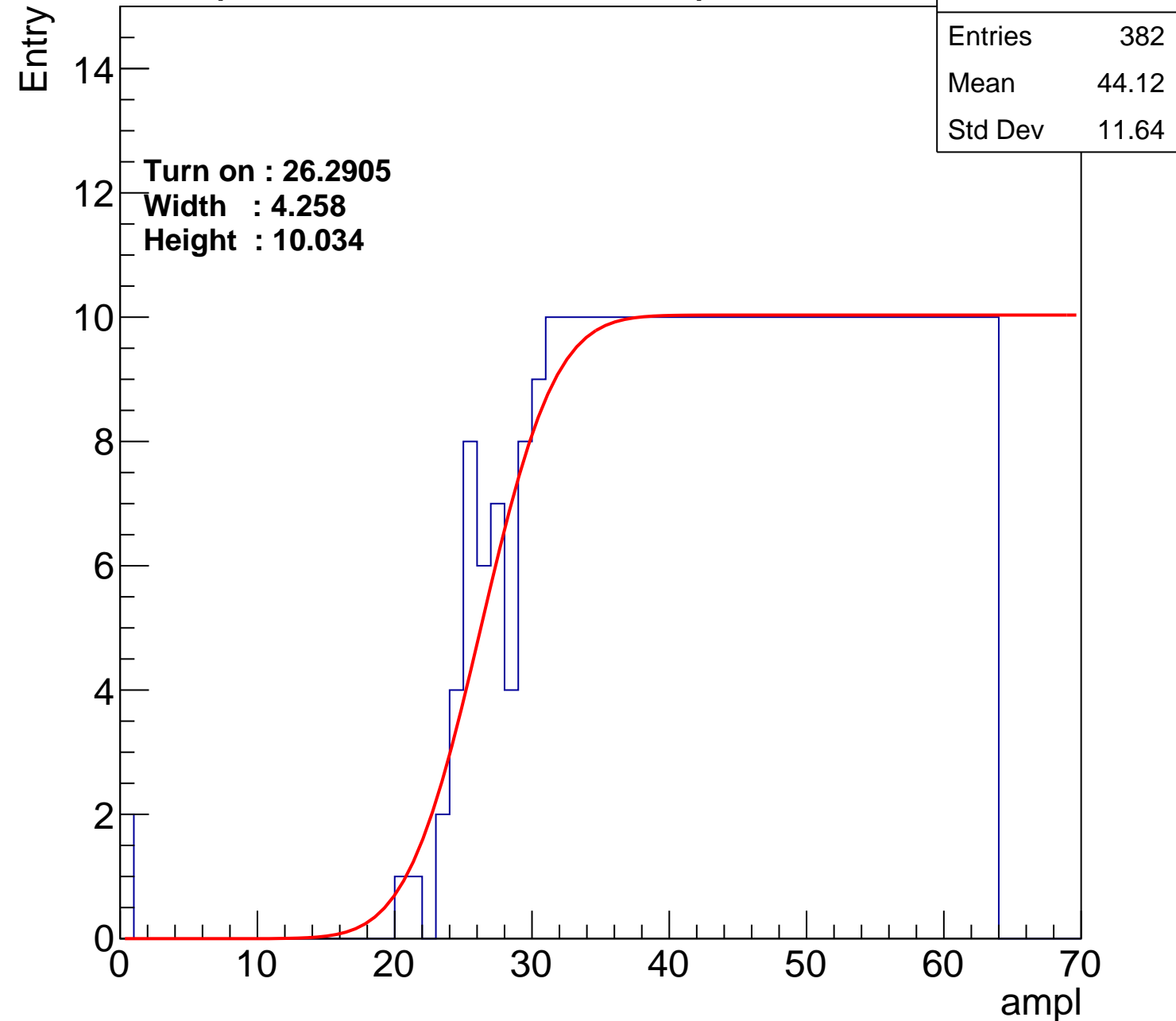
Width : 4.258

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch68

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.48 |
| Std Dev | 12.27 |

Turn on : 25.3356

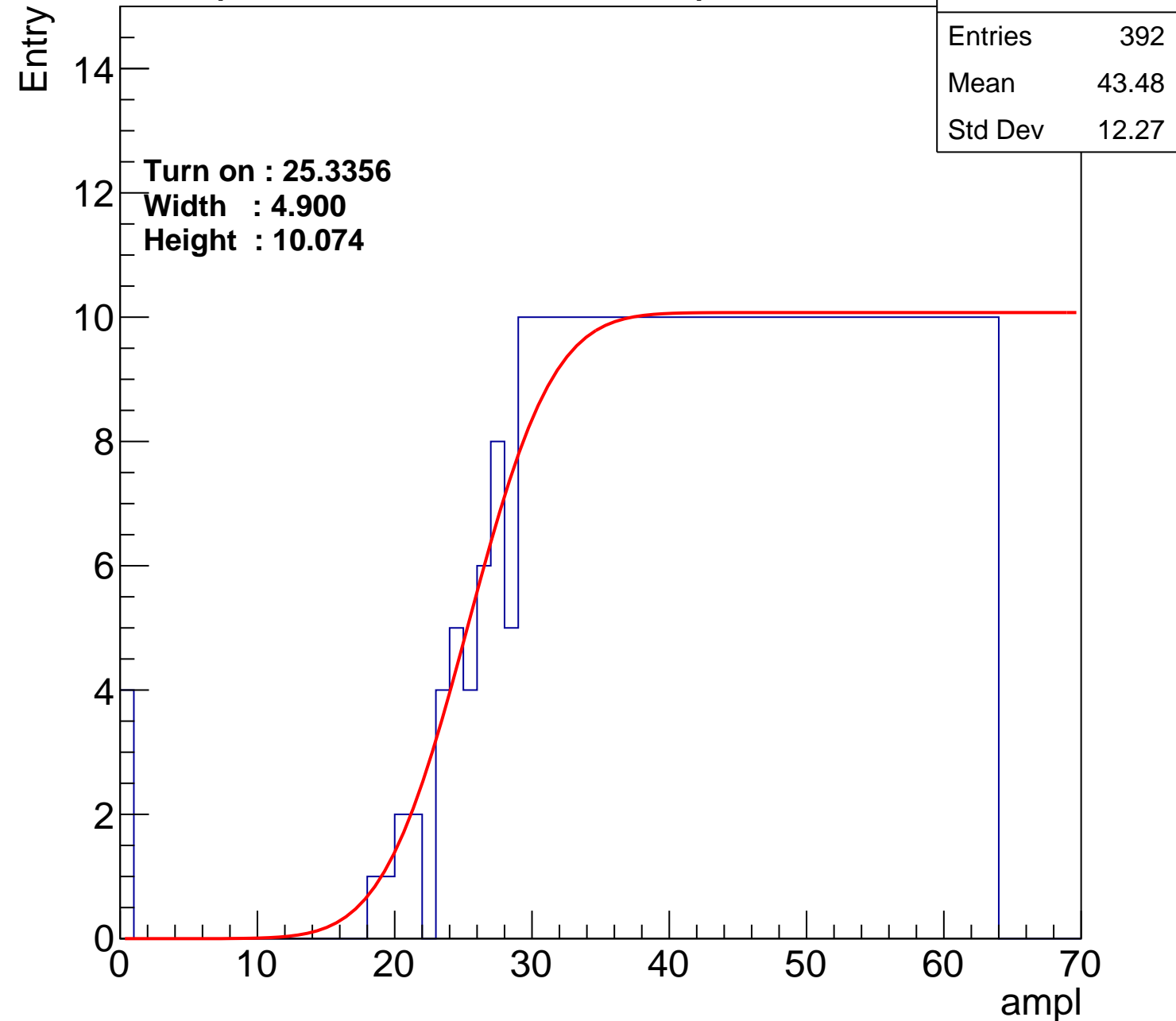
Width : 4.900

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch69

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.3 |
| Std Dev | 11.56 |

Turn on : 26.2064

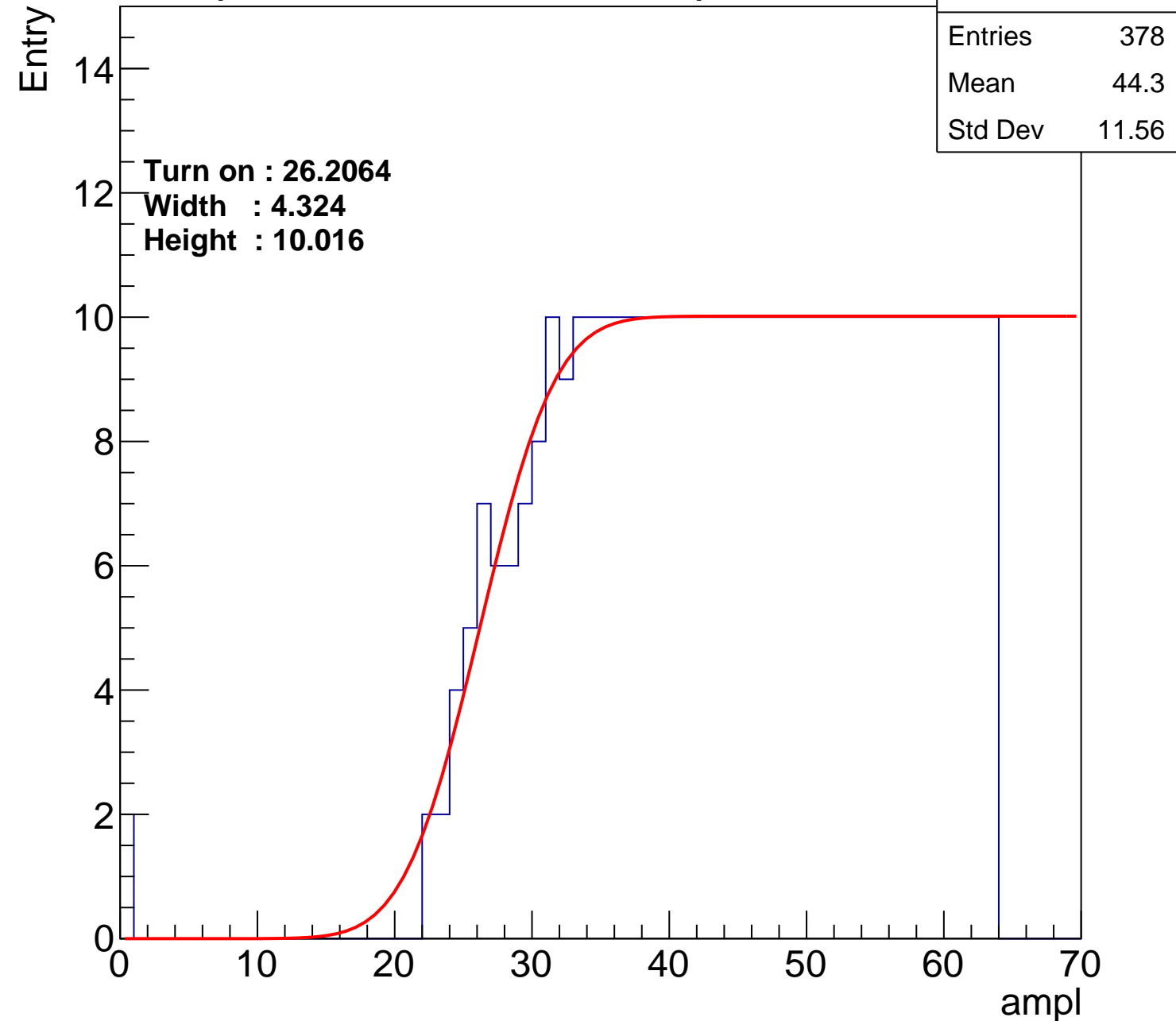
Width : 4.324

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch70

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.8 |
| Std Dev | 11.44 |

Turn on : 27.6435

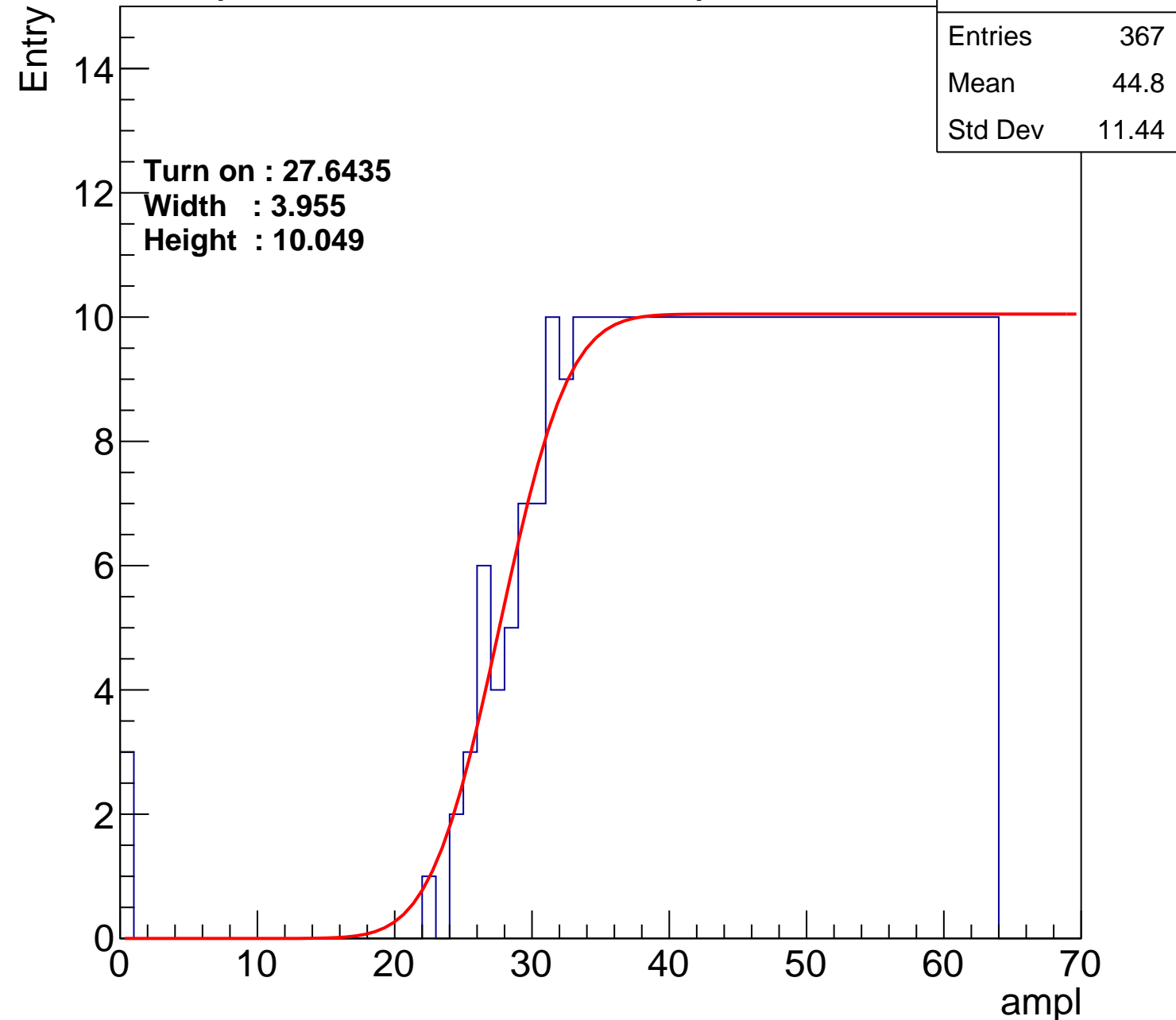
Width : 3.955

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch71

calib_packv5_042523_0143.root, FC#7, port C2

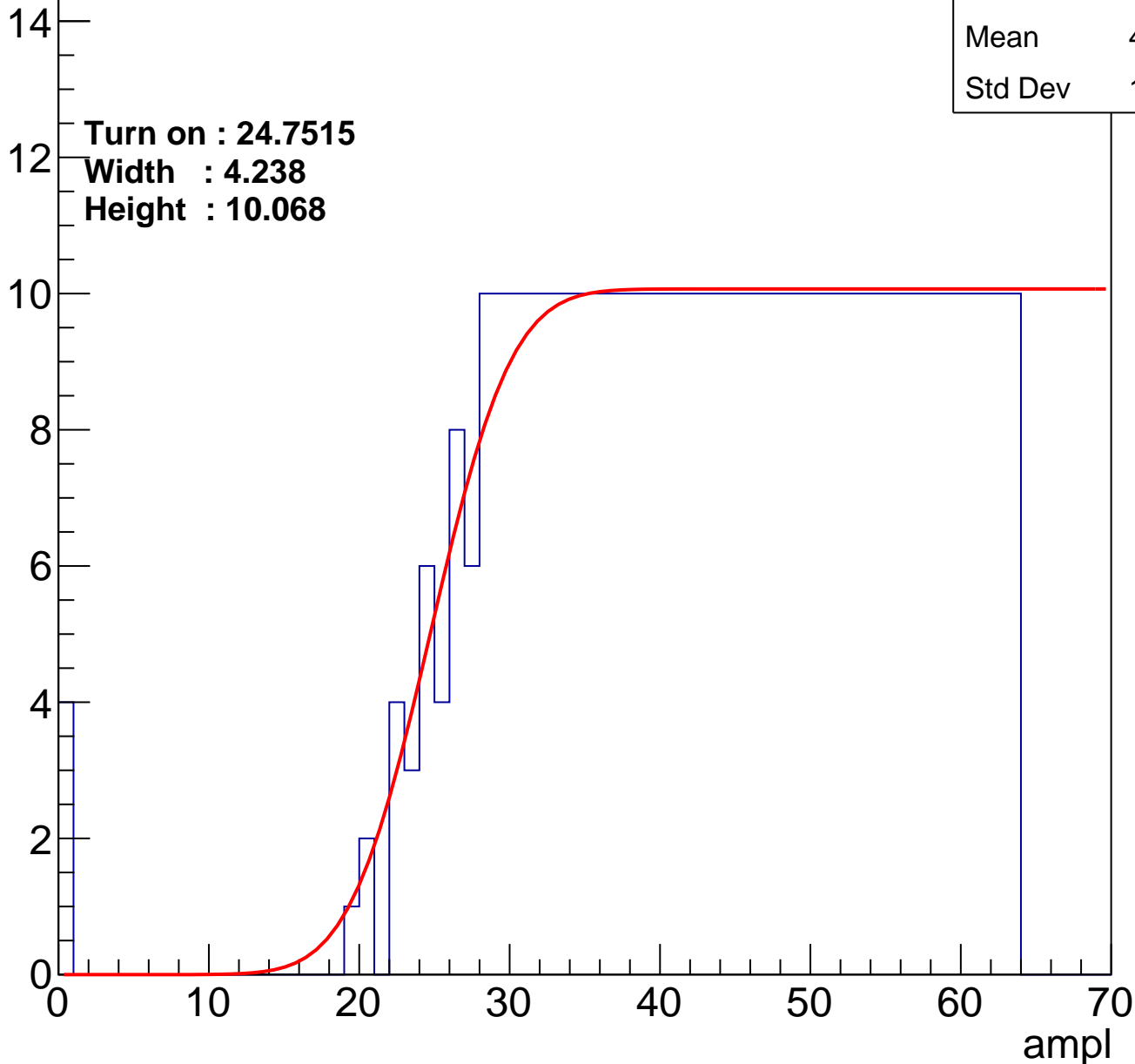
| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.24 |
| Std Dev | 12.32 |

Turn on : 24.7515

Width : 4.238

Height : 10.068

Entry



B1L103S, U6-ch72

calib_packv5_042523_0143.root, FC#7, port C2

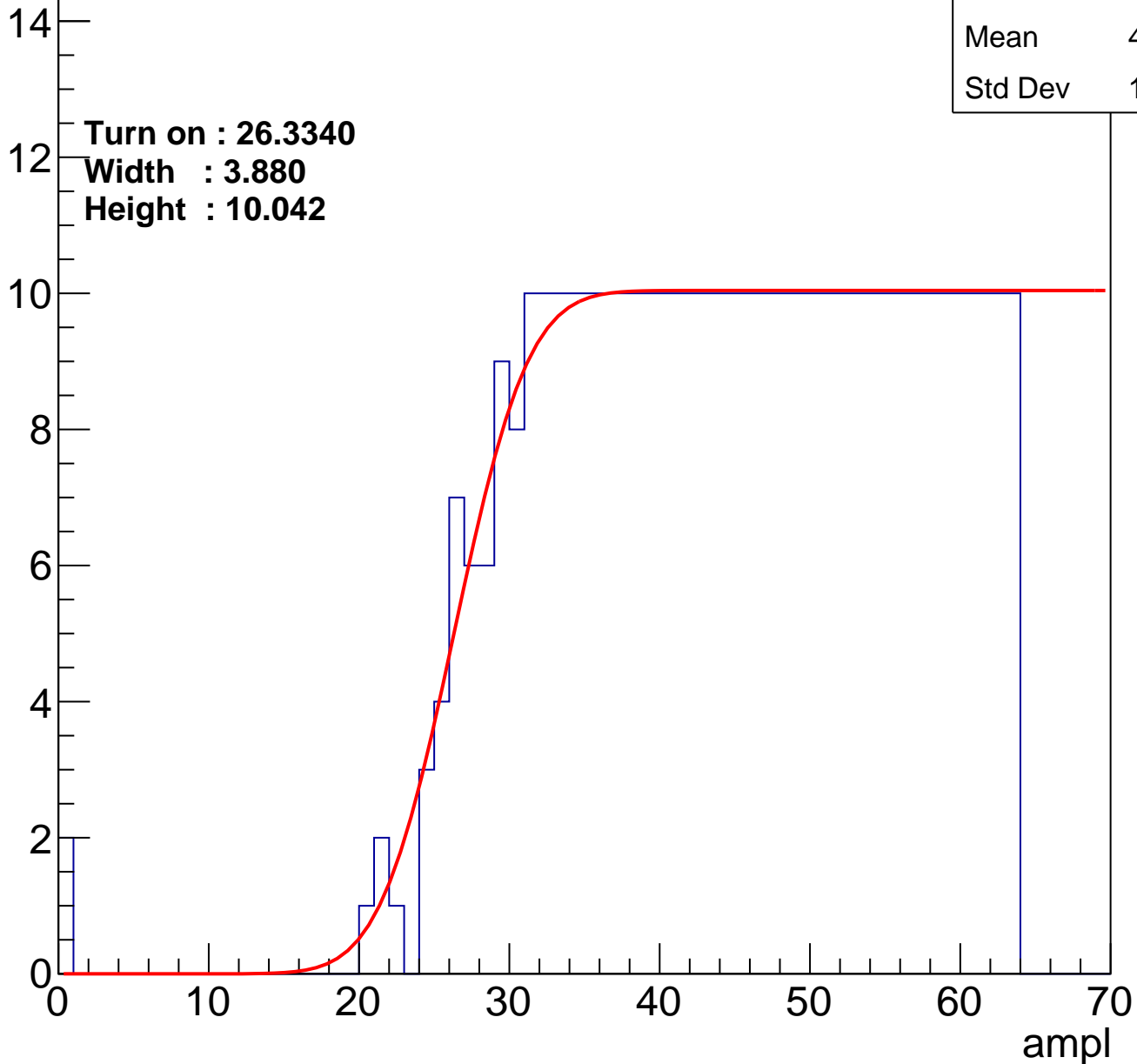
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.27 |
| Std Dev | 11.56 |

Turn on : 26.3340

Width : 3.880

Height : 10.042

Entry



B1L103S, U6-ch73

calib_packv5_042523_0143.root, FC#7, port C2

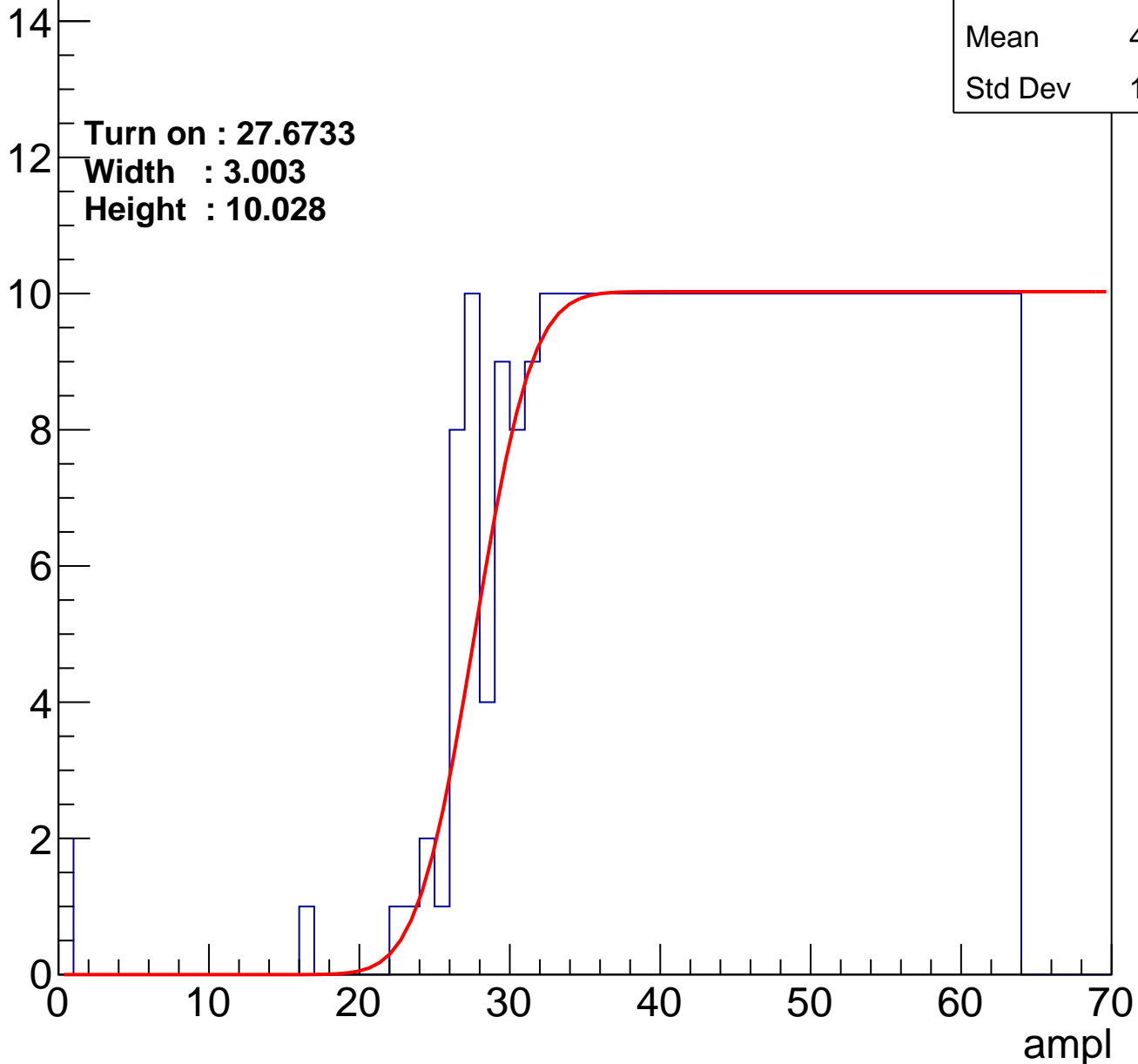
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.43 |
| Std Dev | 11.47 |

Turn on : 27.6733

Width : 3.003

Height : 10.028

Entry



B1L103S, U6-ch74

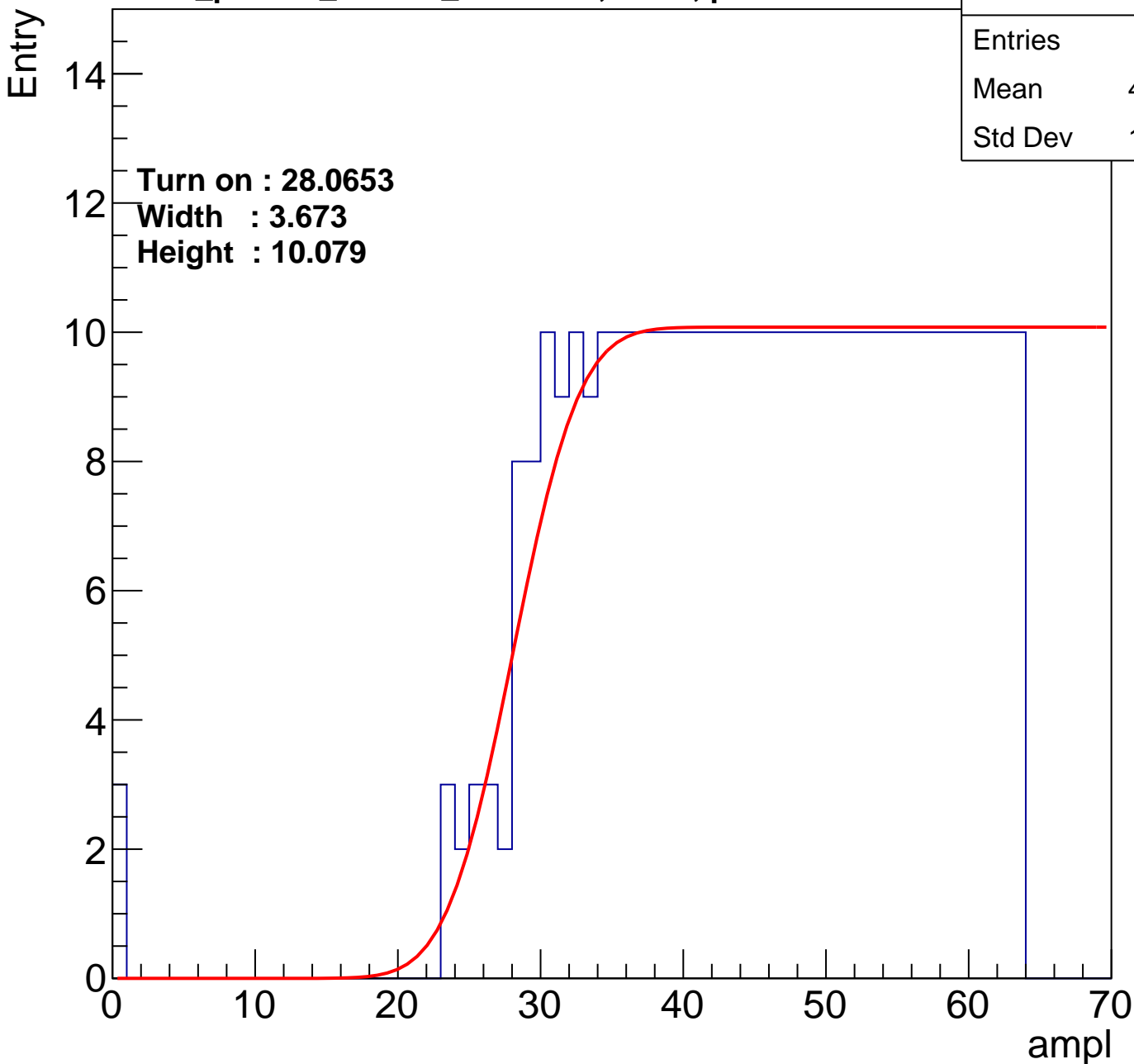
calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.66 |
| Std Dev | 11.49 |

Turn on : 28.0653

Width : 3.673

Height : 10.079



B1L103S, U6-ch75

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.75 |
| Std Dev | 12.16 |

Turn on : 25.5115

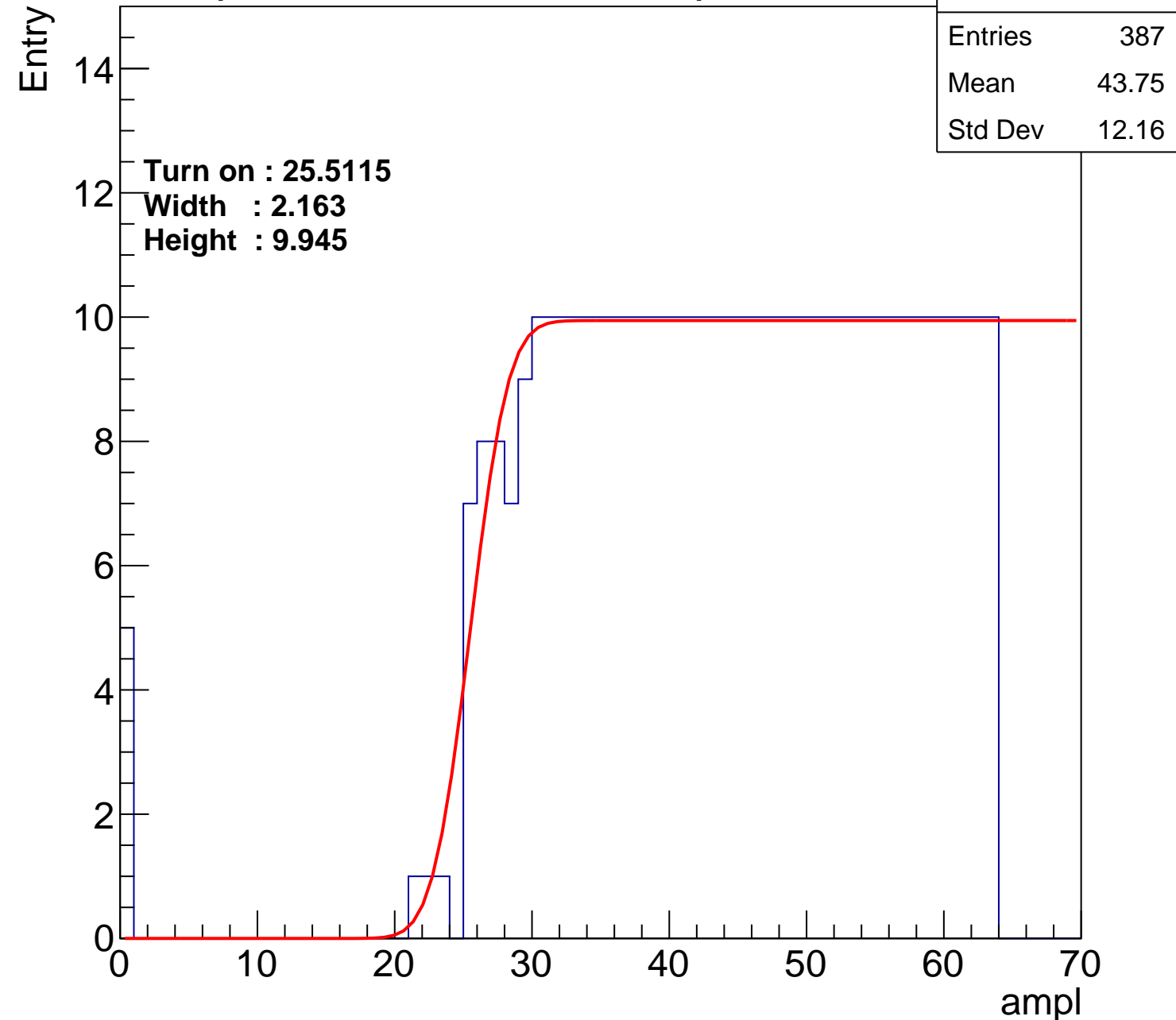
Width : 2.163

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch76

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.9 |
| Std Dev | 11.71 |

Turn on : 25.2267

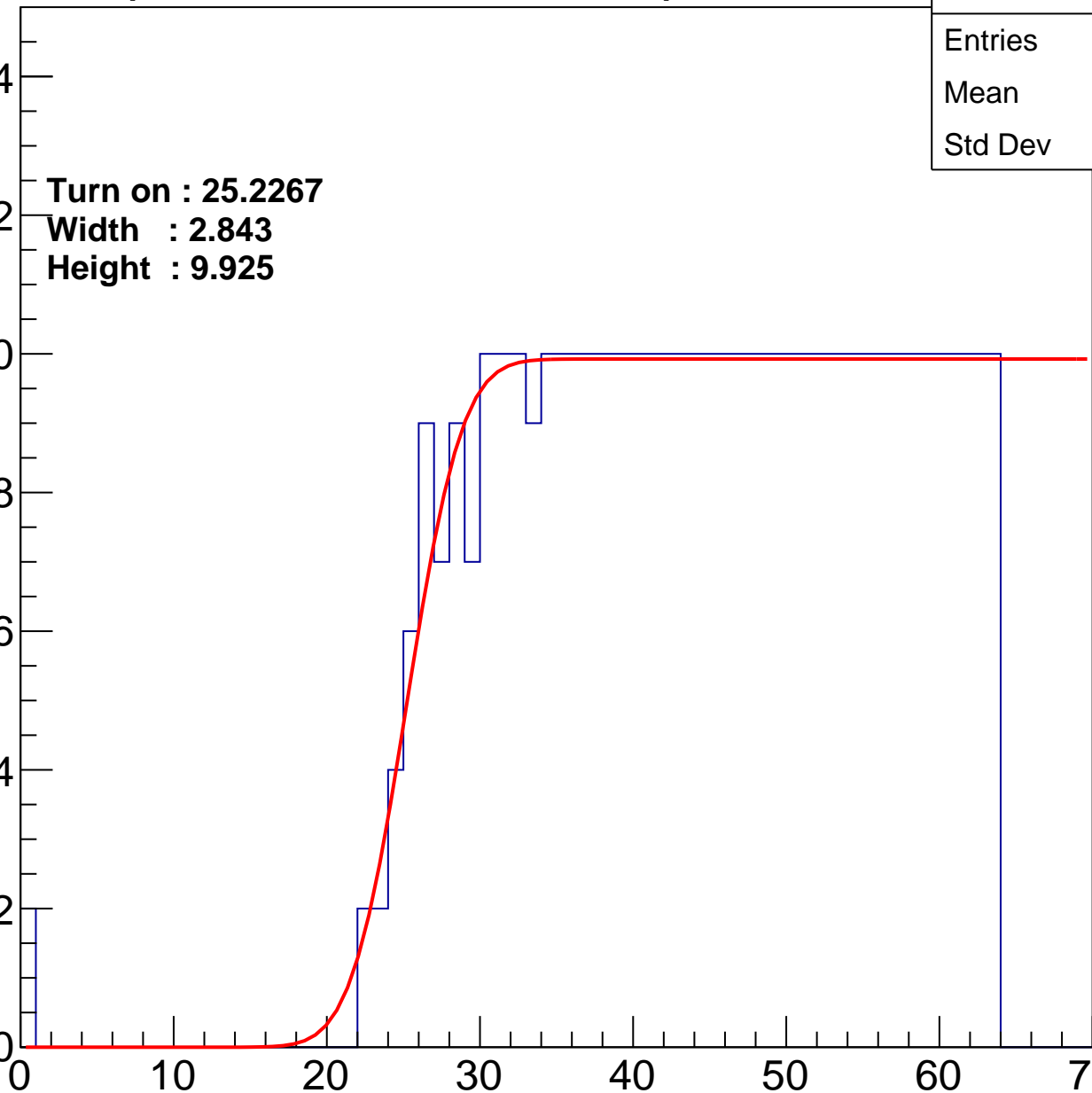
Width : 2.843

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch77

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.05 |
| Std Dev | 11.65 |

Turn on : 26.2600

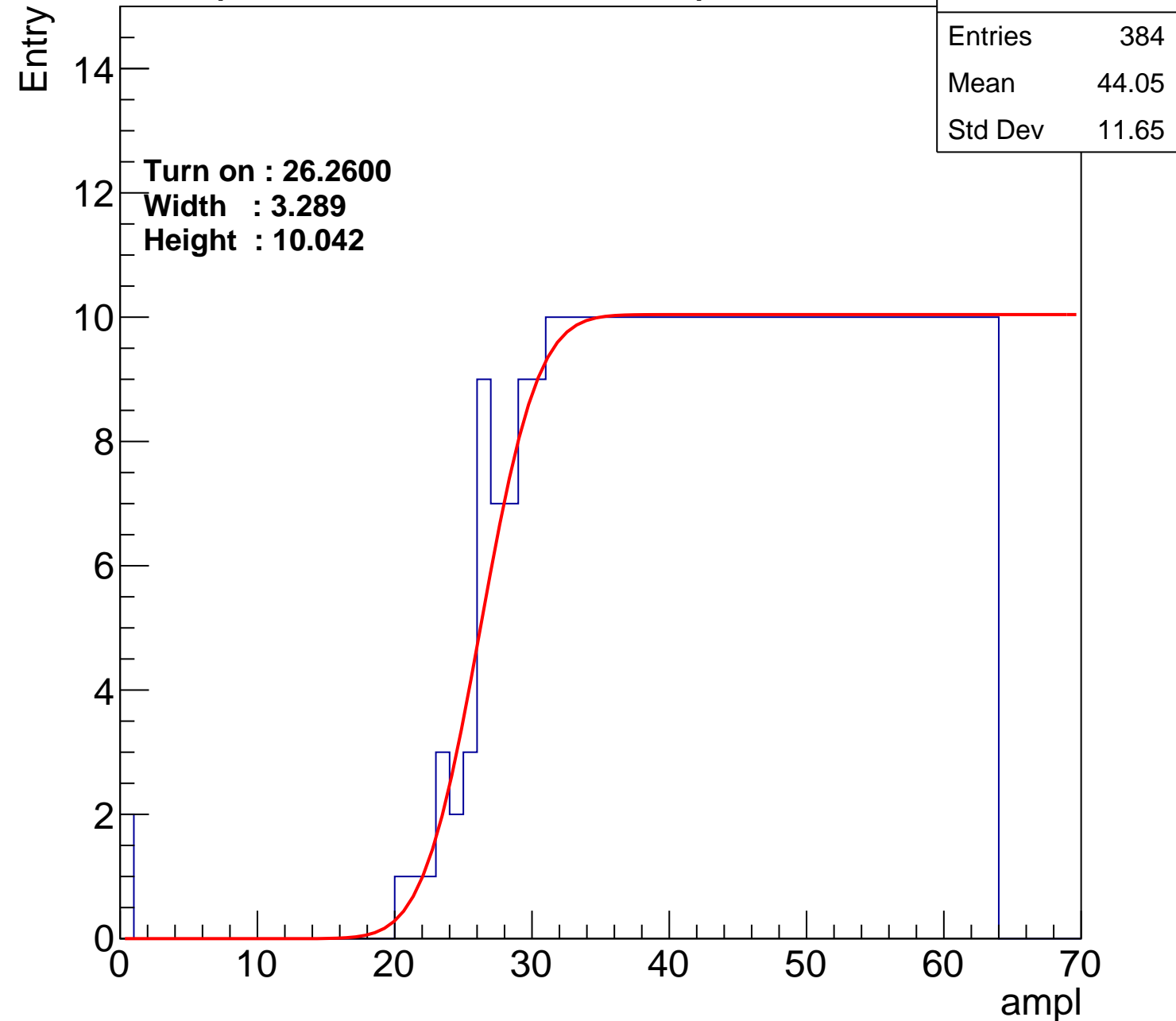
Width : 3.289

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch78

calib_packv5_042523_0143.root, FC#7, port C2

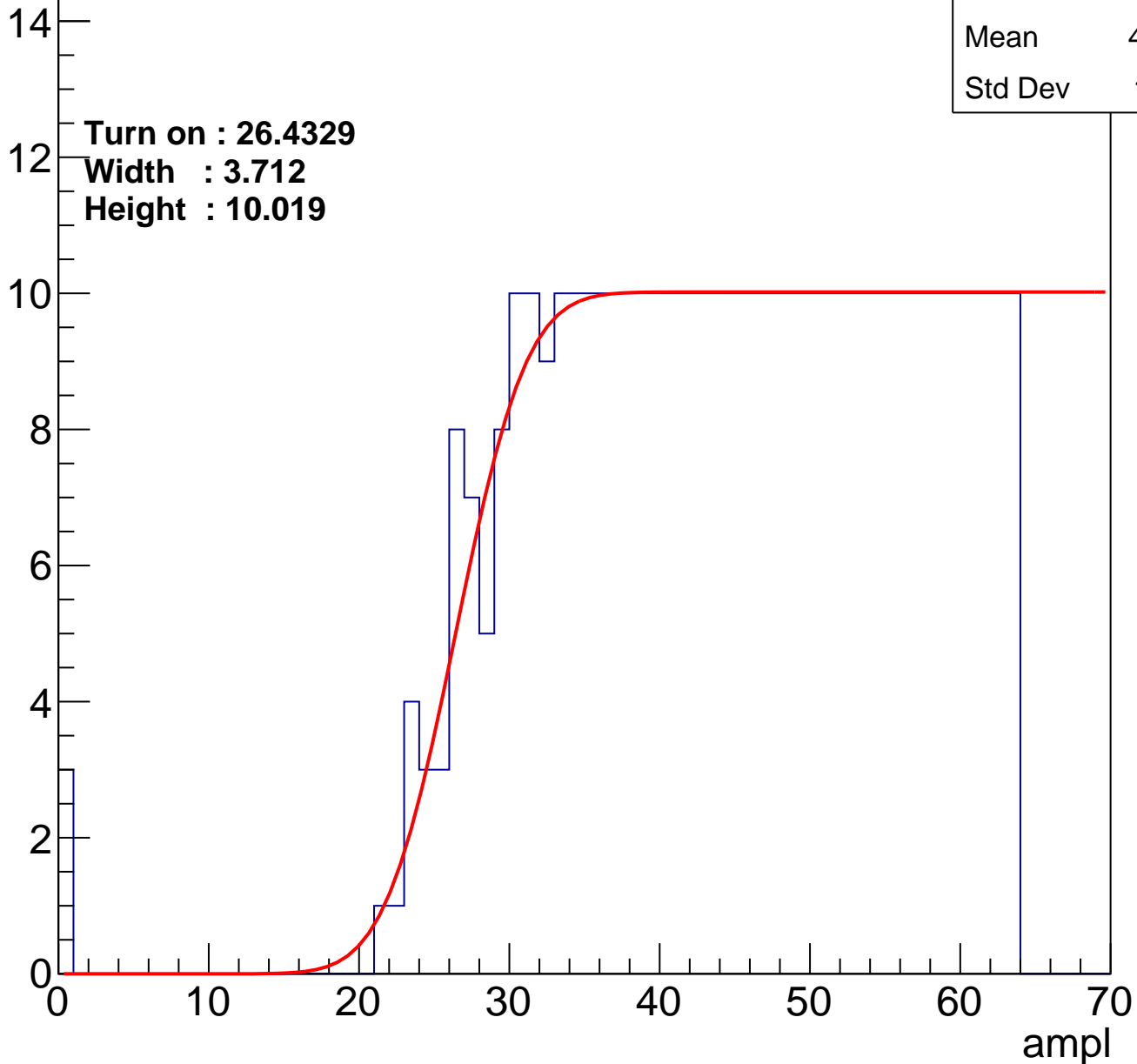
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.05 |
| Std Dev | 11.81 |

Turn on : 26.4329

Width : 3.712

Height : 10.019

Entry



B1L103S, U6-ch79

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.13 |
| Std Dev | 11.49 |

Turn on : 26.0759

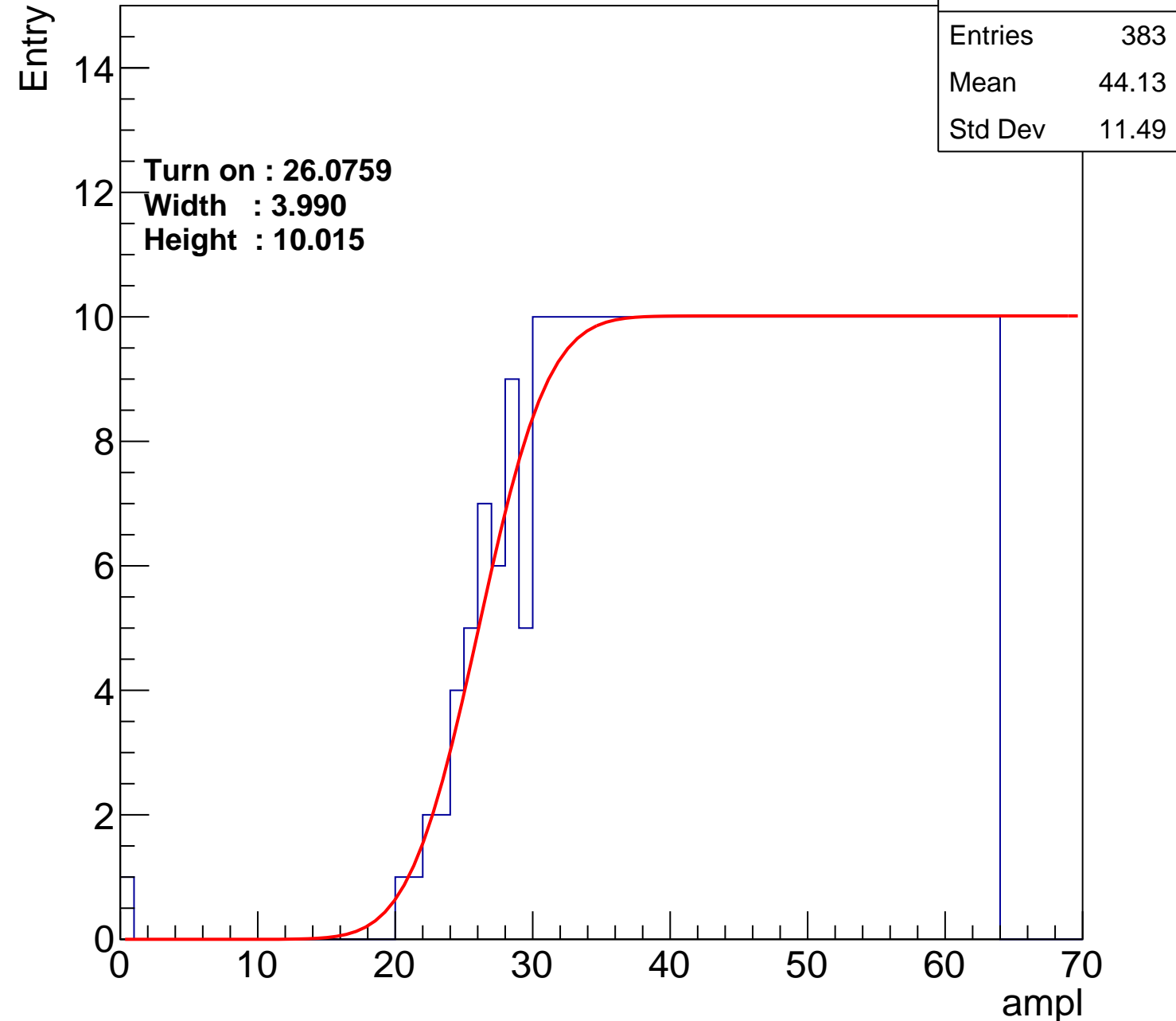
Width : 3.990

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch80

calib_packv5_042523_0143.root, FC#7, port C2

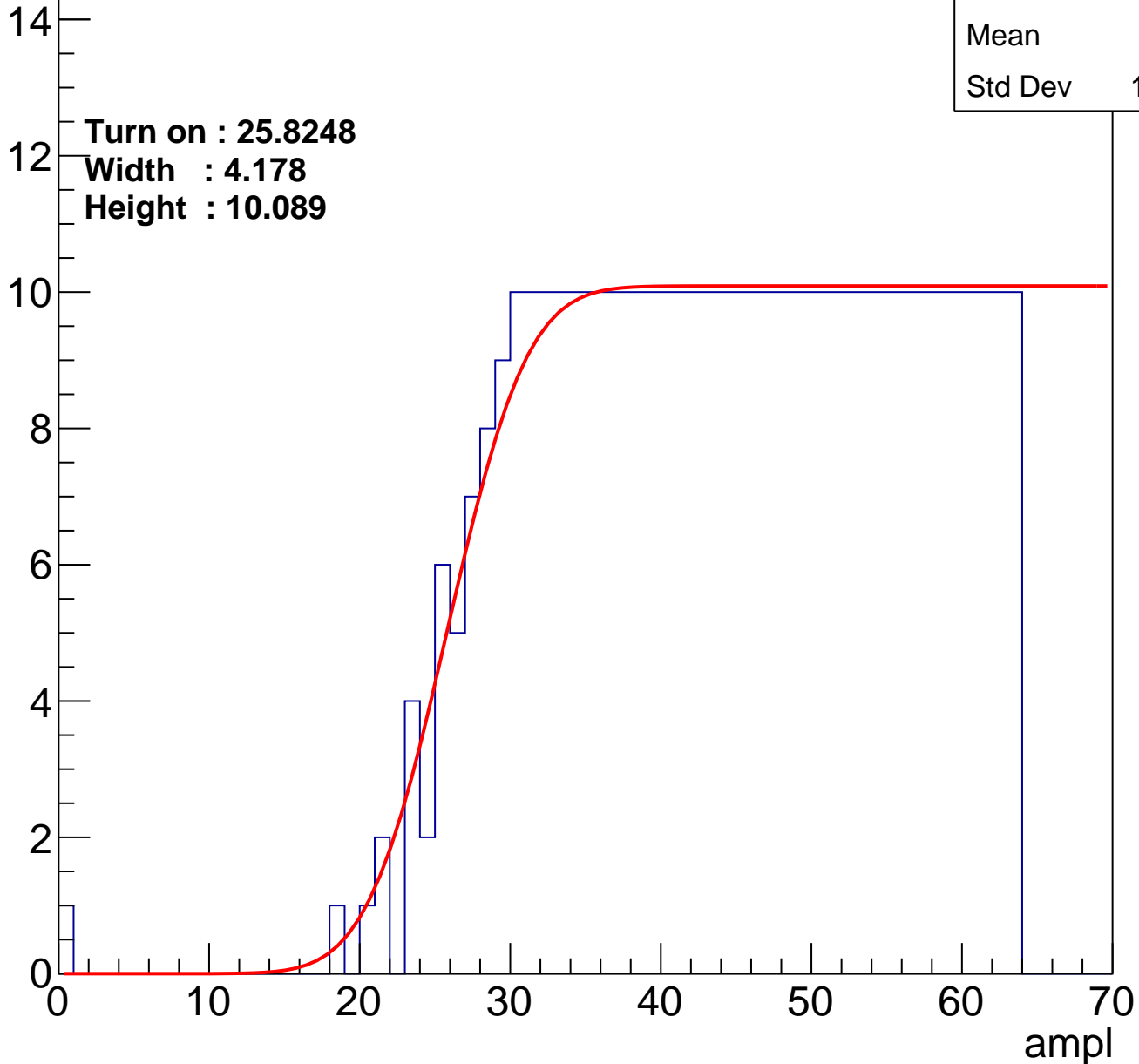
| | |
|---------|-------|
| Entries | 386 |
| Mean | 44 |
| Std Dev | 11.56 |

Turn on : 25.8248

Width : 4.178

Height : 10.089

Entry



B1L103S, U6-ch81

calib_packv5_042523_0143.root, FC#7, port C2

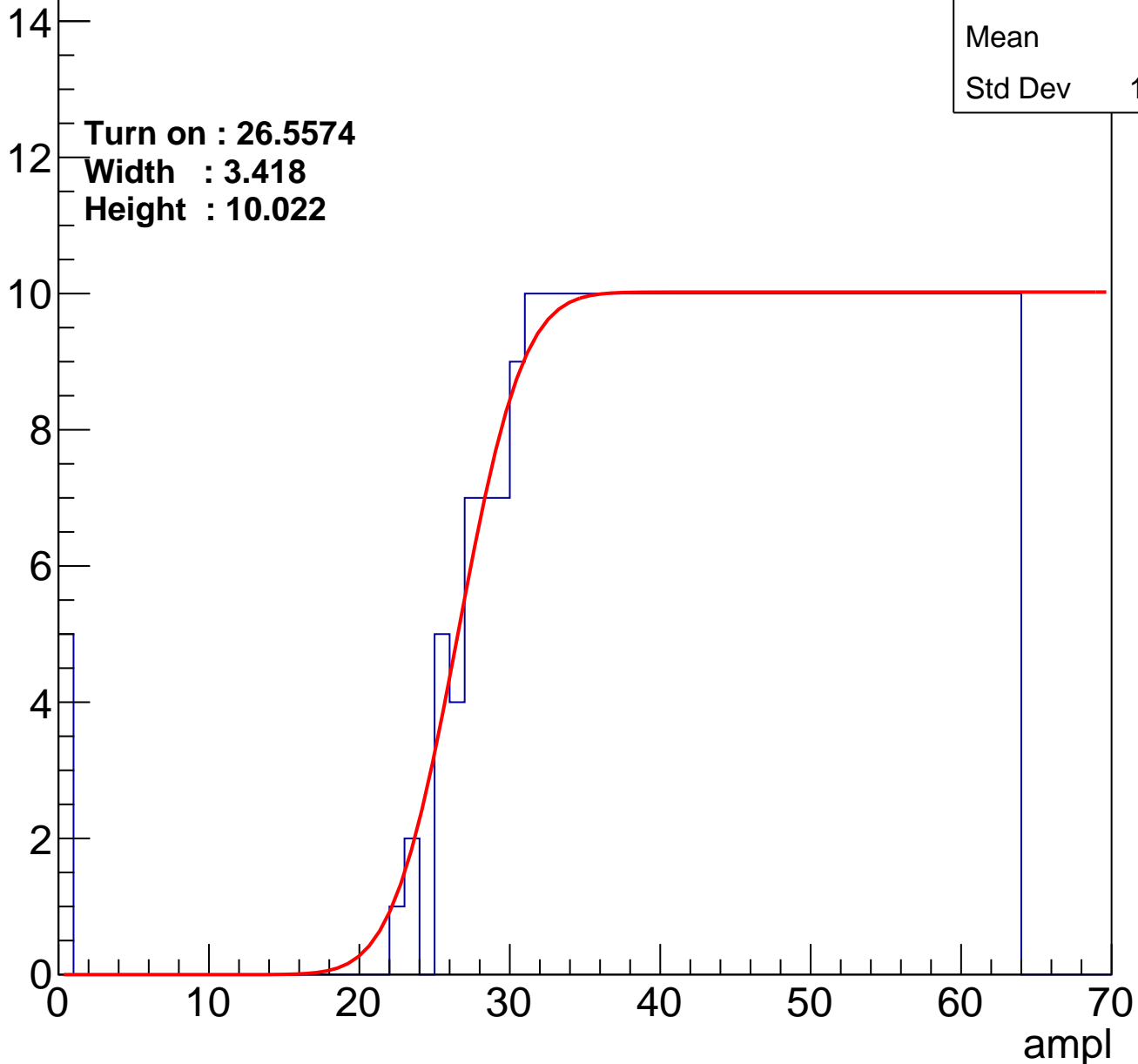
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.2 |
| Std Dev | 11.99 |

Turn on : 26.5574

Width : 3.418

Height : 10.022

Entry



B1L103S, U6-ch82

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.99 |
| Std Dev | 11.67 |

Turn on : 26.0053

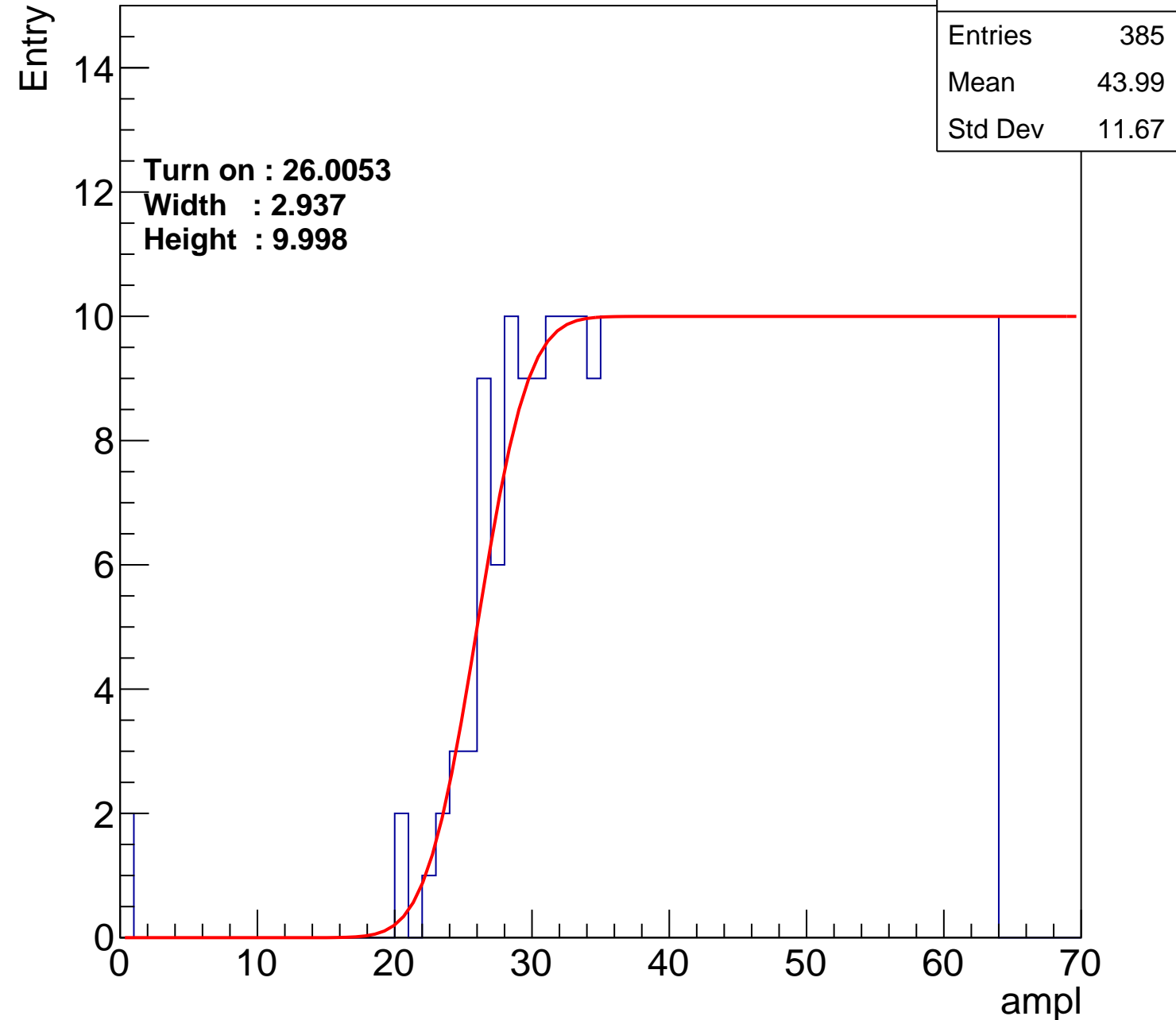
Width : 2.937

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch83

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.04 |
| Std Dev | 11.18 |

Turn on : 27.5926

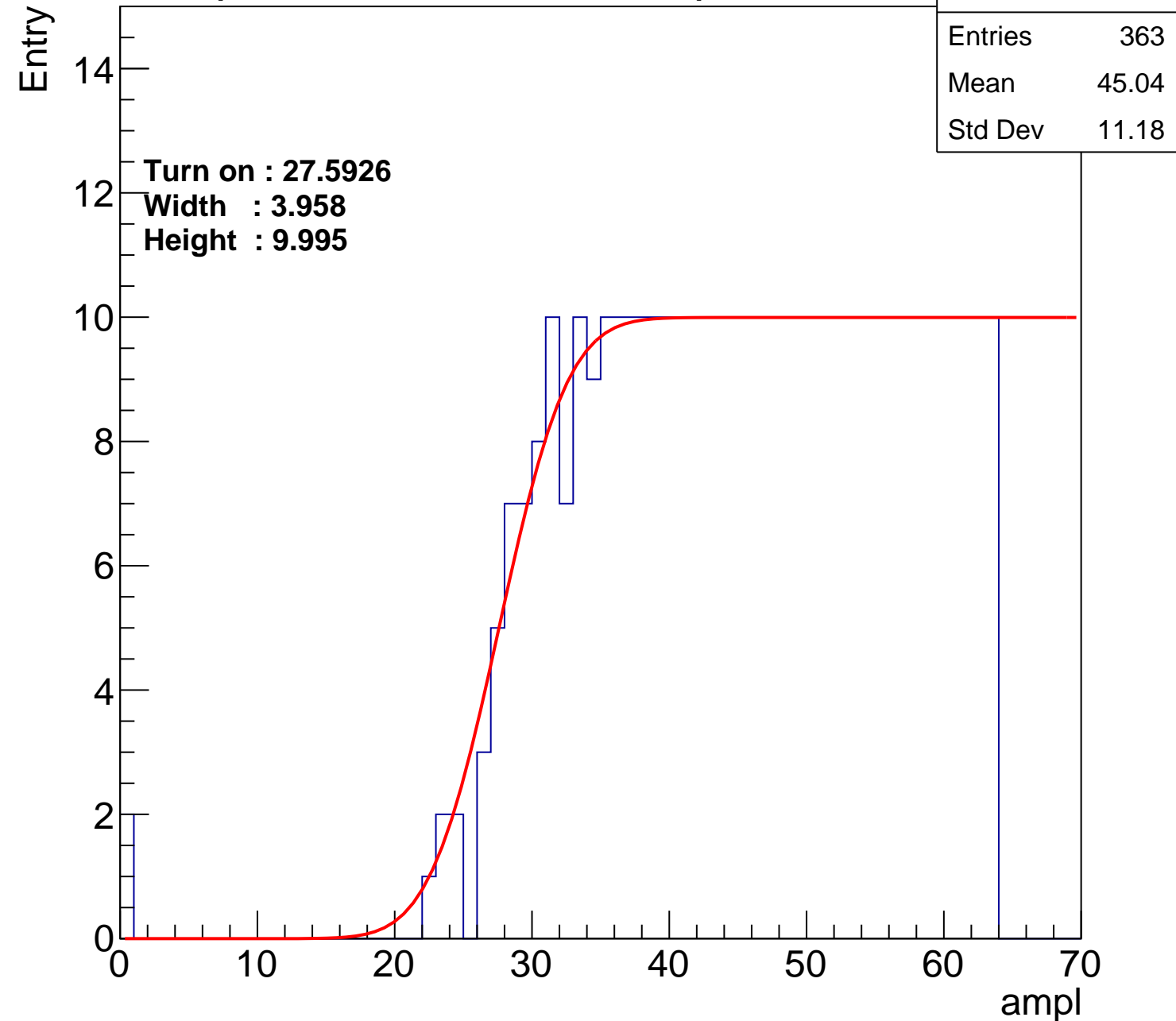
Width : 3.958

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch84

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.79 |
| Std Dev | 12.39 |

Turn on : 23.4688

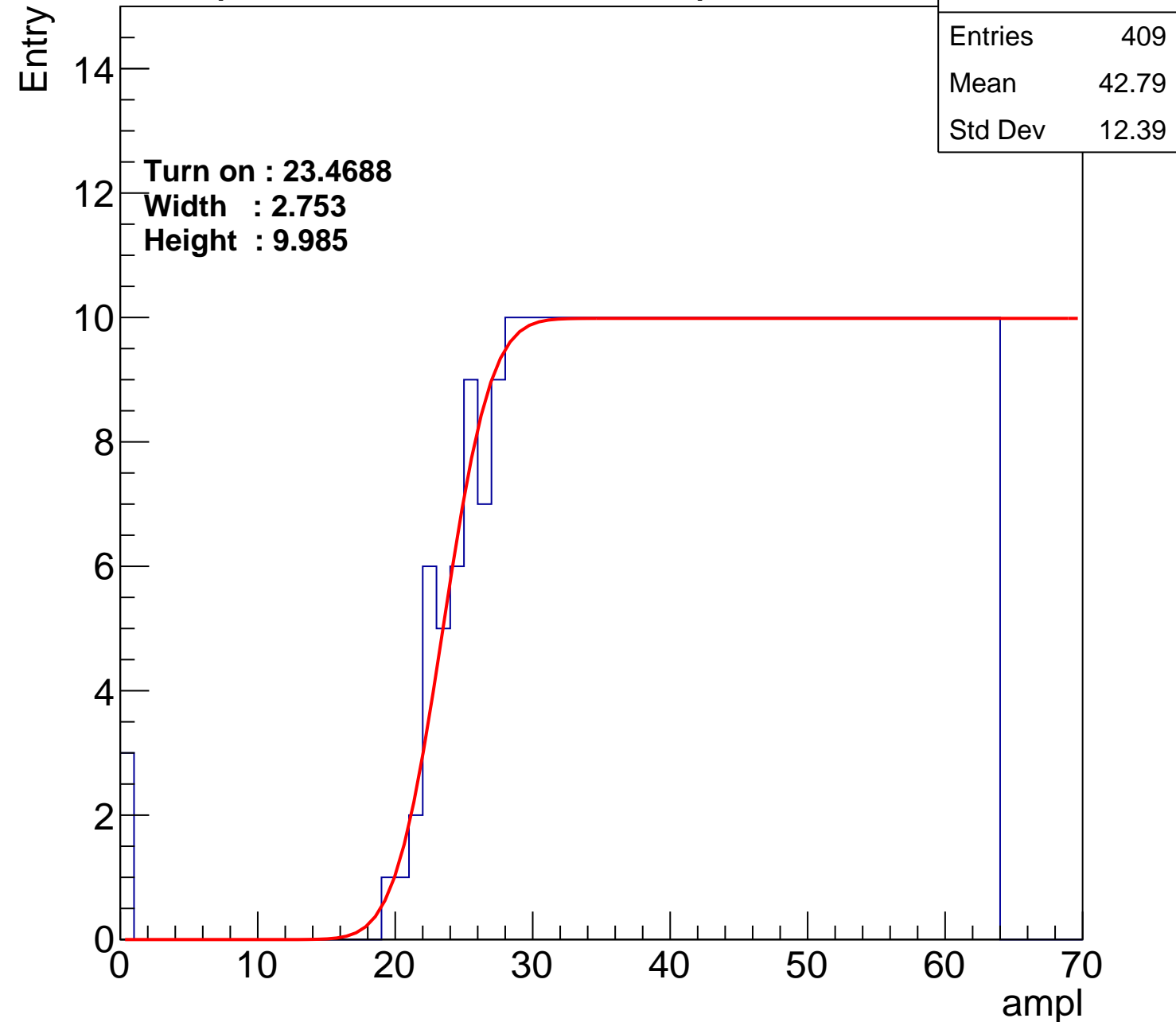
Width : 2.753

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch85

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.05 |
| Std Dev | 11.94 |

Turn on : 26.7882

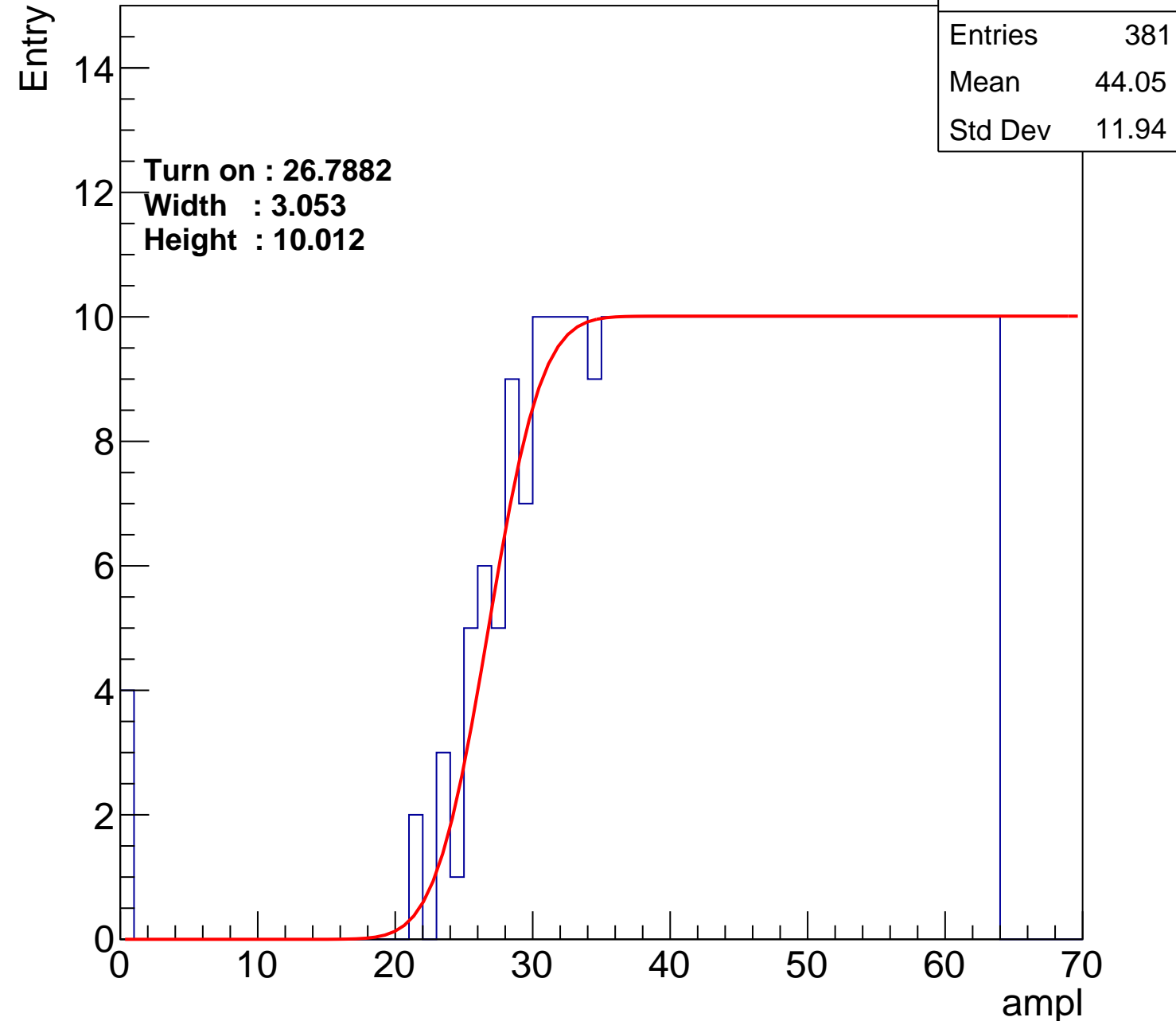
Width : 3.053

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch86

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.11 |
| Std Dev | 12.26 |

Turn on : 24.9192

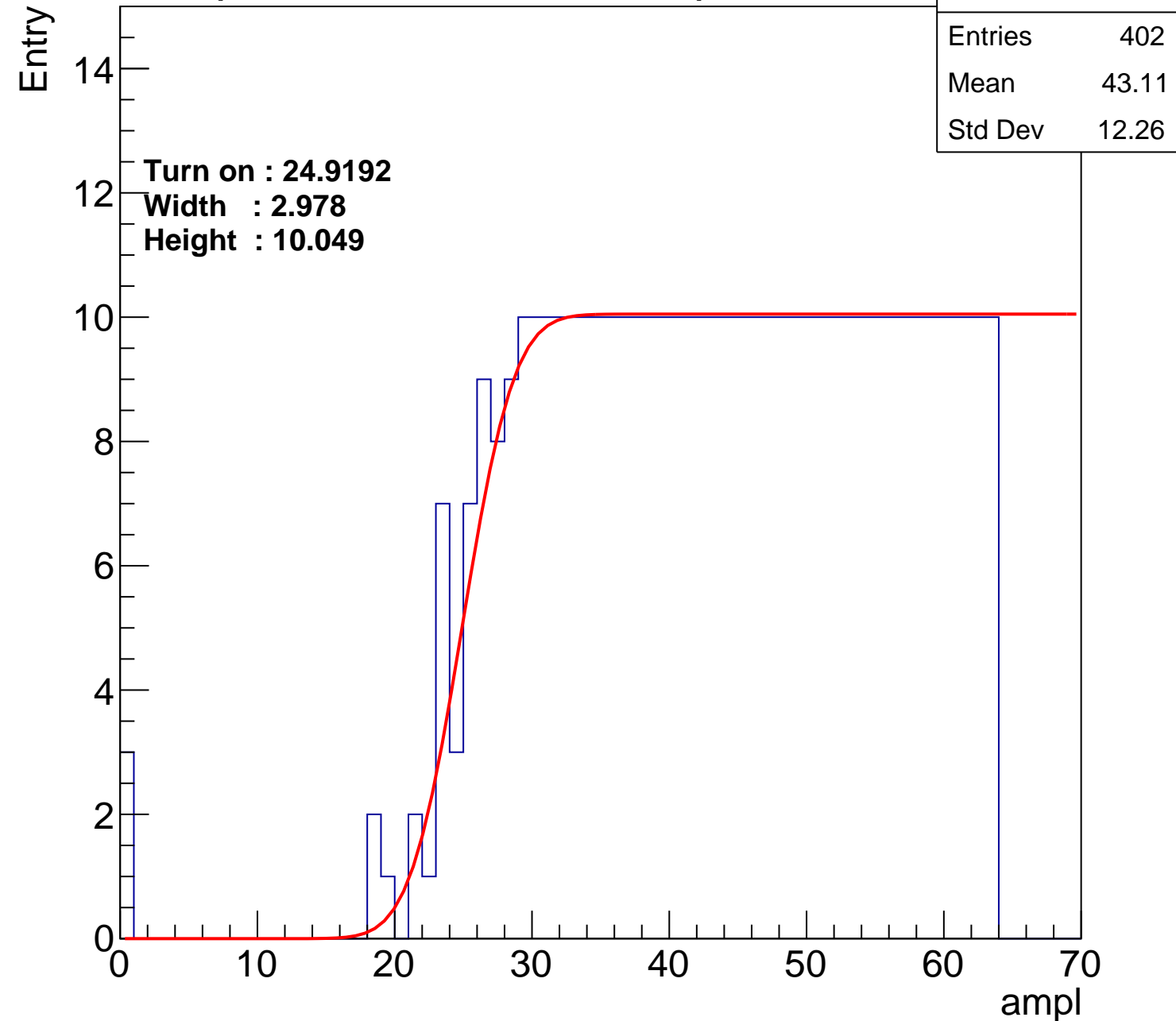
Width : 2.978

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch87

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.17 |
| Std Dev | 12.21 |

Turn on : 24.0598

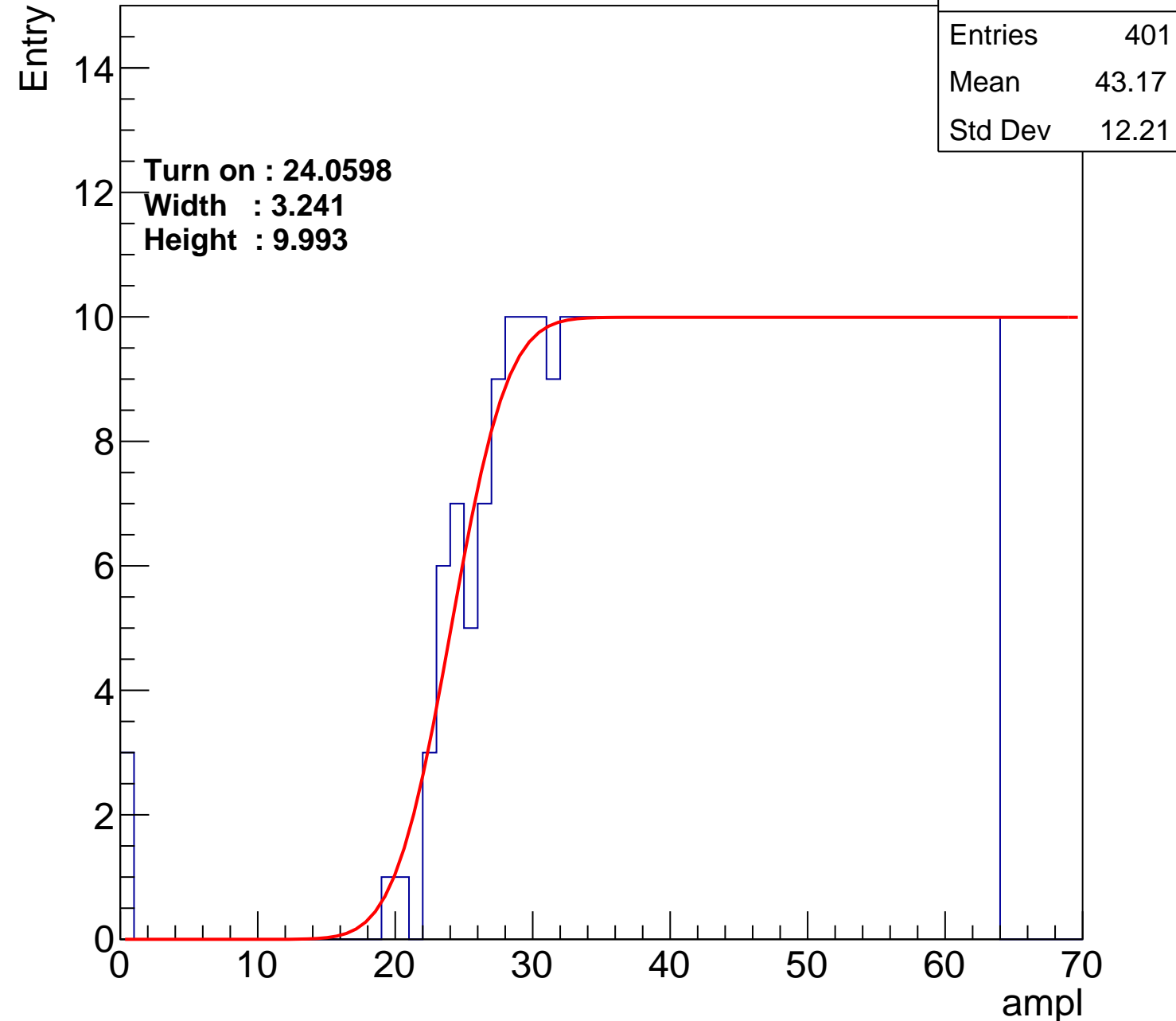
Width : 3.241

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch88

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43 |
| Std Dev | 12.58 |

Turn on : 24.1388

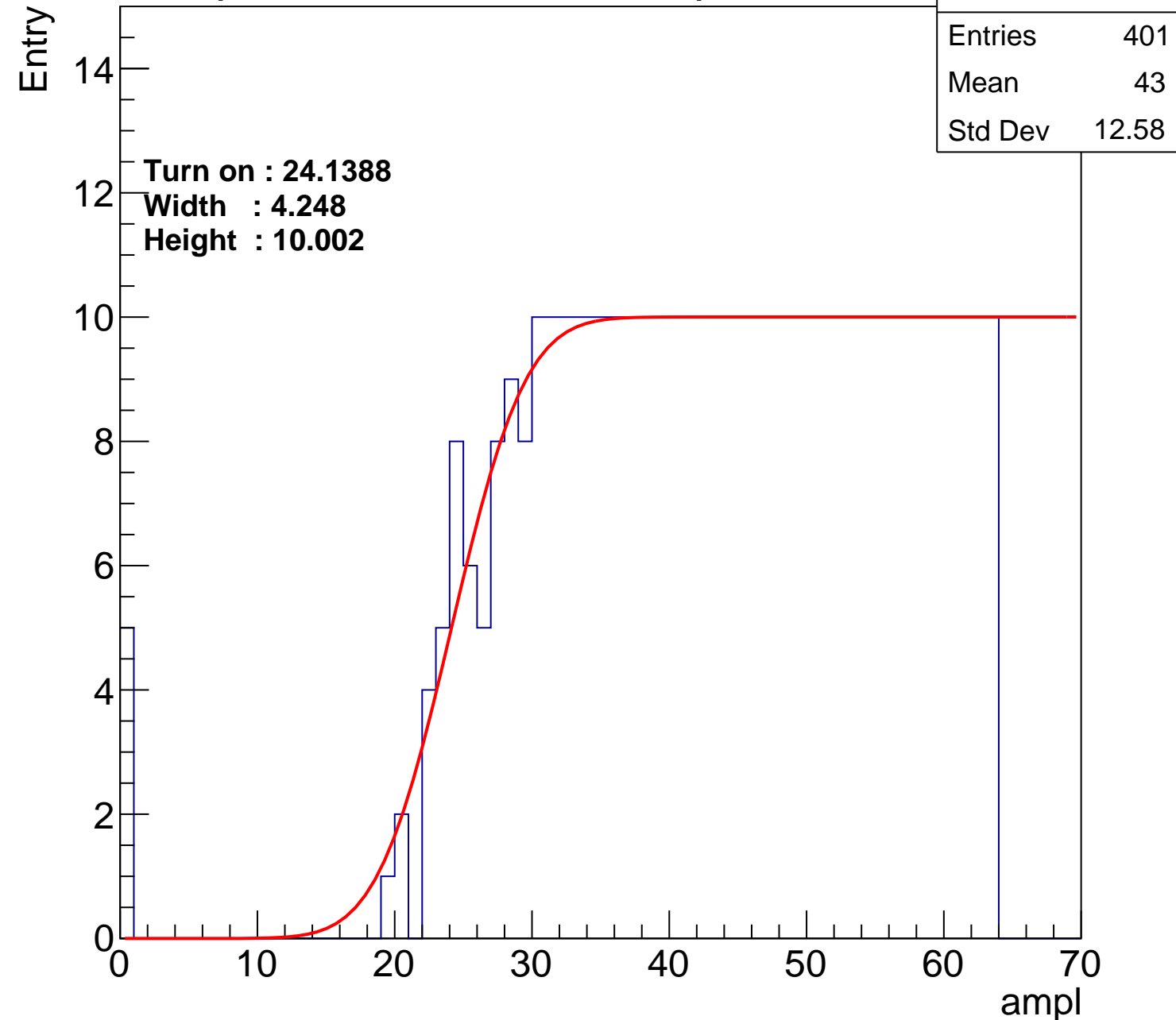
Width : 4.248

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch89

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.77 |
| Std Dev | 11.56 |

Turn on : 27.5567

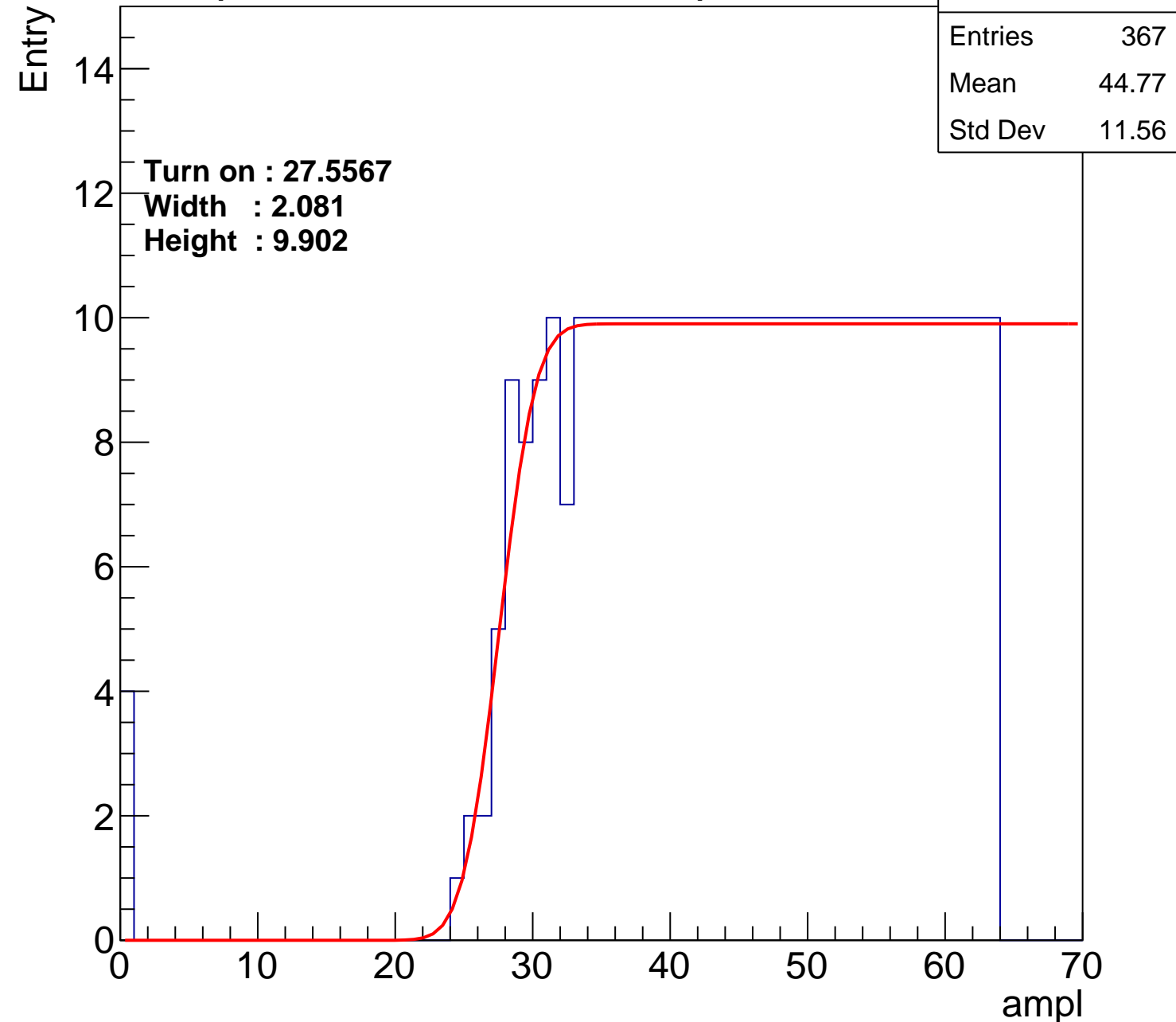
Width : 2.081

Height : 9.902

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch90

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.91 |
| Std Dev | 11.89 |

Turn on : 25.9671

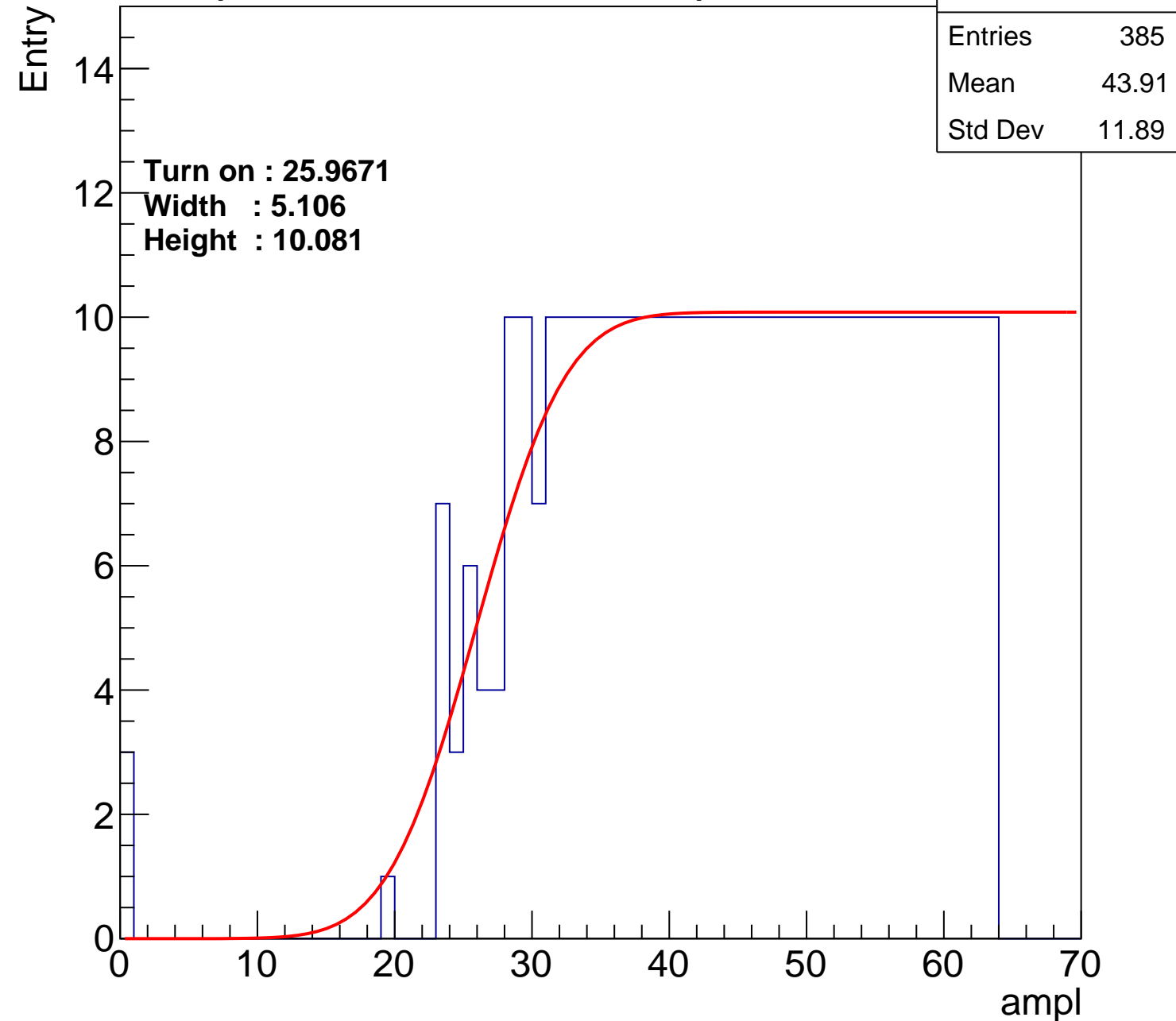
Width : 5.106

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch91

calib_packv5_042523_0143.root, FC#7, port C2

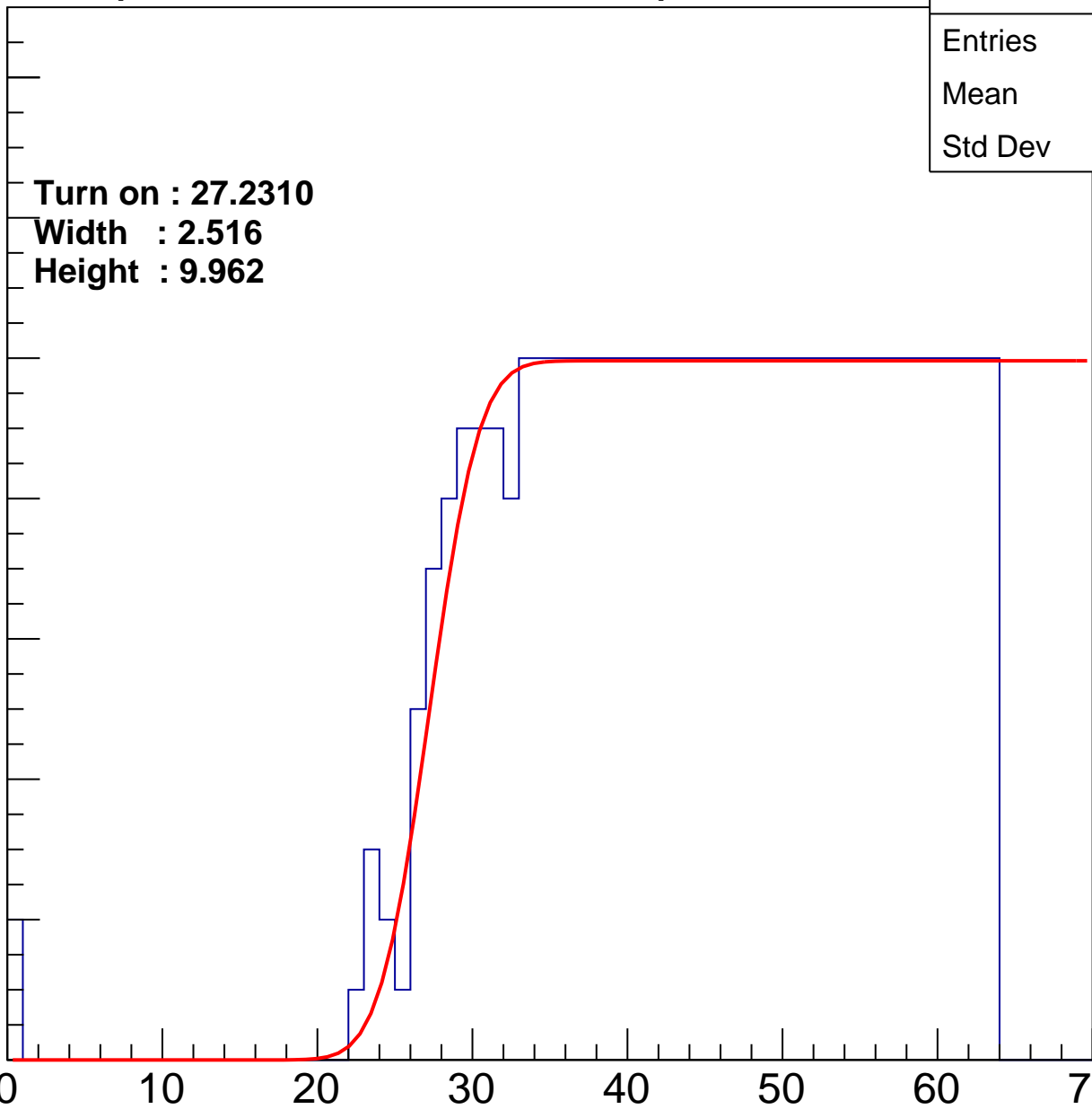
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2310
Width : 2.516
Height : 9.962

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.53 |
| Std Dev | 11.41 |

ampl



B1L103S, U6-ch92

calib_packv5_042523_0143.root, FC#7, port C2

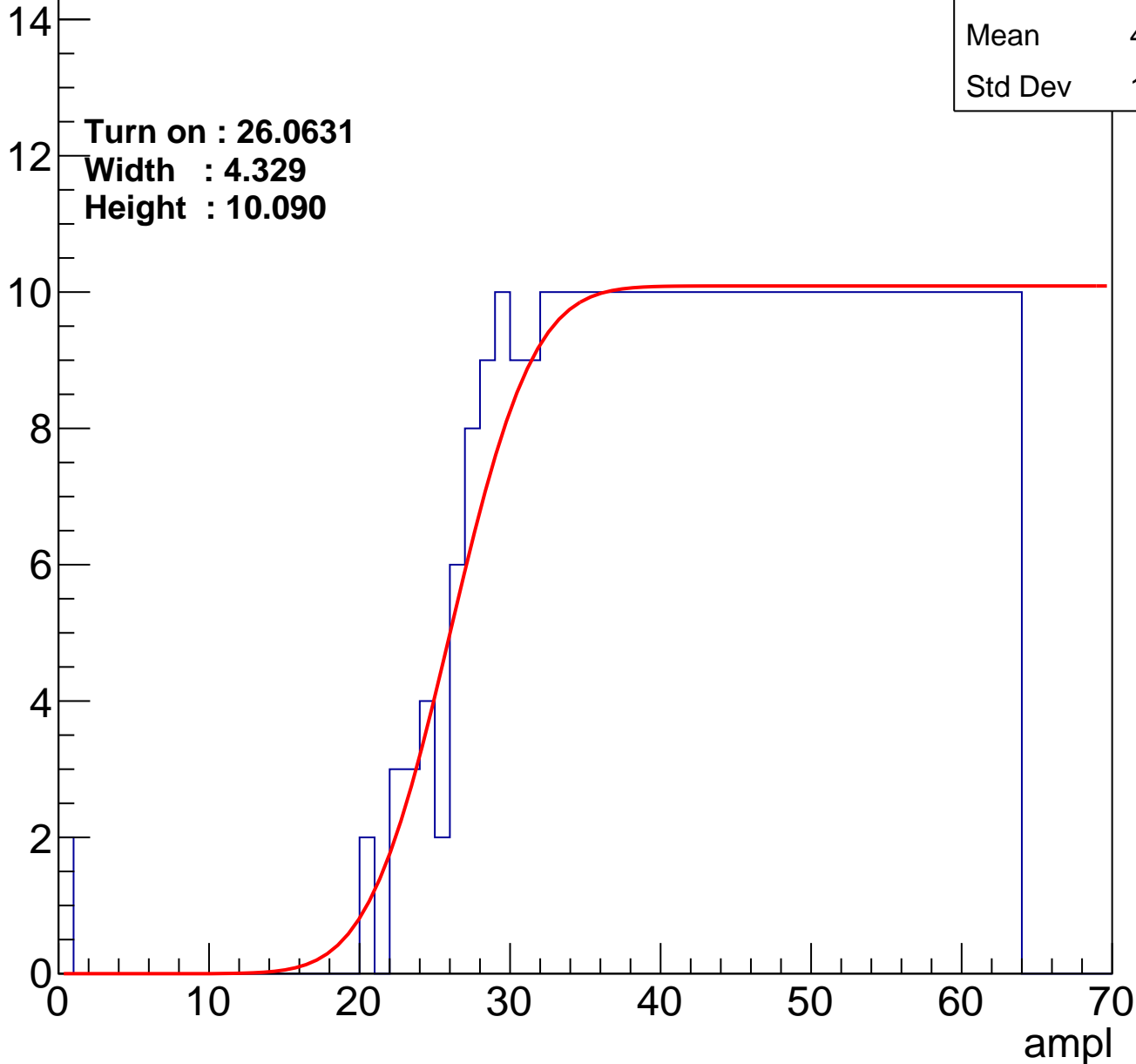
| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.89 |
| Std Dev | 11.75 |

Turn on : 26.0631

Width : 4.329

Height : 10.090

Entry



B1L103S, U6-ch93

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.12 |
| Std Dev | 10.9 |

Turn on : 28.2036

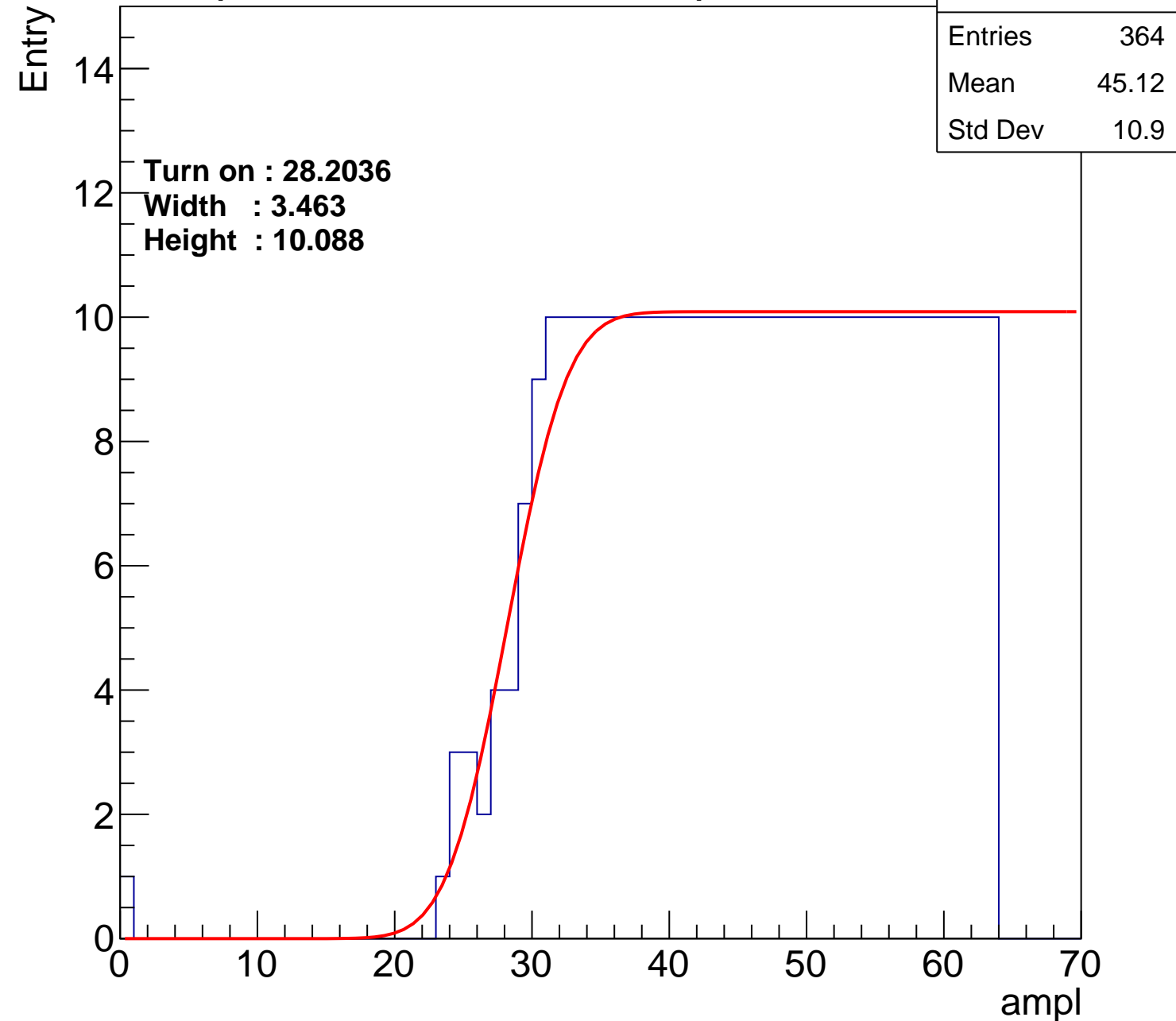
Width : 3.463

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch94

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.72 |
| Std Dev | 12.21 |

Turn on : 26.7166

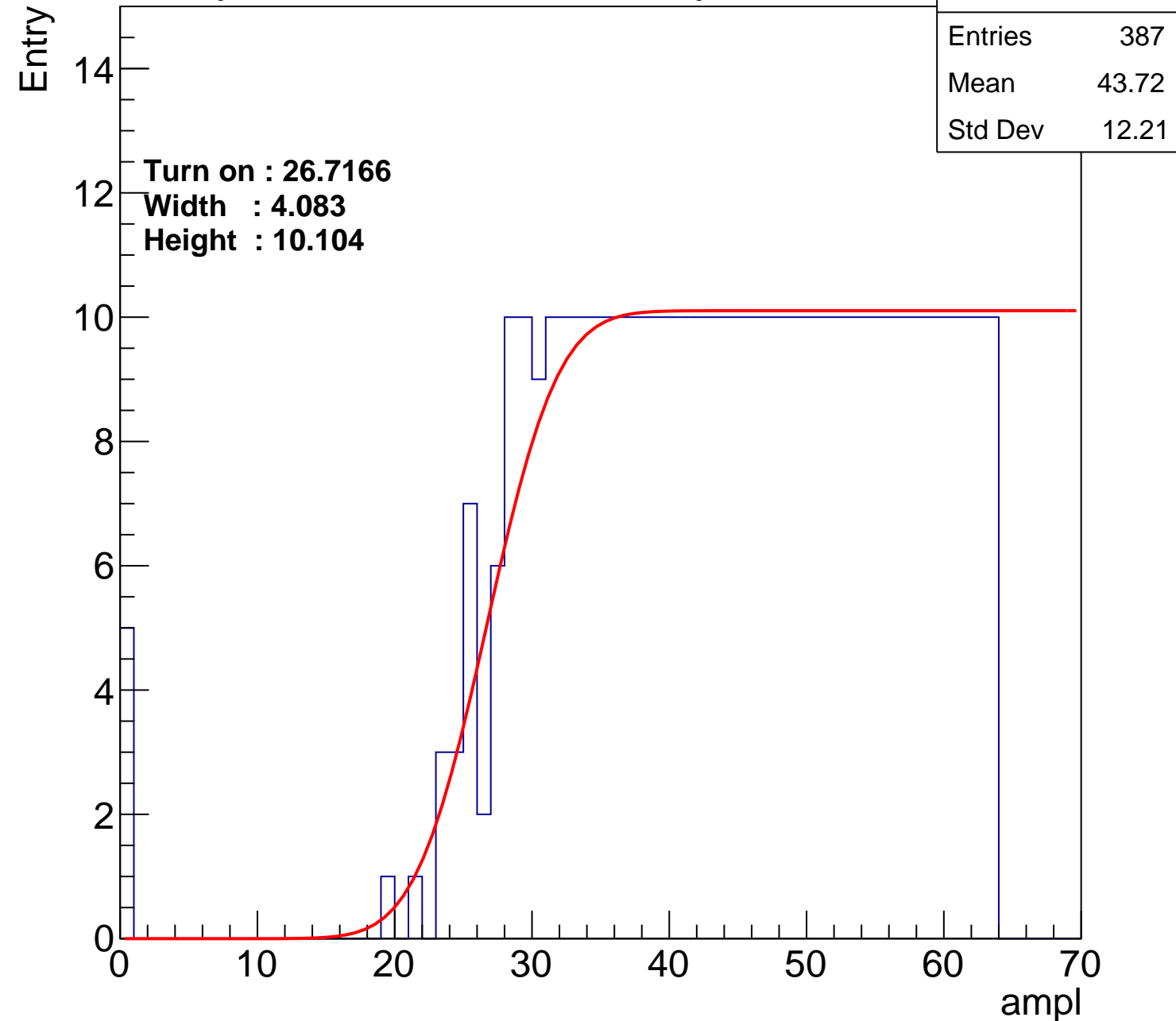
Width : 4.083

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch95

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.43 |
| Std Dev | 11.27 |

Turn on : 26.5361

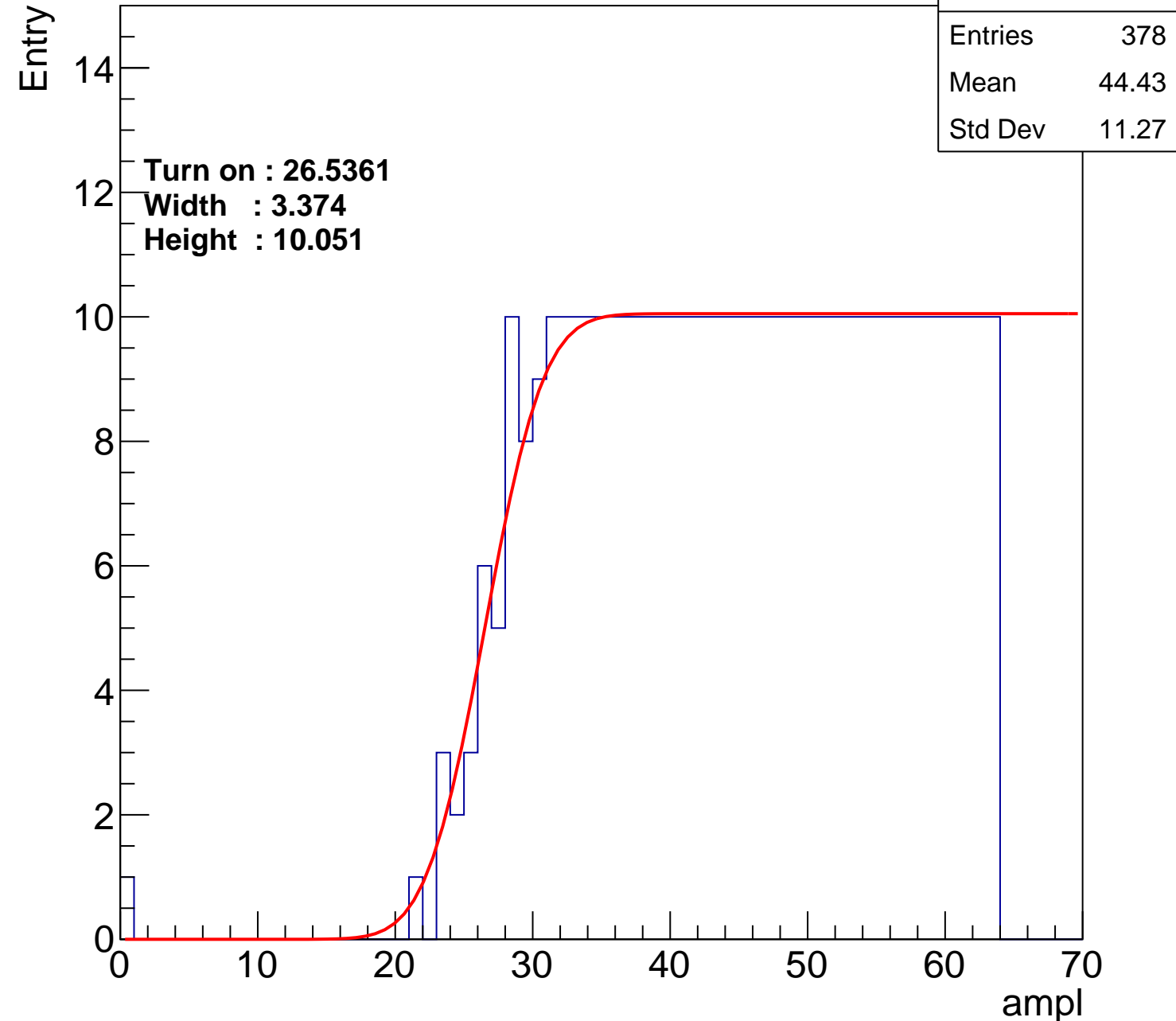
Width : 3.374

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch96

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 408 |
| Mean | 42.79 |
| Std Dev | 12.44 |

Turn on : 24.3965

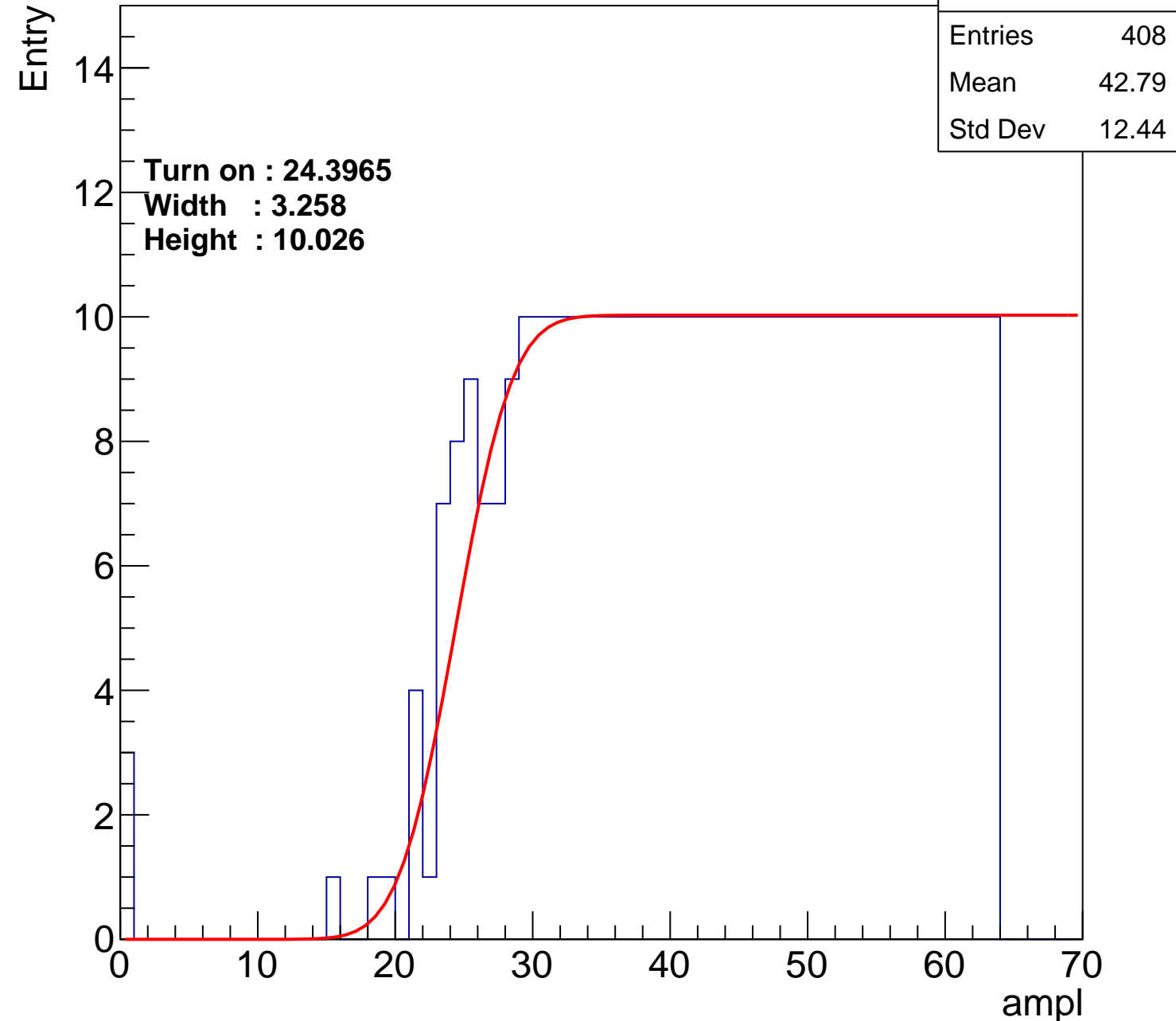
Width : 3.258

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch97

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.81 |
| Std Dev | 11.4 |

Turn on : 28.0472

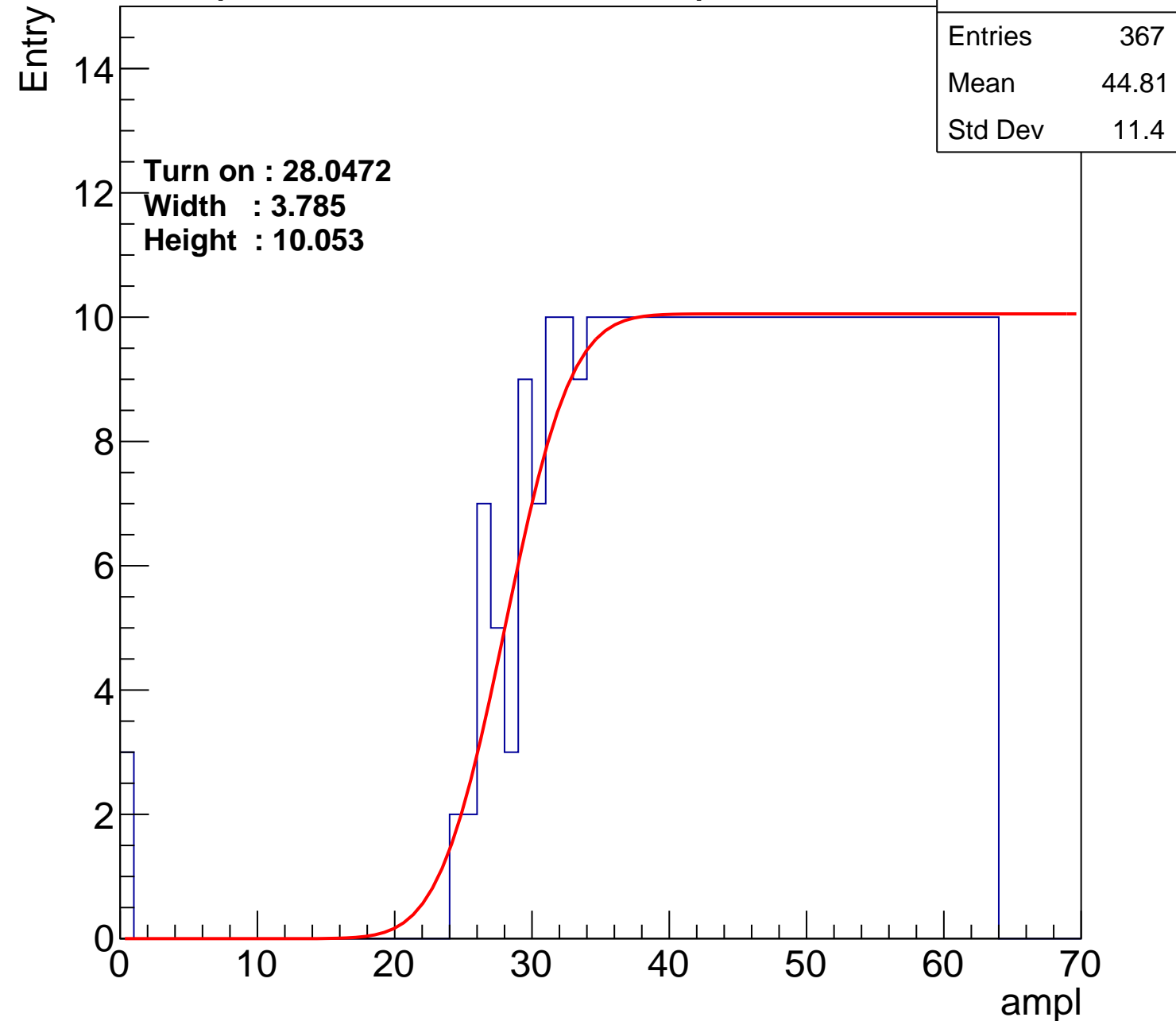
Width : 3.785

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch98

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.34 |
| Std Dev | 12.04 |

Turn on : 24.8304

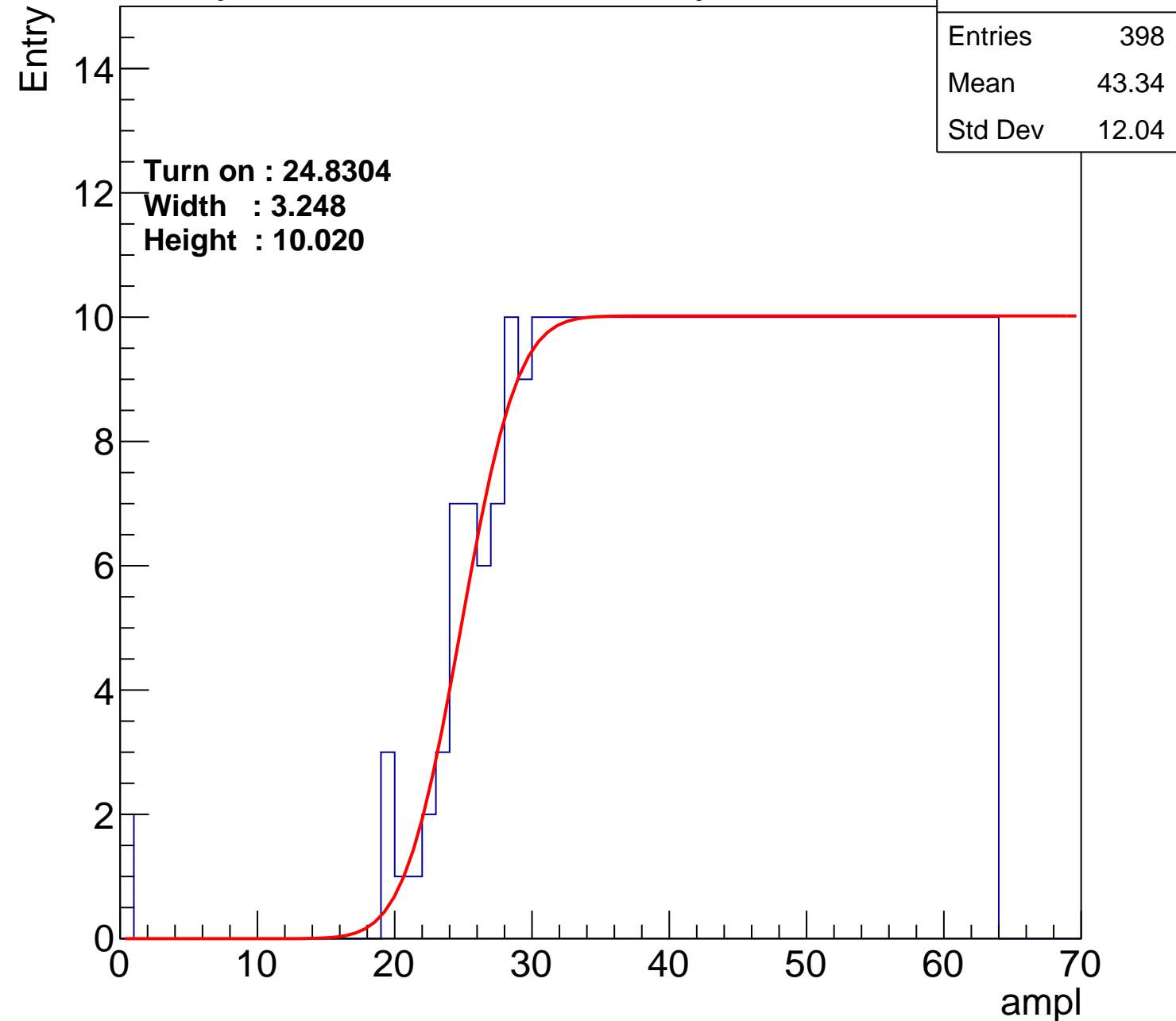
Width : 3.248

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch99

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.72 |
| Std Dev | 11.32 |

Turn on : 27.6568

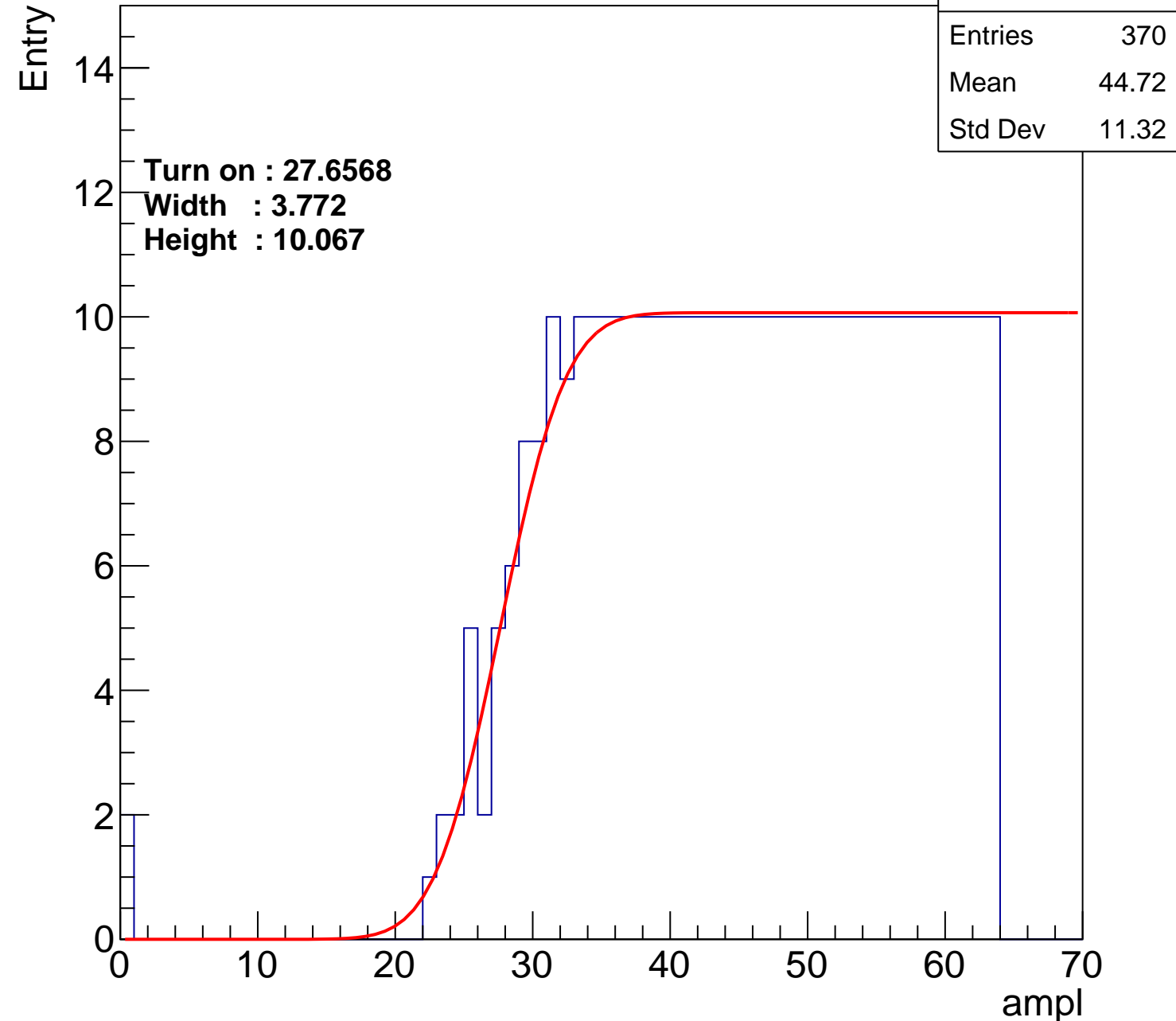
Width : 3.772

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch100

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.69 |
| Std Dev | 11.46 |

Turn on : 27.8869

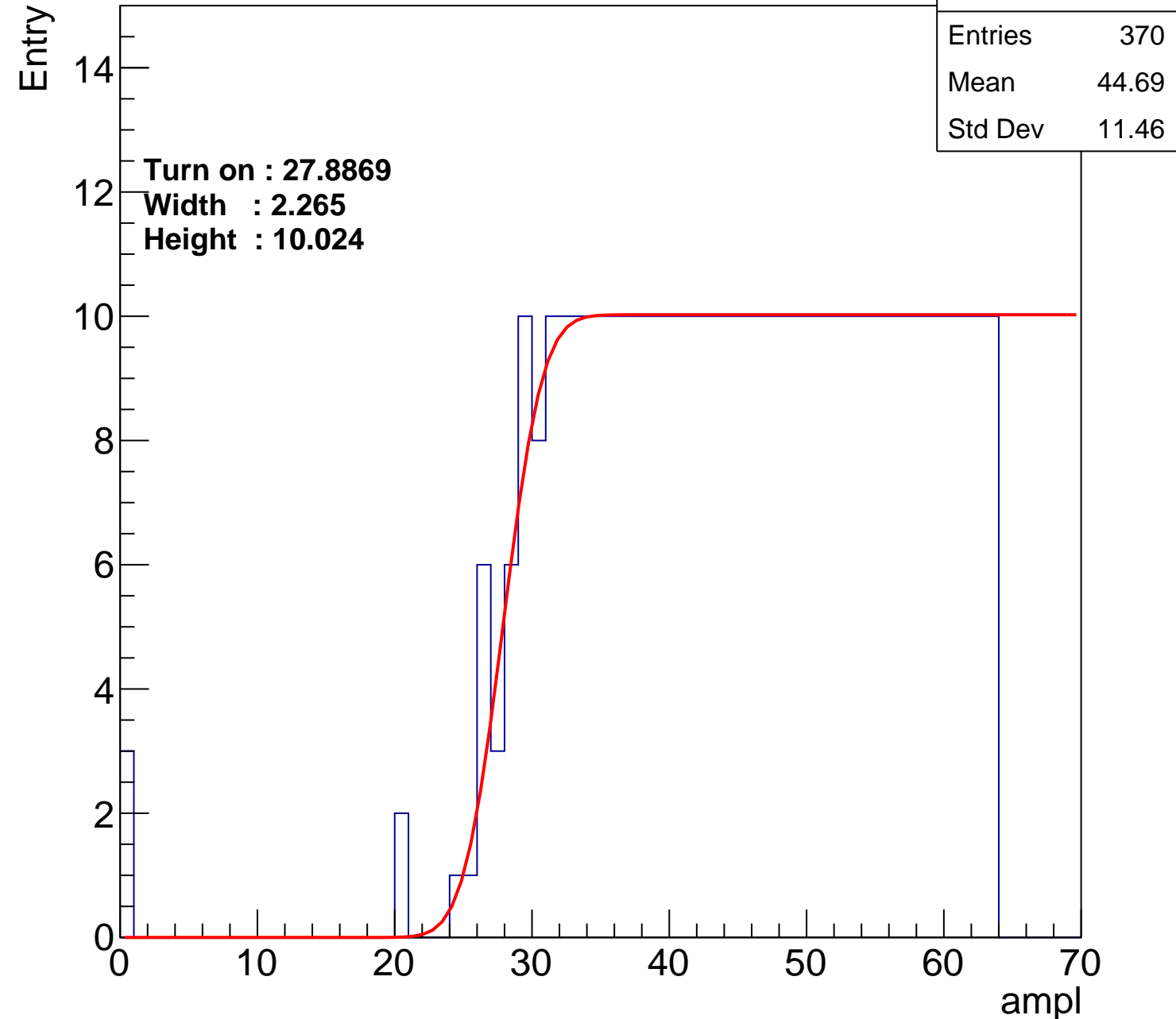
Width : 2.265

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch101

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 43.98 |
| Std Dev | 12.14 |

Turn on : 26.9698

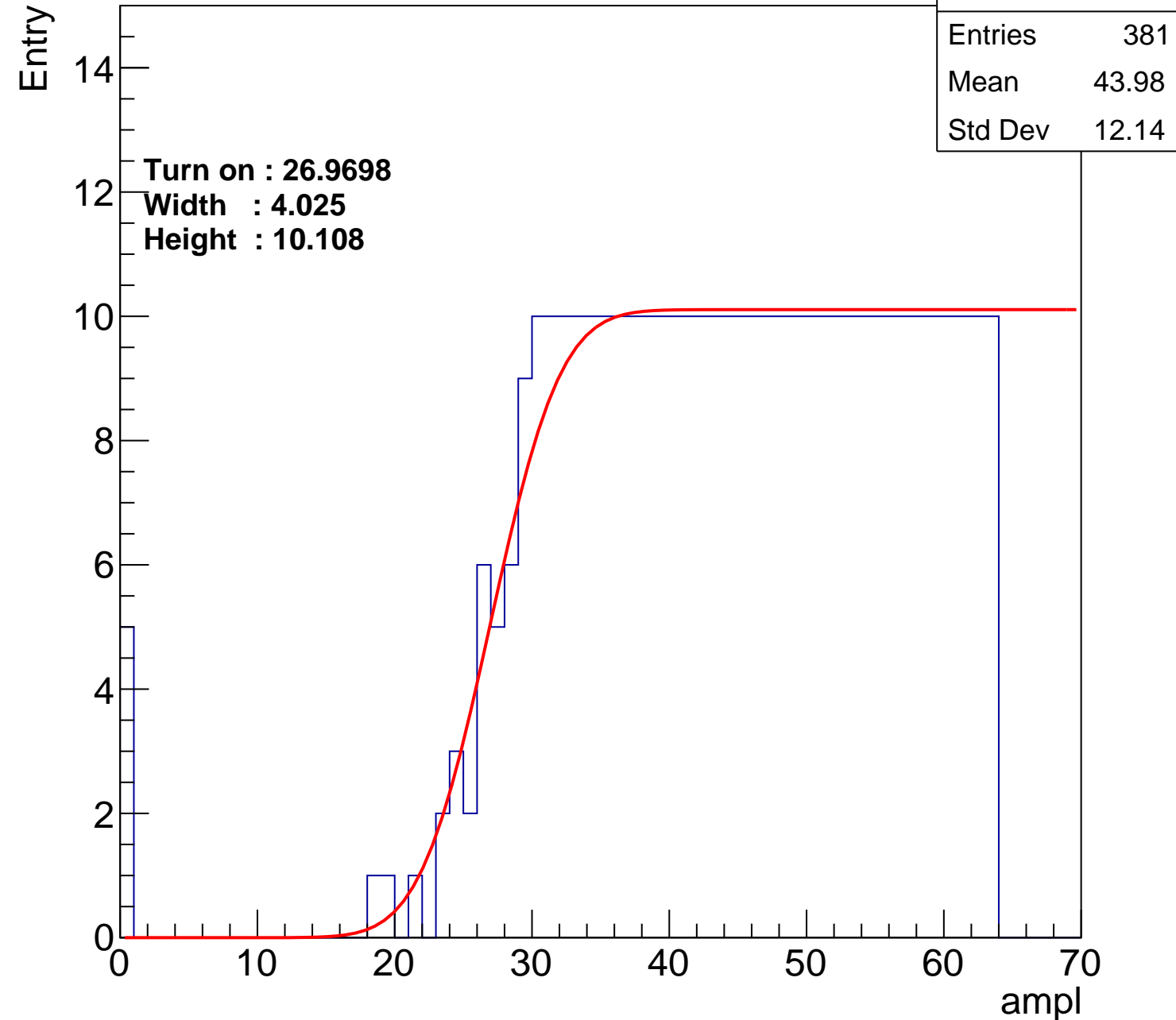
Width : 4.025

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch102

calib_packv5_042523_0143.root, FC#7, port C2

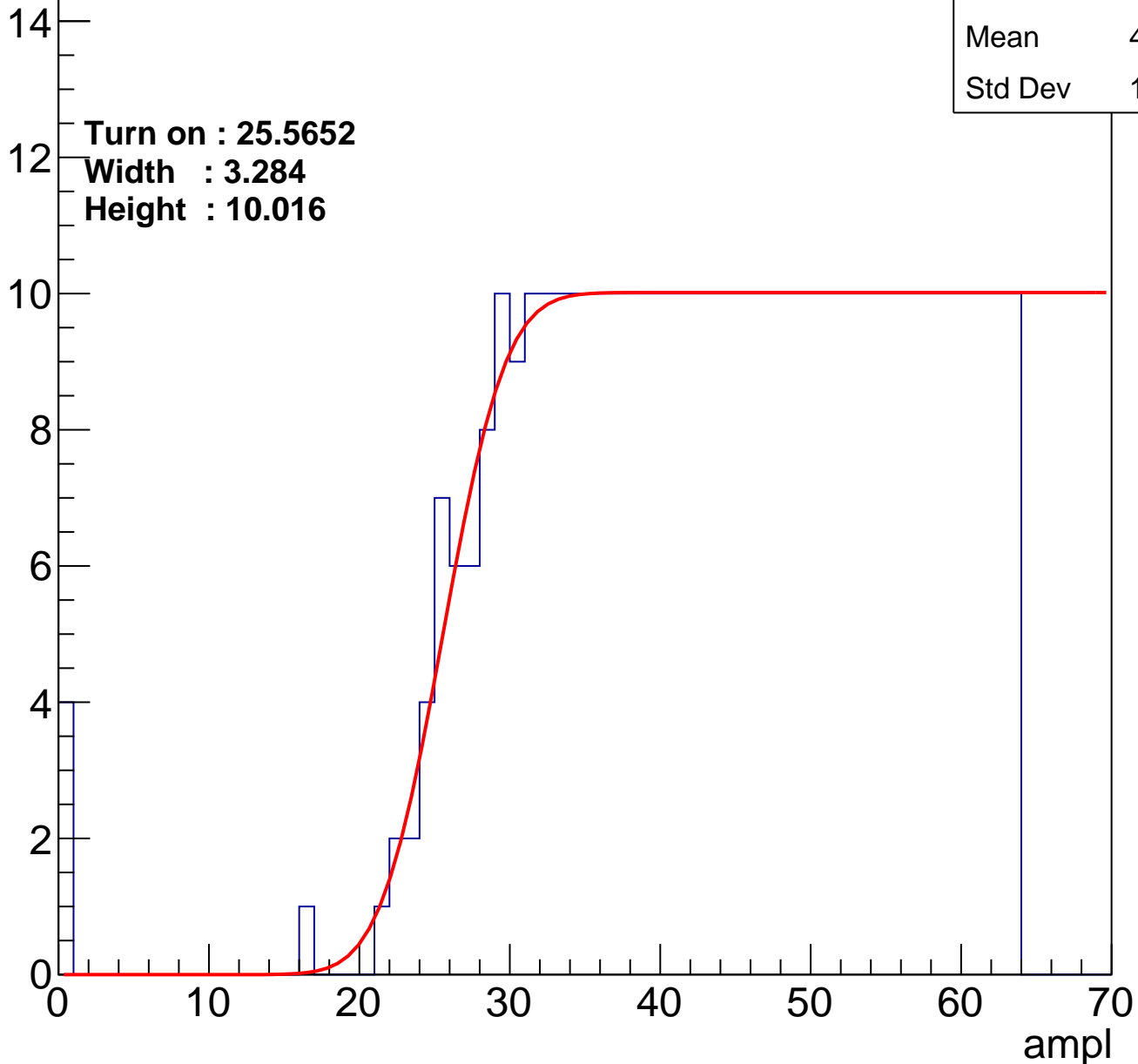
| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.62 |
| Std Dev | 12.15 |

Turn on : 25.5652

Width : 3.284

Height : 10.016

Entry



B1L103S, U6-ch103

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.22 |
| Std Dev | 11.76 |

Turn on : 26.6395

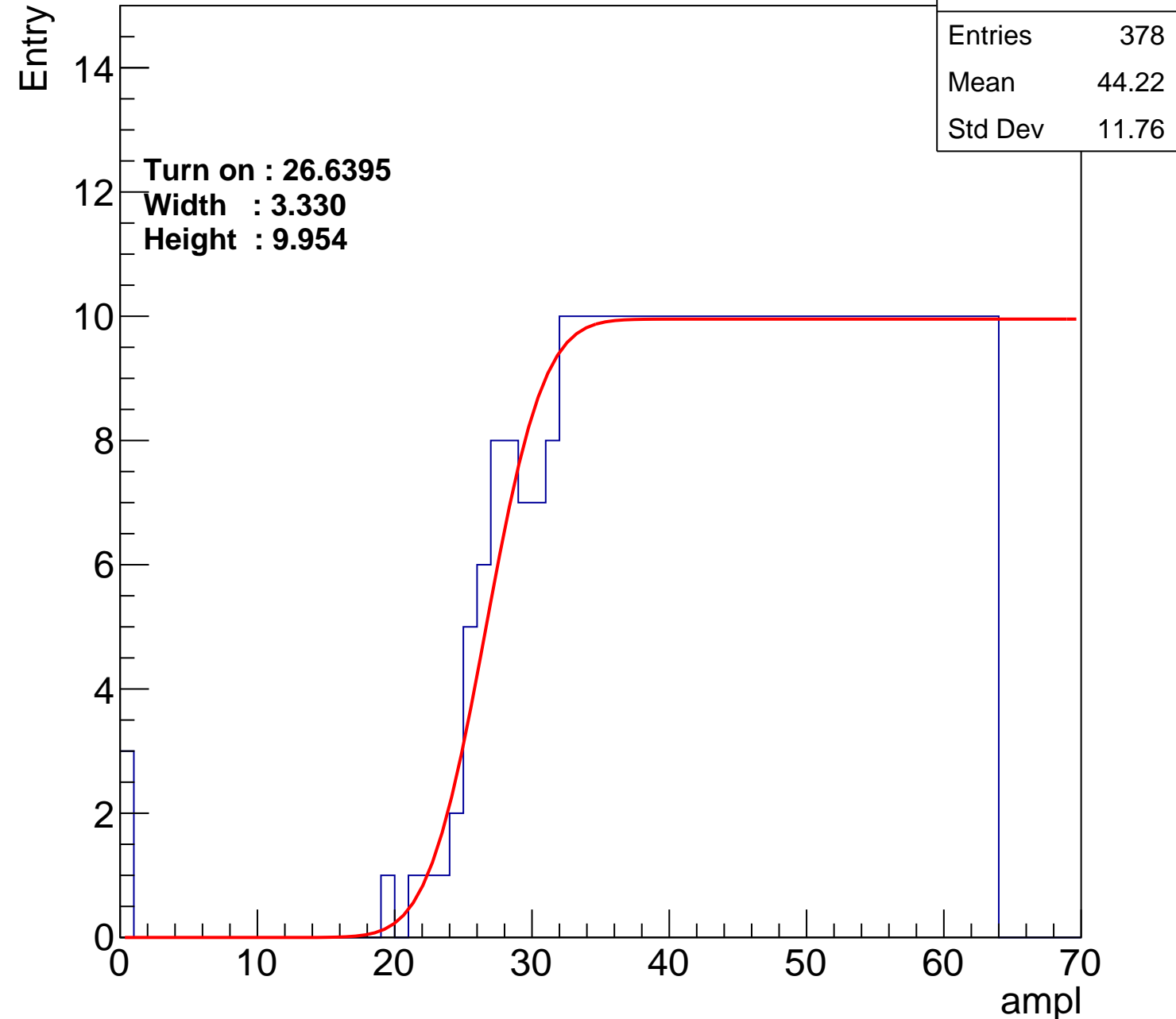
Width : 3.330

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch104

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.65 |
| Std Dev | 11.37 |

Turn on : 28.1828

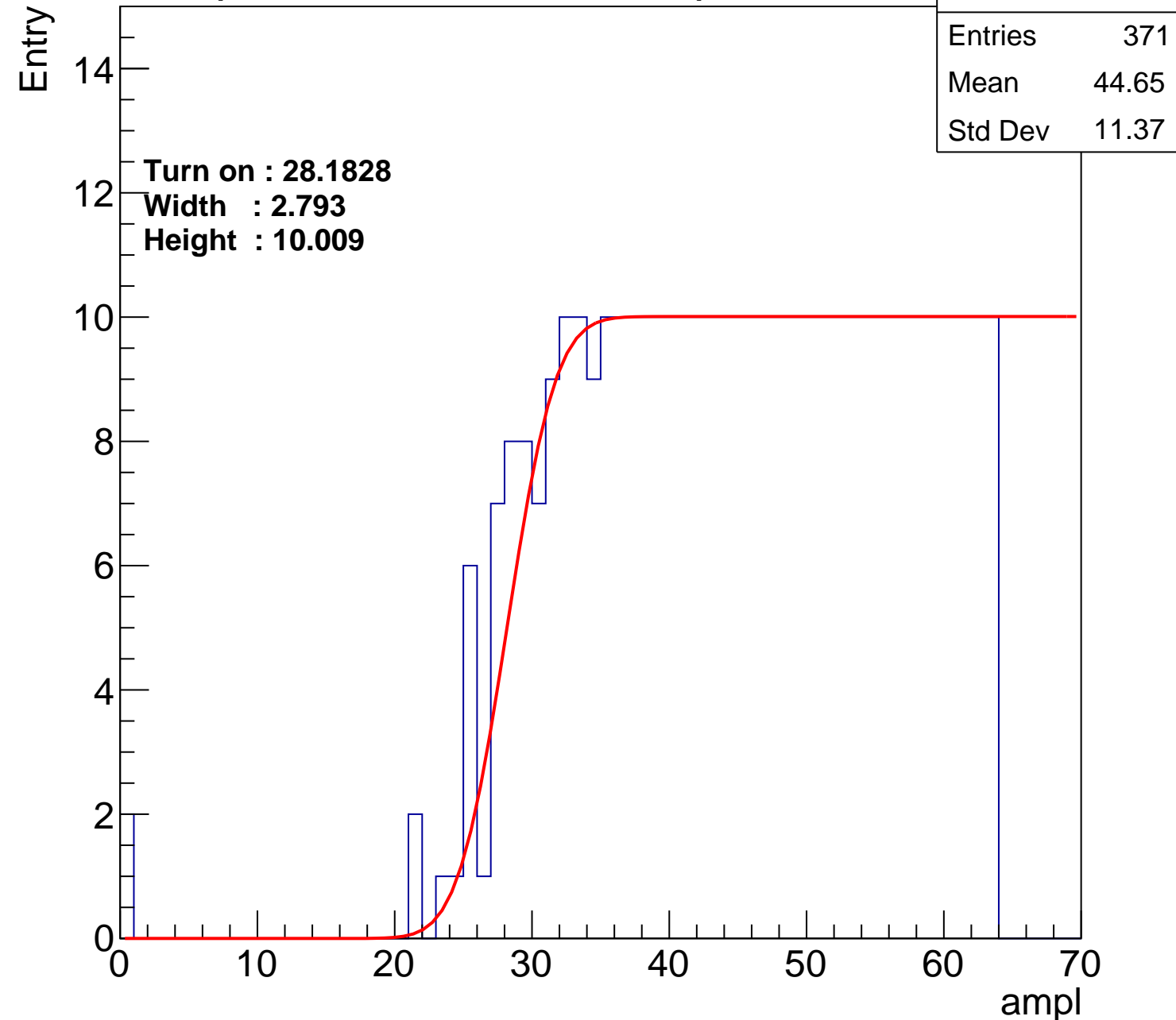
Width : 2.793

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch105

calib_packv5_042523_0143.root, FC#7, port C2

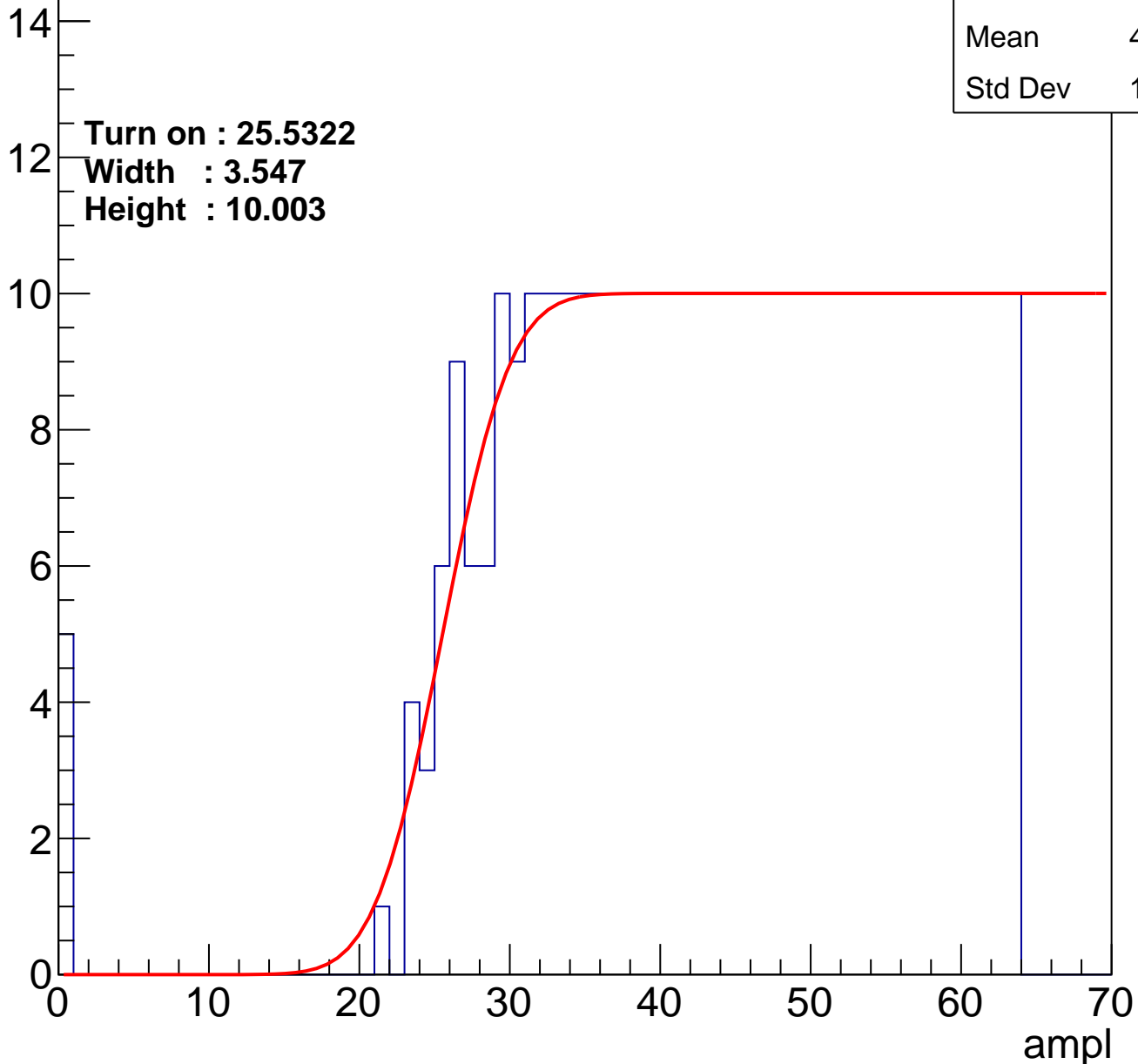
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.62 |
| Std Dev | 12.25 |

Turn on : 25.5322

Width : 3.547

Height : 10.003

Entry



B1L103S, U6-ch106

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.38 |
| Std Dev | 12.36 |

Turn on : 24.5692

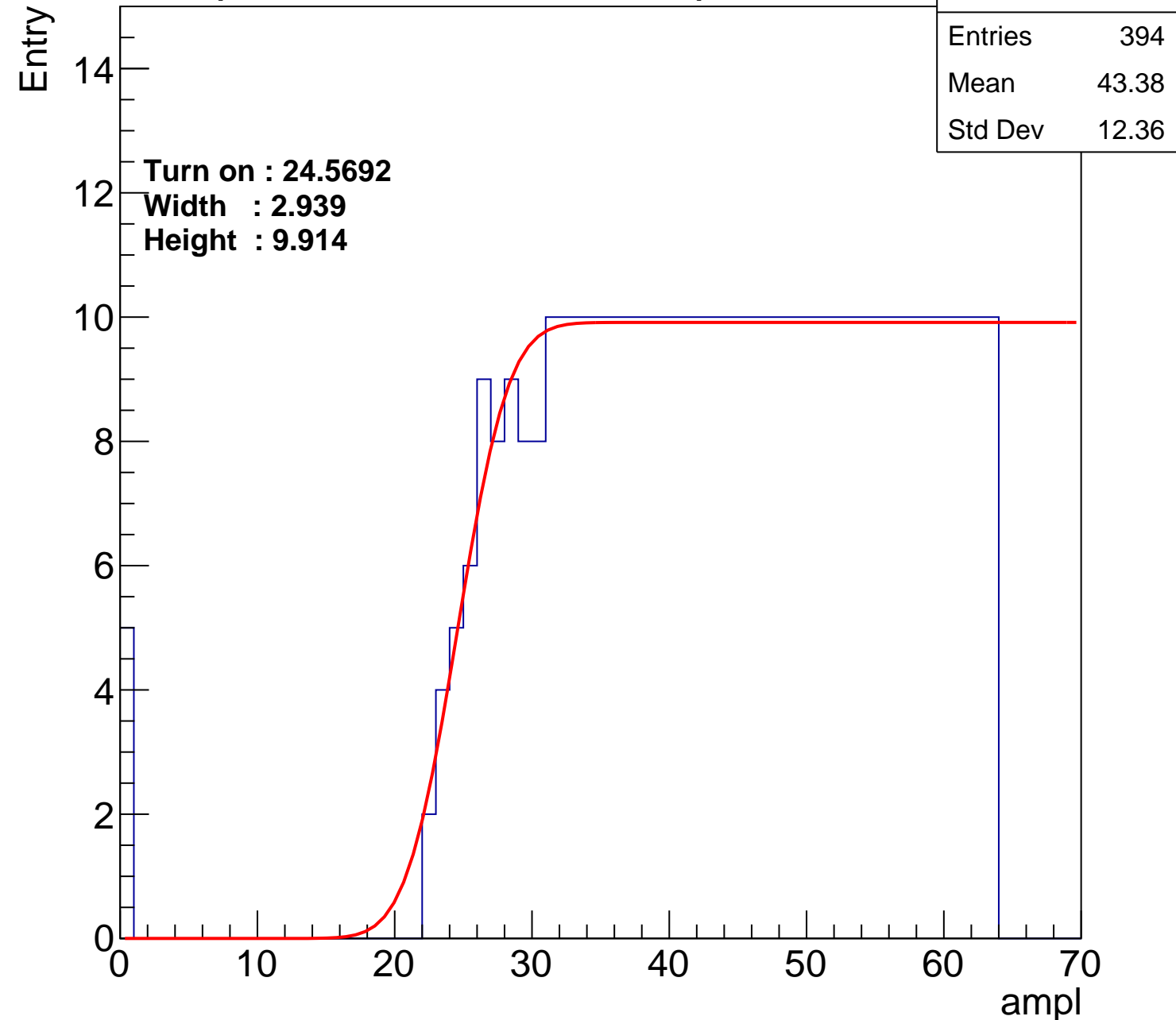
Width : 2.939

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch107

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.7 |
| Std Dev | 11.95 |

Turn on : 25.6170

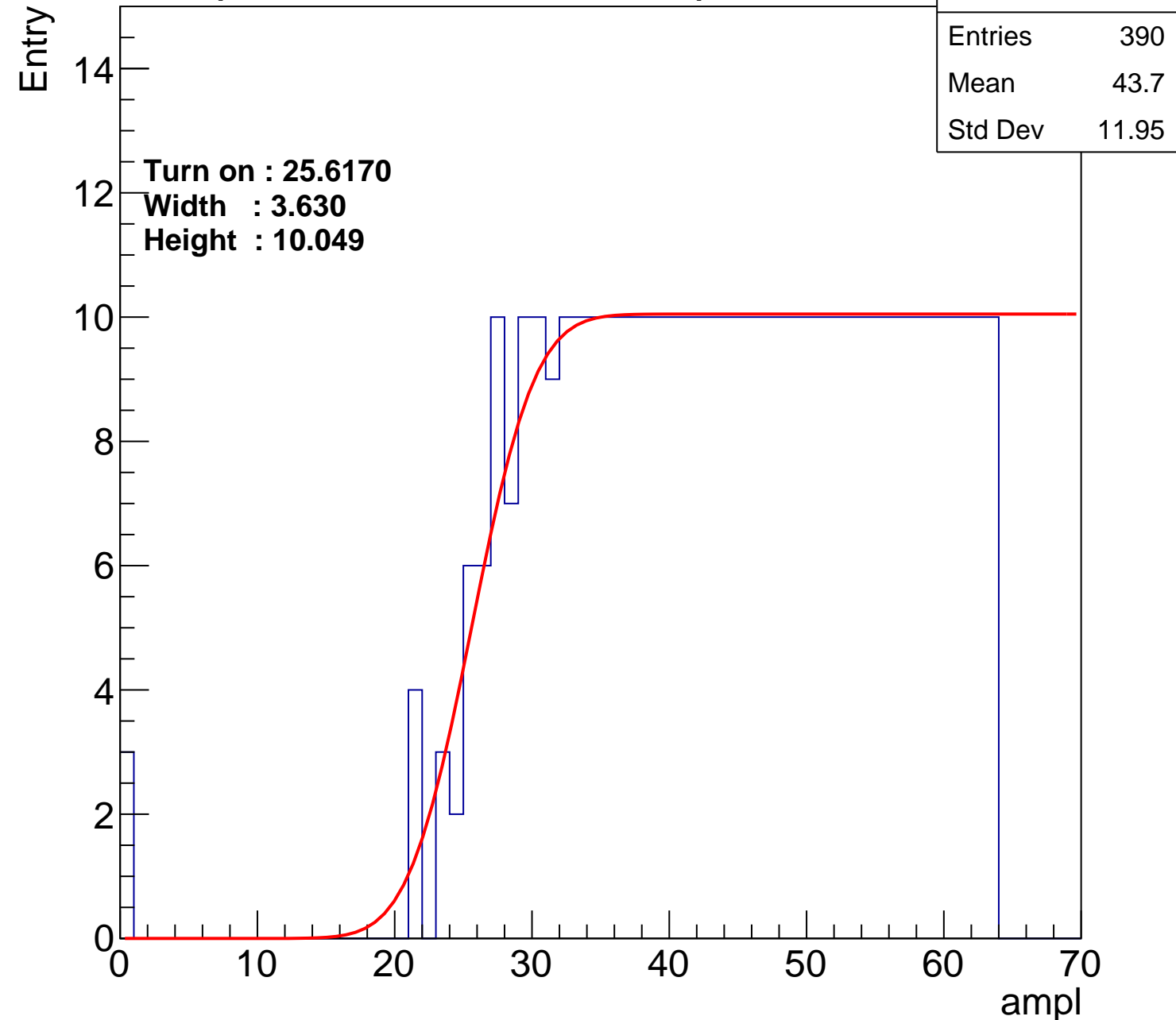
Width : 3.630

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch108

calib_packv5_042523_0143.root, FC#7, port C2

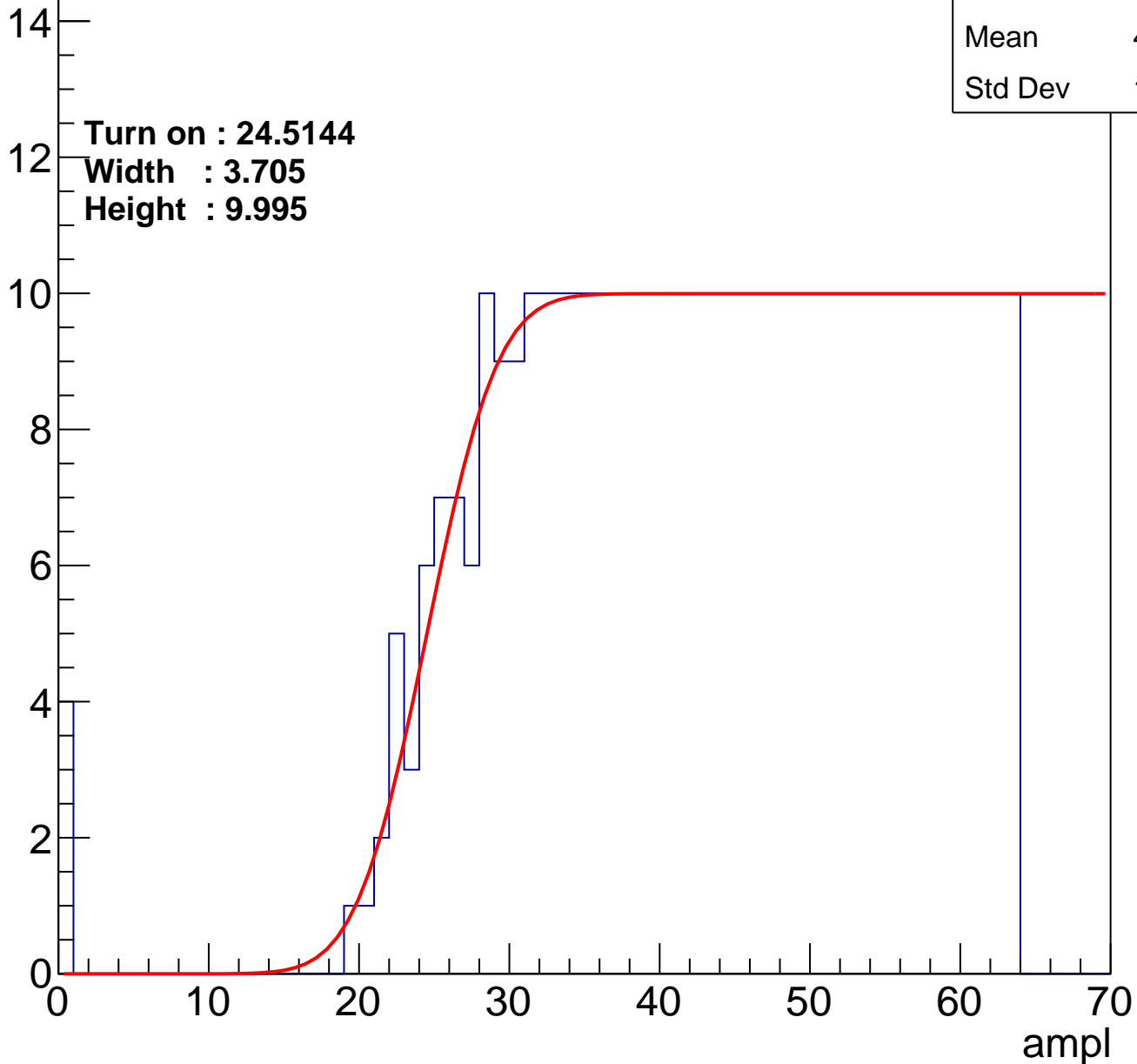
| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.11 |
| Std Dev | 12.41 |

Turn on : 24.5144

Width : 3.705

Height : 9.995

Entry



B1L103S, U6-ch109

calib_packv5_042523_0143.root, FC#7, port C2

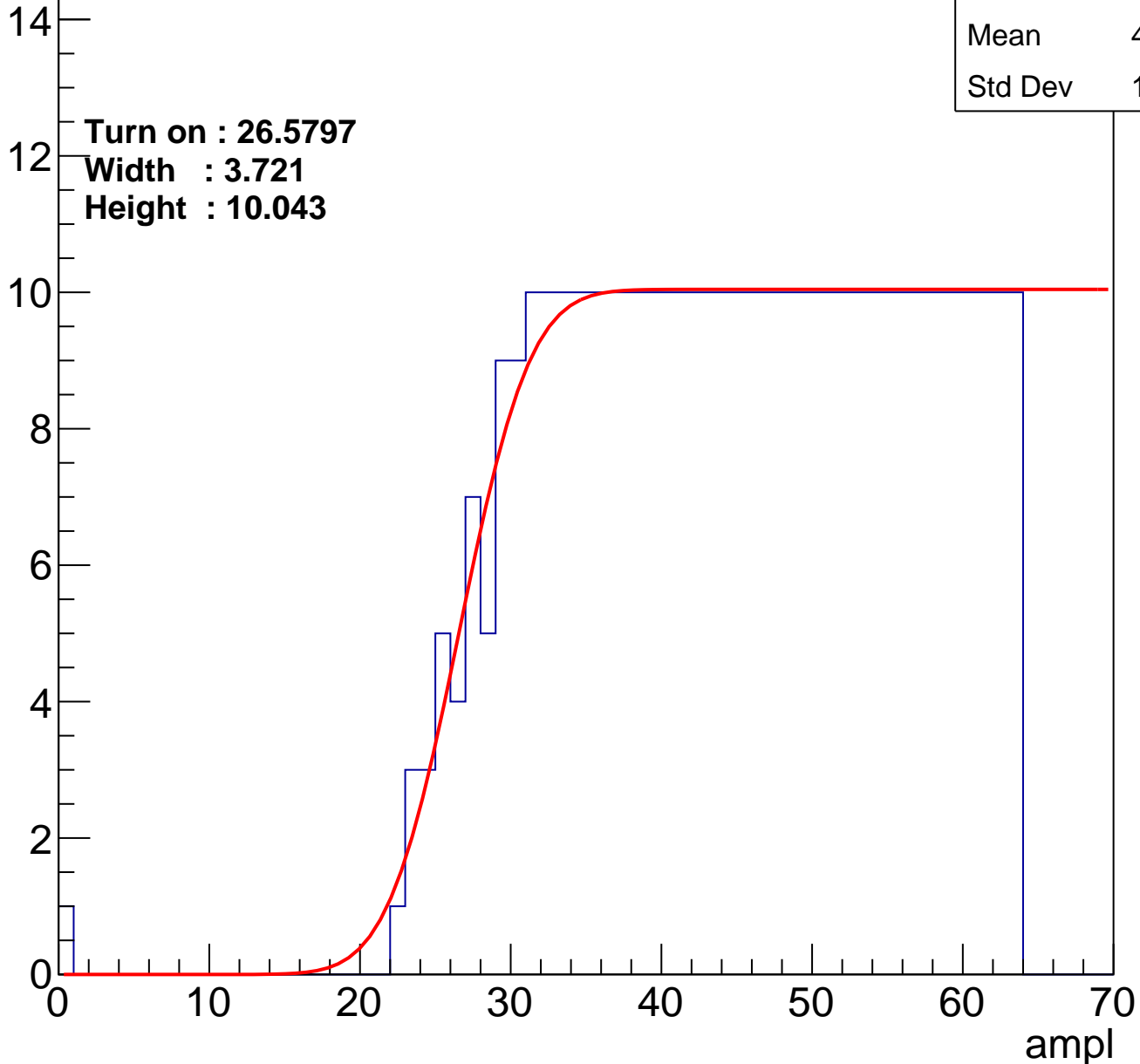
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.46 |
| Std Dev | 11.28 |

Turn on : 26.5797

Width : 3.721

Height : 10.043

Entry



B1L103S, U6-ch110

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.63 |
| Std Dev | 12.12 |

Turn on : 25.7425

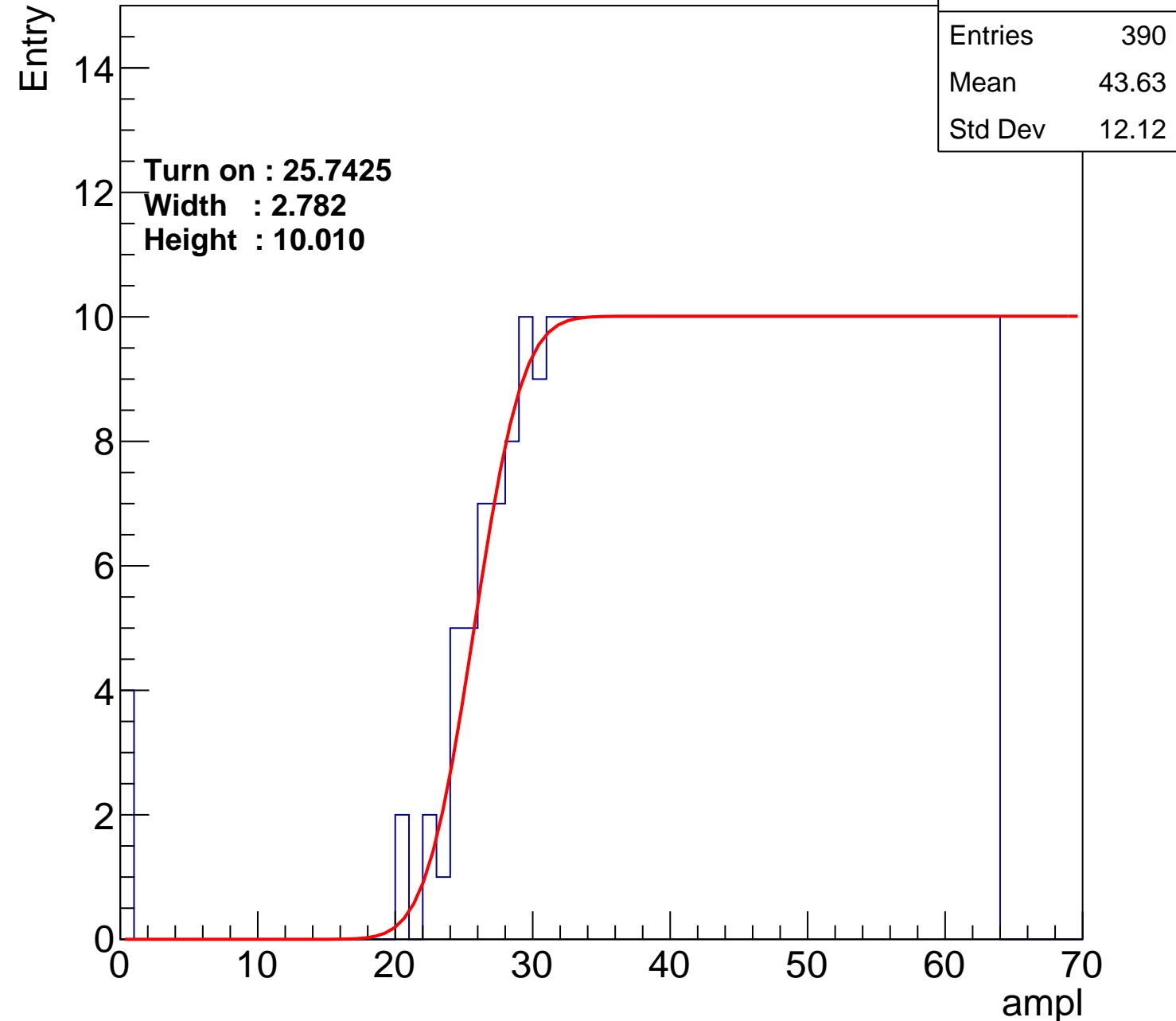
Width : 2.782

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch111

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.35 |
| Std Dev | 11.49 |

Turn on : 27.3028

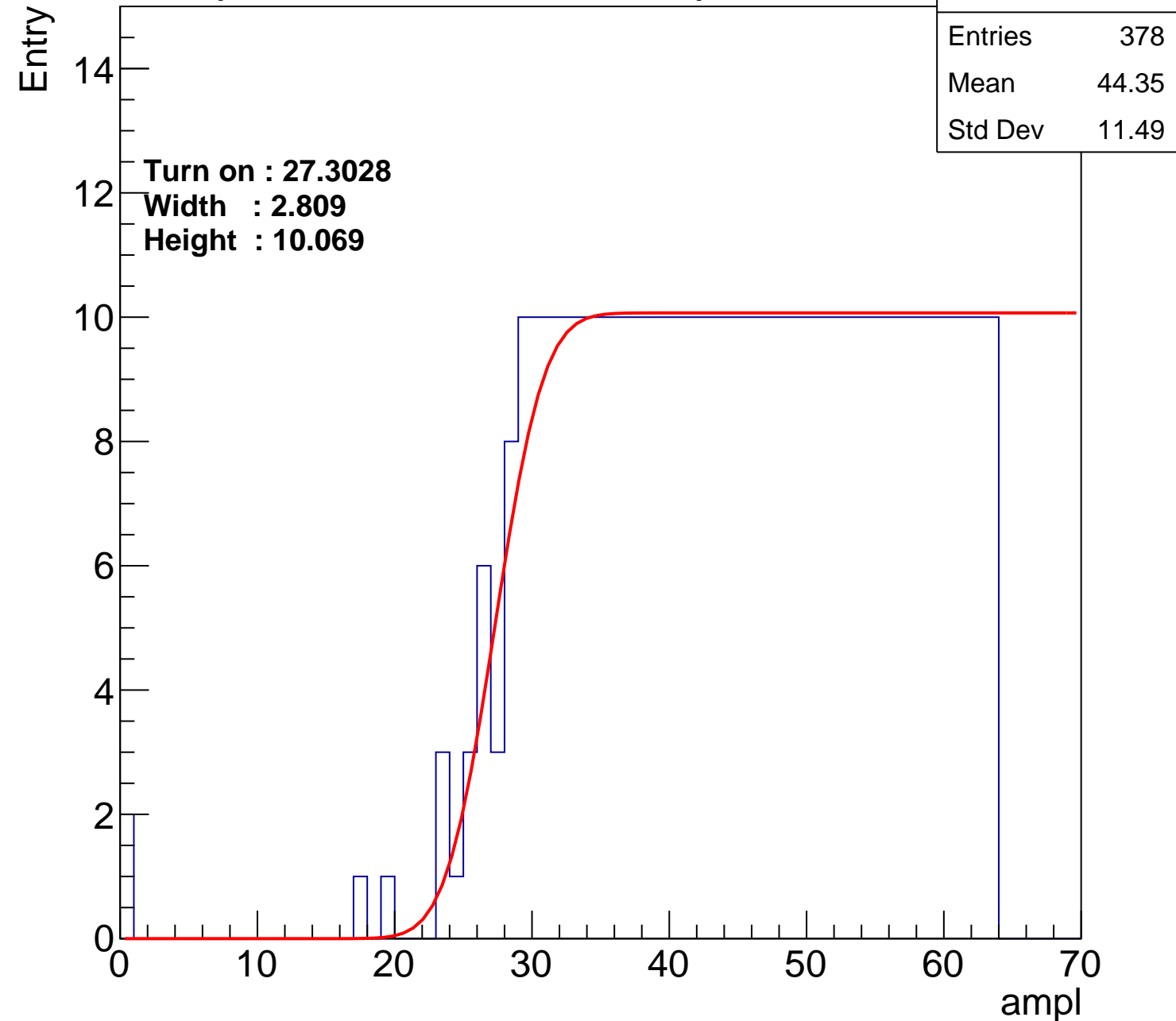
Width : 2.809

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch112

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.55 |
| Std Dev | 12.21 |

Turn on : 26.1103

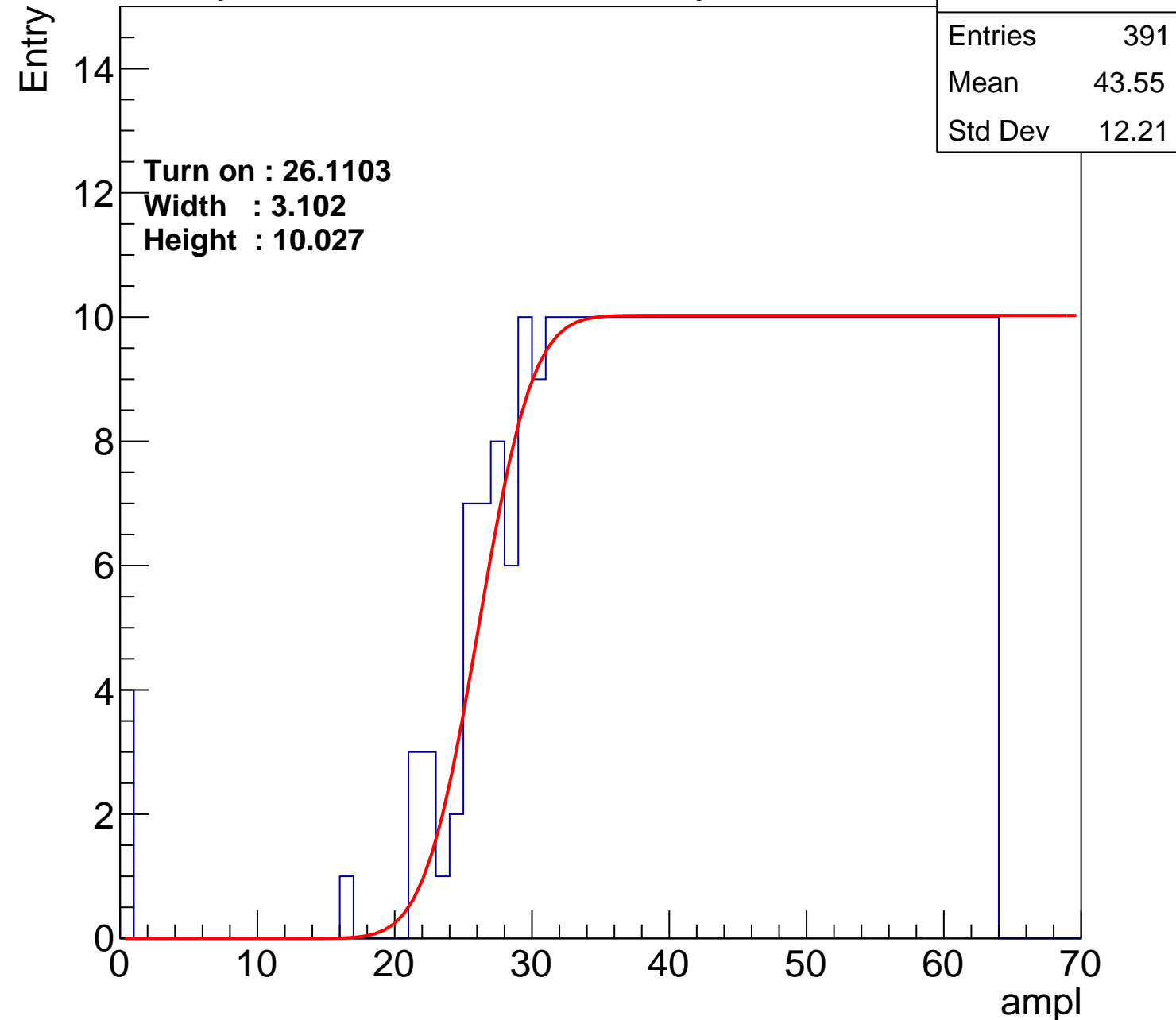
Width : 3.102

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

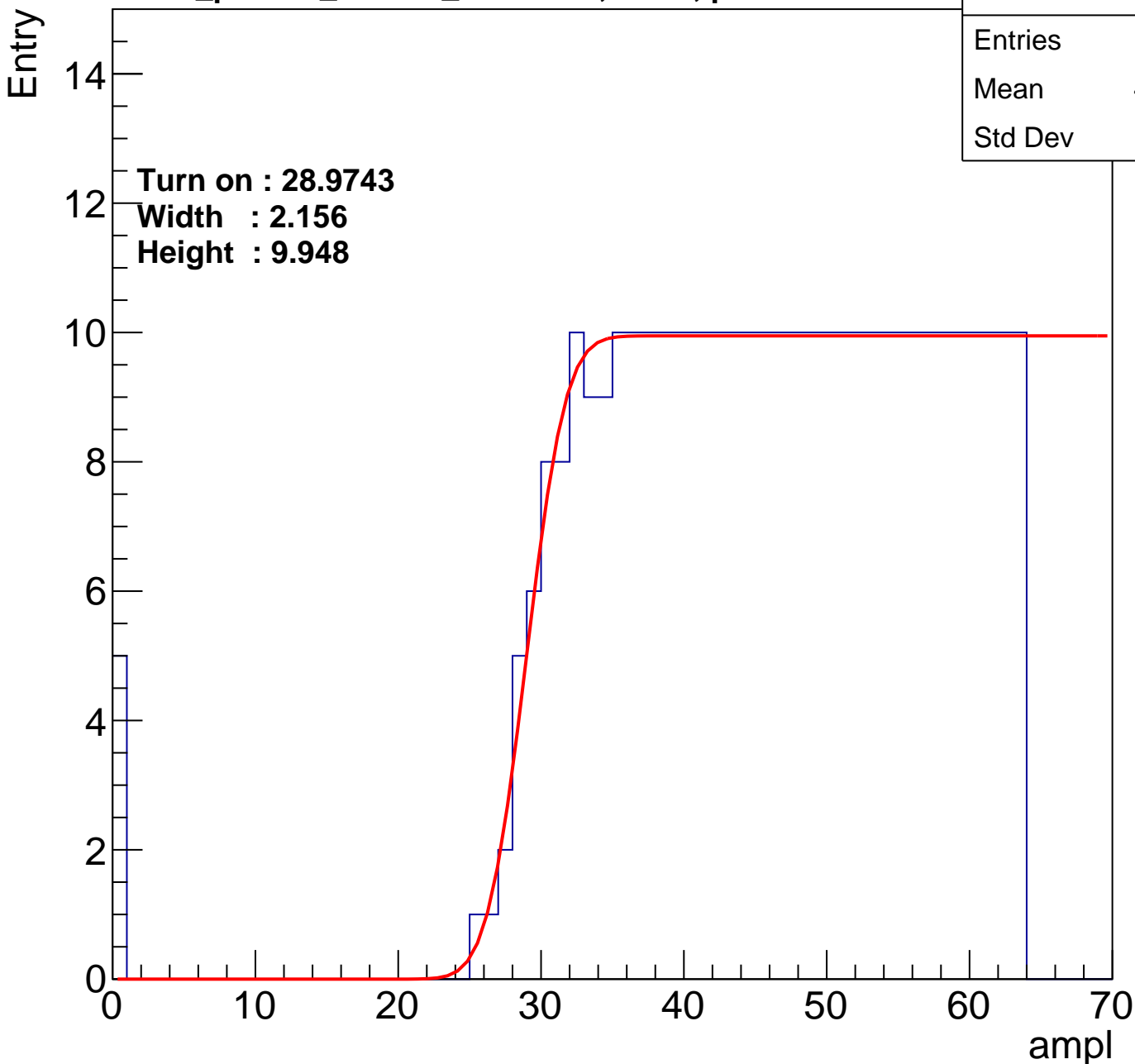


calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.31 |
| Std Dev | 11.51 |

Height : 9.948



B1L103S, U6-ch114

calib_packv5_042523_0143.root, FC#7, port C2

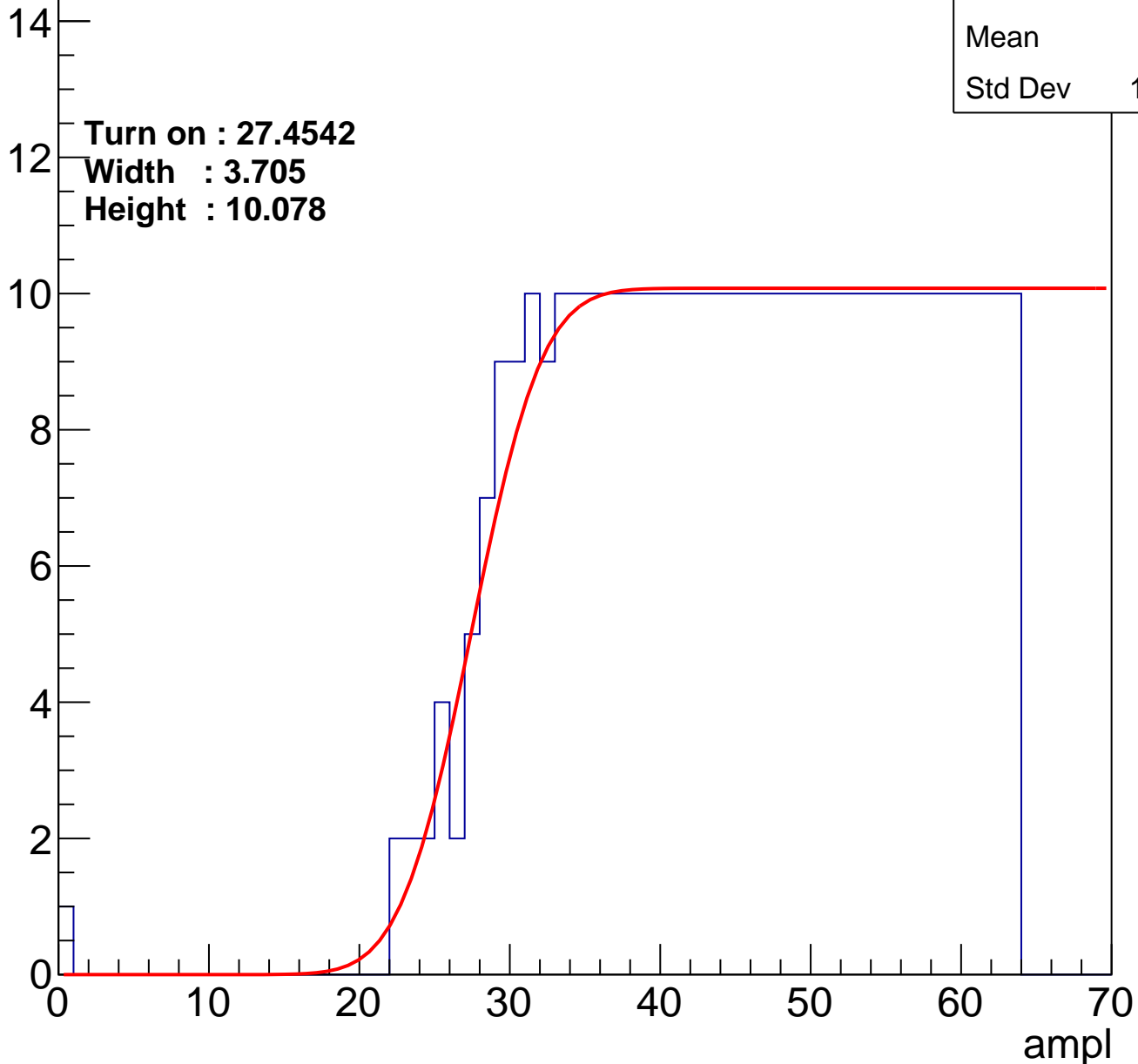
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.7 |
| Std Dev | 11.15 |

Turn on : 27.4542

Width : 3.705

Height : 10.078

Entry



B1L103S, U6-ch115

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.11 |
| Std Dev | 12.36 |

Turn on : 26.7321

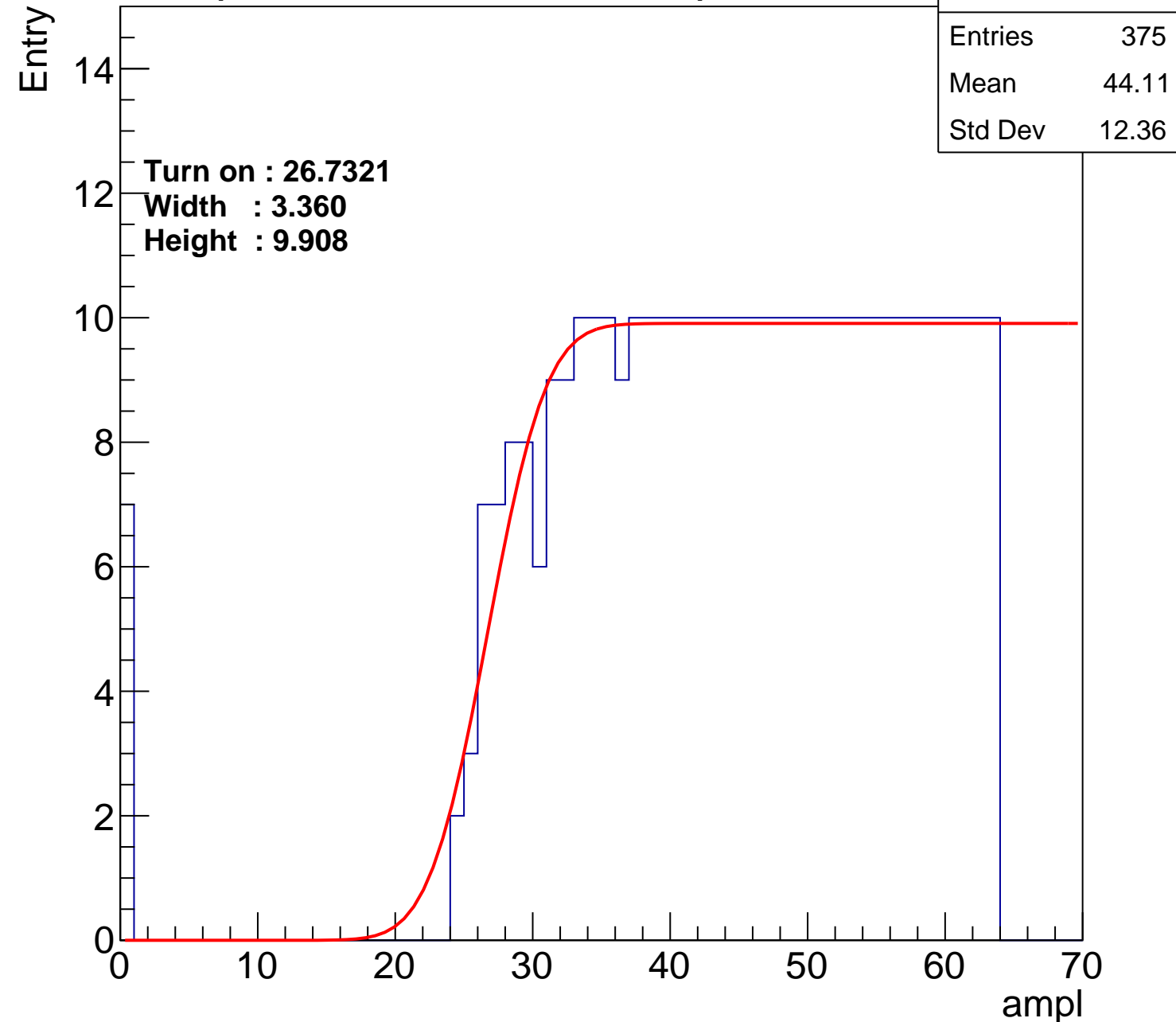
Width : 3.360

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch116

calib_packv5_042523_0143.root, FC#7, port C2

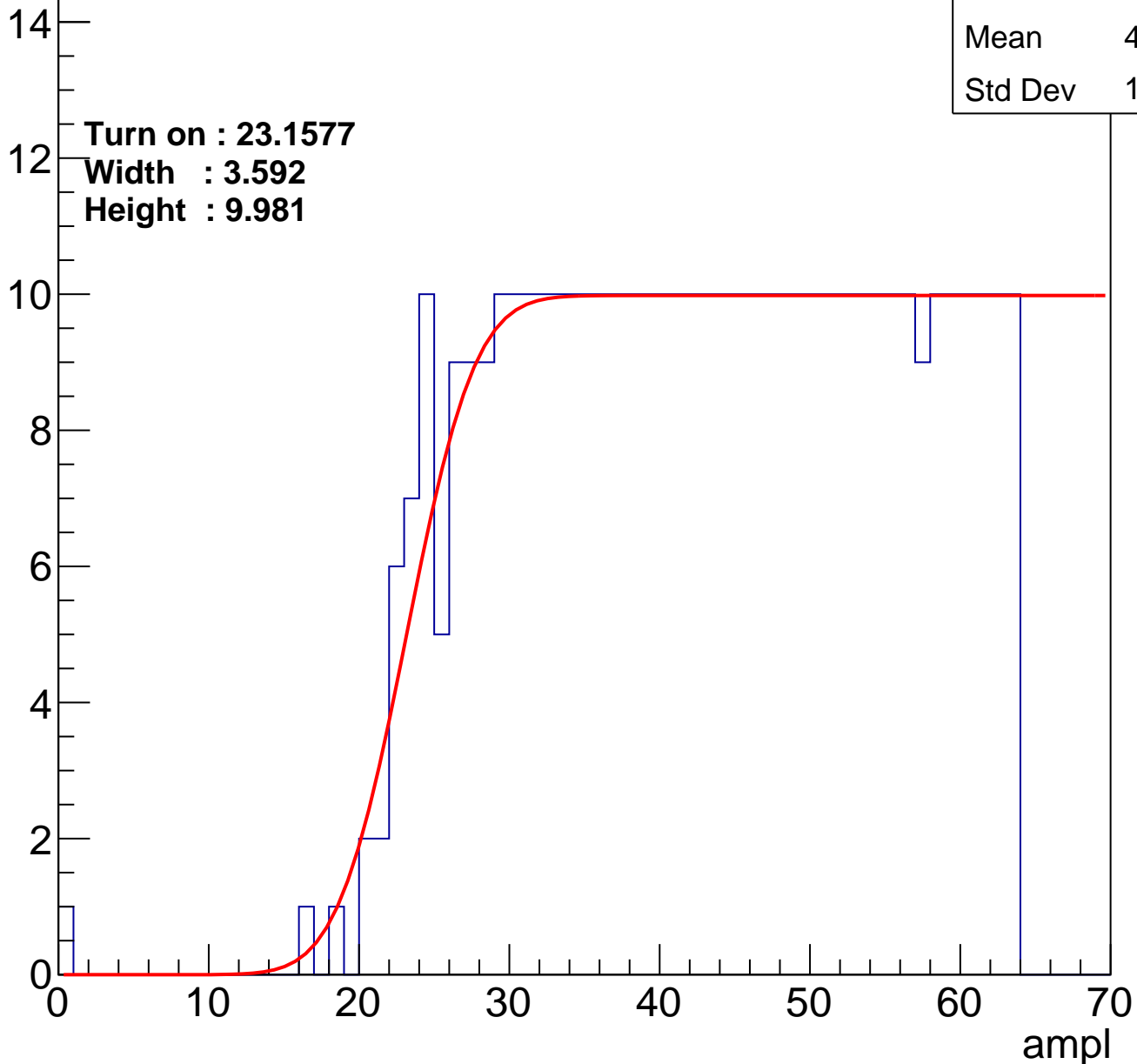
| | |
|---------|-------|
| Entries | 411 |
| Mean | 42.69 |
| Std Dev | 12.23 |

Turn on : 23.1577

Width : 3.592

Height : 9.981

Entry



B1L103S, U6-ch117

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.39 |
| Std Dev | 11.41 |

Turn on : 26.1463

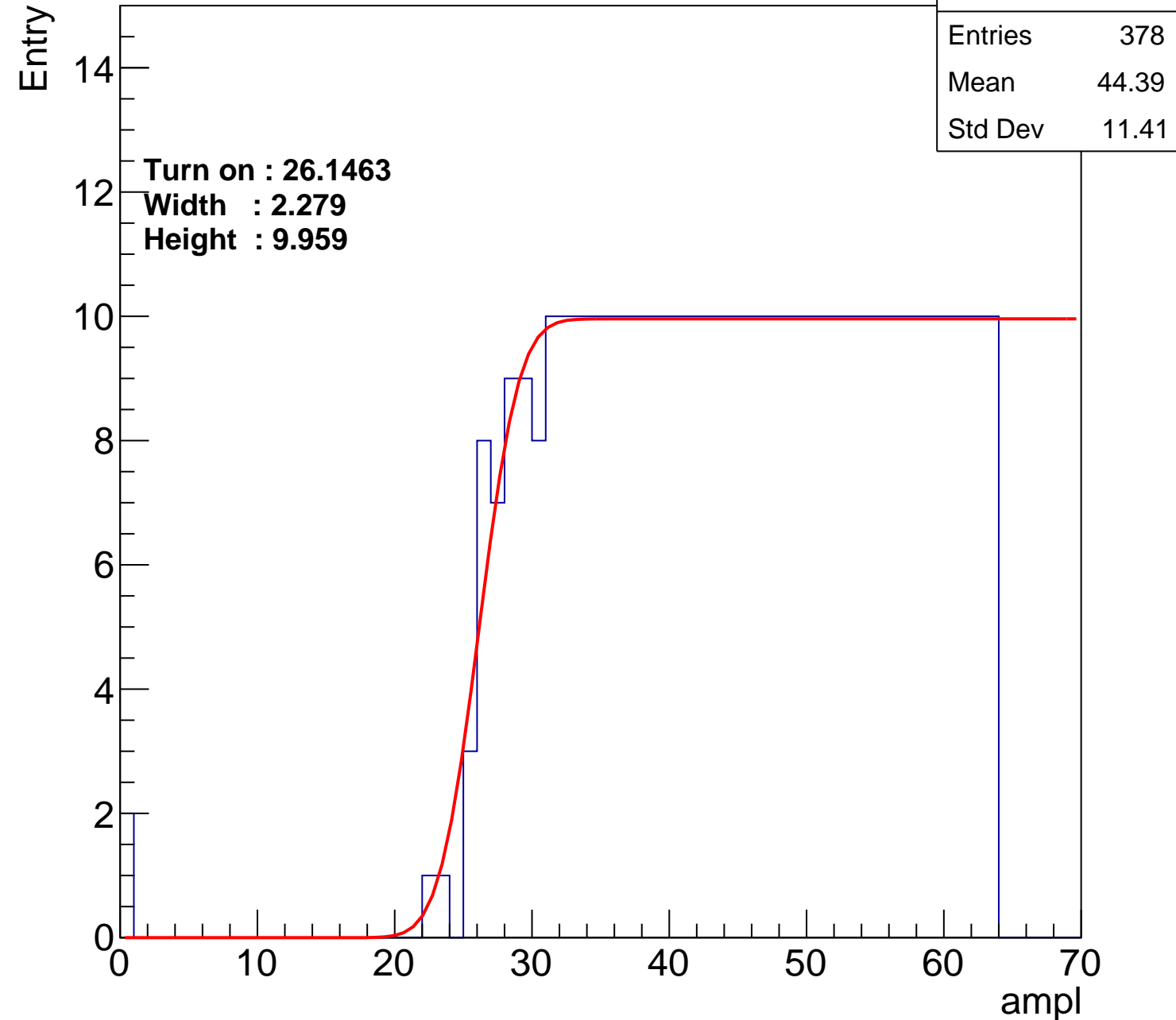
Width : 2.279

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch118

calib_packv5_042523_0143.root, FC#7, port C2

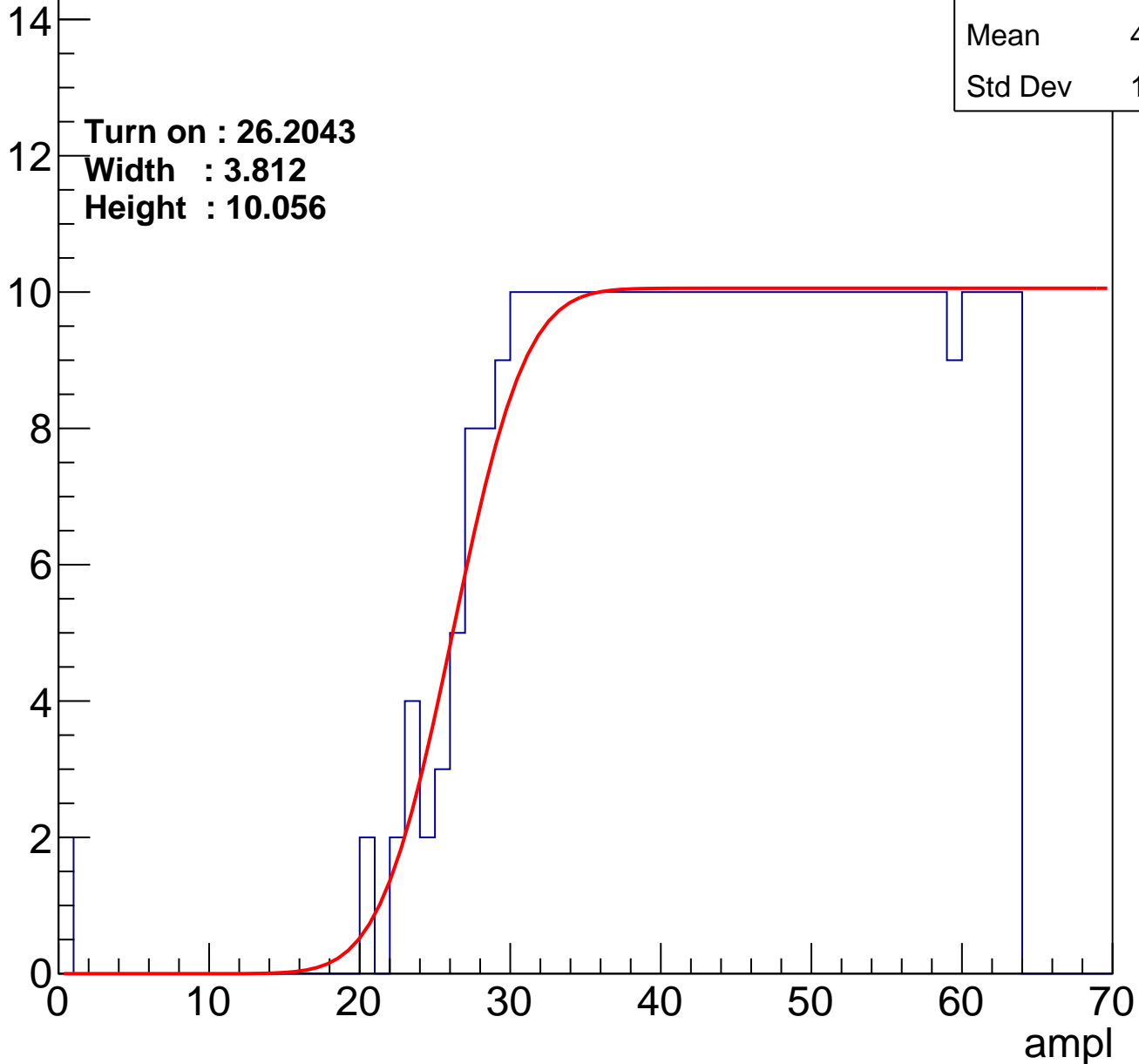
| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.96 |
| Std Dev | 11.67 |

Turn on : 26.2043

Width : 3.812

Height : 10.056

Entry



B1L103S, U6-ch119

calib_packv5_042523_0143.root, FC#7, port C2

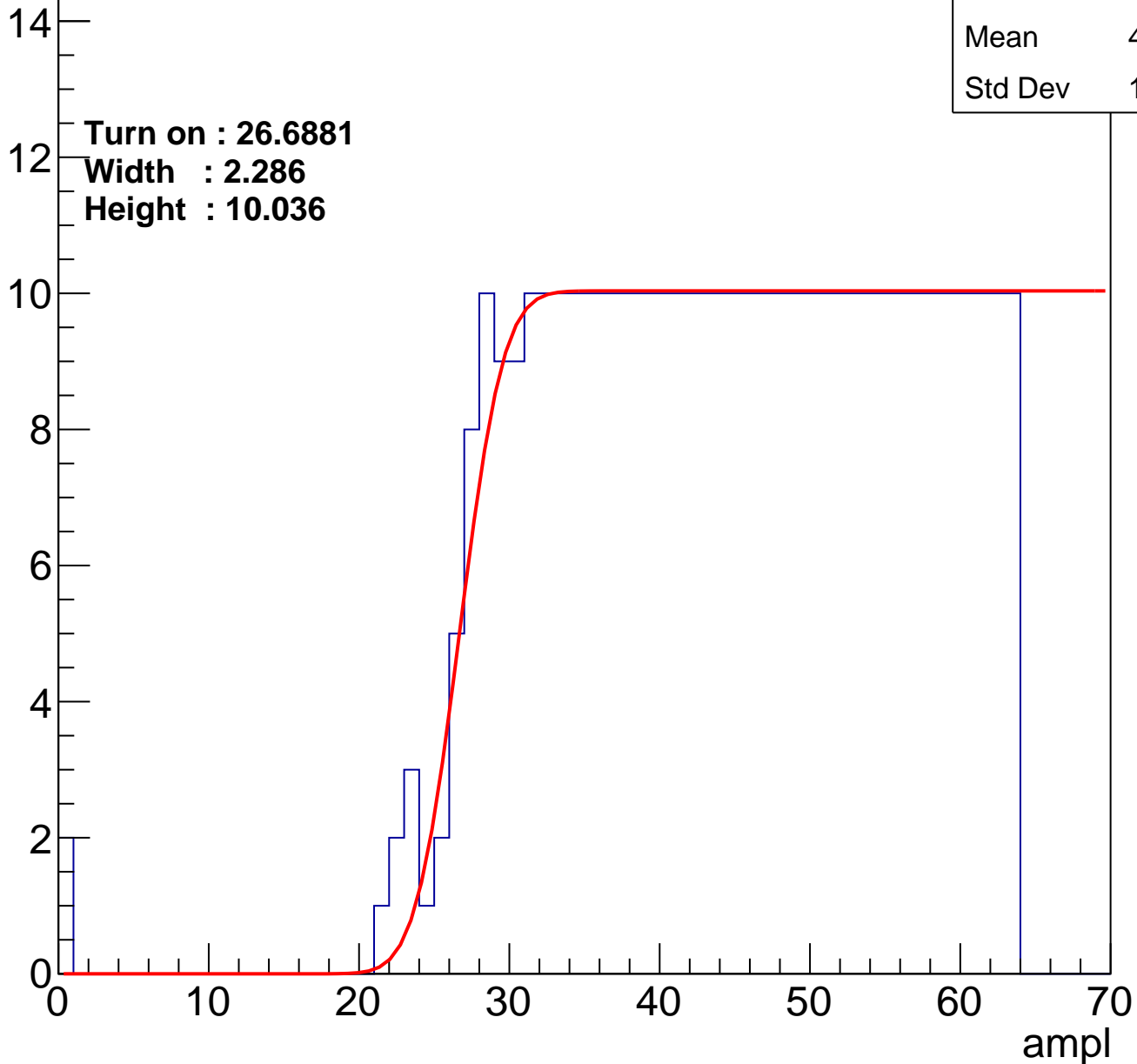
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.18 |
| Std Dev | 11.55 |

Turn on : 26.6881

Width : 2.286

Height : 10.036

Entry



B1L103S, U6-ch120

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.22 |
| Std Dev | 11.45 |

Turn on : 26.2392

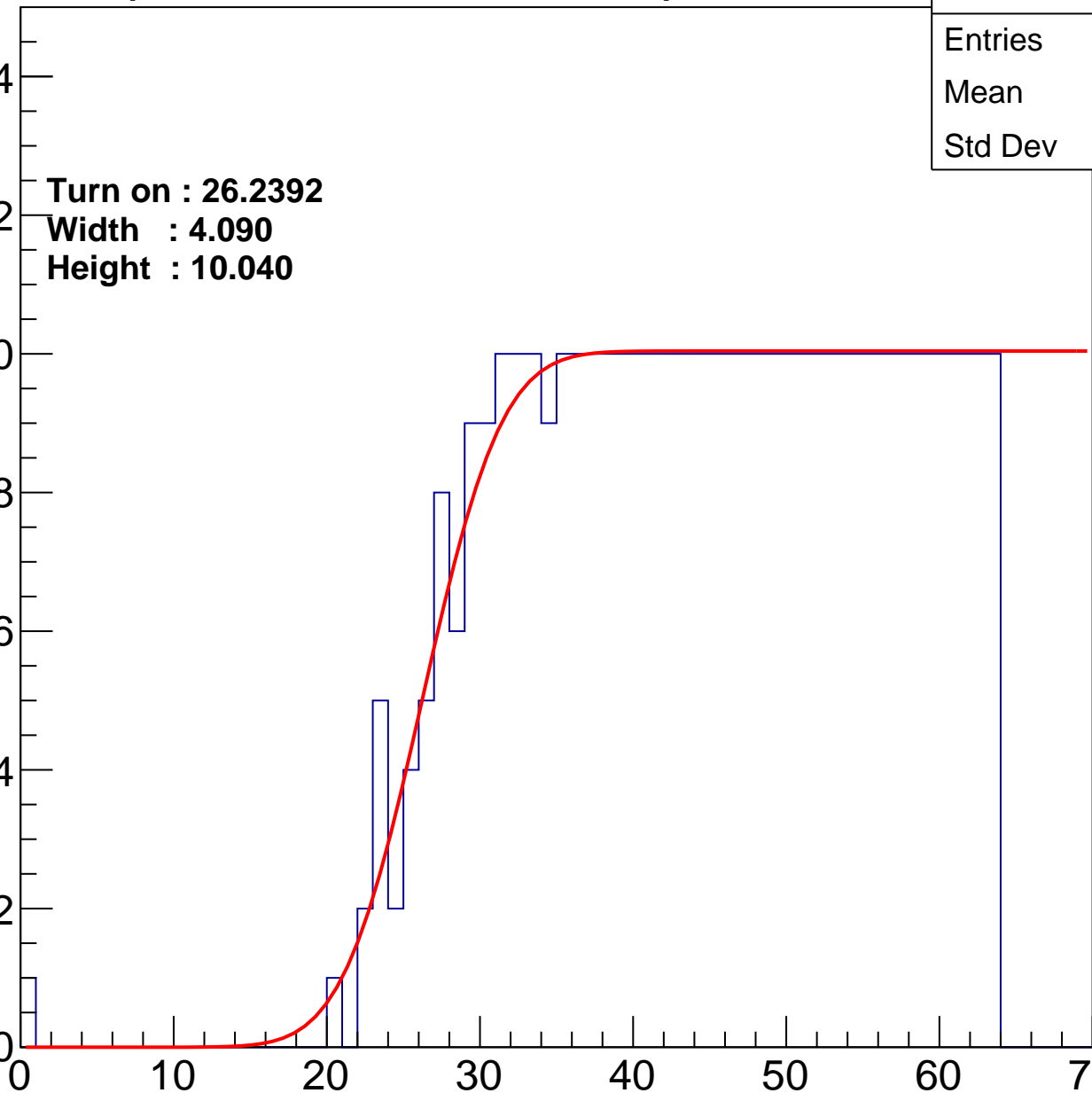
Width : 4.090

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch121

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.06 |
| Std Dev | 12.11 |

Turn on : 26.9728

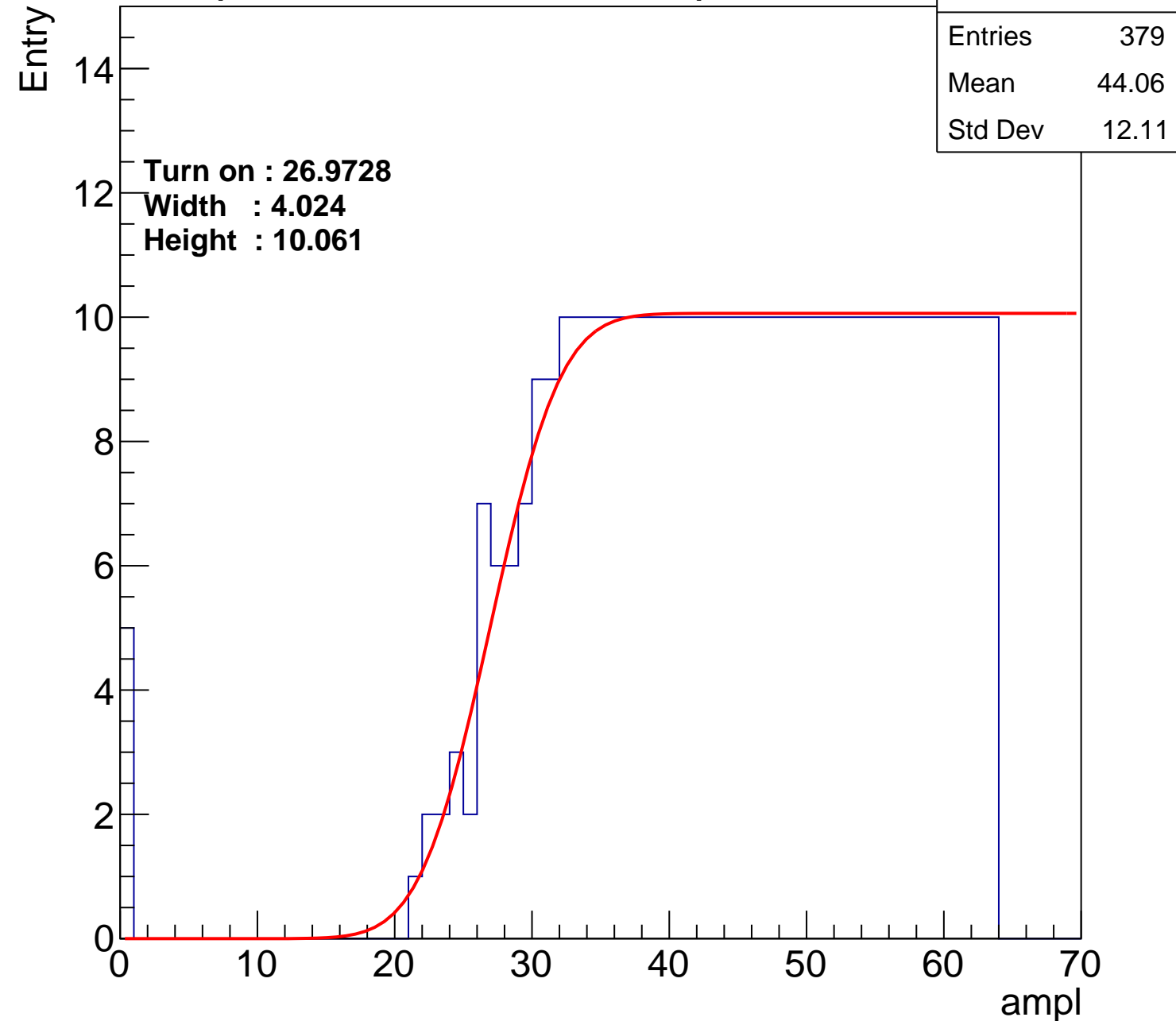
Width : 4.024

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch122

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.26 |
| Std Dev | 11.58 |

Turn on : 26.7853

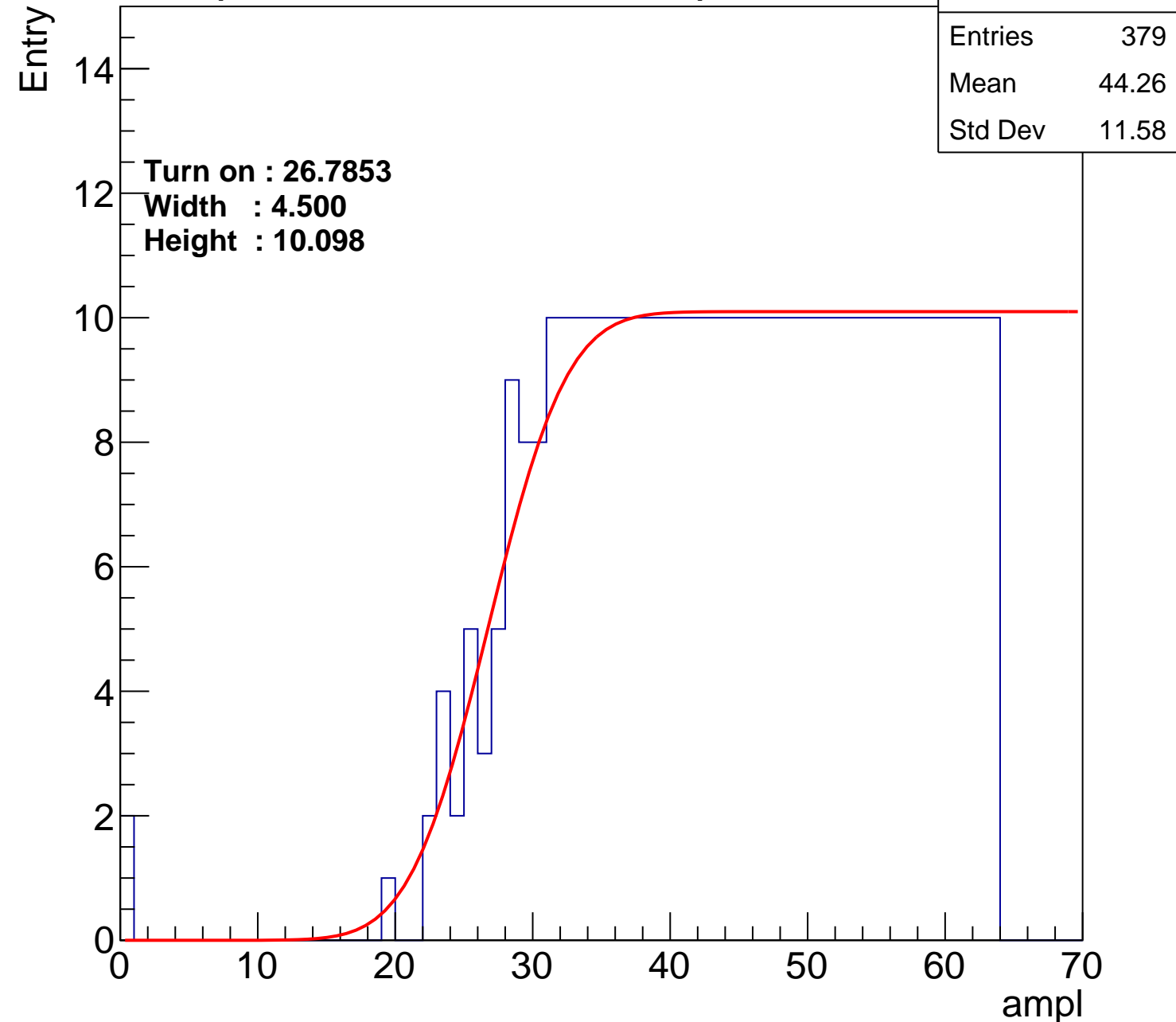
Width : 4.500

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch123

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.62 |
| Std Dev | 11.5 |

Turn on : 27.2316

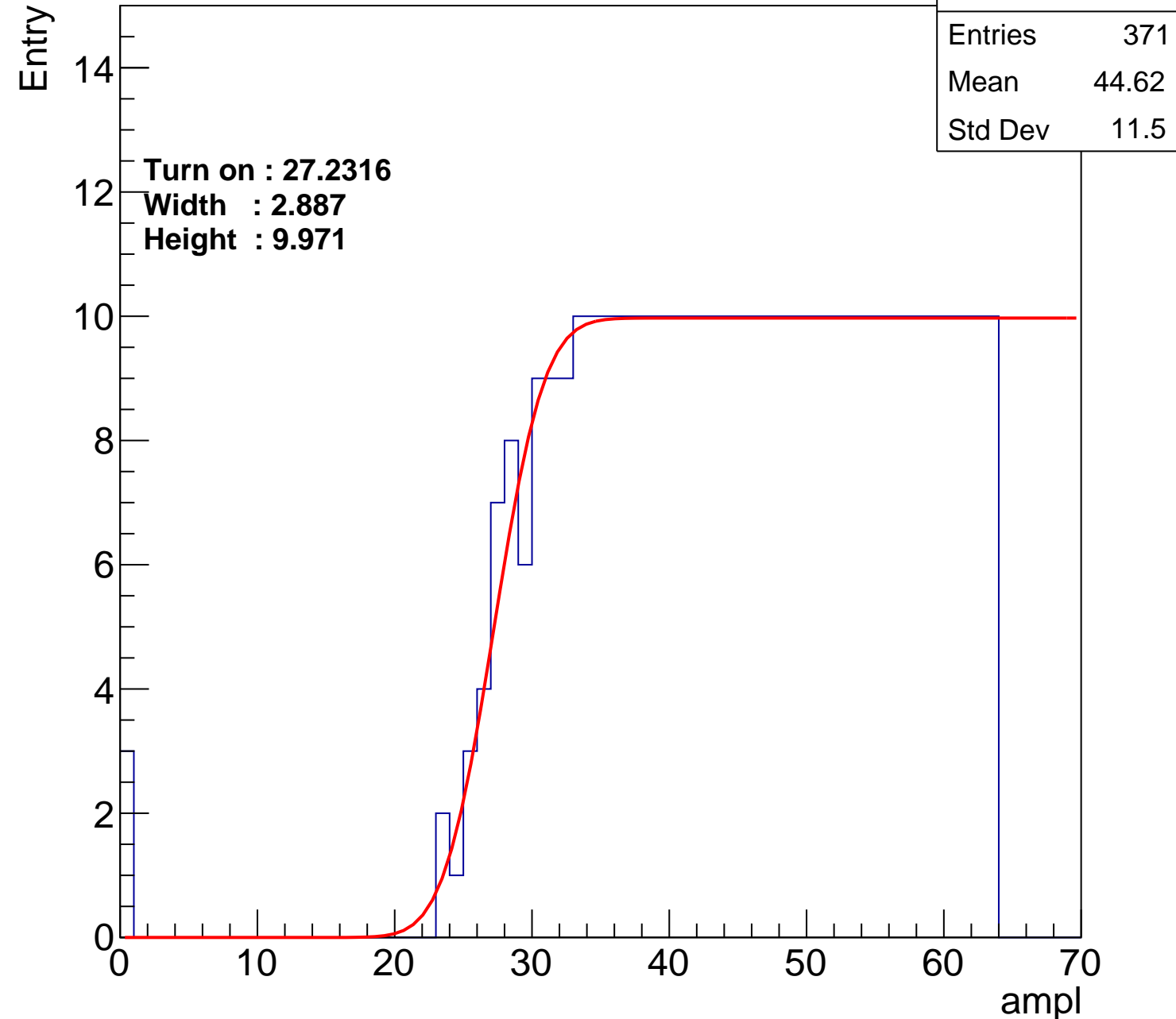
Width : 2.887

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch124

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.19 |
| Std Dev | 12.23 |

Turn on : 23.9336

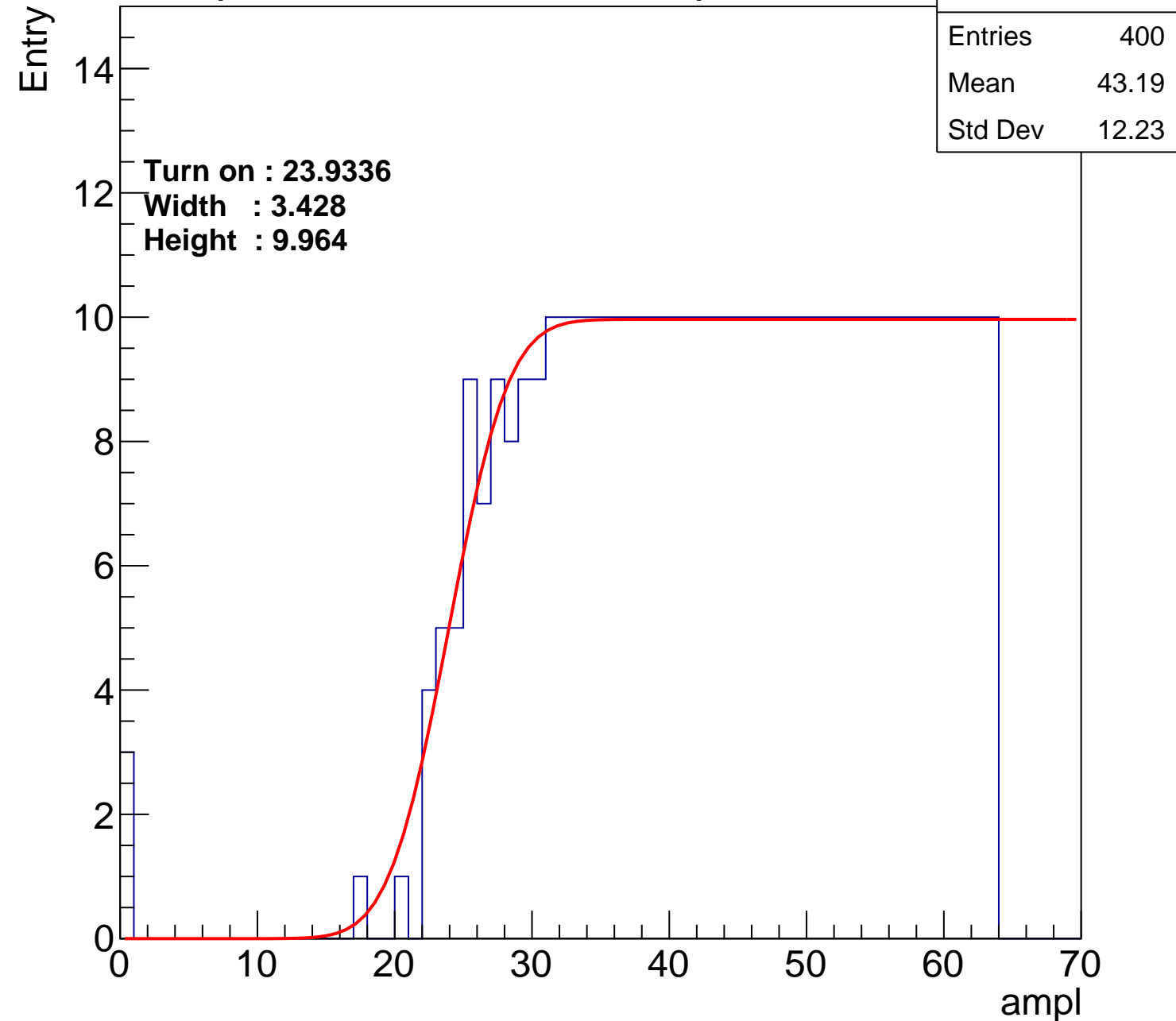
Width : 3.428

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch125

calib_packv5_042523_0143.root, FC#7, port C2

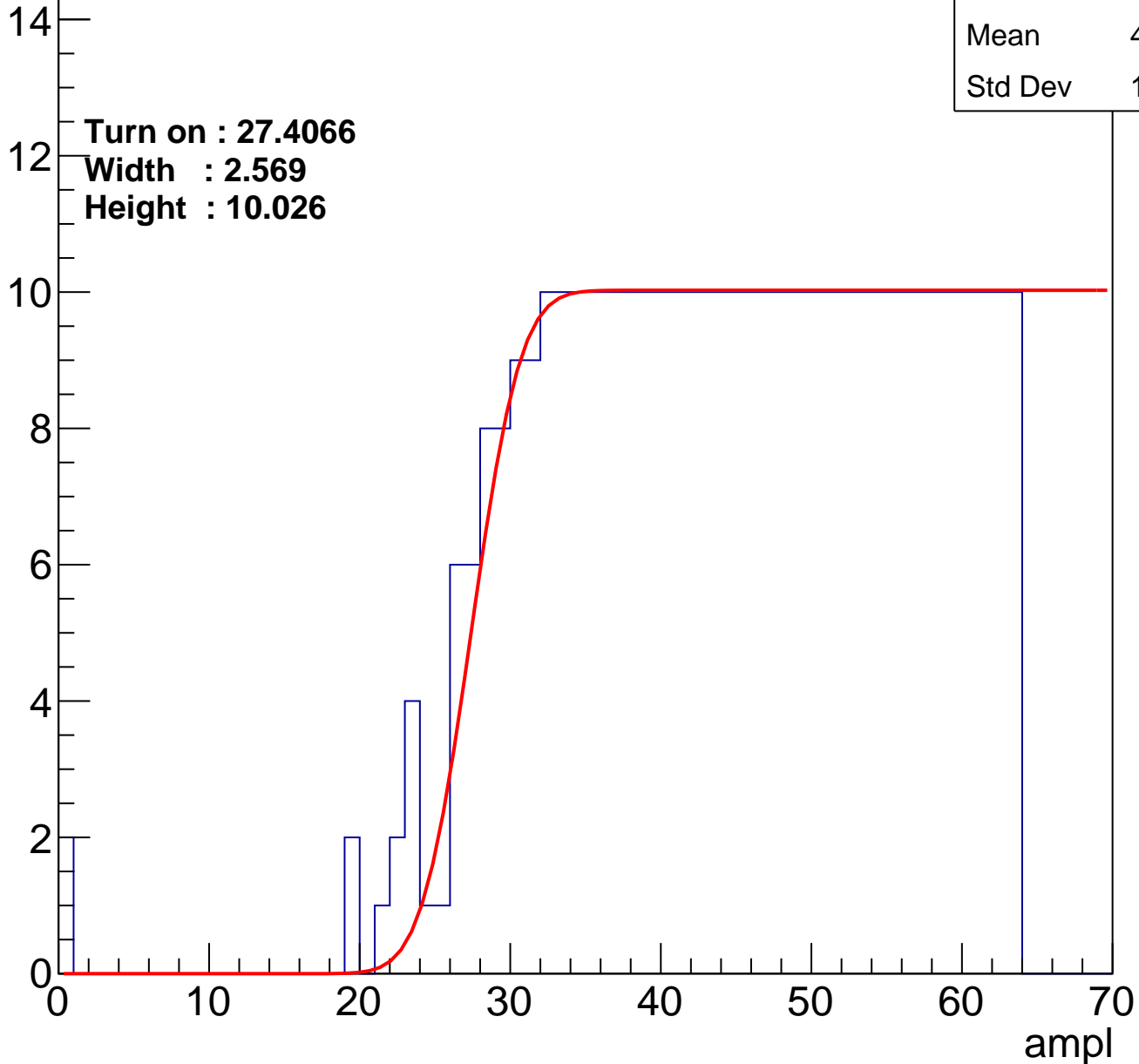
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.24 |
| Std Dev | 11.62 |

Turn on : 27.4066

Width : 2.569

Height : 10.026

Entry



B1L103S, U6-ch126

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 405 |
| Mean | 42.82 |
| Std Dev | 12.61 |

Turn on : 24.5141

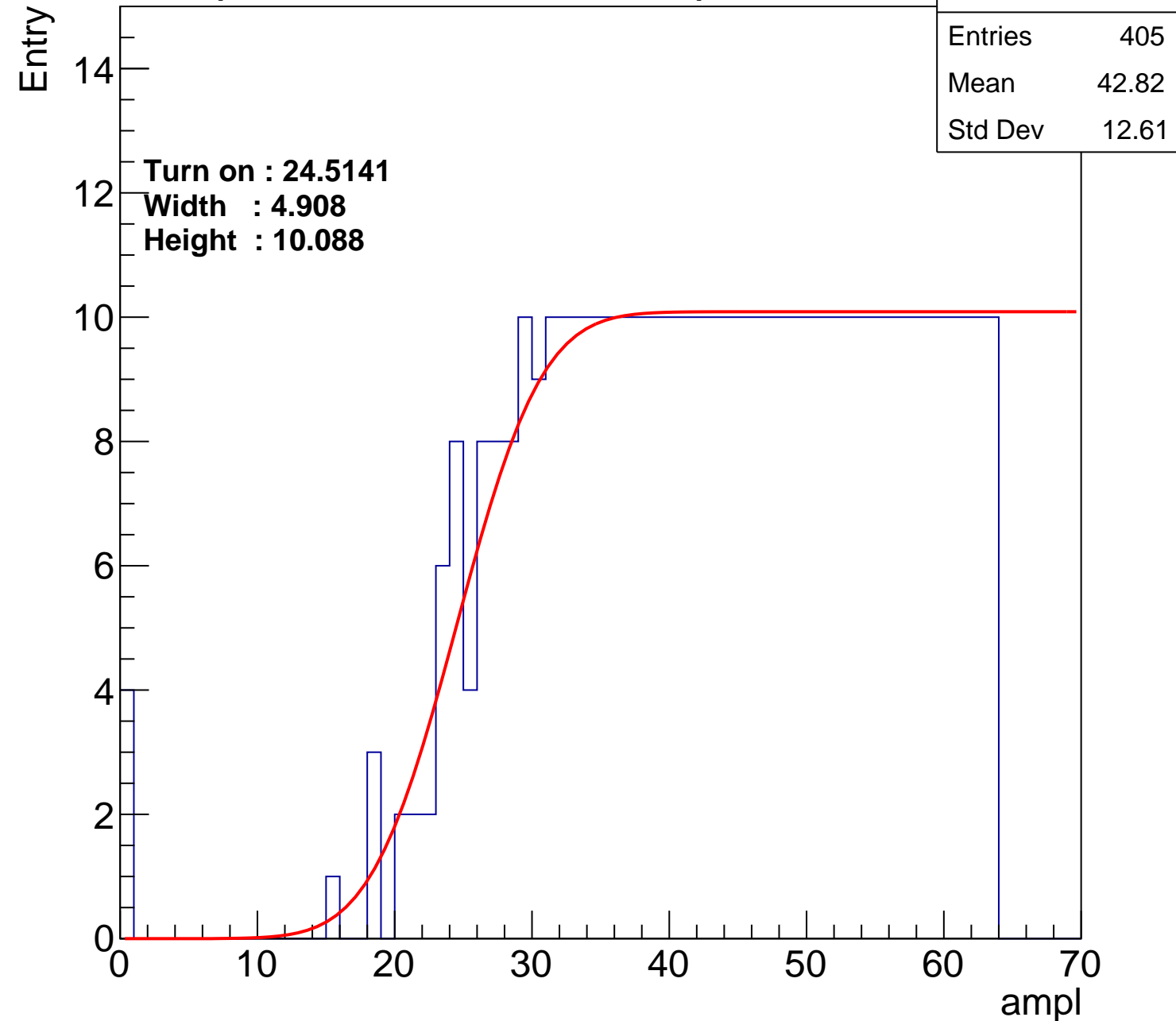
Width : 4.908

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U6-ch127

calib_packv5_042523_0143.root, FC#7, port C2

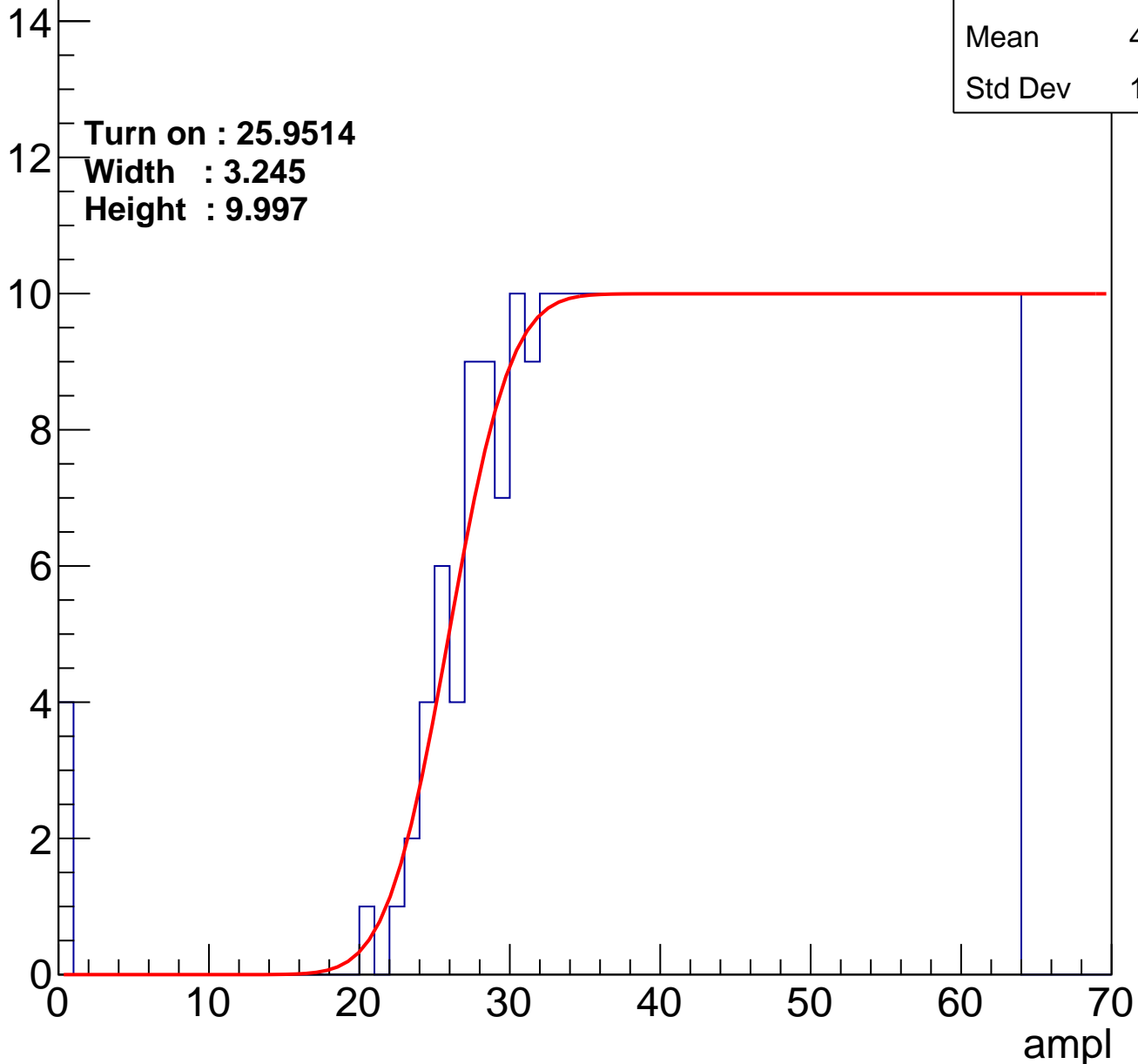
| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.82 |
| Std Dev | 12.03 |

Turn on : 25.9514

Width : 3.245

Height : 9.997

Entry



B1L103S, U6-ch127

calib_packv5_042523_0143.root, FC#7, port C2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.82 |
| Std Dev | 12.03 |

Turn on : 25.9514

Width : 3.245

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl

