



# B1L002S, U6-ch0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.11
Std Dev	11.59

Turn on : 25.9868

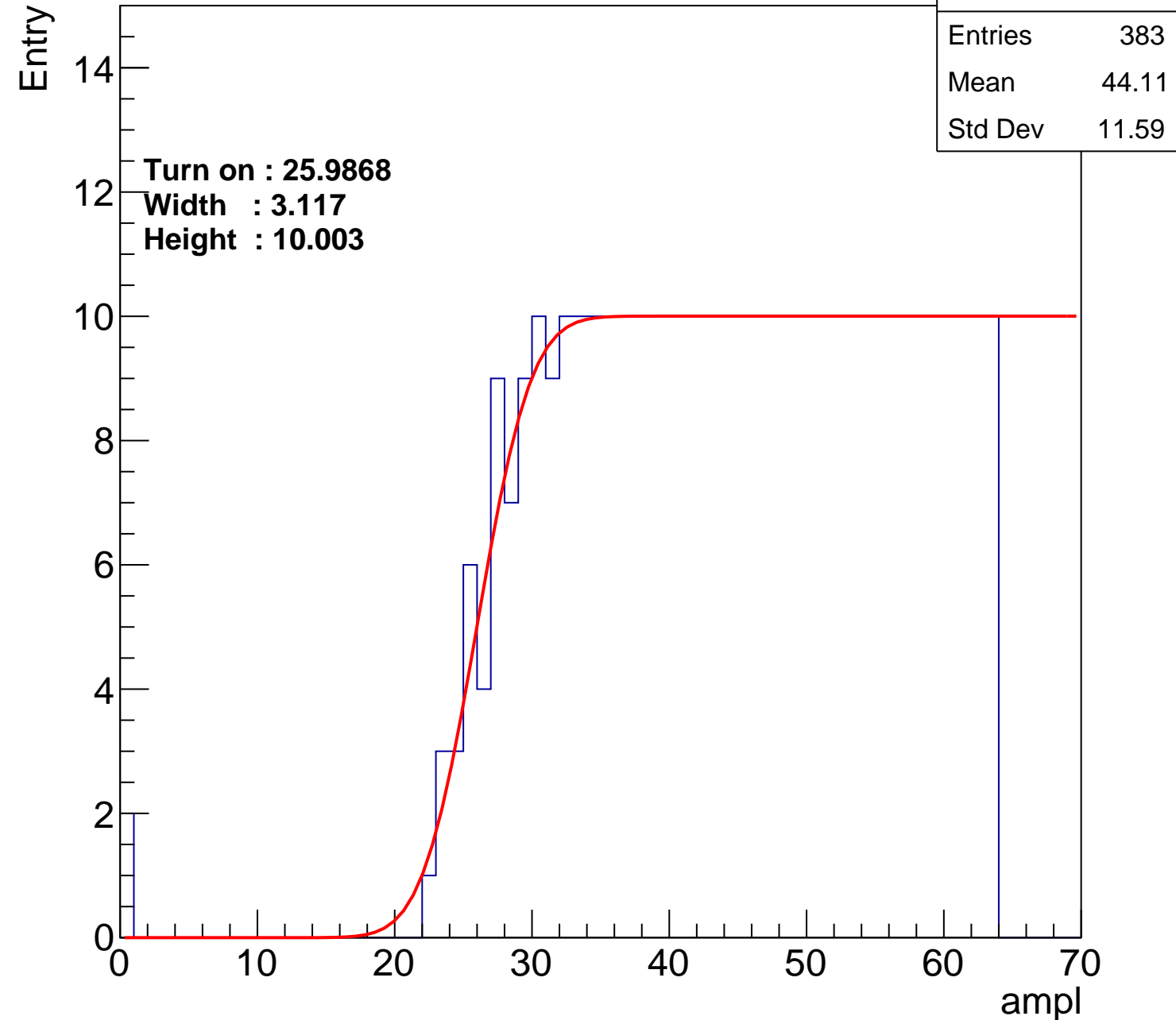
Width : 3.117

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.98
Std Dev	11.48

Turn on : 25.8090

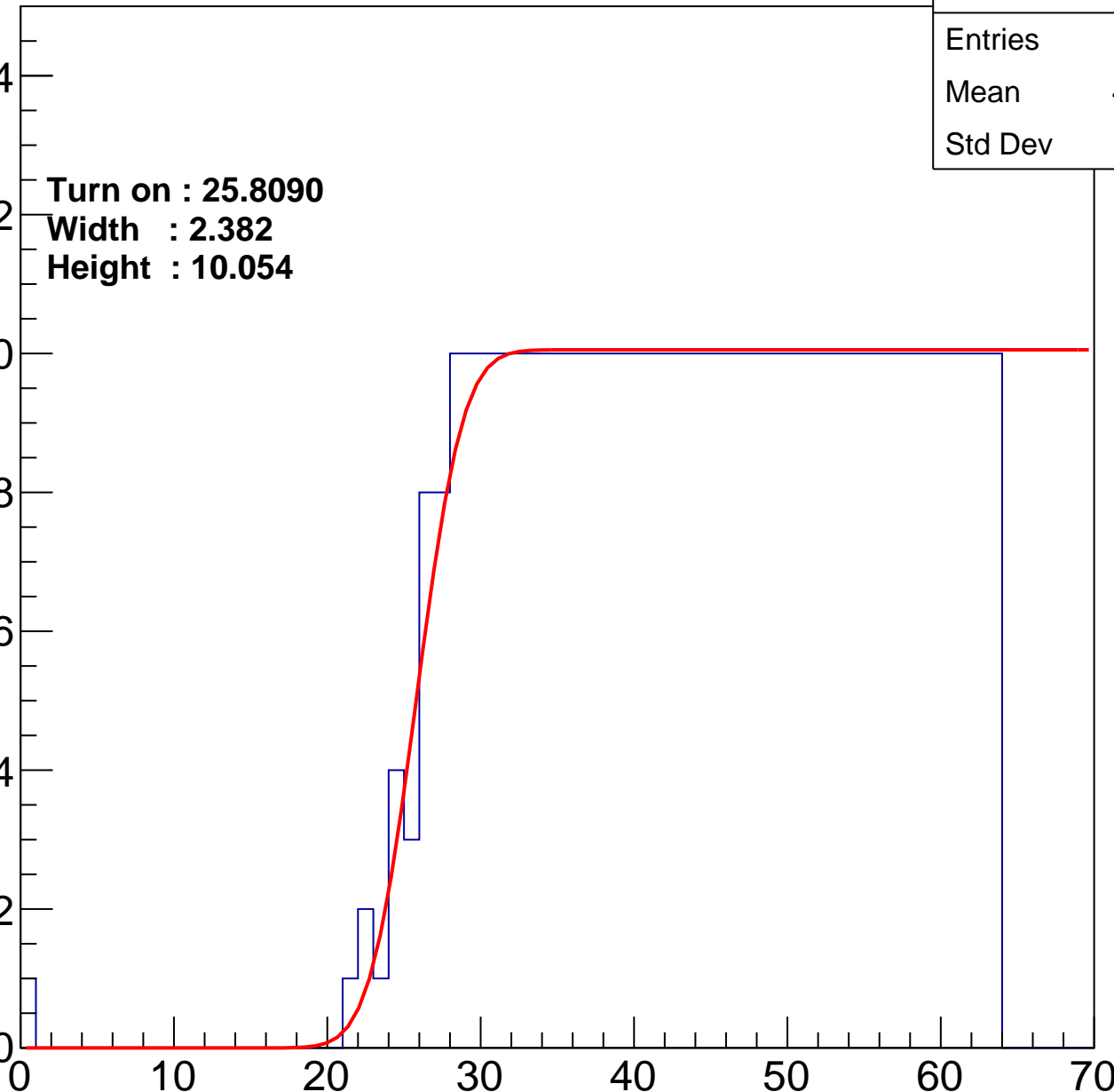
Width : 2.382

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch2

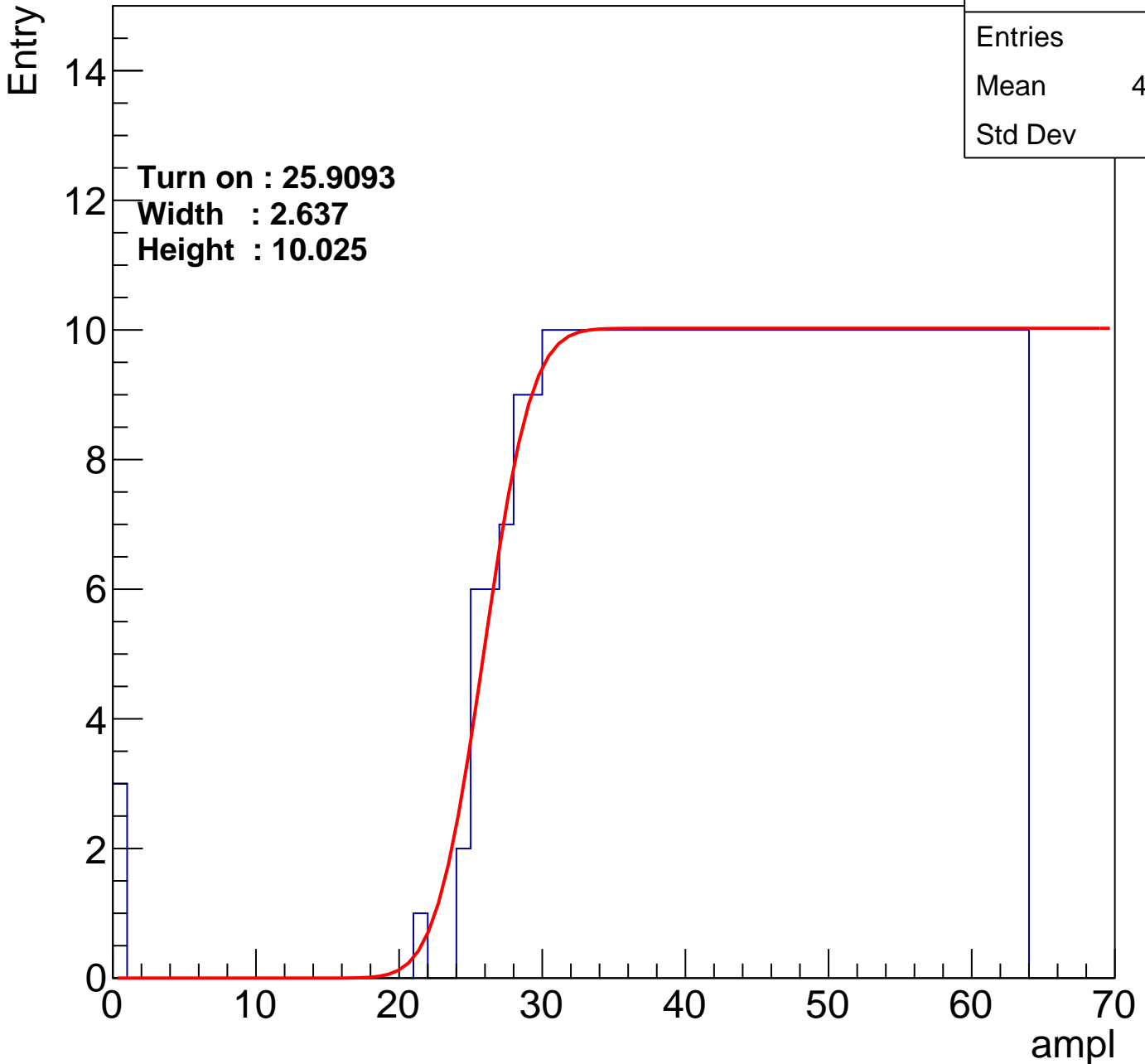
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.09
Std Dev	11.7

**Turn on : 25.9093**

**Width : 2.637**

**Height : 10.025**



# B1L002S, U6-ch3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	387
Mean	43.96
Std Dev	11.62

Turn on : 25.6323

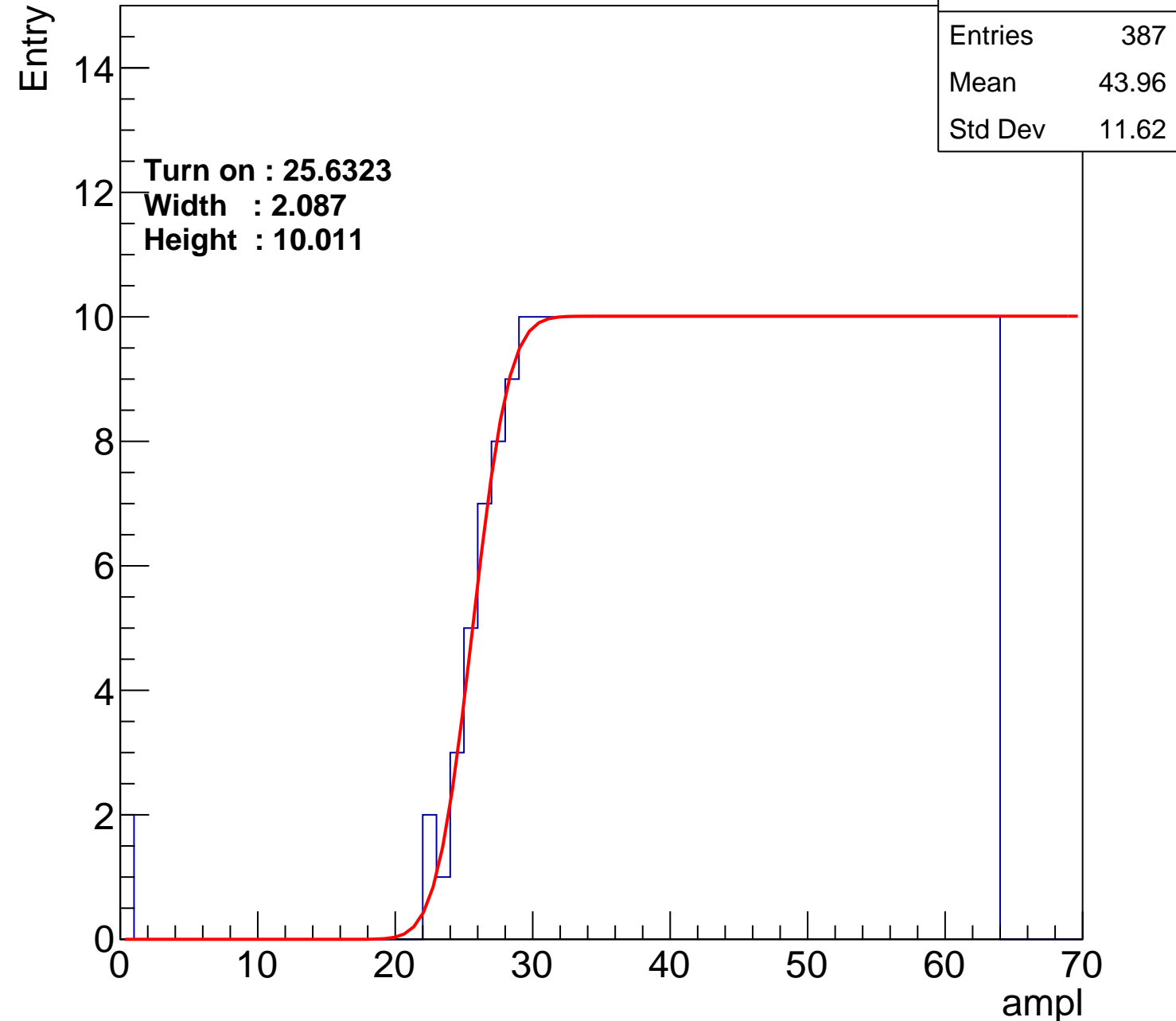
Width : 2.087

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch4

calib\_packv5\_042523\_0143.root, FC#10, port B3

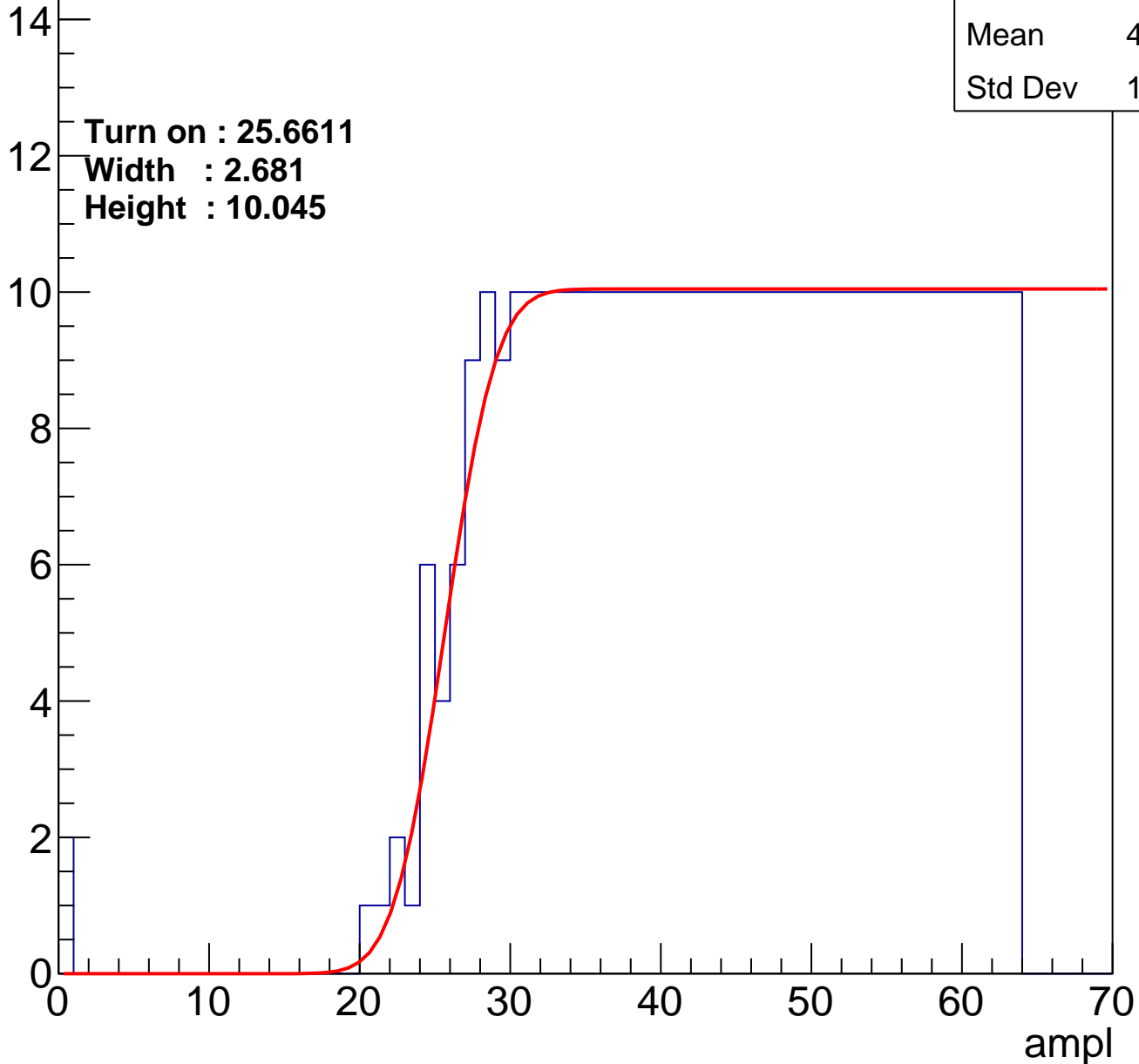
Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.6611

Width : 2.681

Height : 10.045

Entry



# B1L002S, U6-ch5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.58
Std Dev	11.99

Turn on : 25.6867

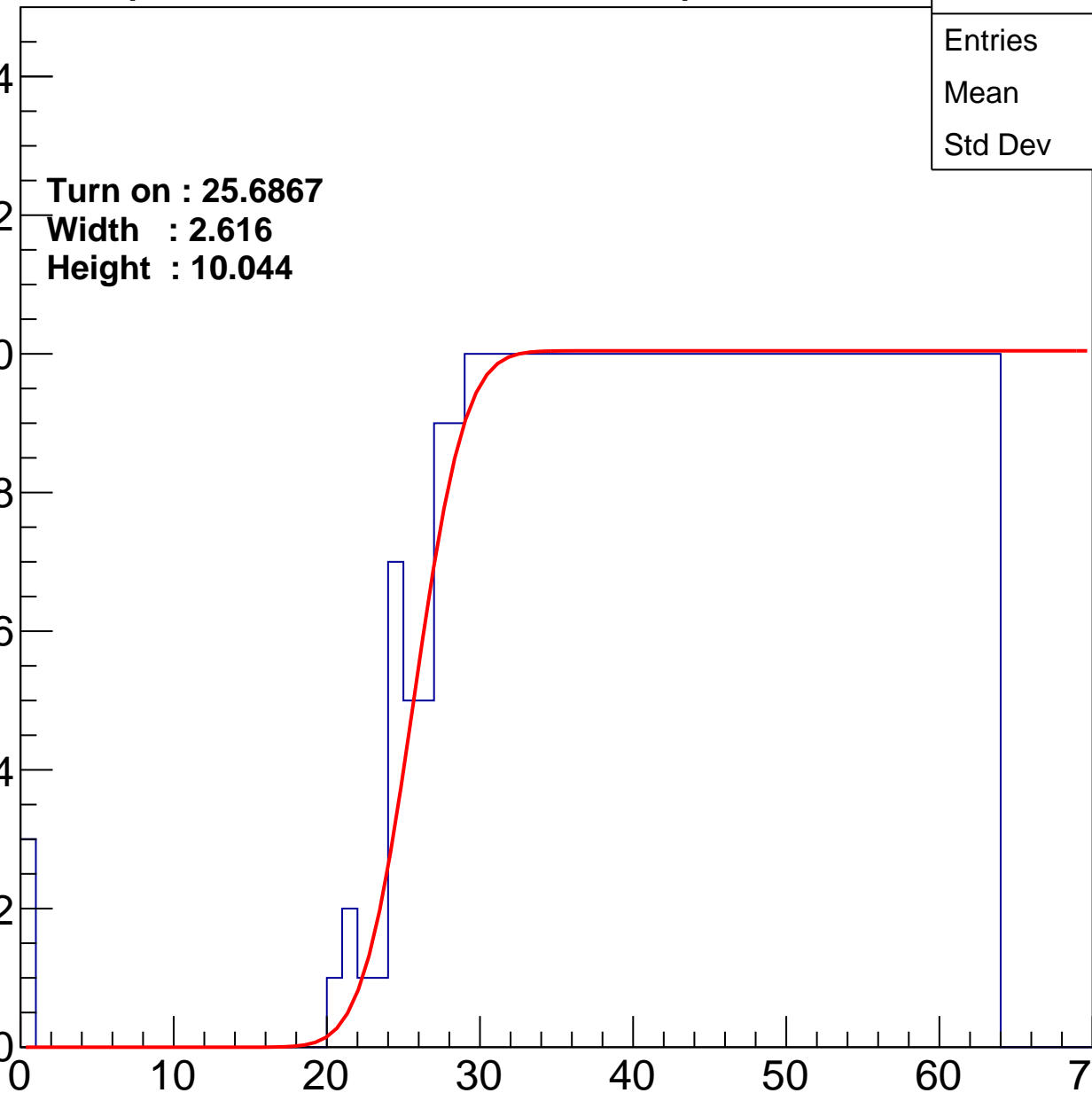
Width : 2.616

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.86
Std Dev	11.56

Turn on : 25.1624

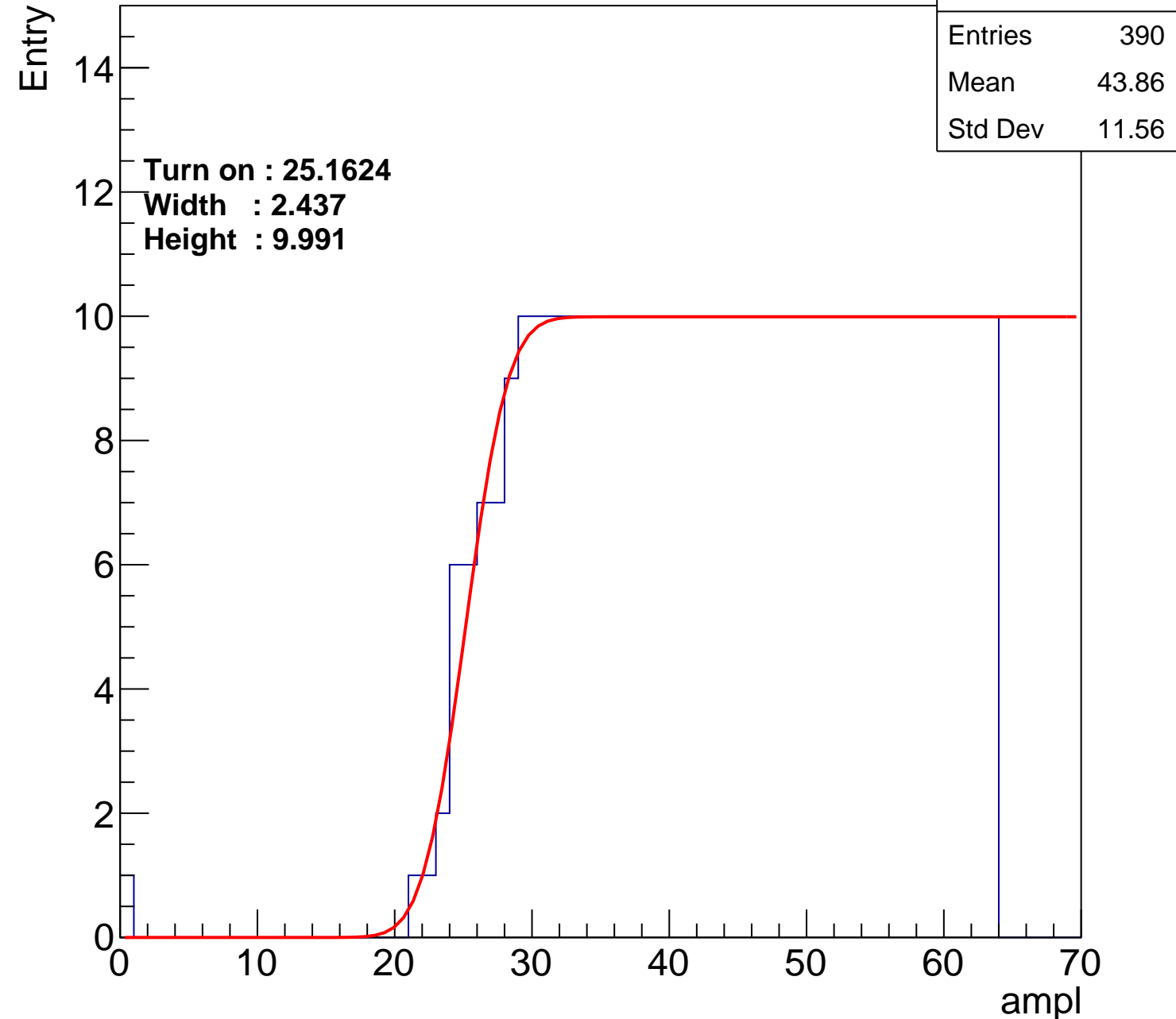
Width : 2.437

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch7

calib\_packv5\_042523\_0143.root, FC#10, port B3

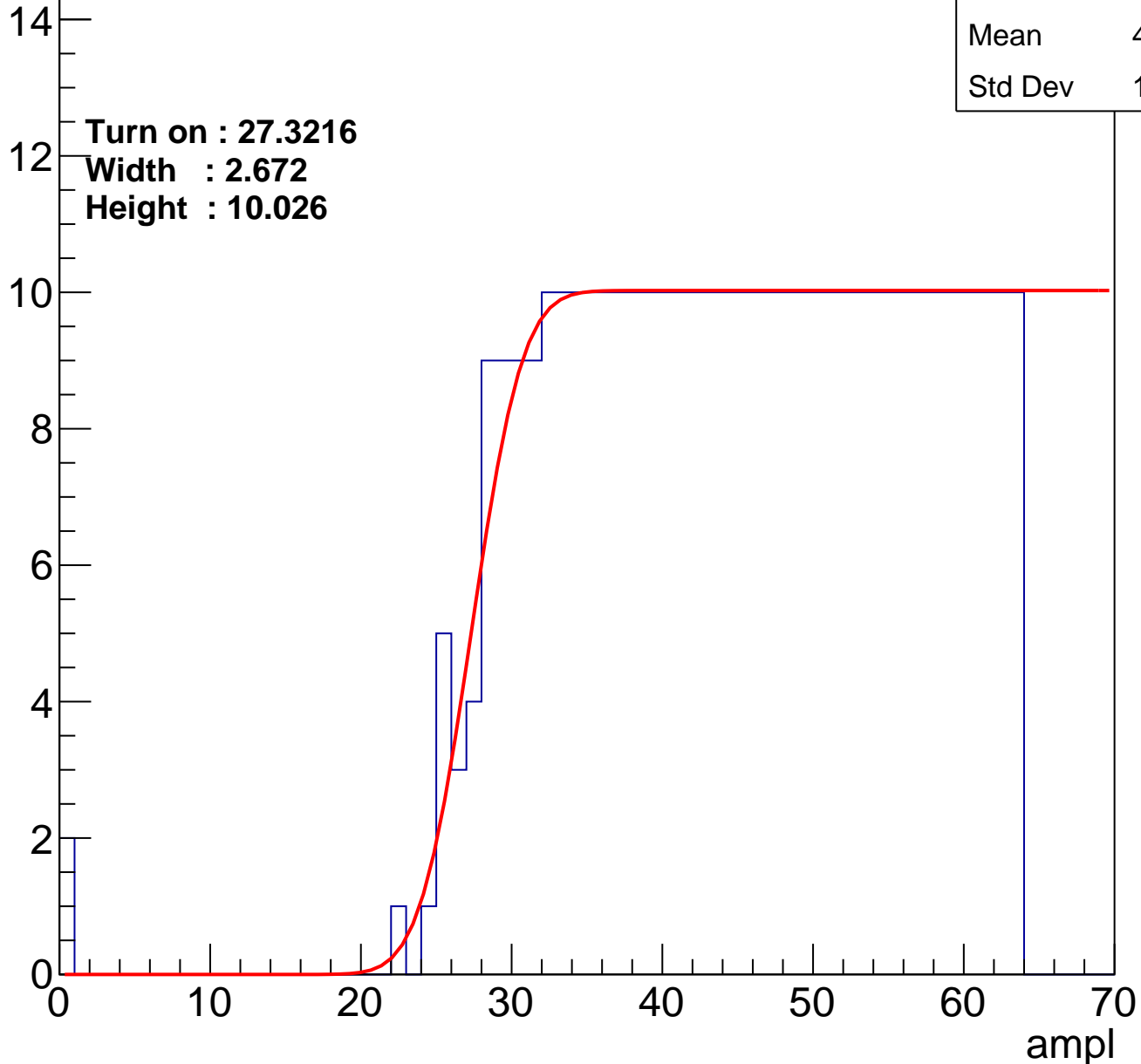
Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 27.3216

Width : 2.672

Height : 10.026

Entry



# B1L002S, U6-ch8

calib\_packv5\_042523\_0143.root, FC#10, port B3

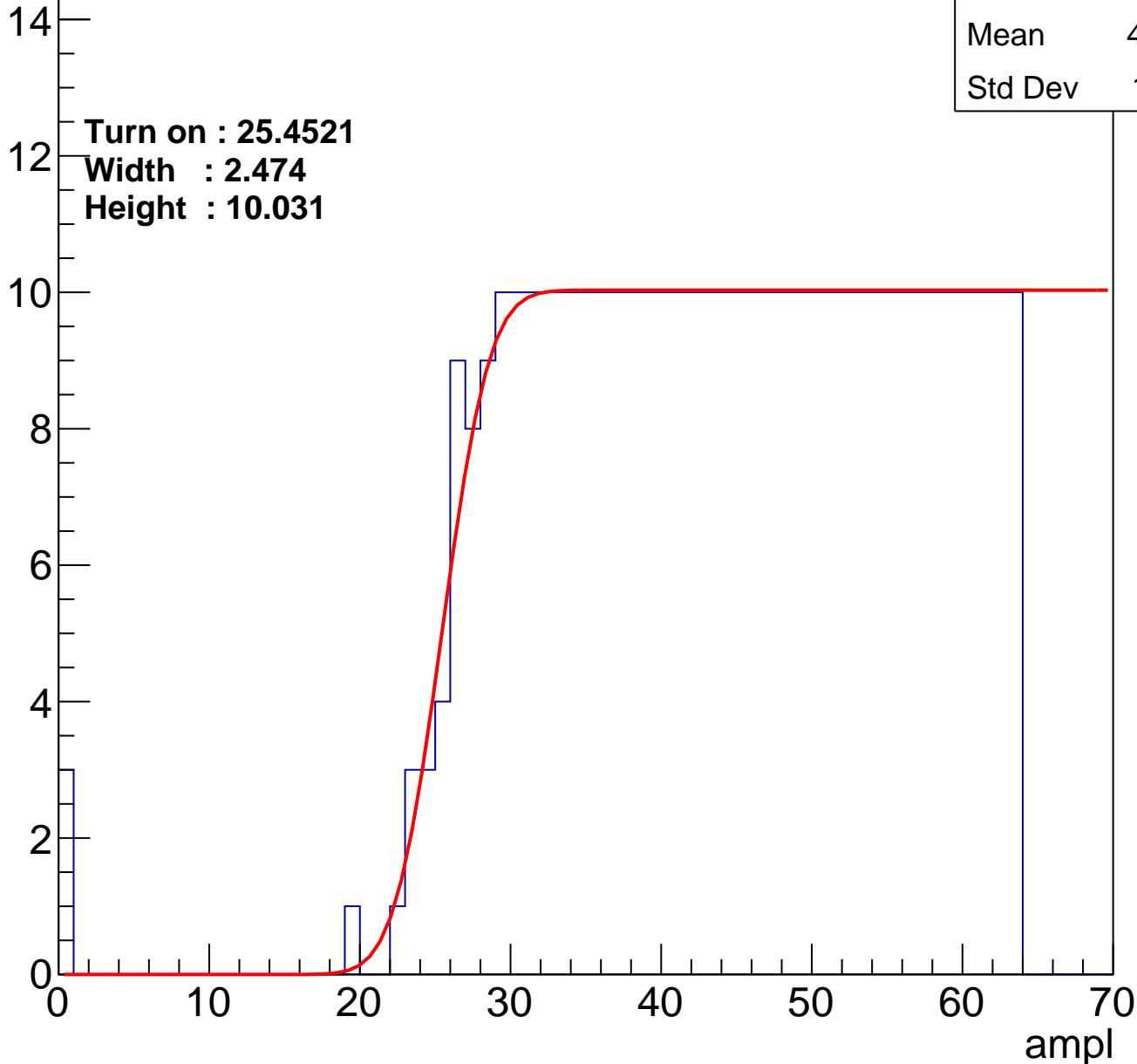
Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.4521

Width : 2.474

Height : 10.031

Entry



# B1L002S, U6-ch9

calib\_packv5\_042523\_0143.root, FC#10, port B3

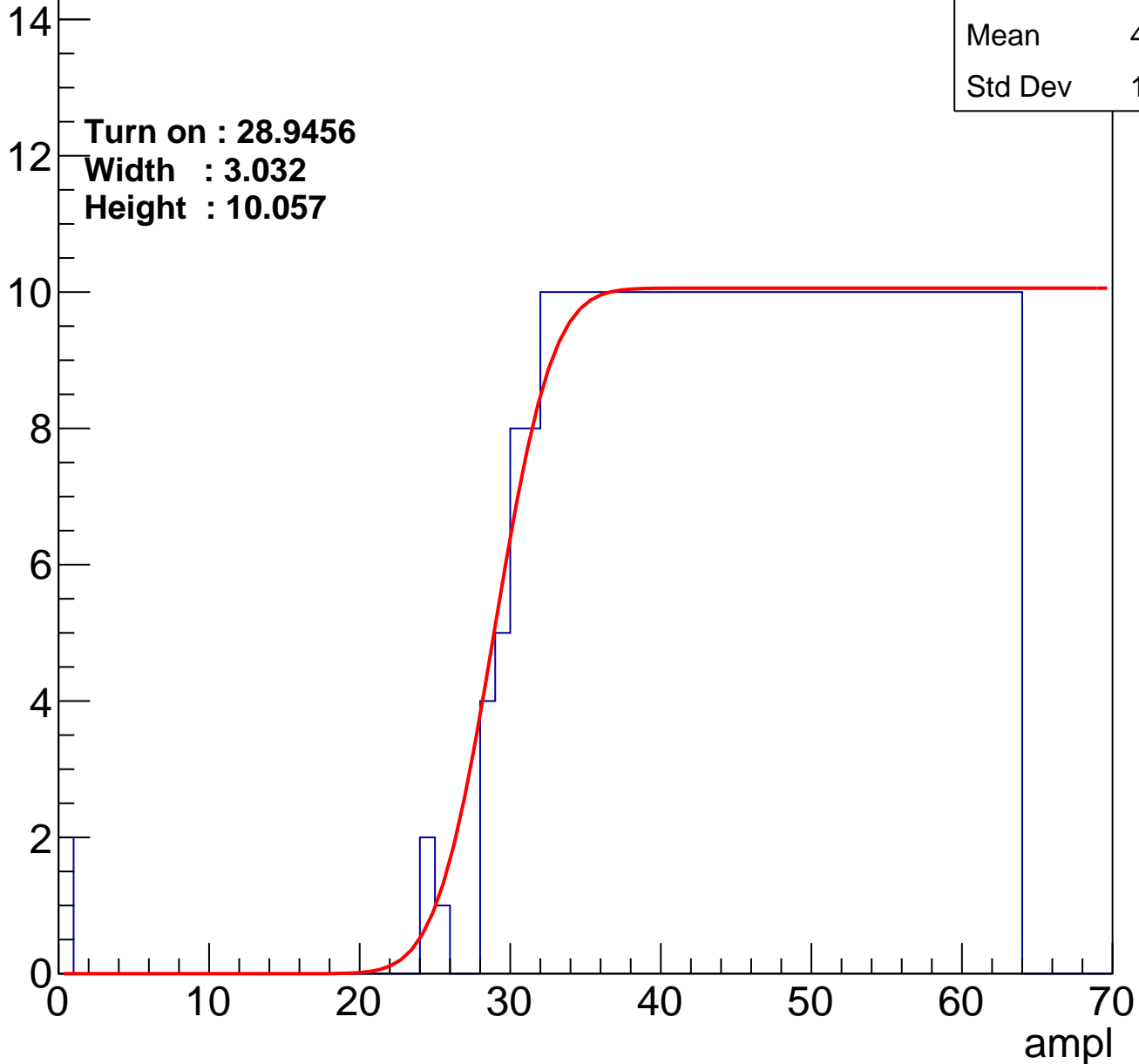
Entries	350
Mean	45.77
Std Dev	10.72

Turn on : 28.9456

Width : 3.032

Height : 10.057

Entry



# B1L002S, U6-ch10

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	399
Mean	43.15
Std Dev	12.46

Turn on : 24.8629

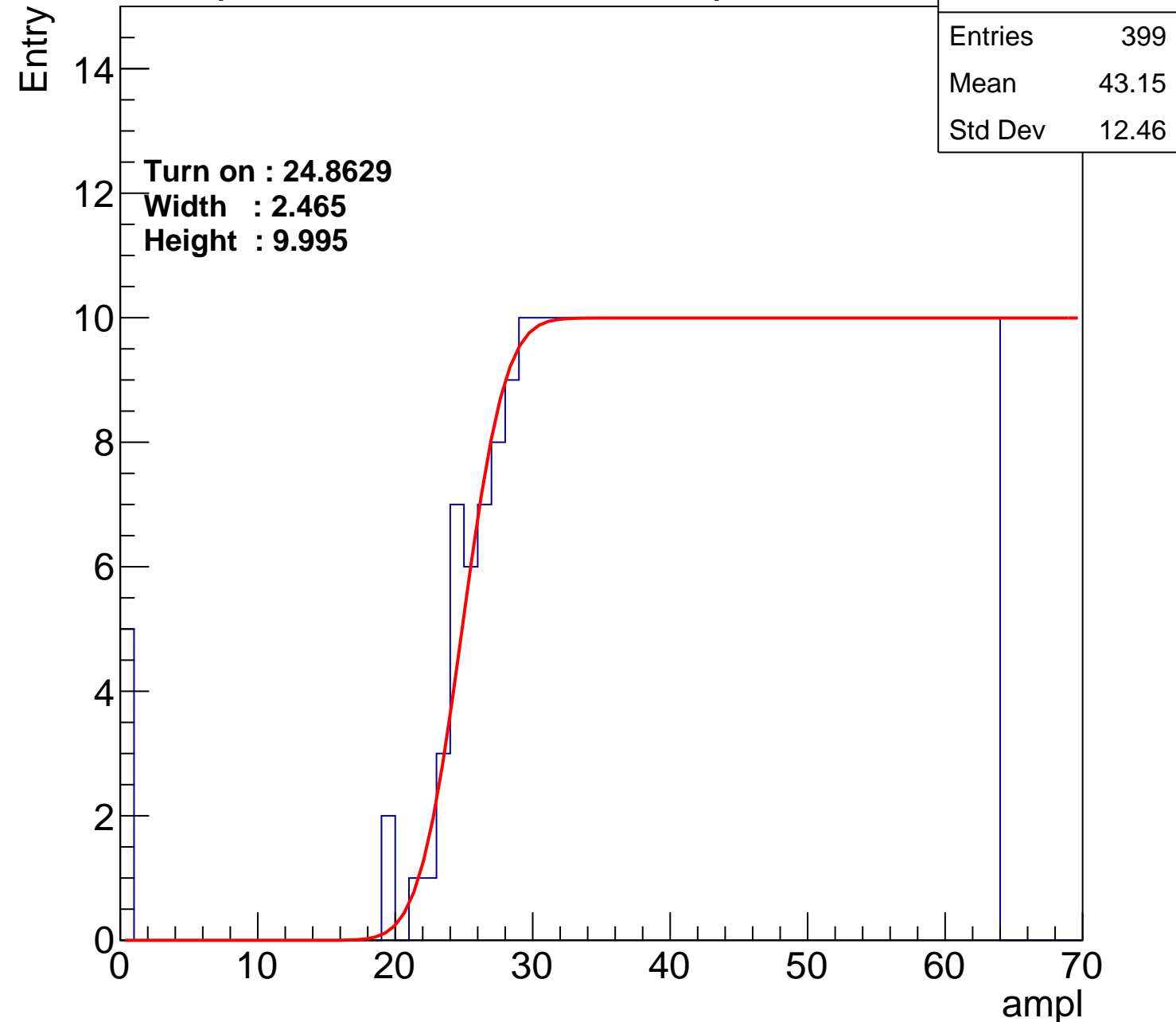
Width : 2.465

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch11

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 25.3695

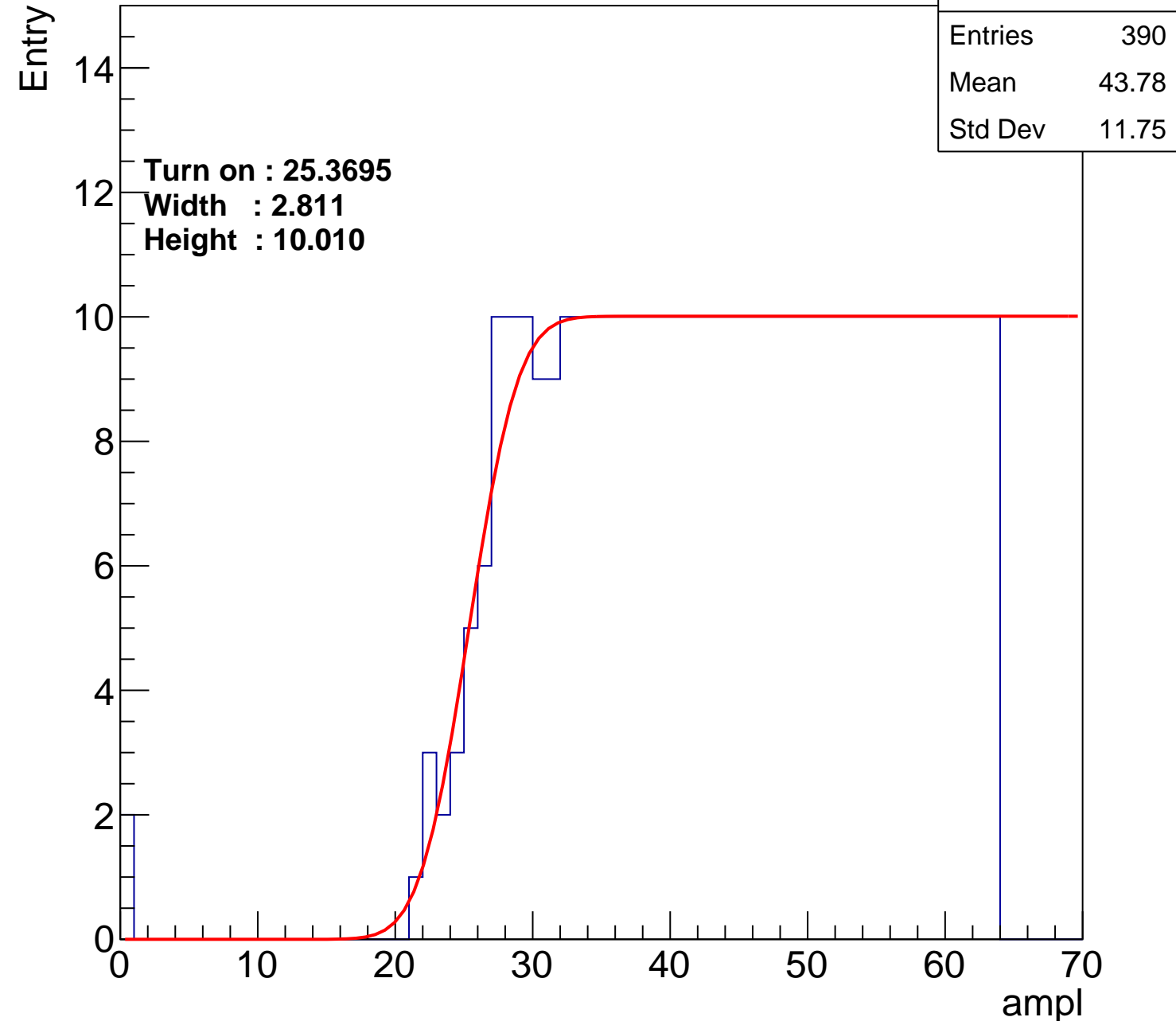
Width : 2.811

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch12

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 25.7563

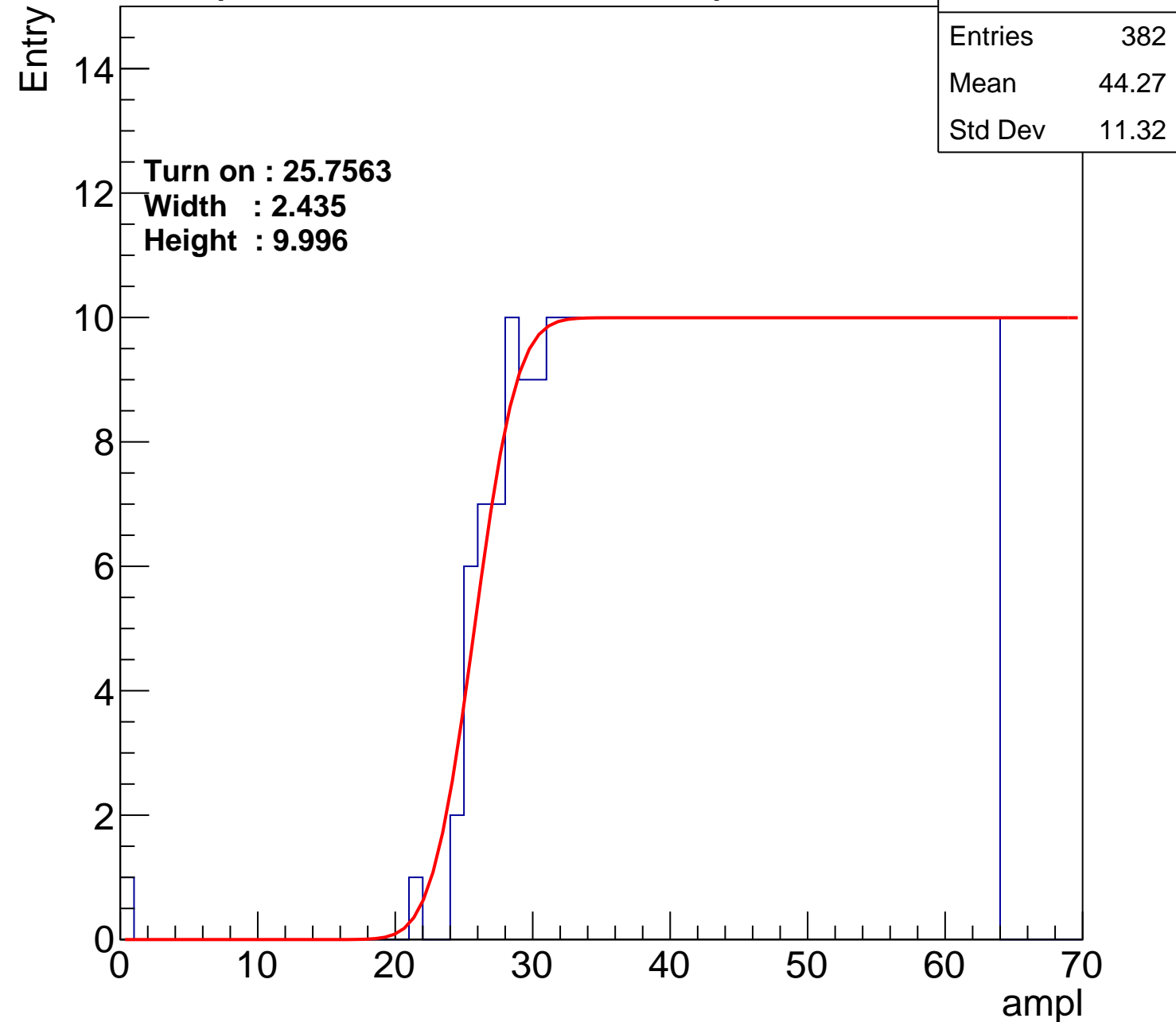
Width : 2.435

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch13

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.4894

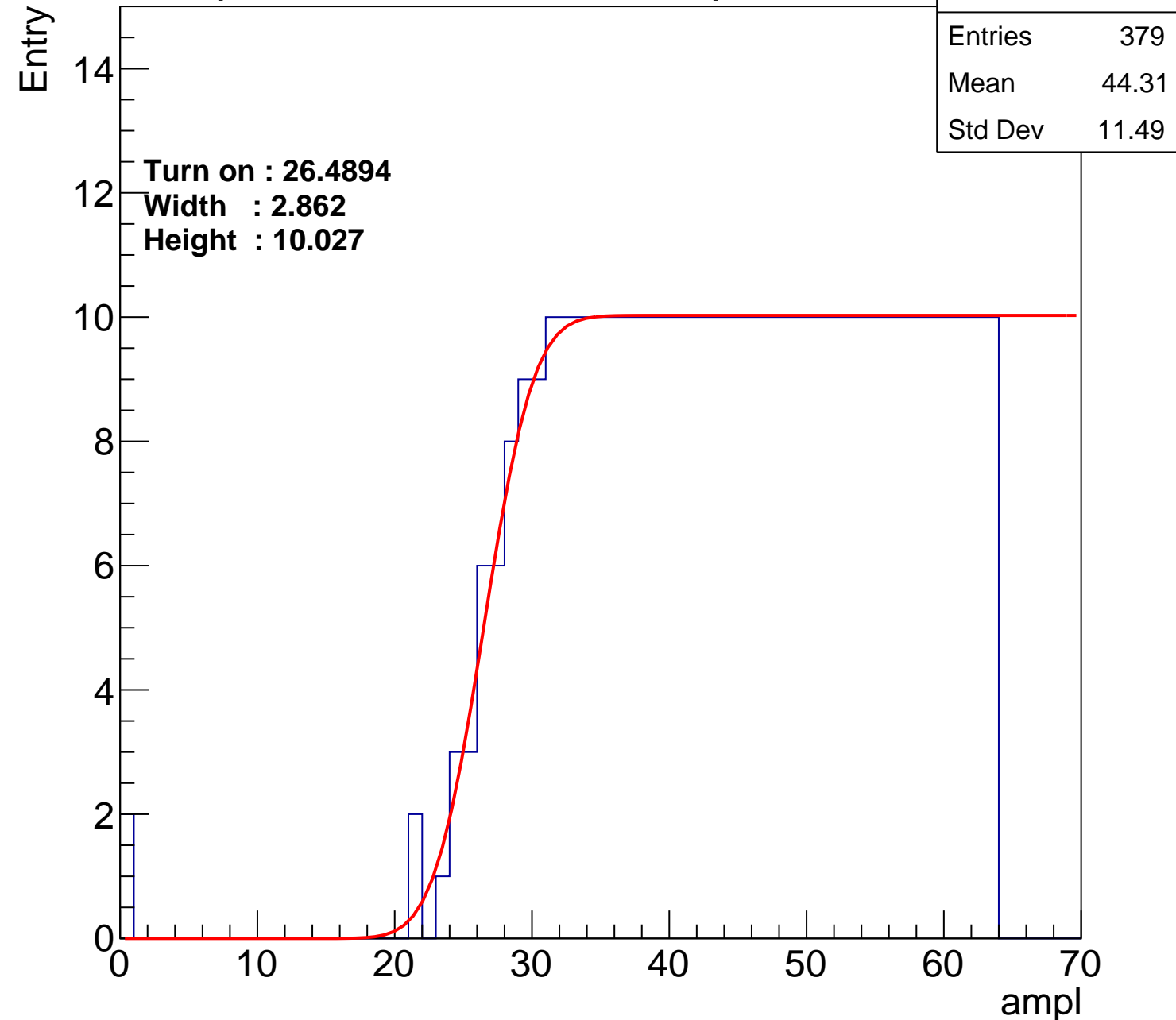
Width : 2.862

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch14

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.83
Std Dev	11.6

Turn on : 25.1977

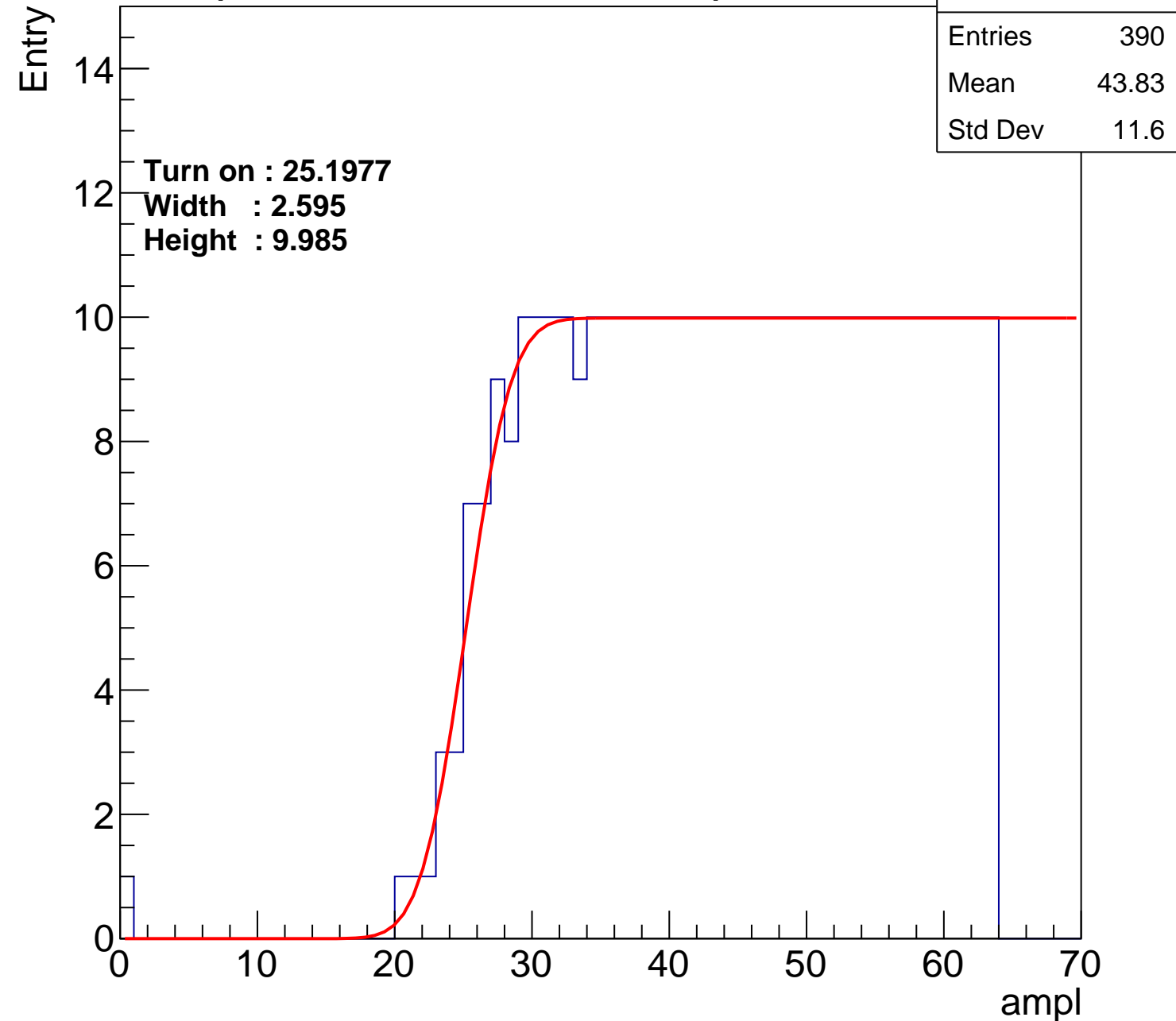
Width : 2.595

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch15

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.57

Turn on : 24.9387

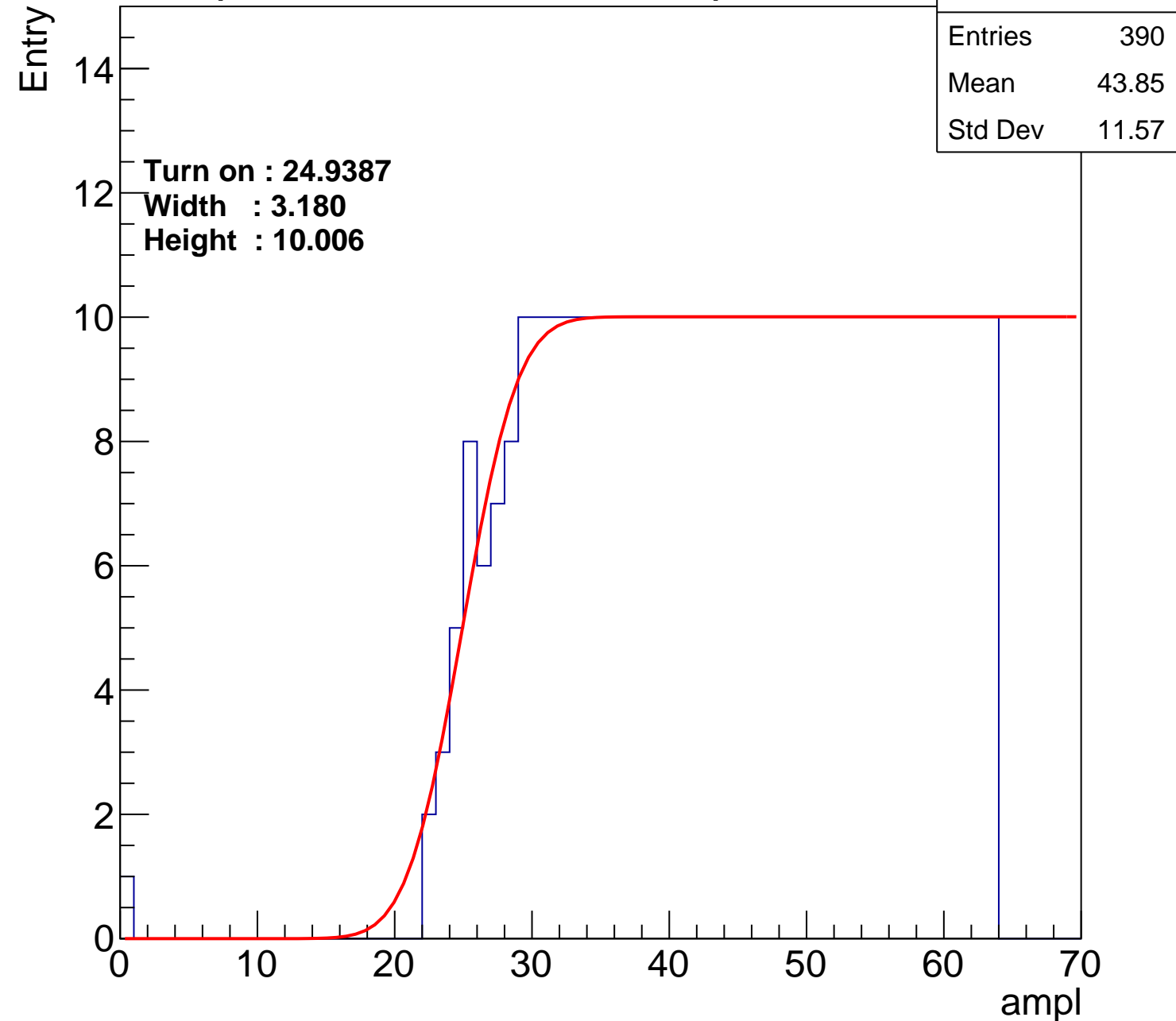
Width : 3.180

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch16

calib\_packv5\_042523\_0143.root, FC#10, port B3

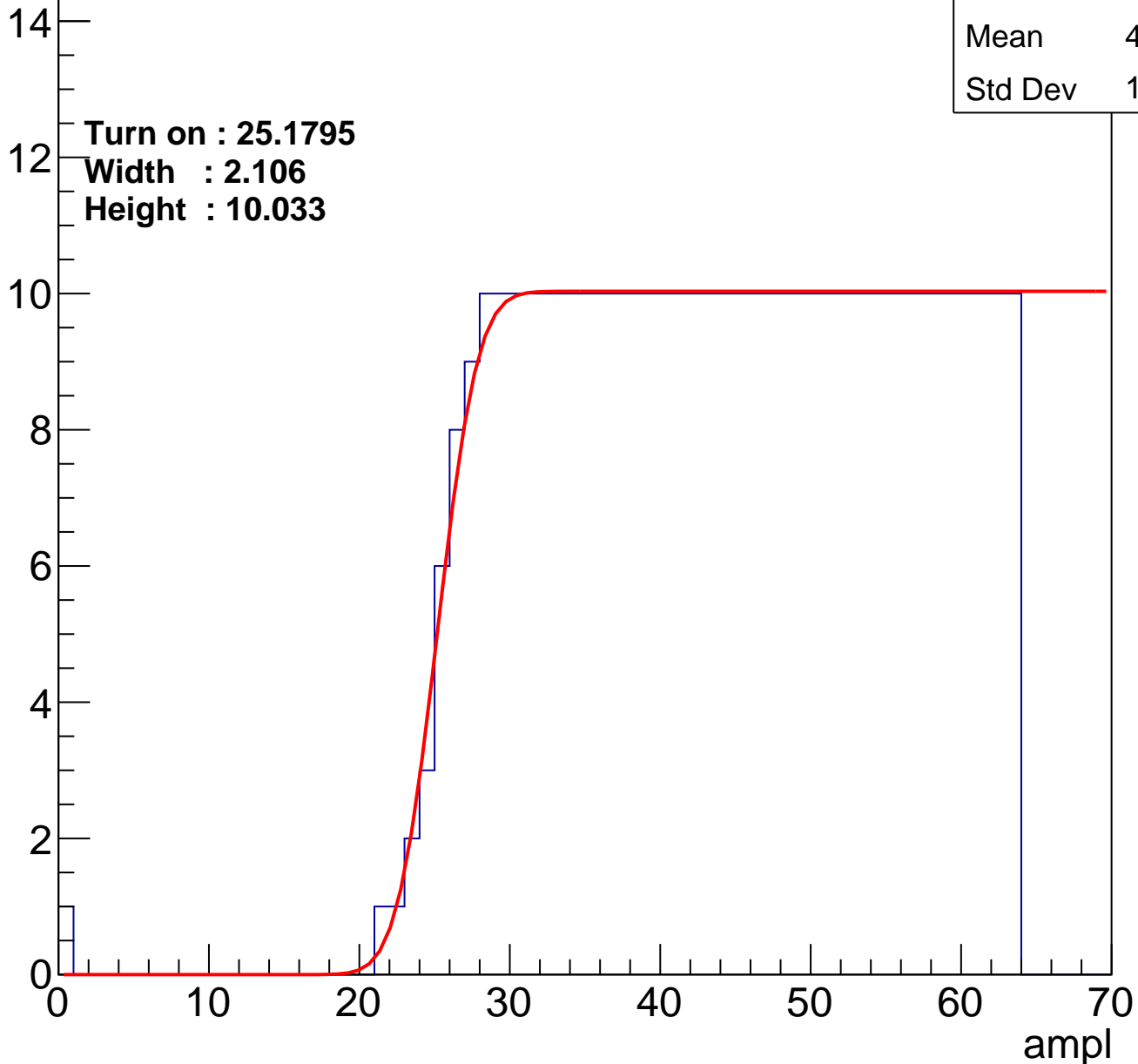
Entries	391
Mean	43.84
Std Dev	11.54

Turn on : 25.1795

Width : 2.106

Height : 10.033

Entry



# B1L002S, U6-ch17

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.48
Std Dev	11.81

Turn on : 25.2240

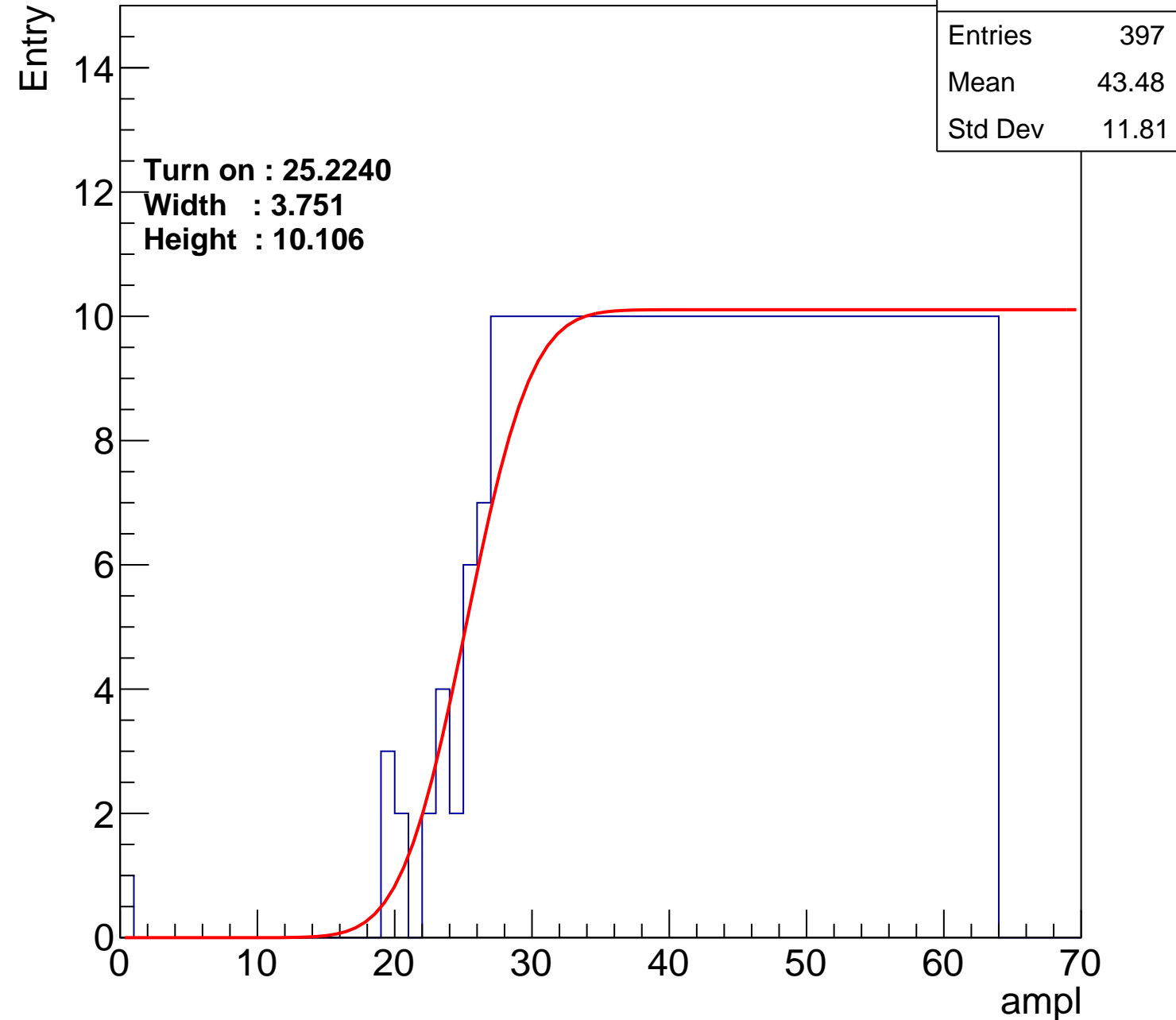
Width : 3.751

Height : 10.106

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch18

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.57
Std Dev	11.48

**Turn on : 26.7815**

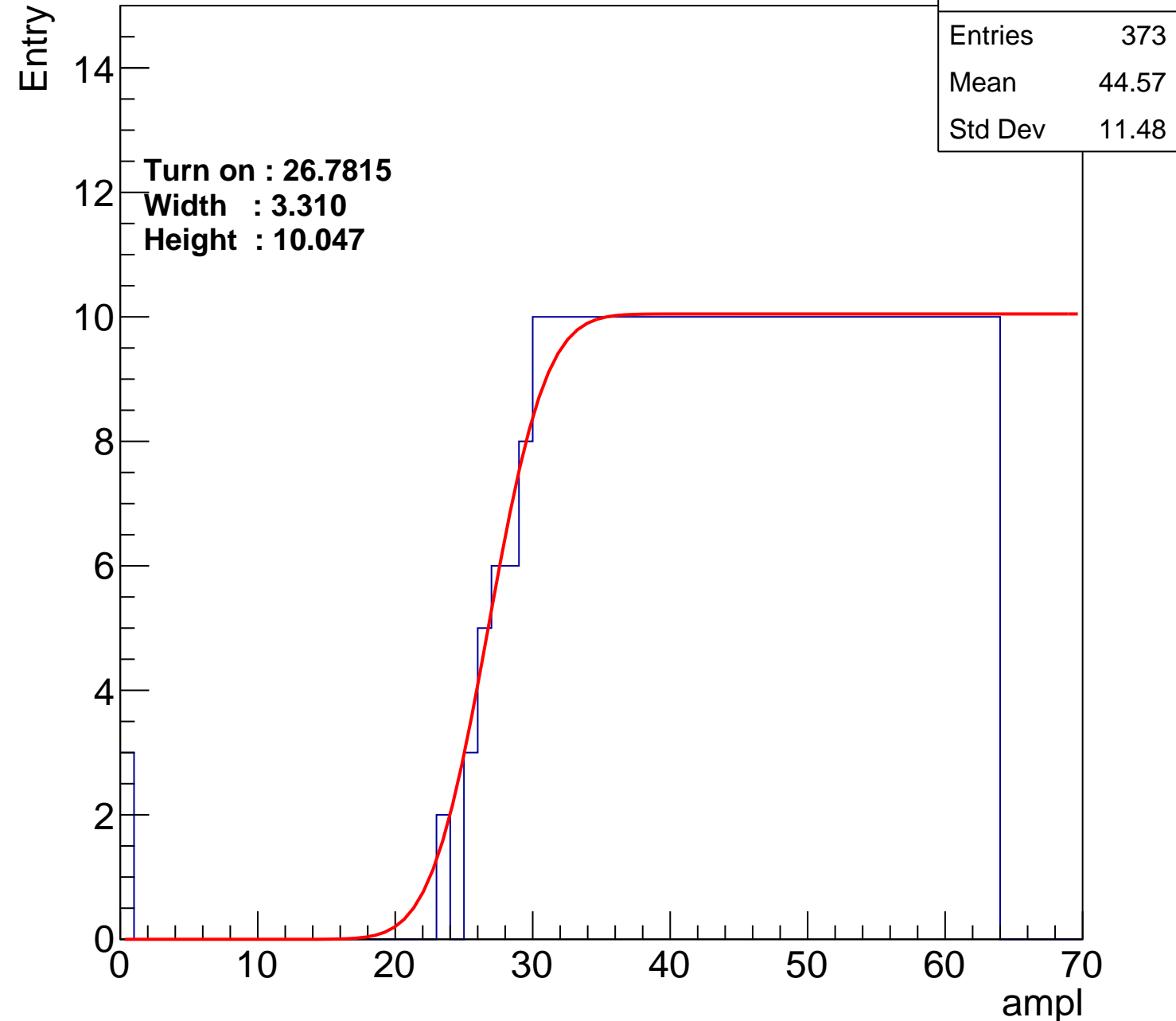
**Width : 3.310**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch19

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.49
Std Dev	12.25

Turn on : 25.3980

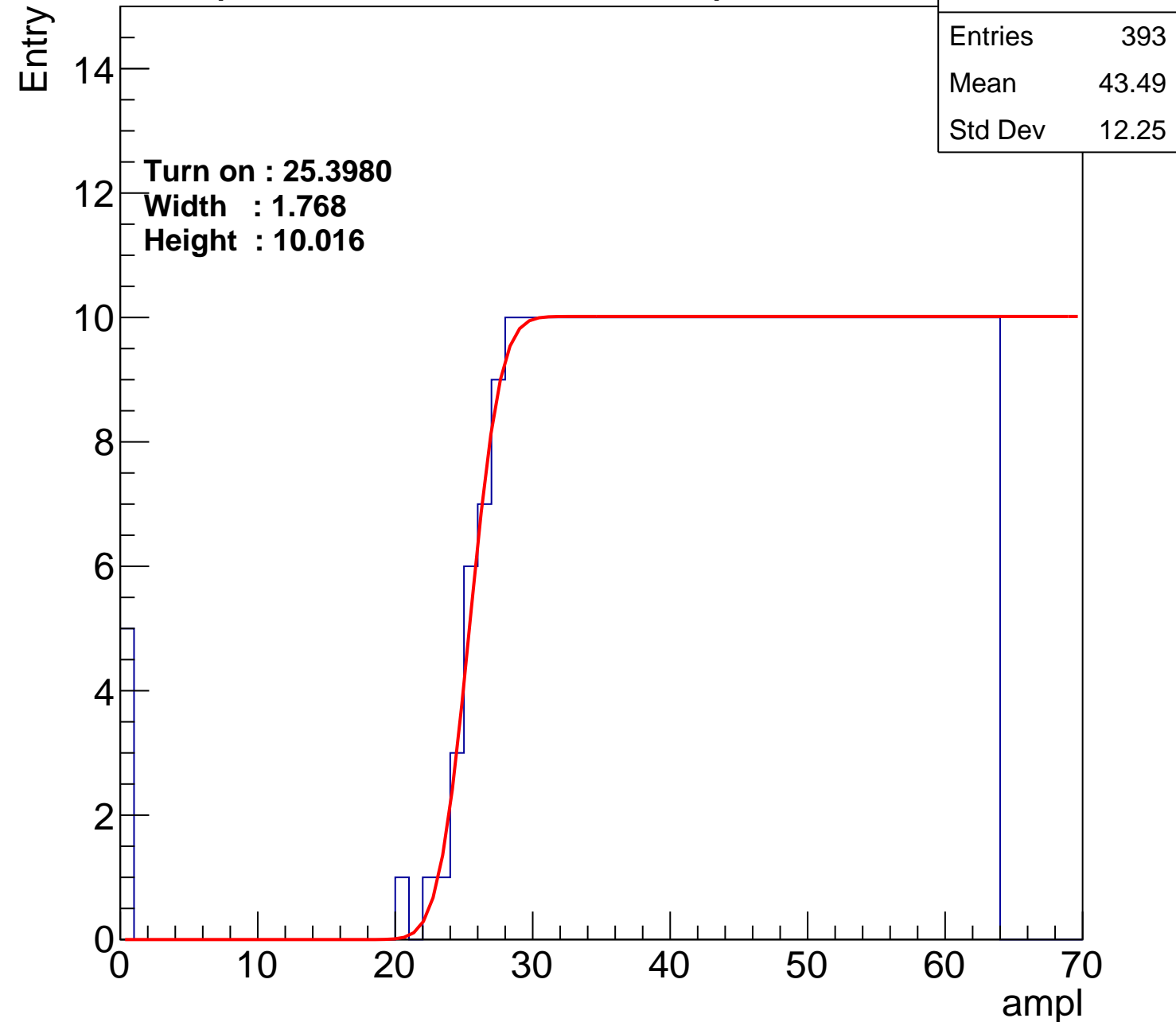
Width : 1.768

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch20

calib\_packv5\_042523\_0143.root, FC#10, port B3

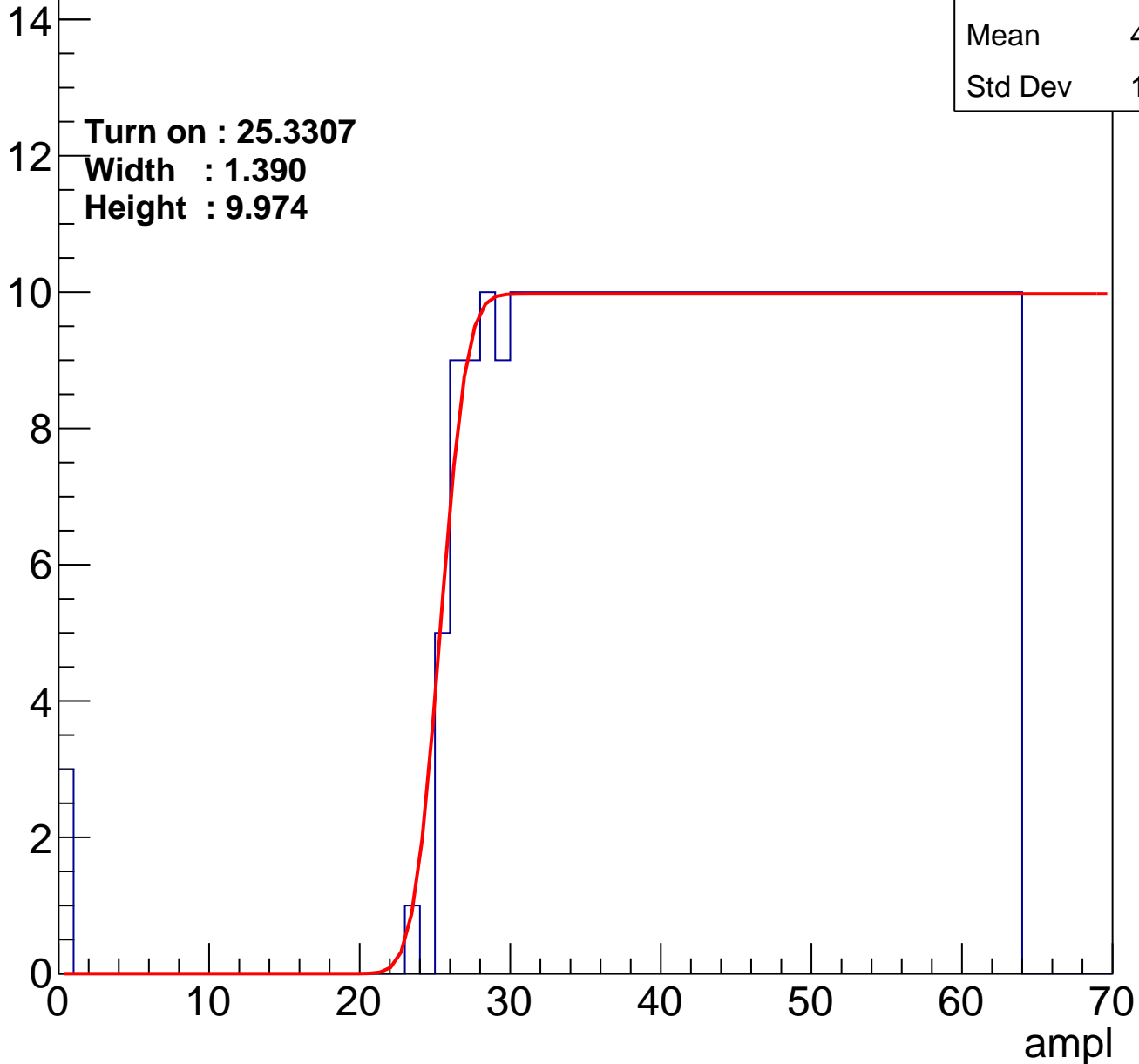
Entries	386
Mean	43.98
Std Dev	11.72

Turn on : 25.3307

Width : 1.390

Height : 9.974

Entry



# B1L002S, U6-ch21

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.14
Std Dev	11.37

Turn on : 25.7410

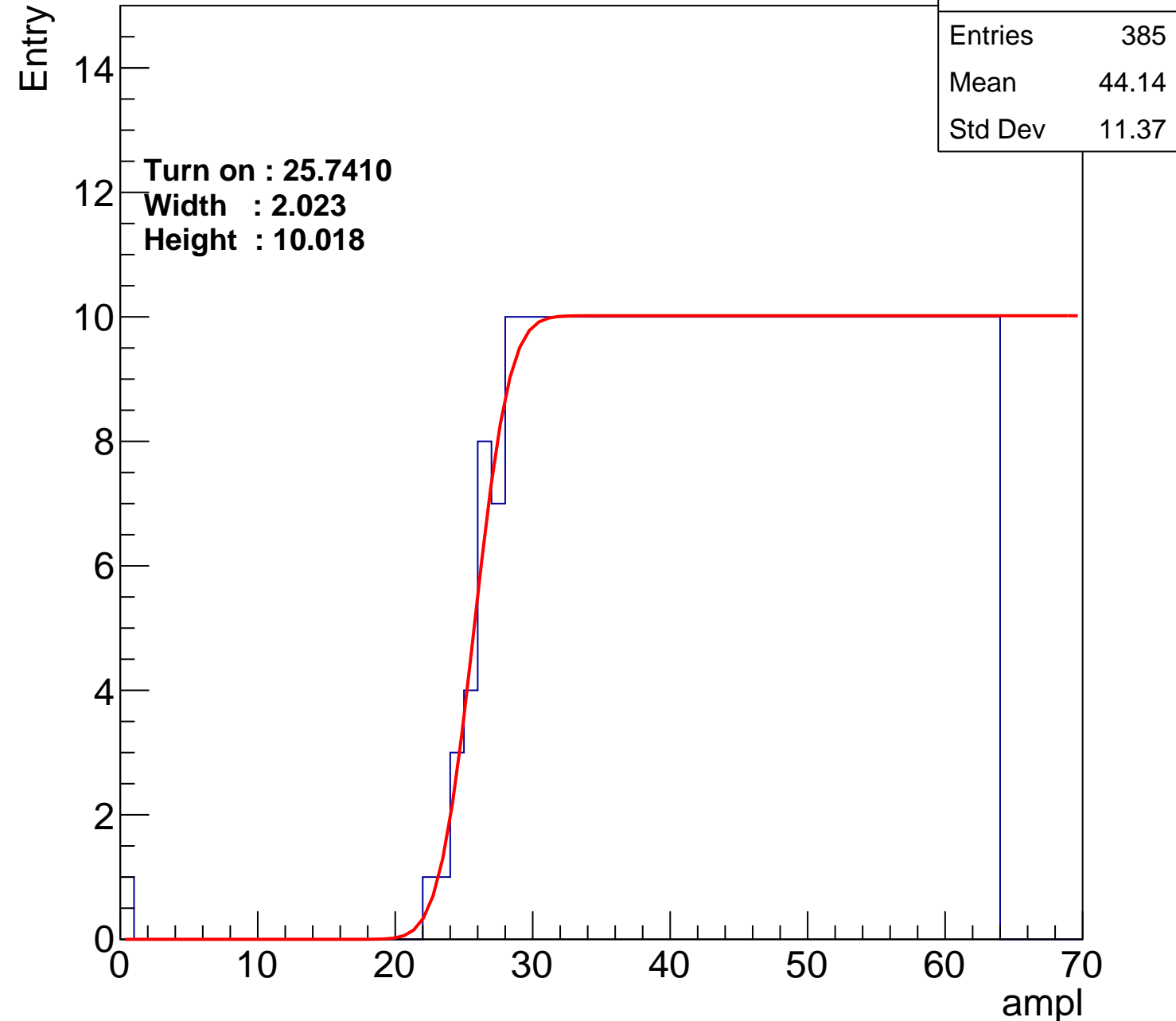
Width : 2.023

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch22

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.77
Std Dev	11.05

Turn on : 26.9025

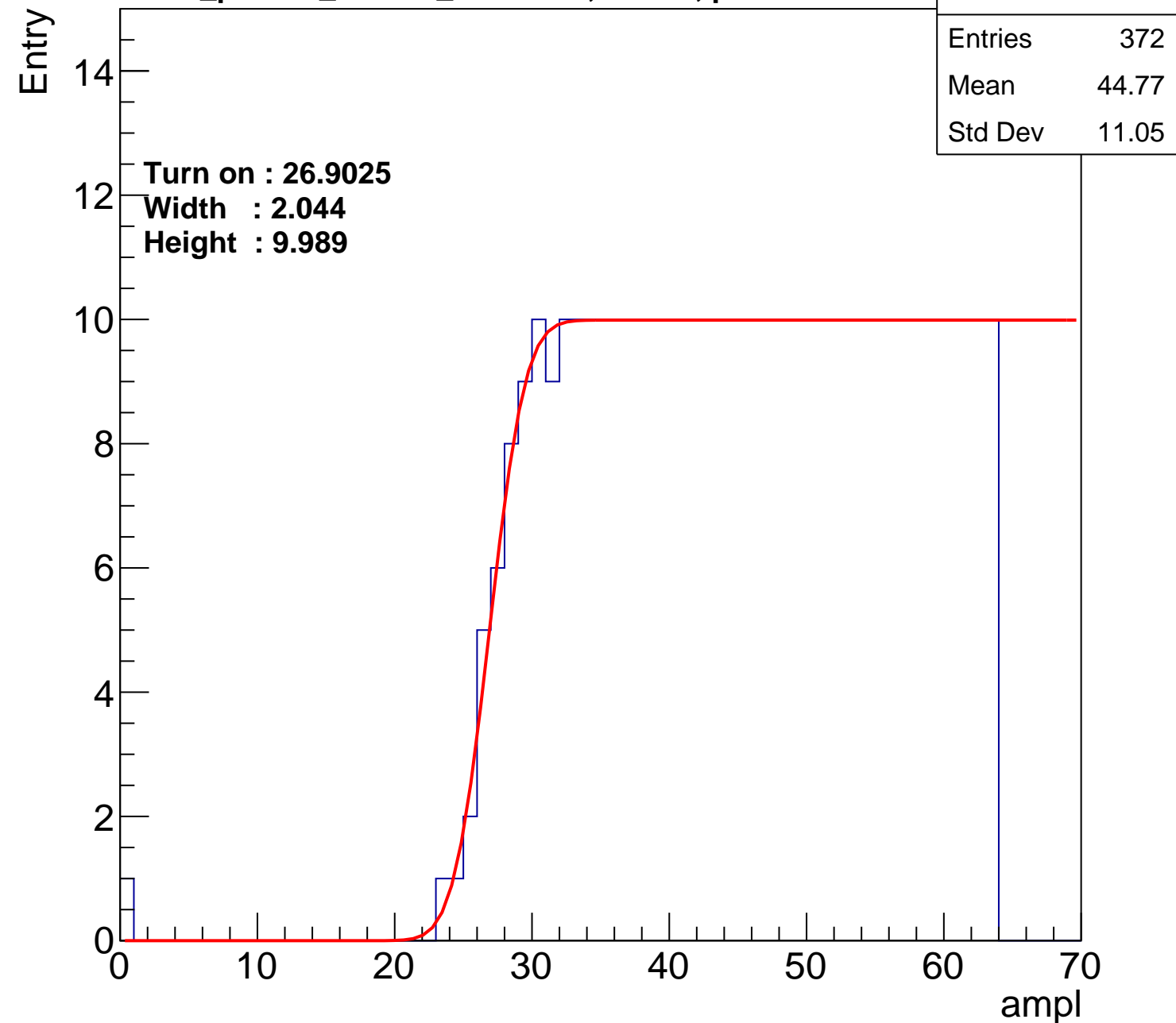
Width : 2.044

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch23

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	400
Mean	43.27
Std Dev	12.04

Turn on : 24.3412

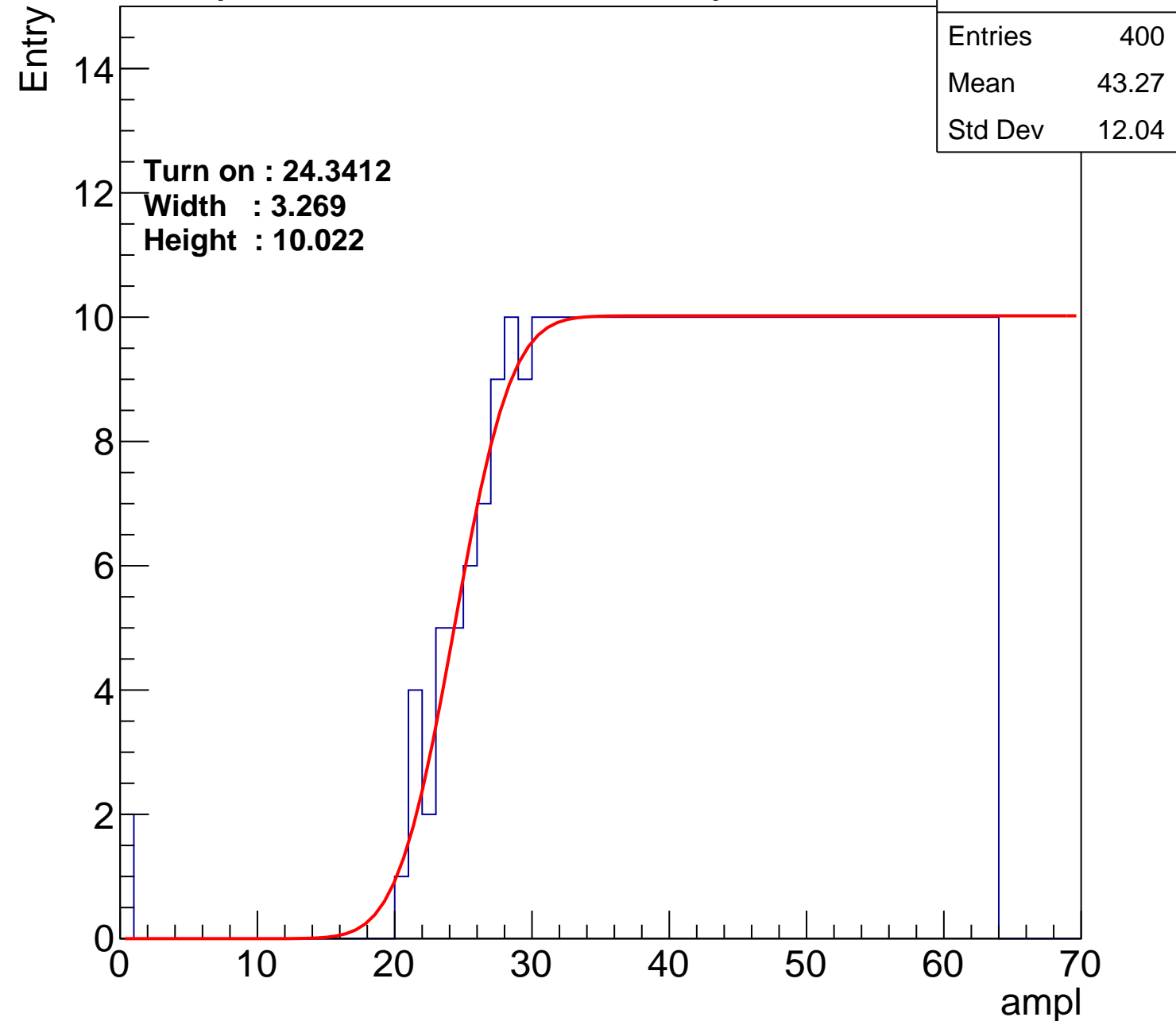
Width : 3.269

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch24

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	43.89
Std Dev	11.98

Turn on : 25.9912

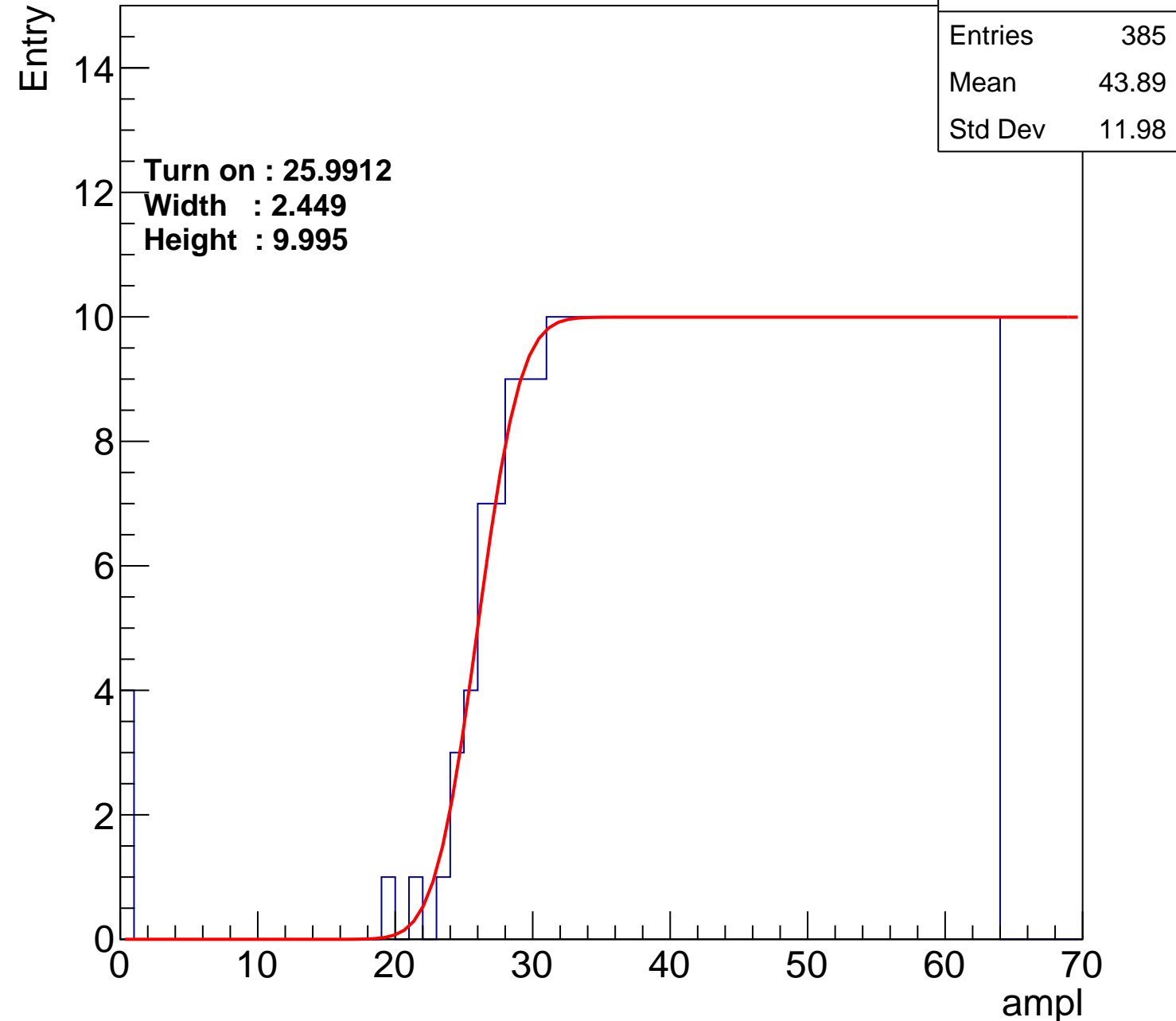
Width : 2.449

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch25

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.1414

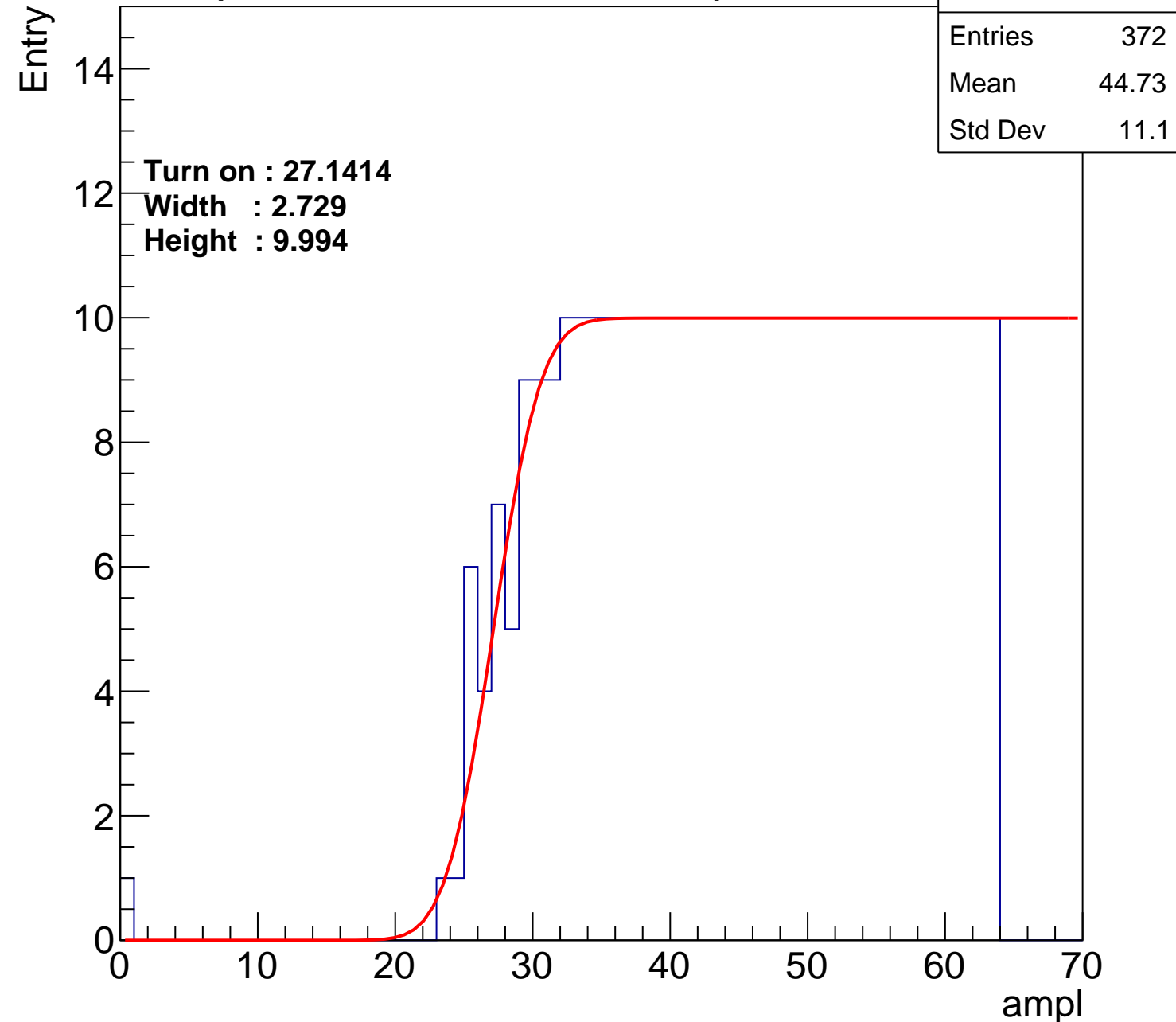
Width : 2.729

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch26

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.77
Std Dev	11.05

Turn on : 26.7658

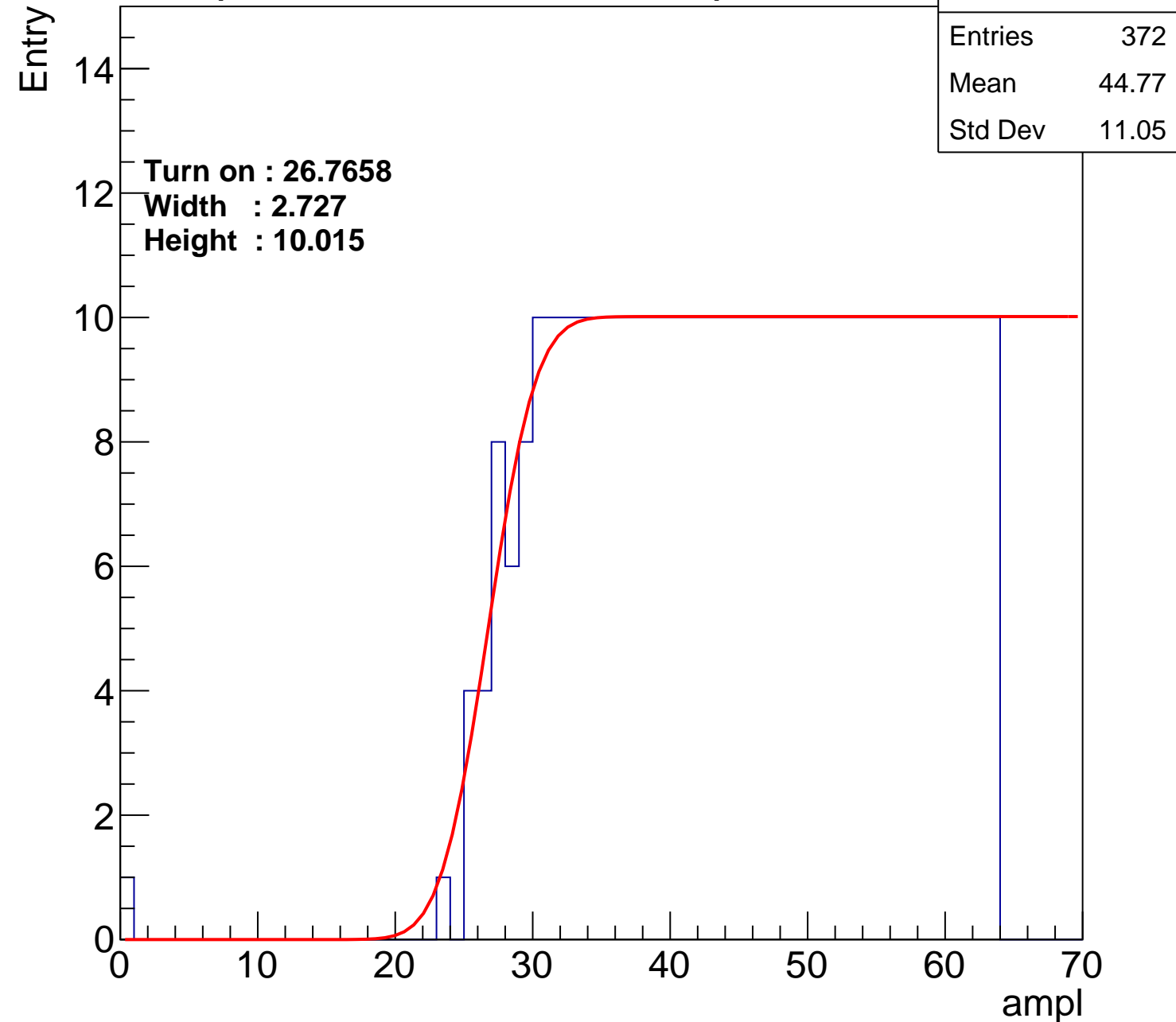
Width : 2.727

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch27

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 26.2323

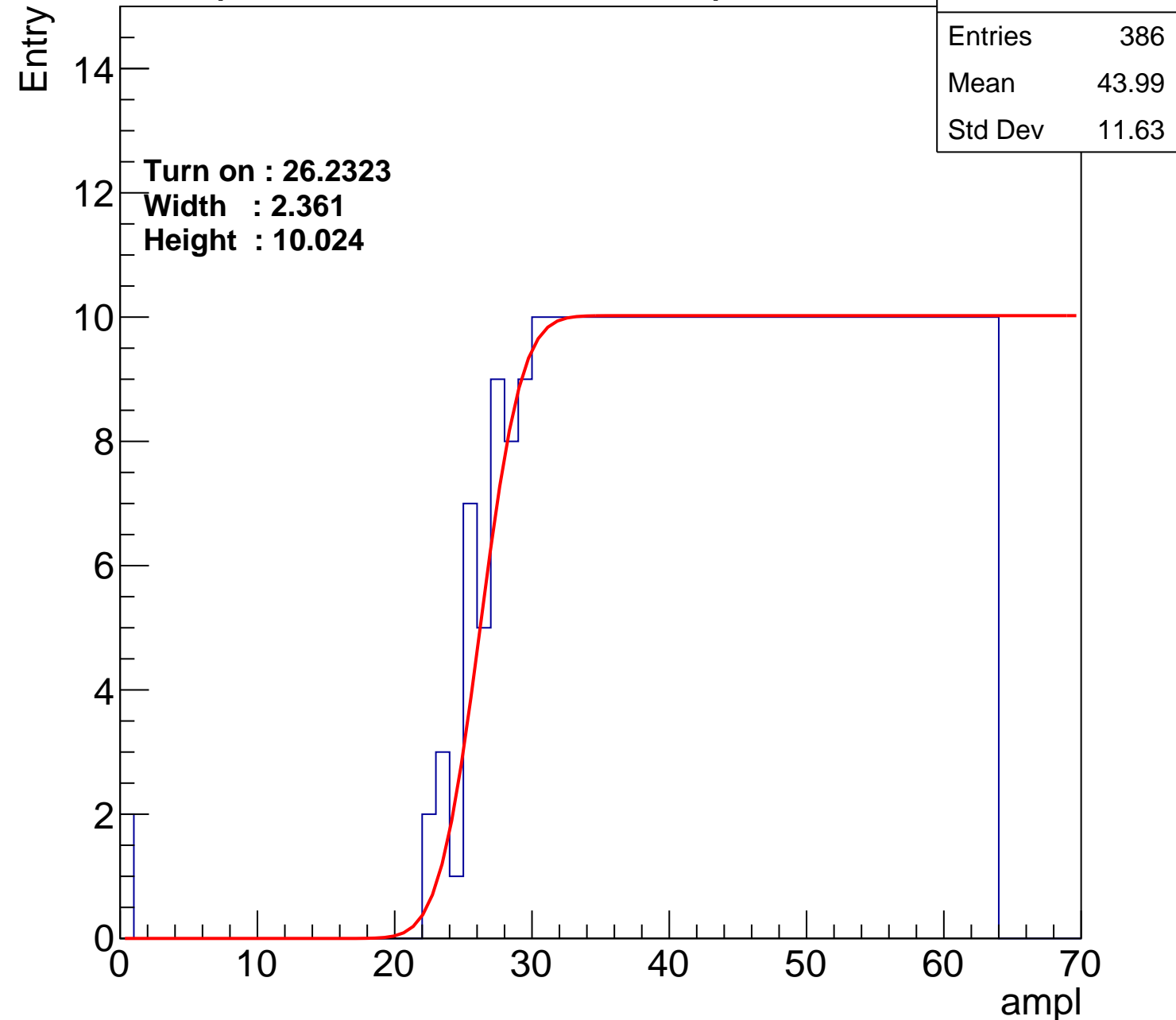
Width : 2.361

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch28

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.32
Std Dev	11.3

Turn on : 26.7746

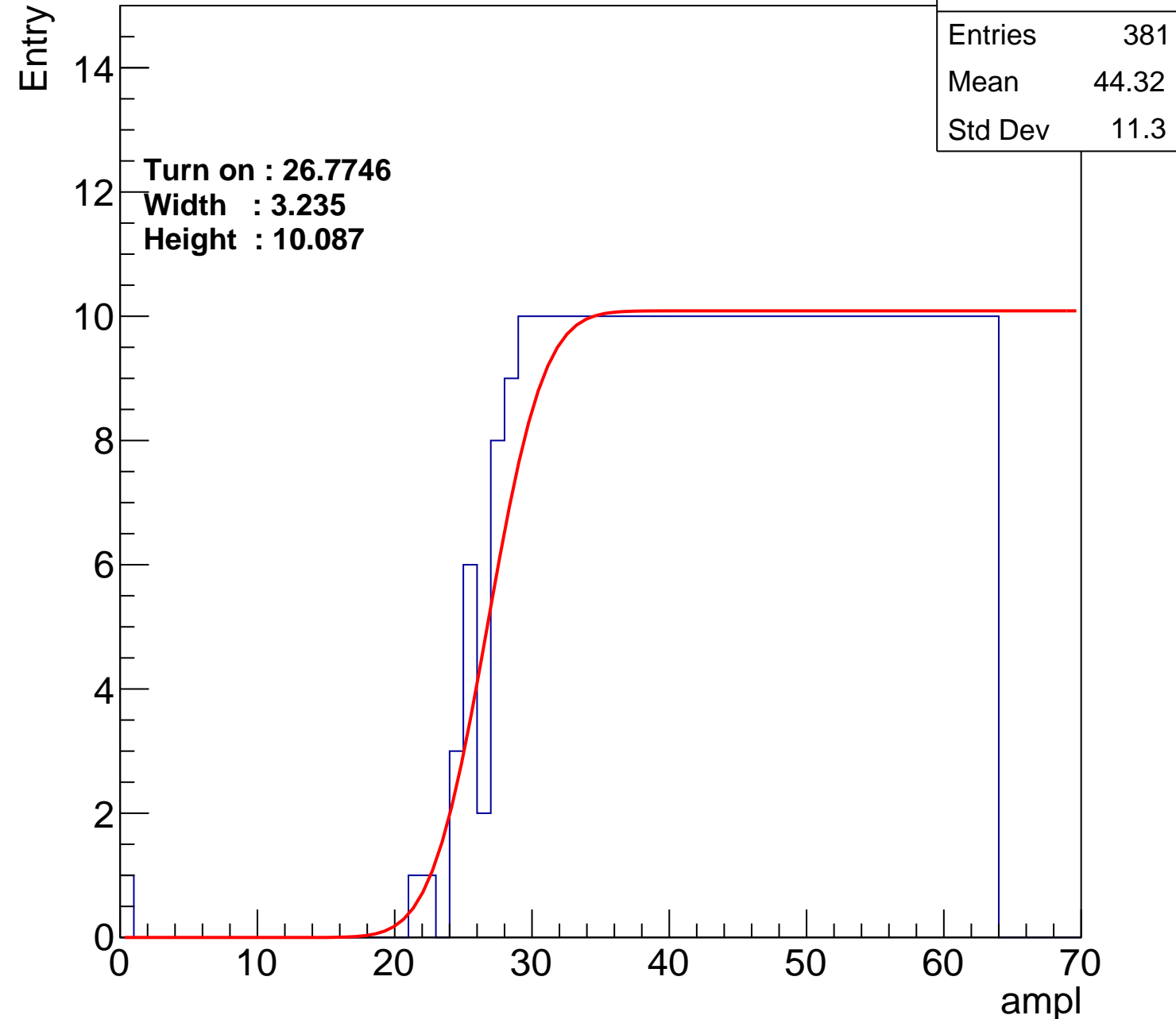
Width : 3.235

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch29

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 26.1319

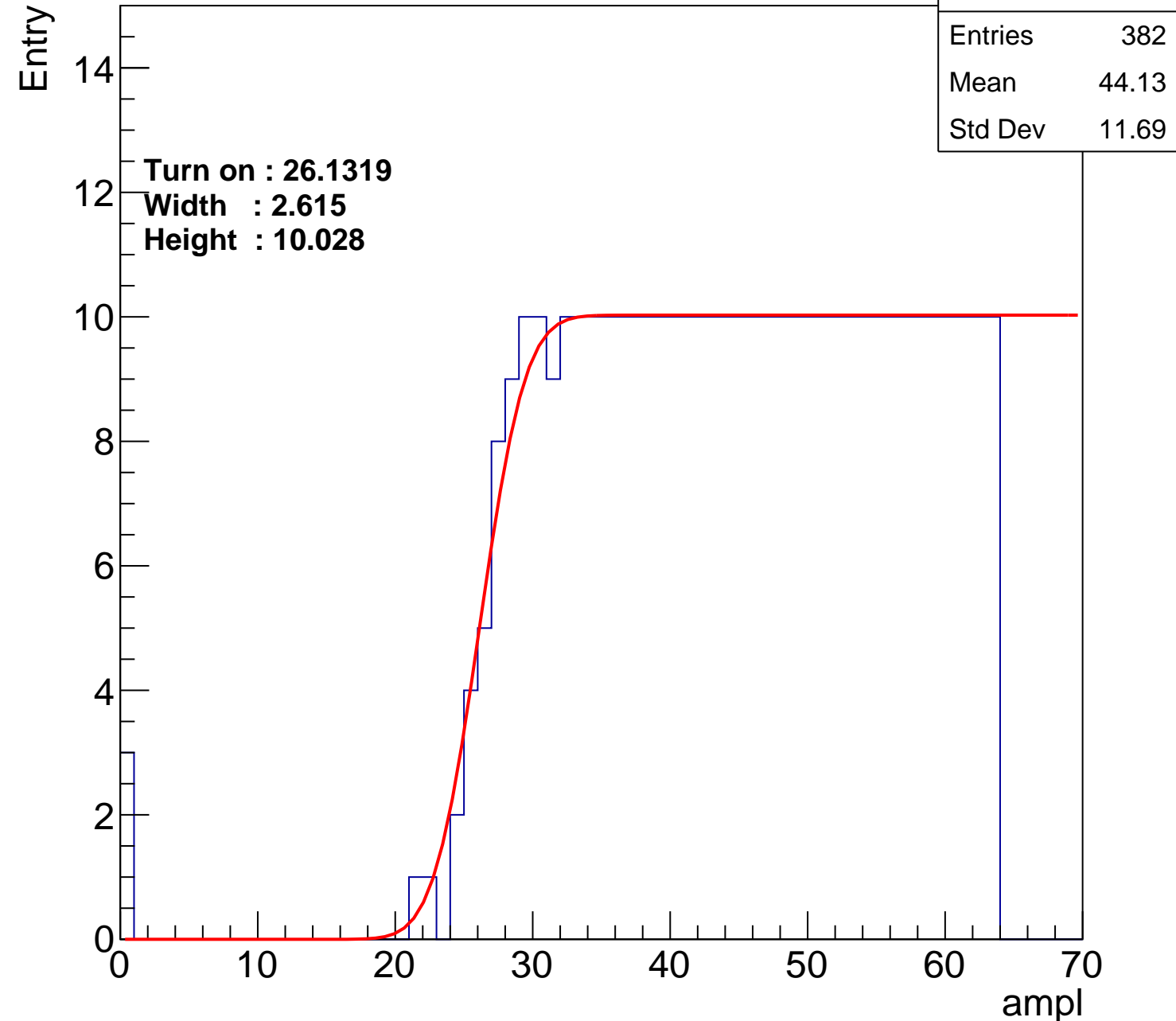
Width : 2.615

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch30

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.4502

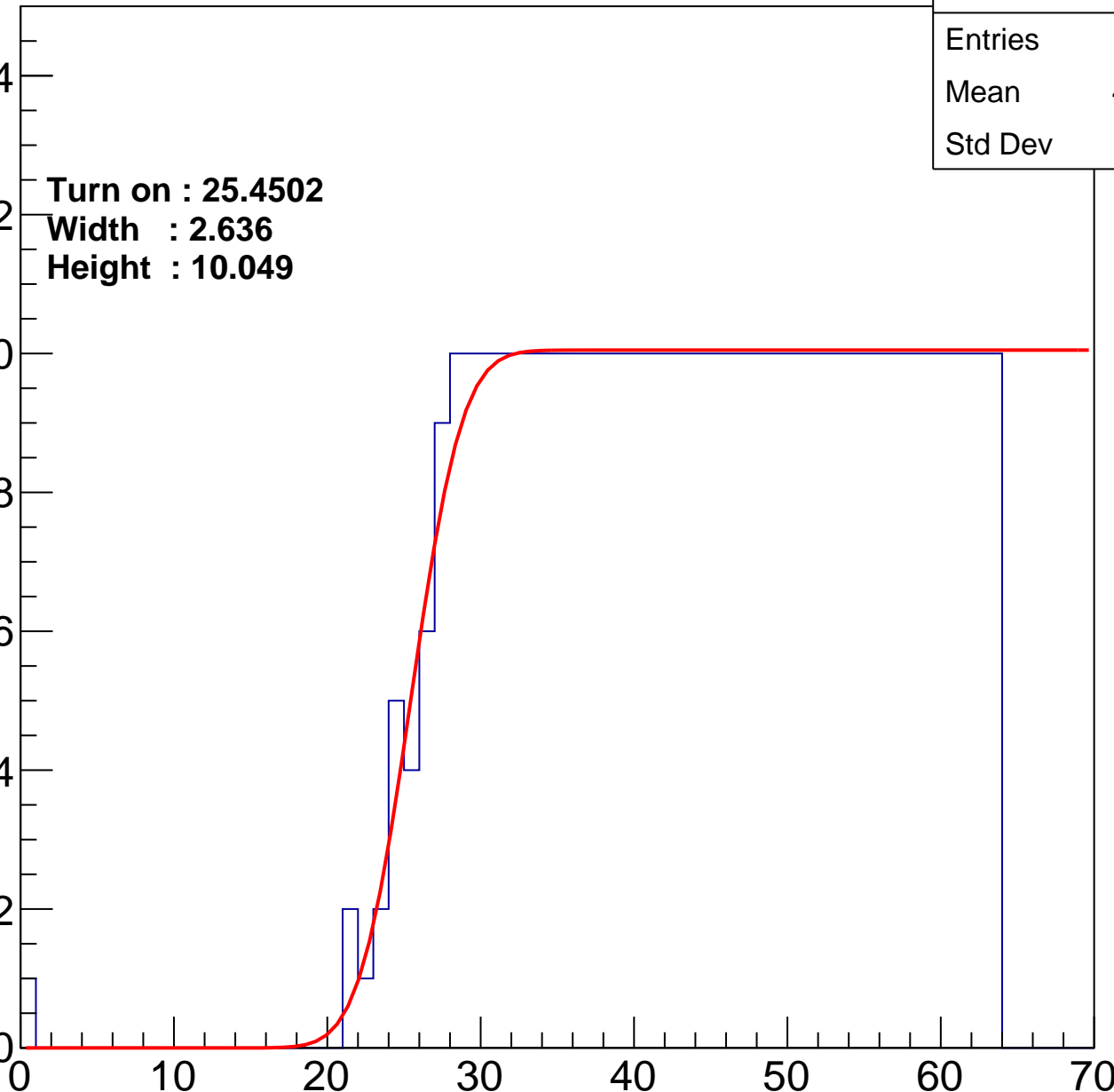
Width : 2.636

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch31

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.67
Std Dev	11.1

Turn on : 27.1261

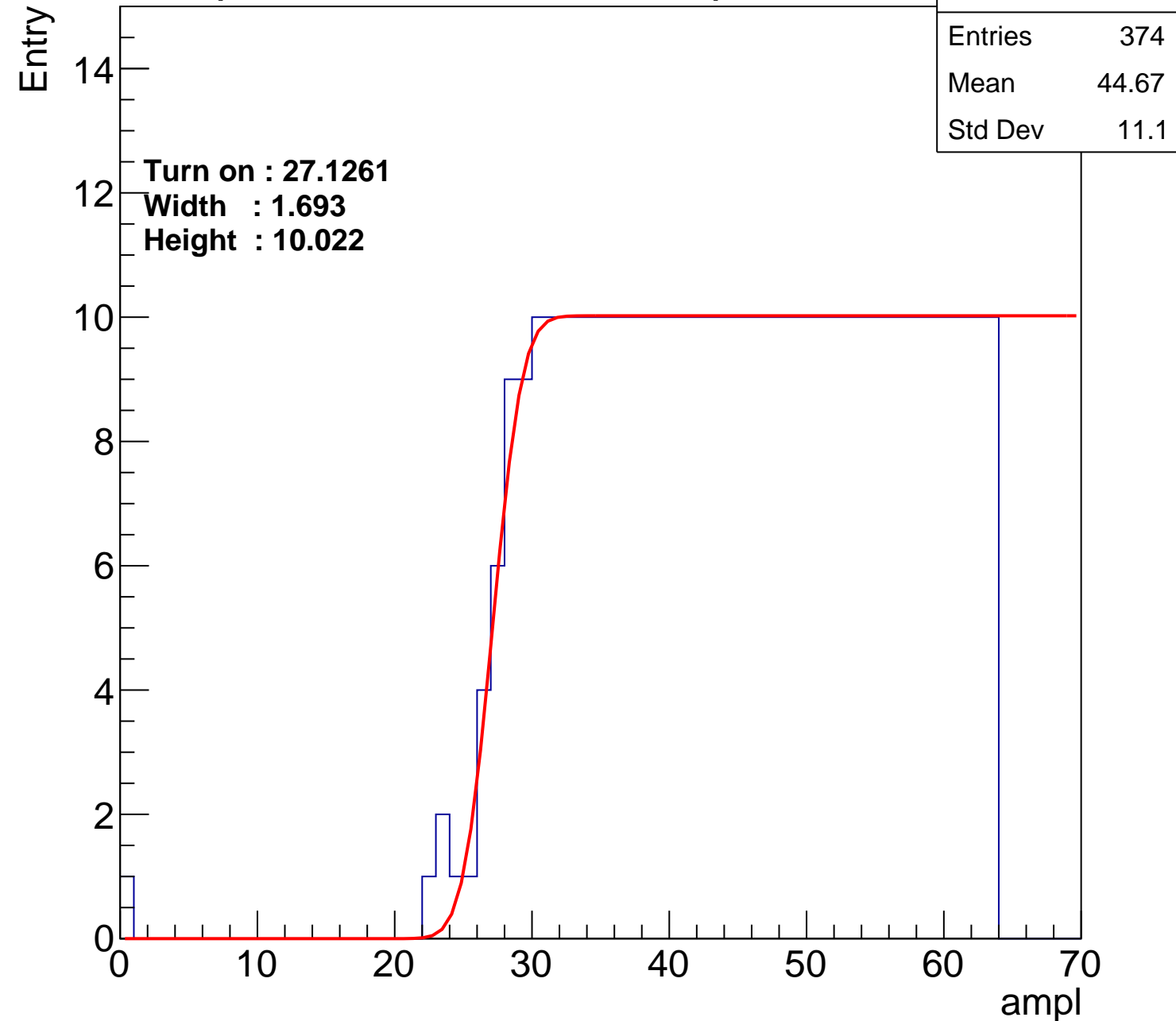
Width : 1.693

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch32

calib\_packv5\_042523\_0143.root, FC#10, port B3

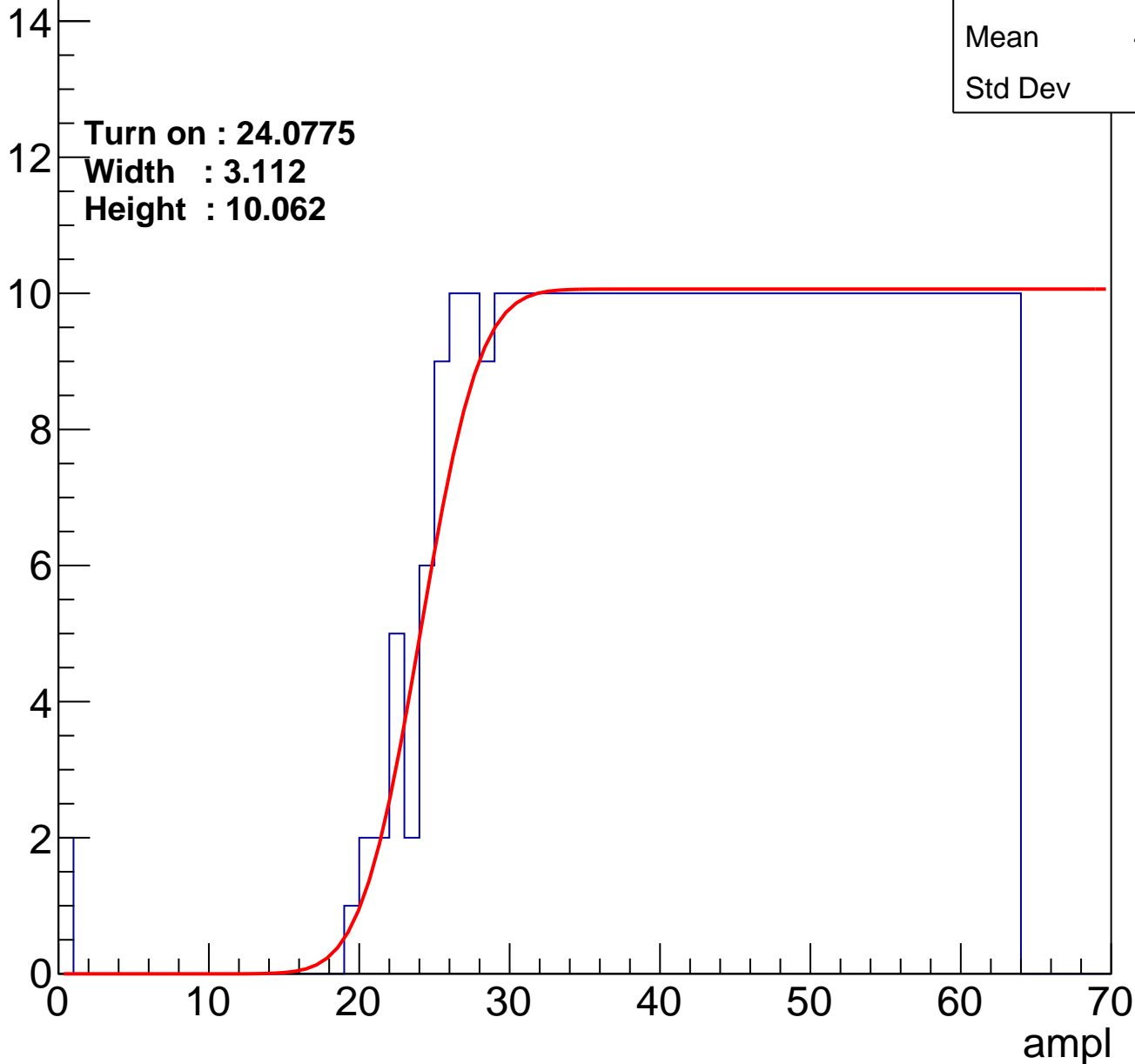
Entry

Entries	408
Mean	42.91
Std Dev	12.2

Turn on : 24.0775

Width : 3.112

Height : 10.062



# B1L002S, U6-ch33

calib\_packv5\_042523\_0143.root, FC#10, port B3

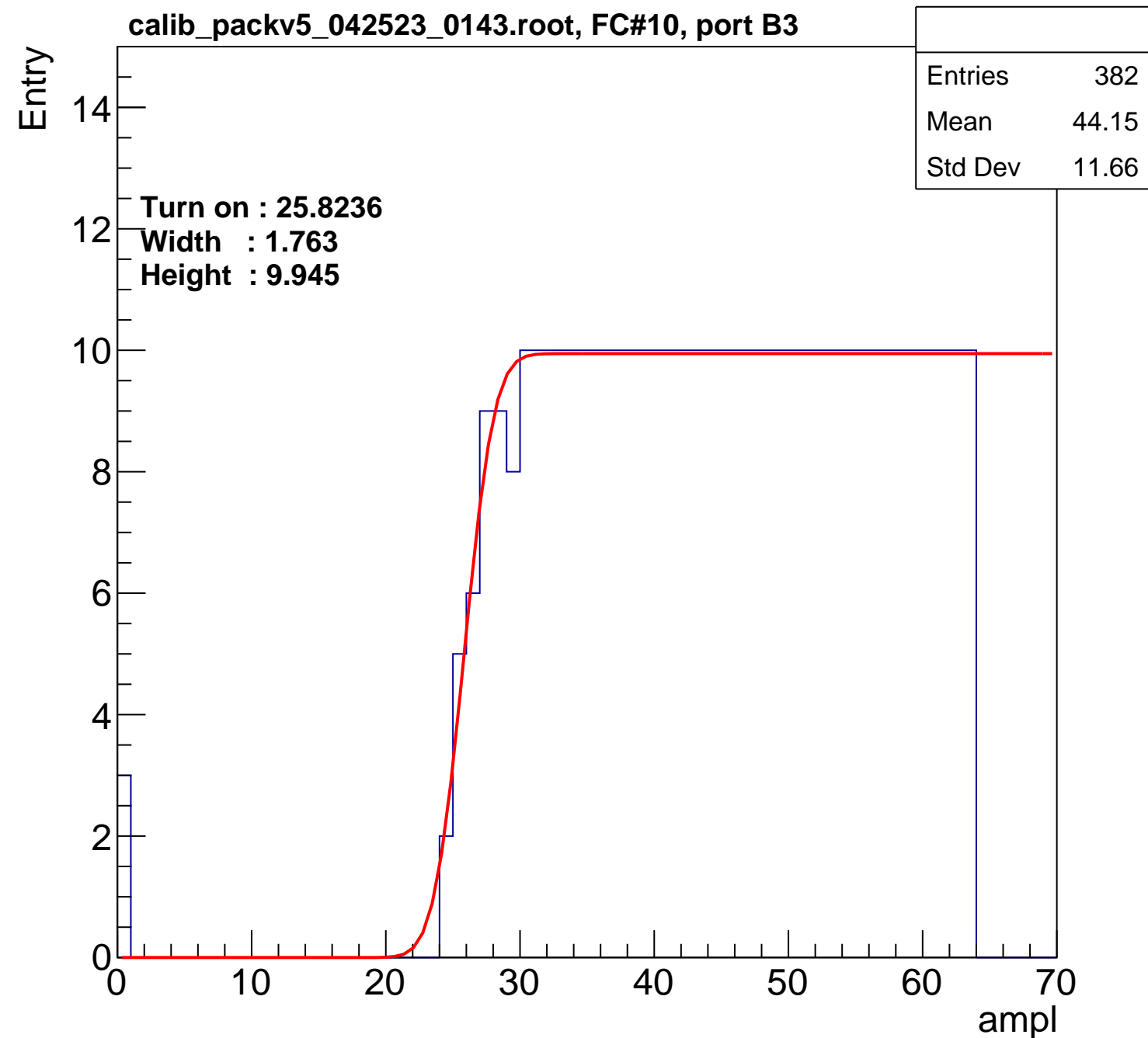
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.8236  
Width : 1.763  
Height : 9.945

Entries	382
Mean	44.15
Std Dev	11.66

ampl



# B1L002S, U6-ch34

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	366
Mean	45.02
Std Dev	10.95

**Turn on : 27.8377**

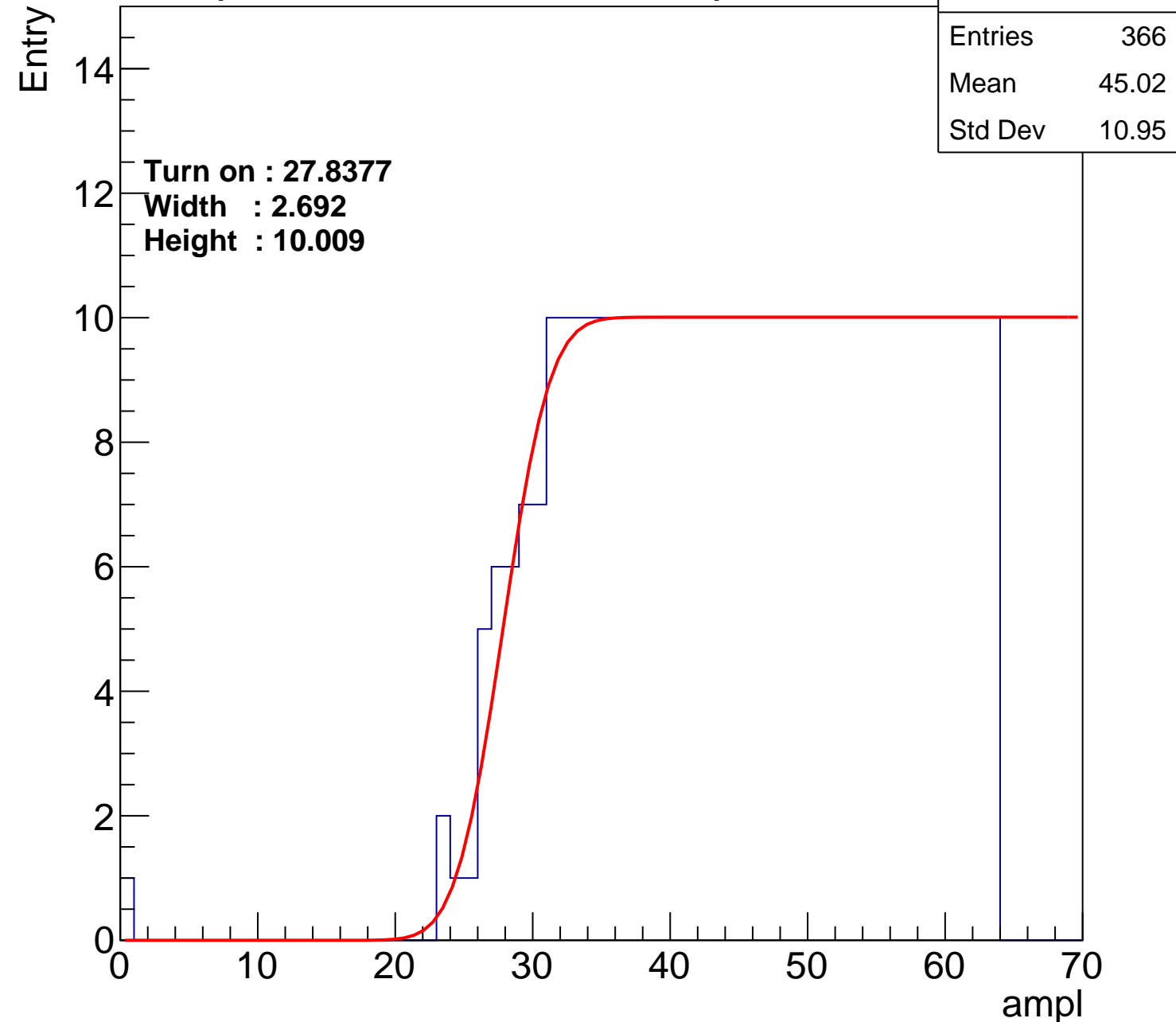
**Width : 2.692**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch35

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.8287

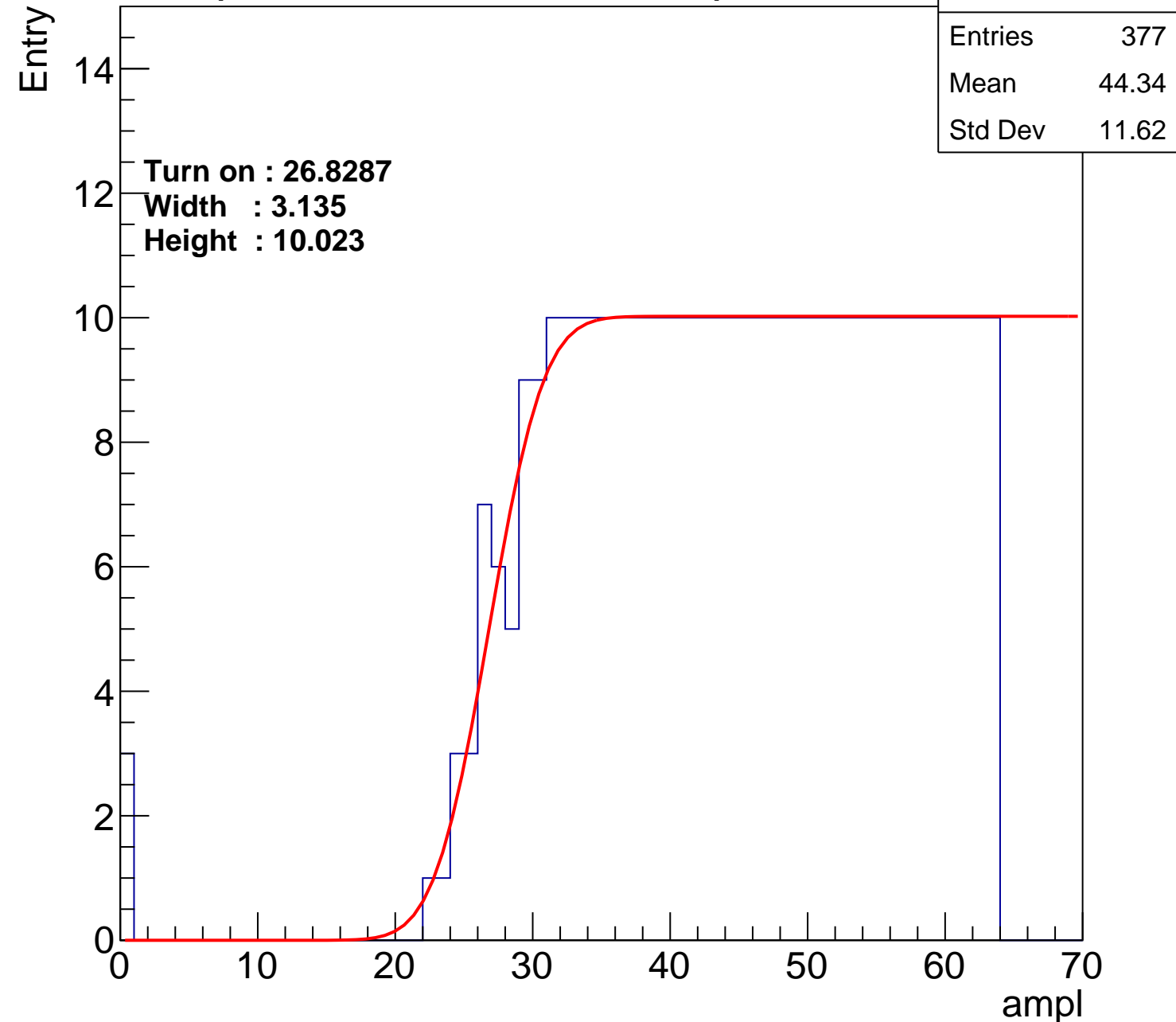
Width : 3.135

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch36

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 26.4833

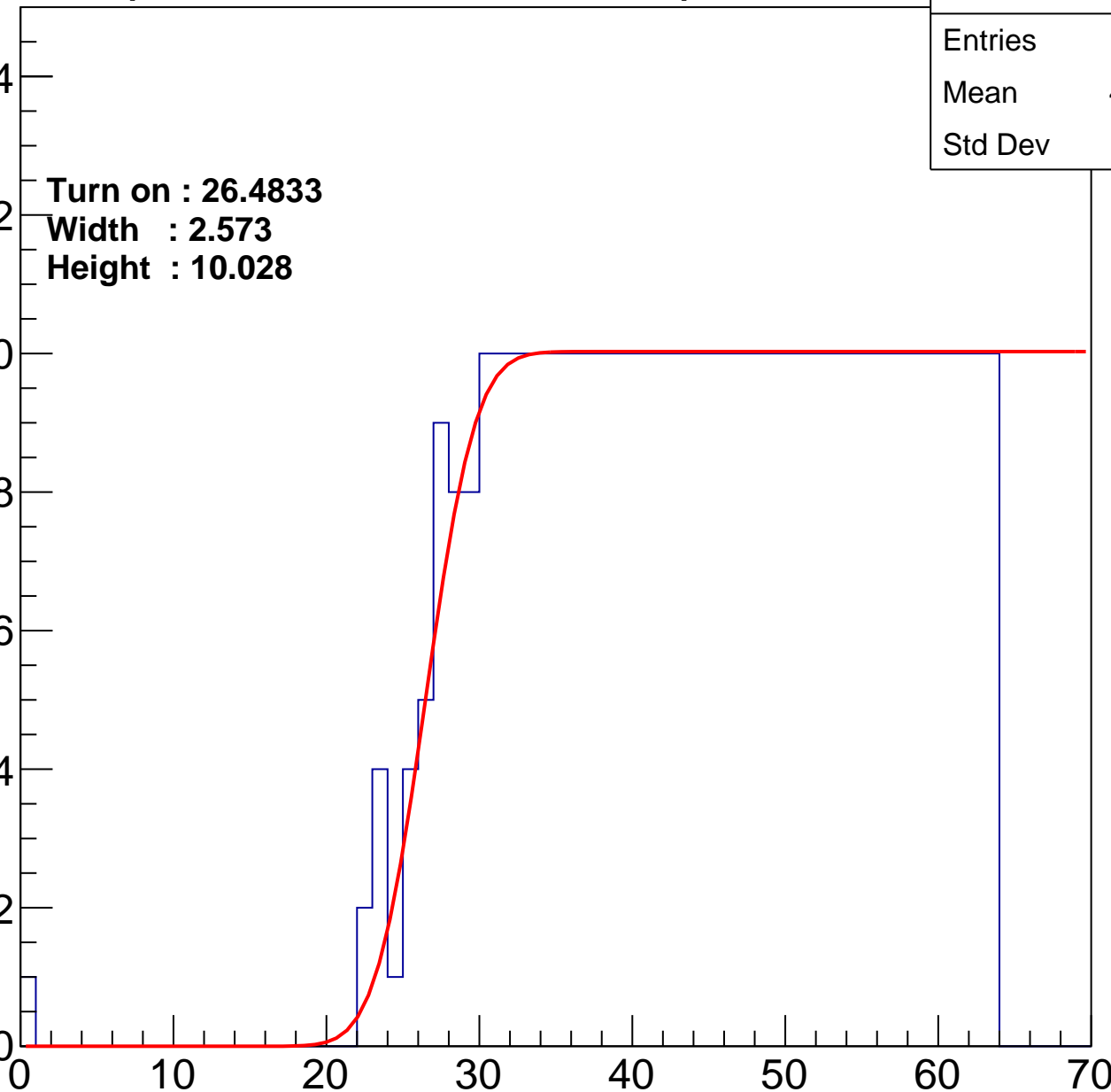
Width : 2.573

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch37

calib\_packv5\_042523\_0143.root, FC#10, port B3

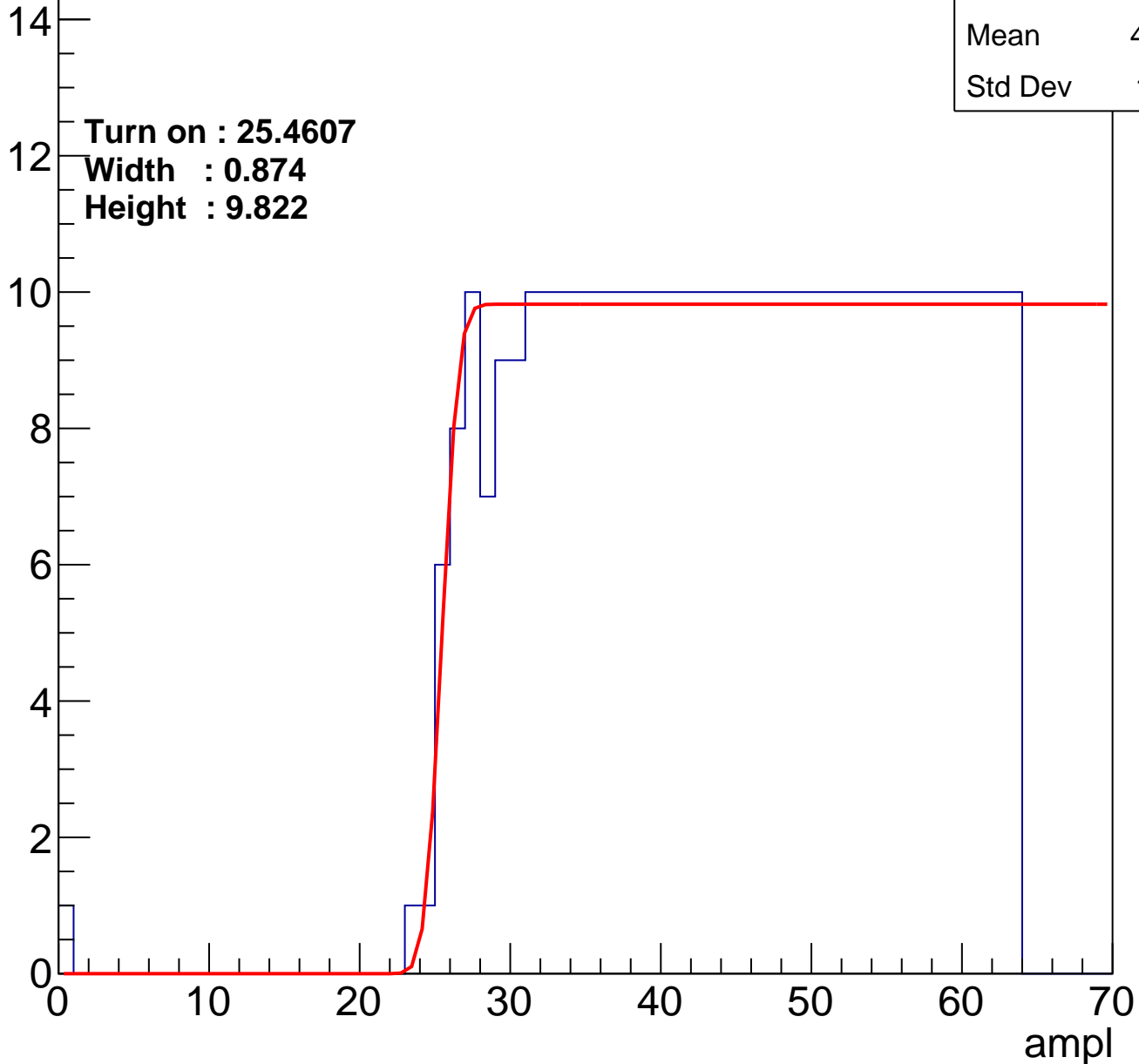
Entries	382
Mean	44.27
Std Dev	11.31

Turn on : 25.4607

Width : 0.874

Height : 9.822

Entry



# B1L002S, U6-ch38

calib\_packv5\_042523\_0143.root, FC#10, port B3

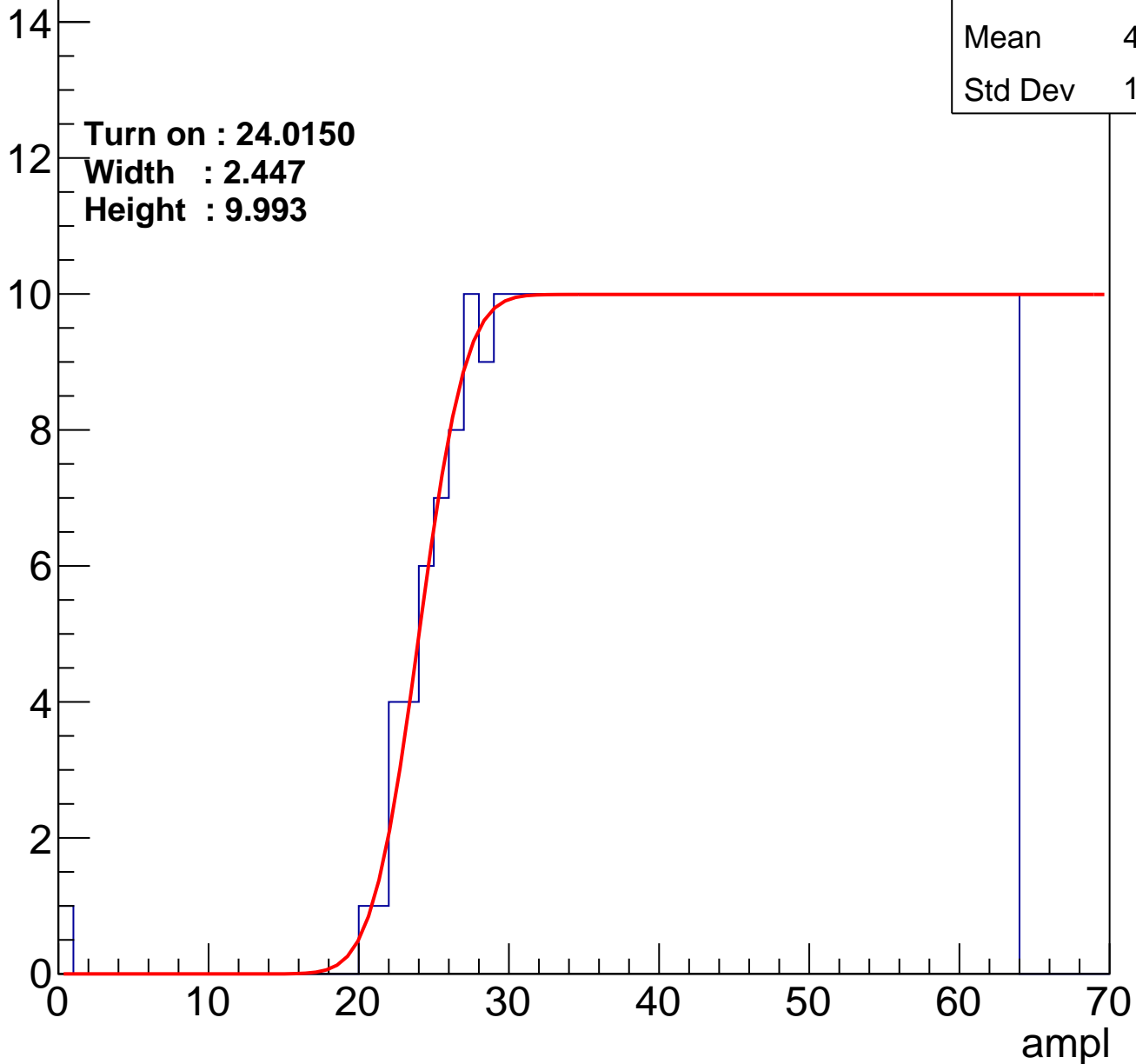
Entries	401
Mean	43.32
Std Dev	11.86

Turn on : 24.0150

Width : 2.447

Height : 9.993

Entry





# B1L002S, U6-ch39

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.55
Std Dev	11.5

Turn on : 27.8672

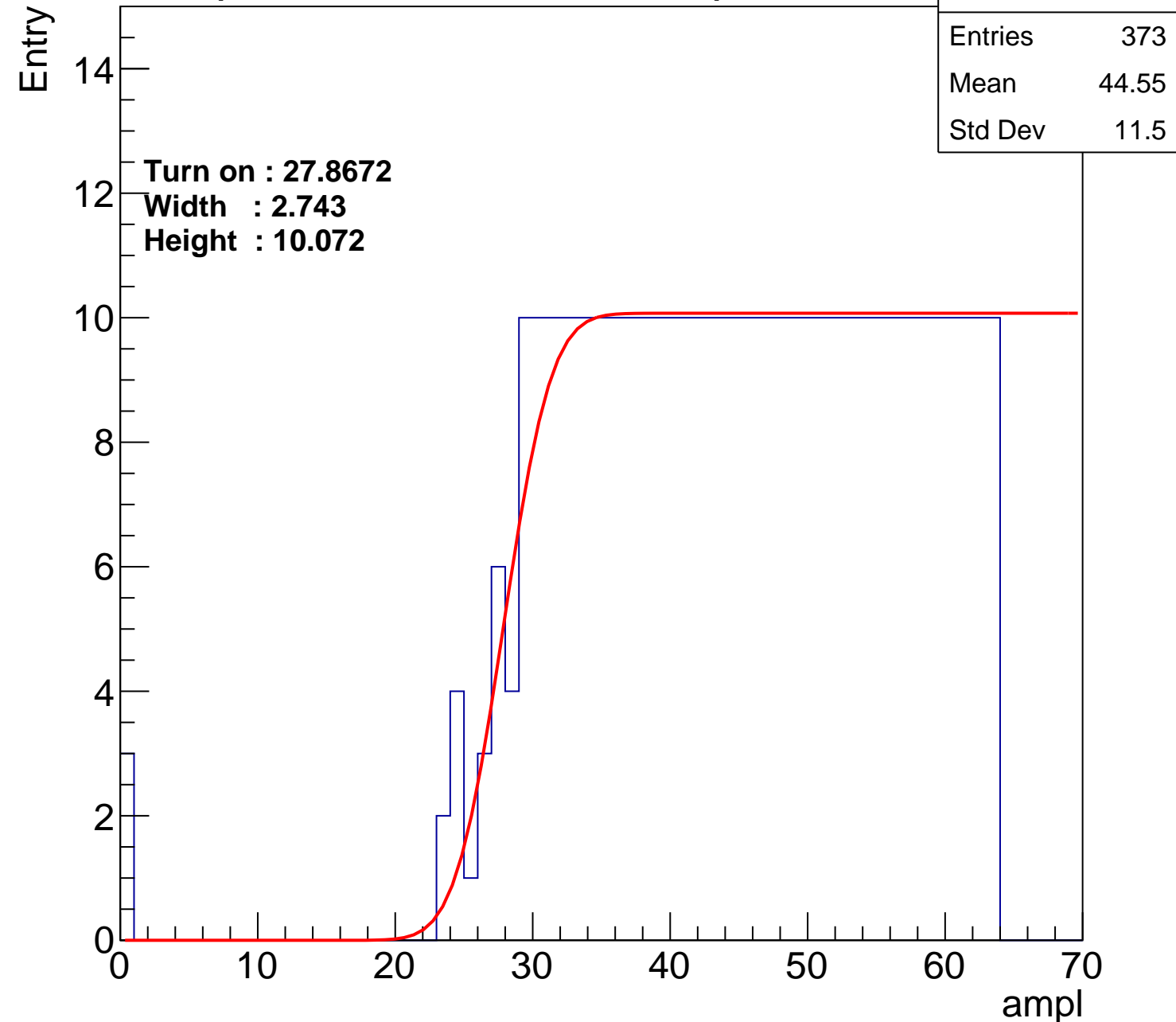
Width : 2.743

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch40

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 27.1060

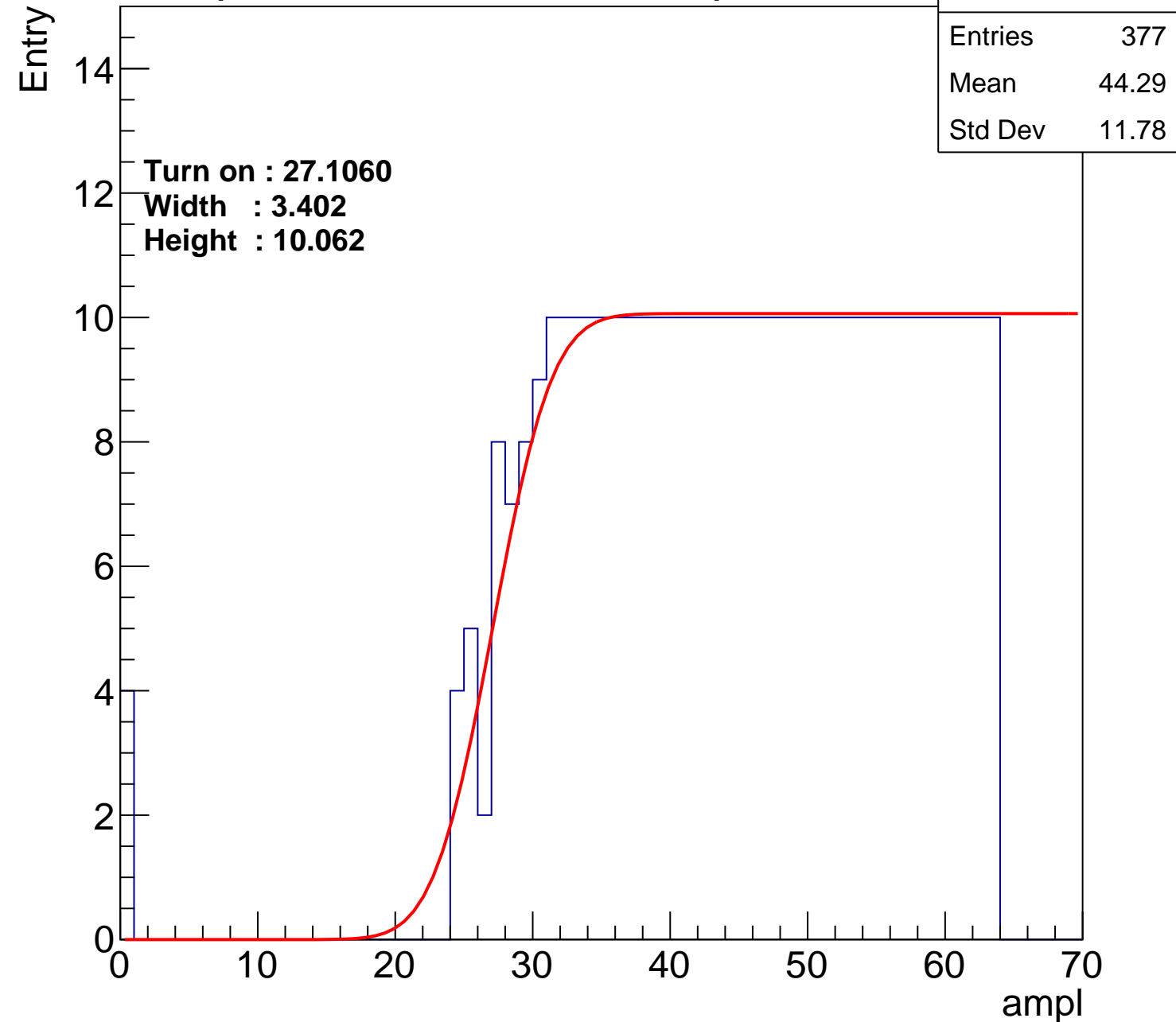
Width : 3.402

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch41

calib\_packv5\_042523\_0143.root, FC#10, port B3

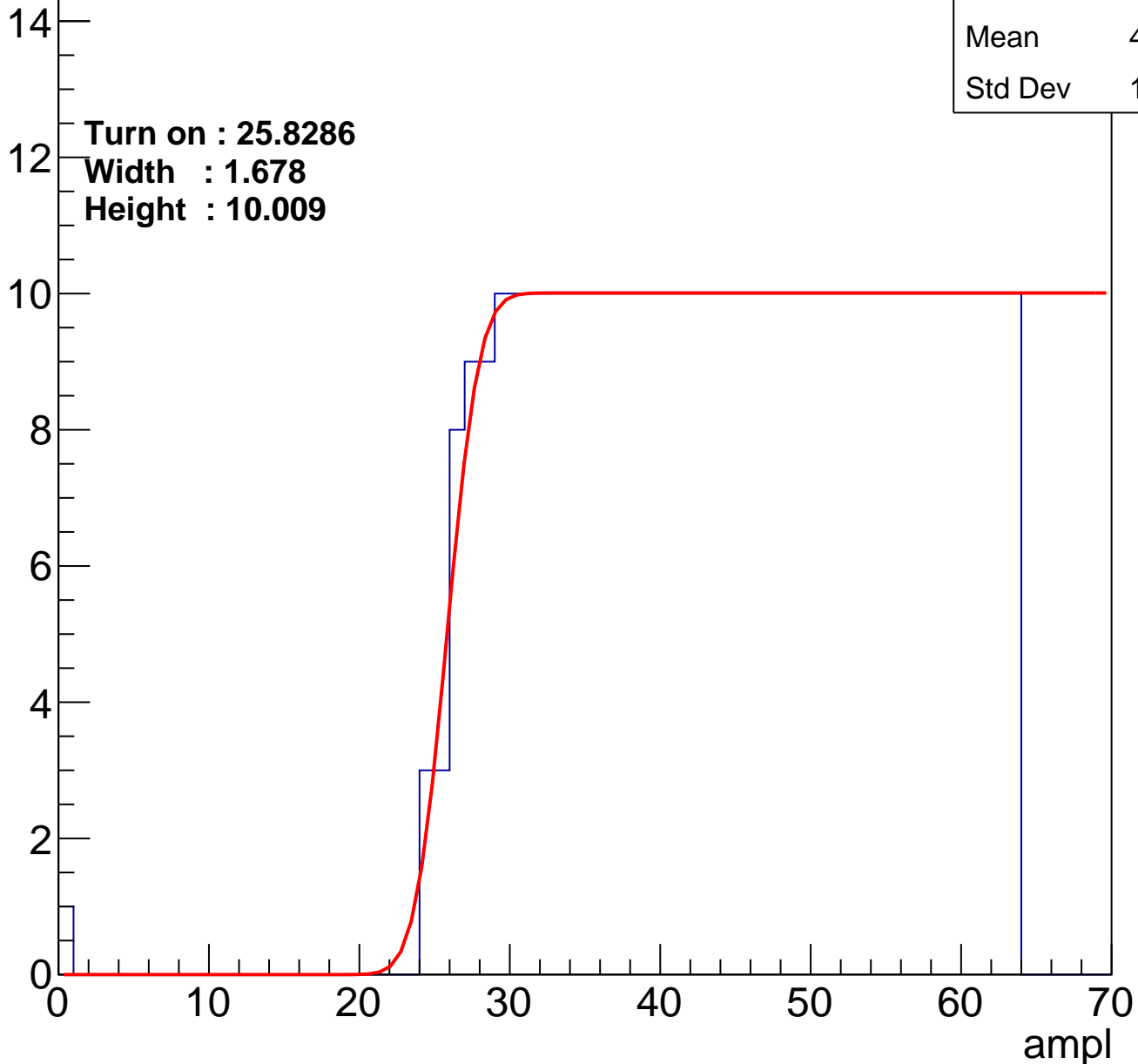
Entries	383
Mean	44.26
Std Dev	11.29

Turn on : 25.8286

Width : 1.678

Height : 10.009

Entry



# B1L002S, U6-ch42

calib\_packv5\_042523\_0143.root, FC#10, port B3

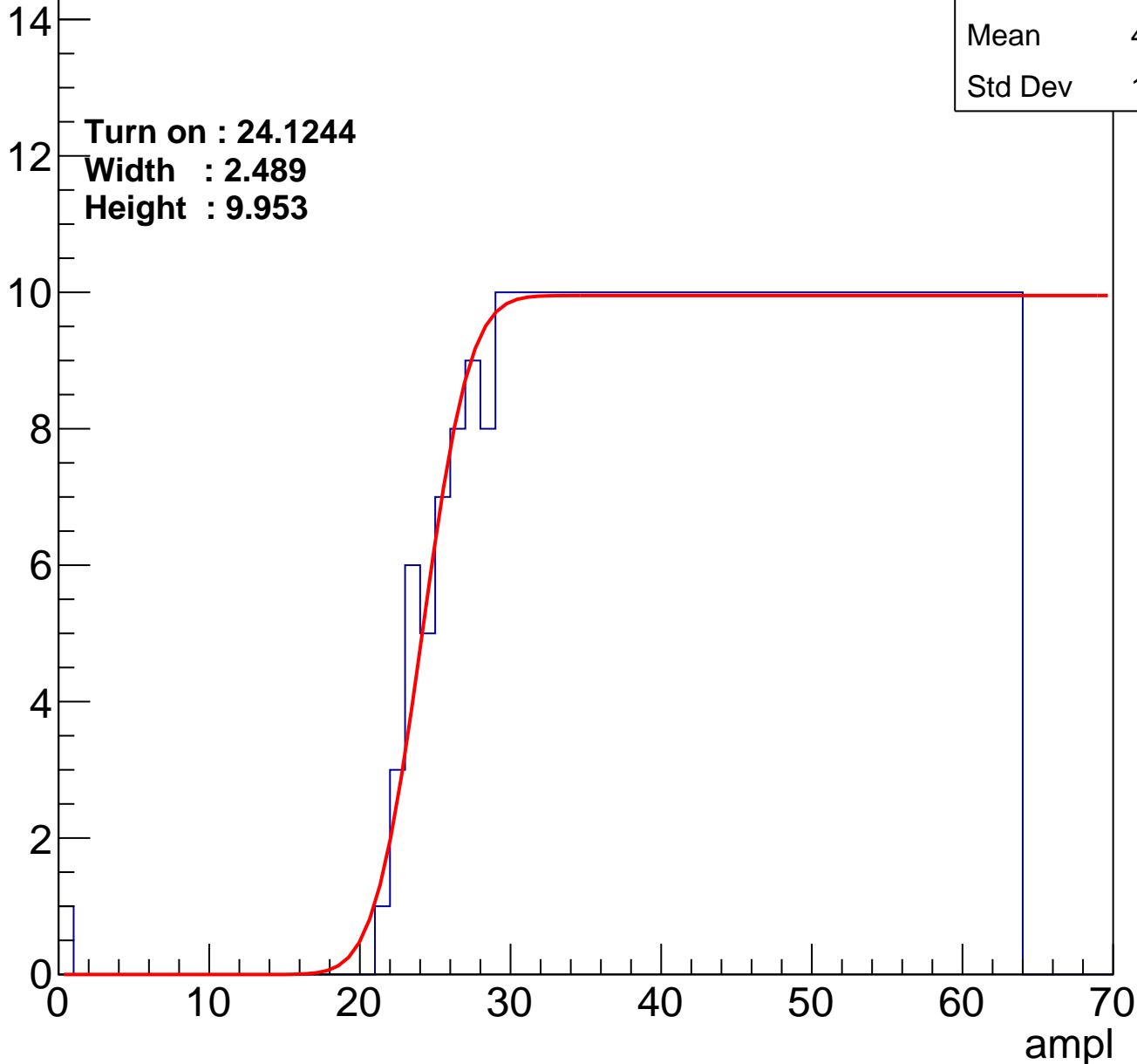
Entry

Entries	398
Mean	43.45
Std Dev	11.79

Turn on : 24.1244

Width : 2.489

Height : 9.953



# B1L002S, U6-ch43

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	43.8
Std Dev	12.25

Turn on : 25.9771

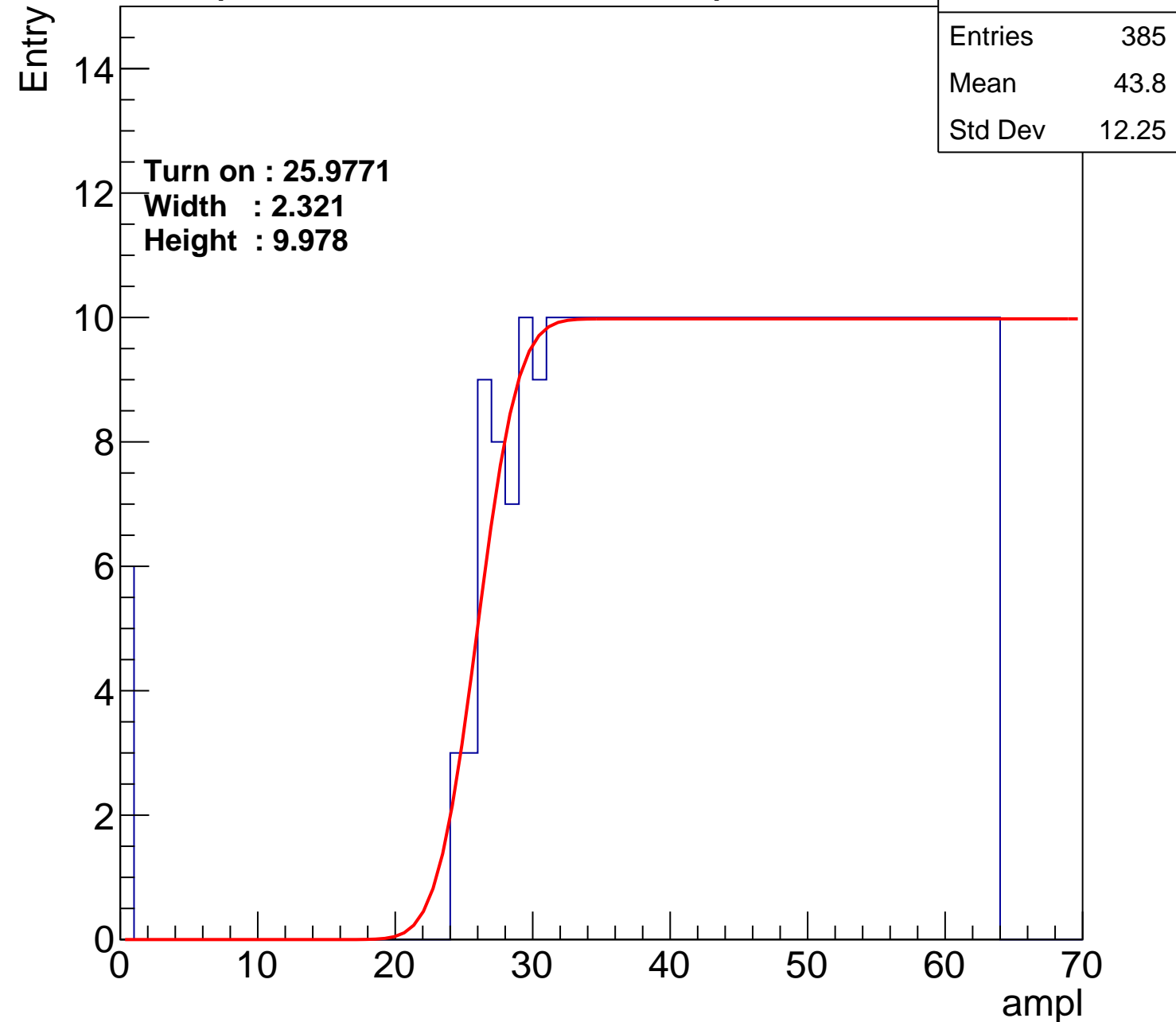
Width : 2.321

Height : 9.978

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch44

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44
Std Dev	11.75

Turn on : 25.9703

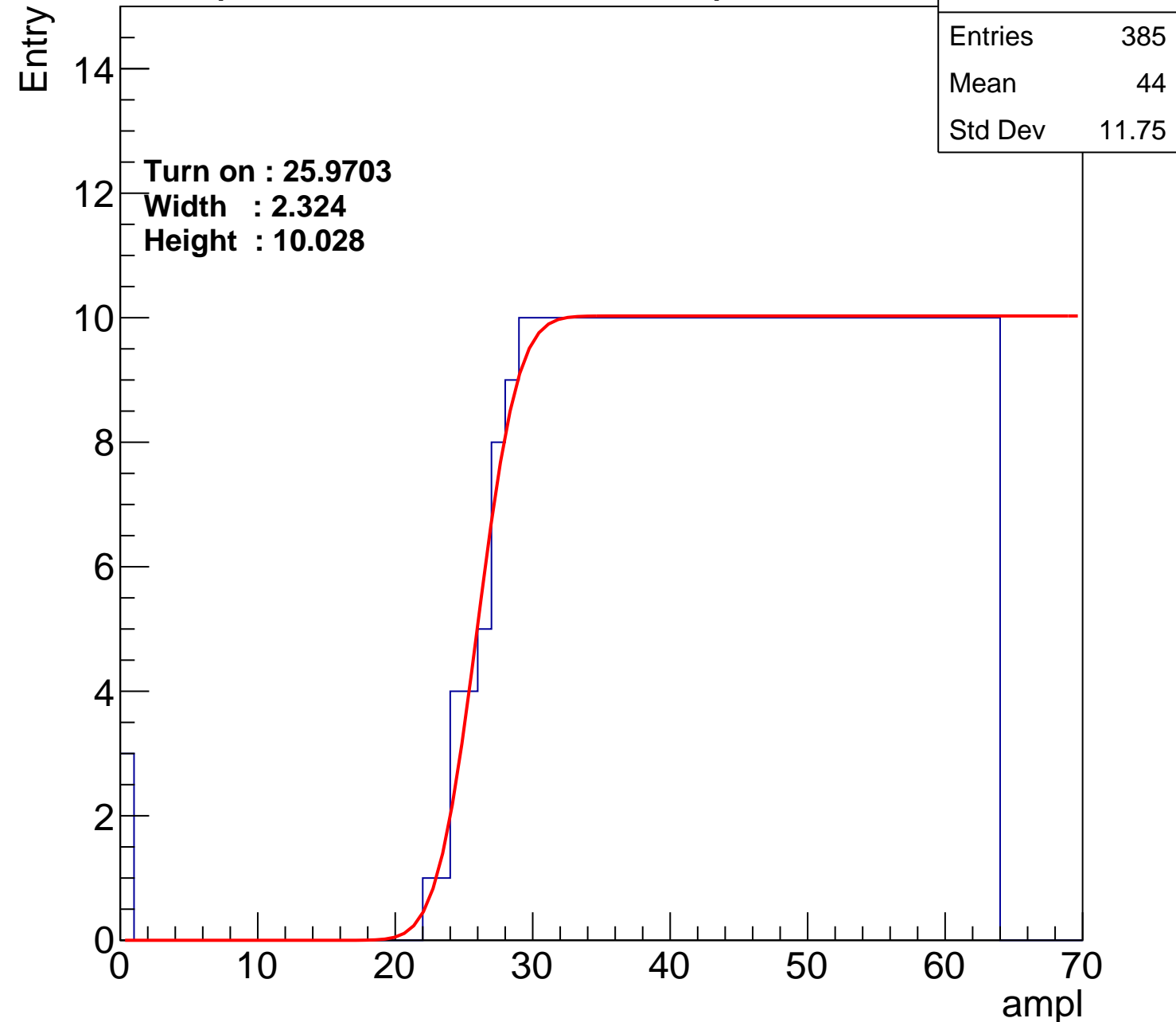
Width : 2.324

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch45

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 26.7278

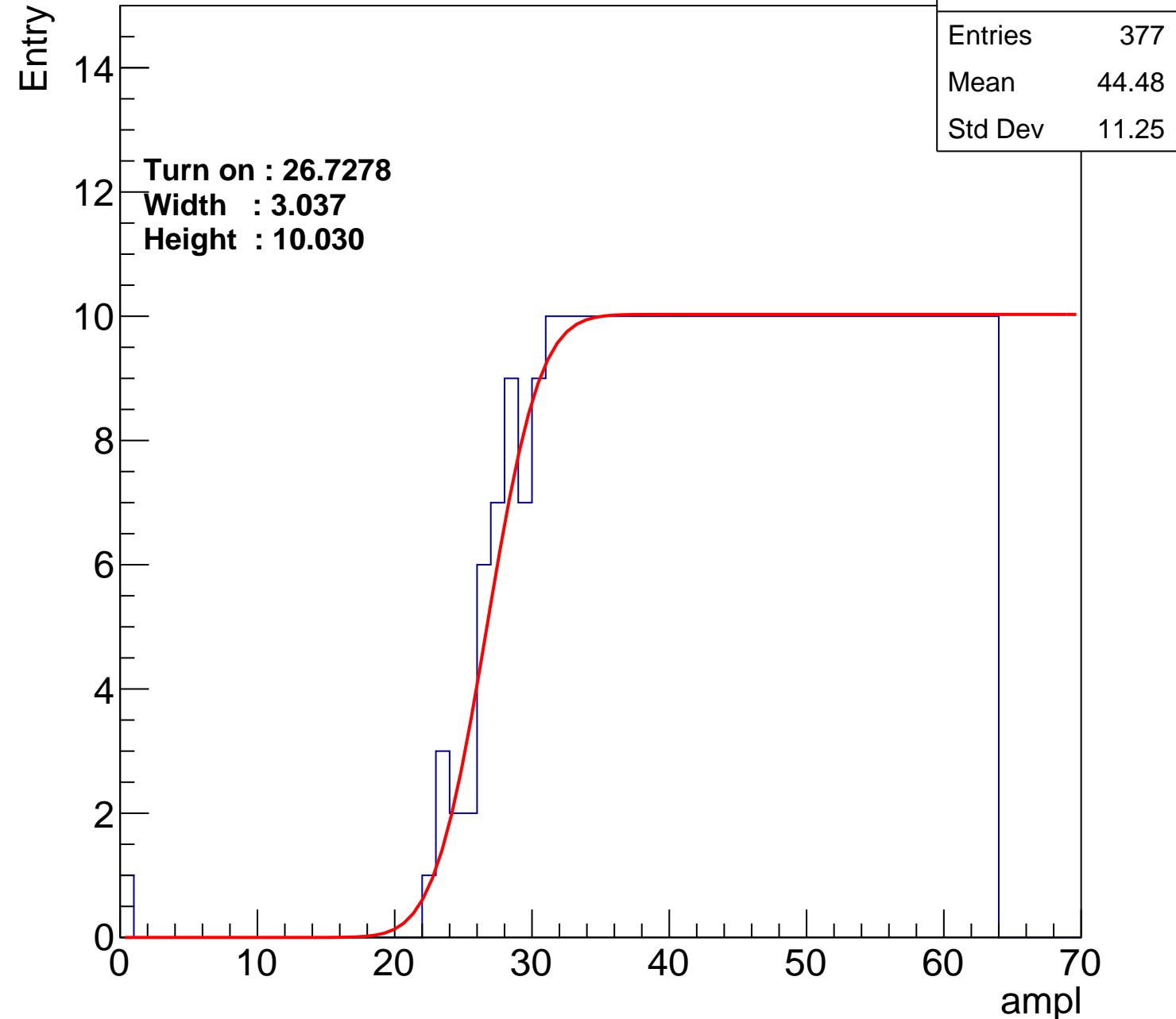
Width : 3.037

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch46

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	397
Mean	43.47
Std Dev	11.88

Turn on : 24.6447

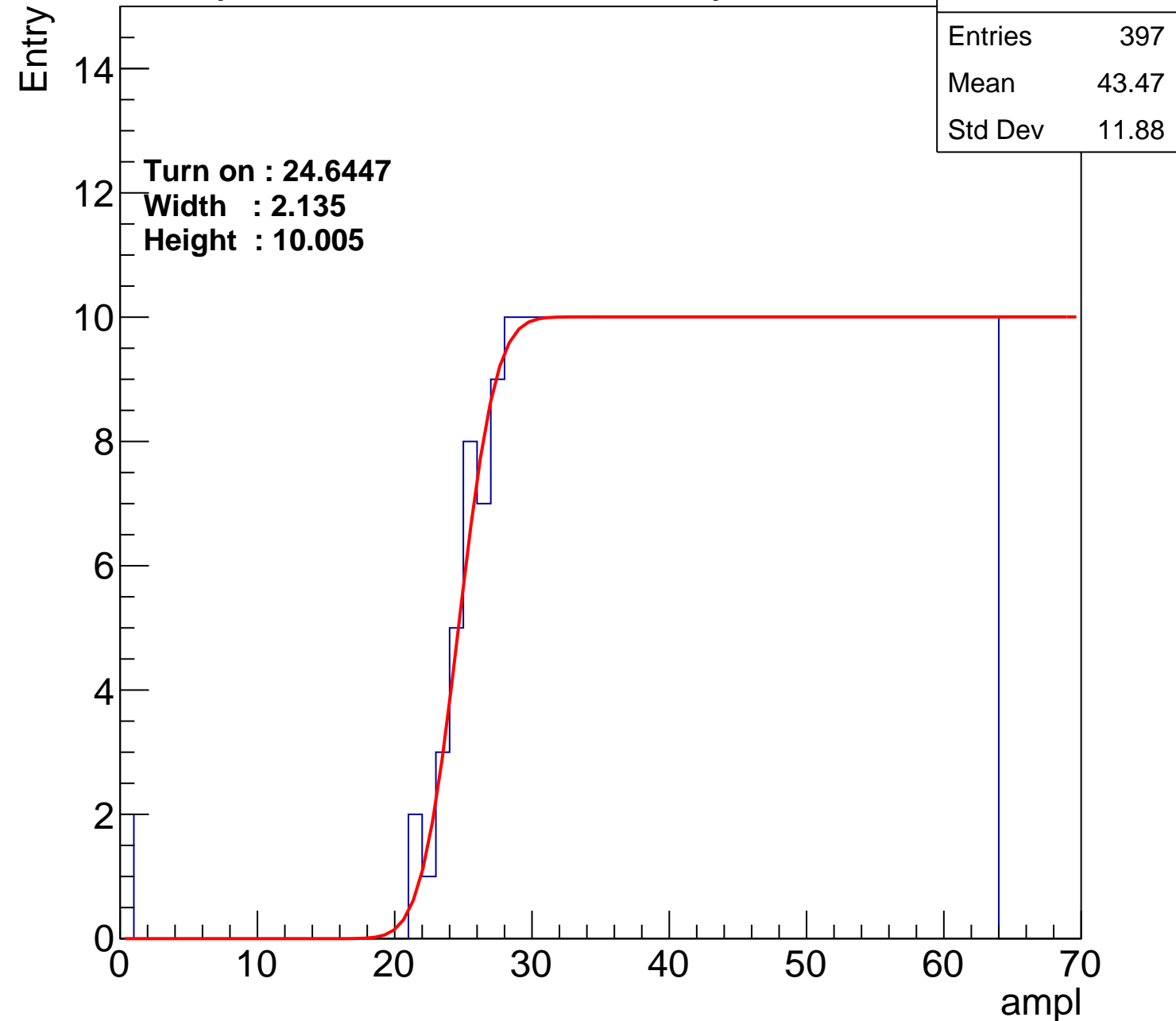
Width : 2.135

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch47

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.84
Std Dev	11.84

Turn on : 25.8989

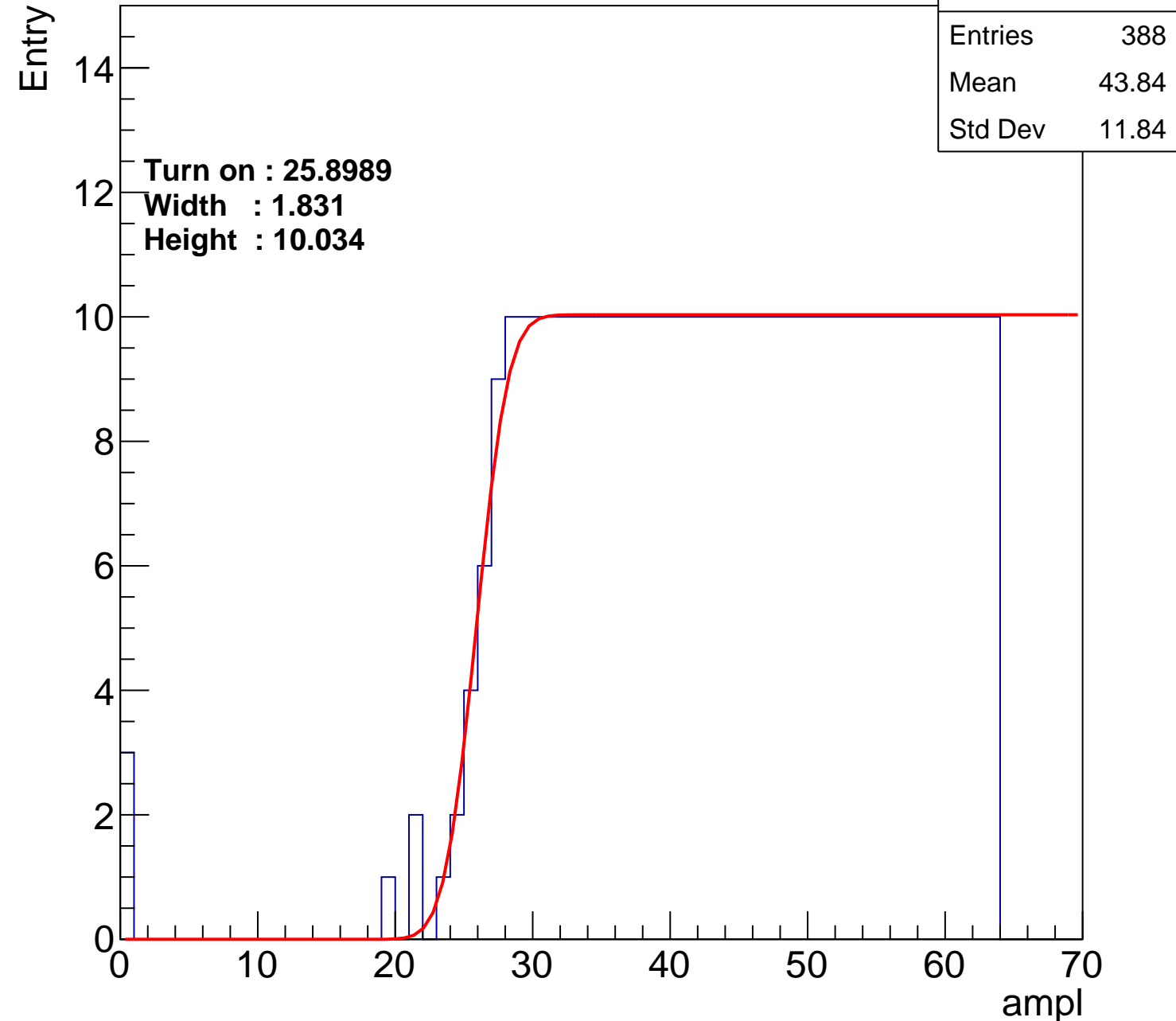
Width : 1.831

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch48

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.69
Std Dev	11.27

Turn on : 27.2466

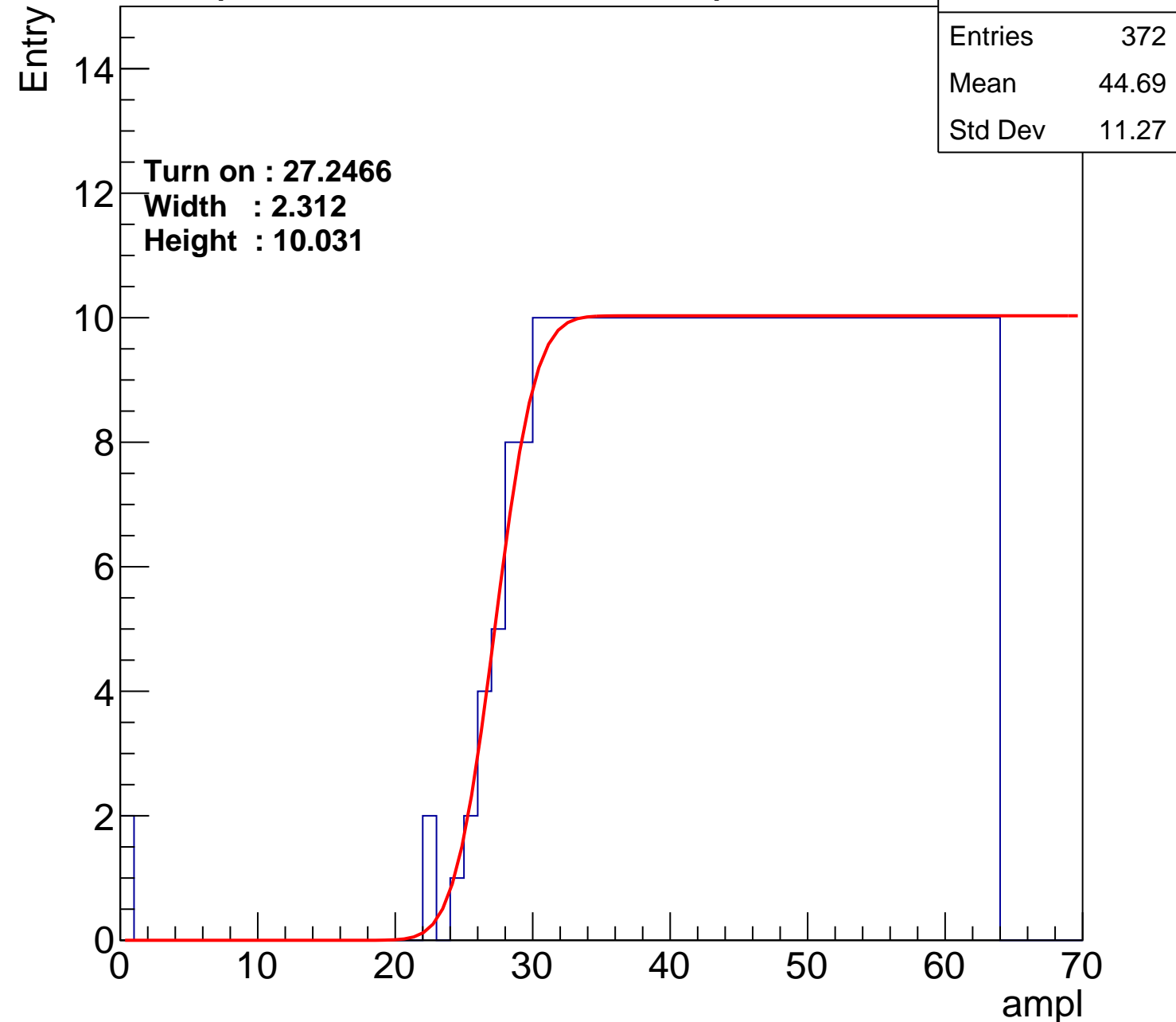
Width : 2.312

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch49

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	399
Mean	43.23
Std Dev	12.27

Turn on : 25.0658

Width : 1.921

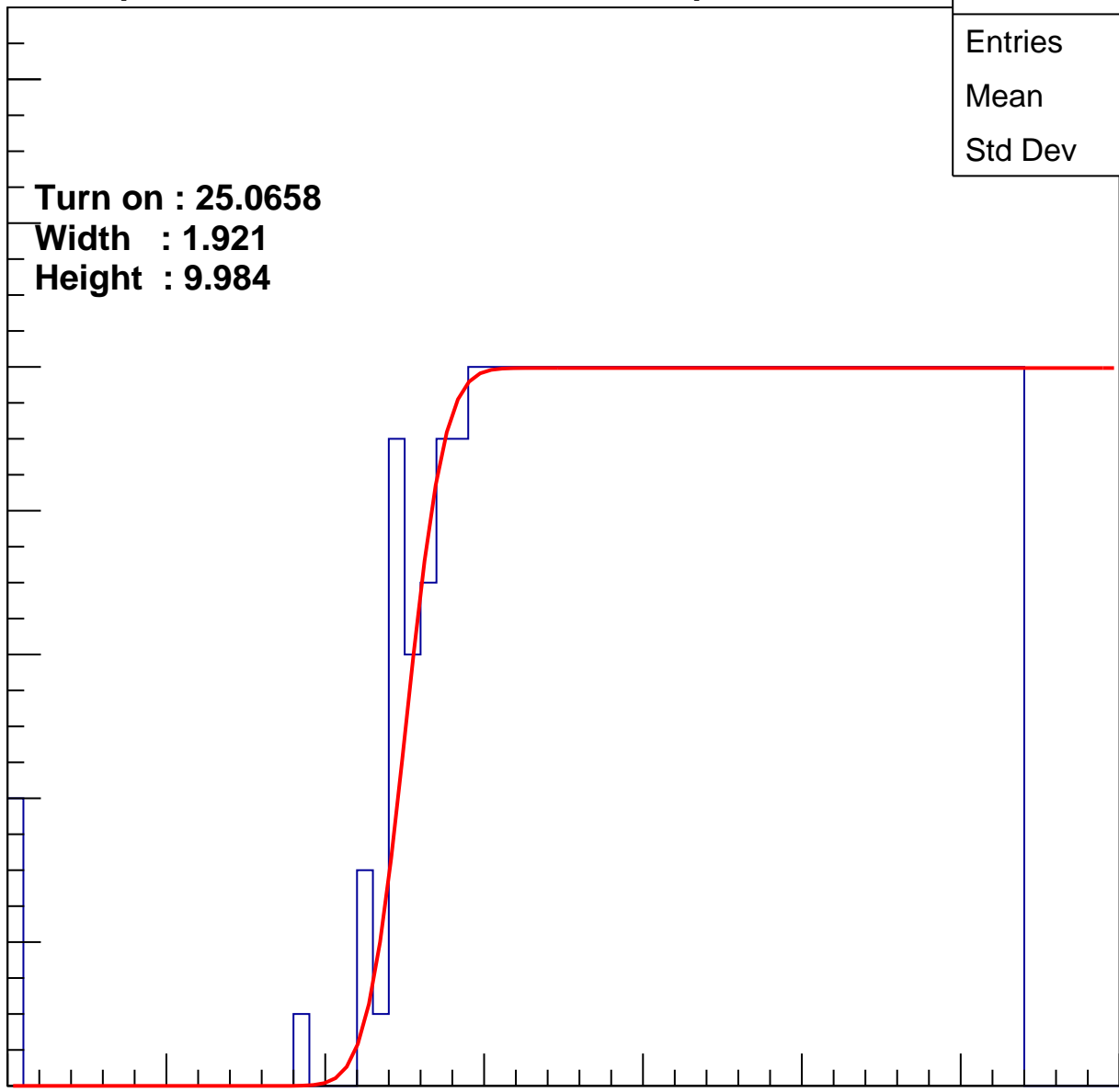
Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U6-ch50

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.49
Std Dev	11.56

Turn on : 27.0792

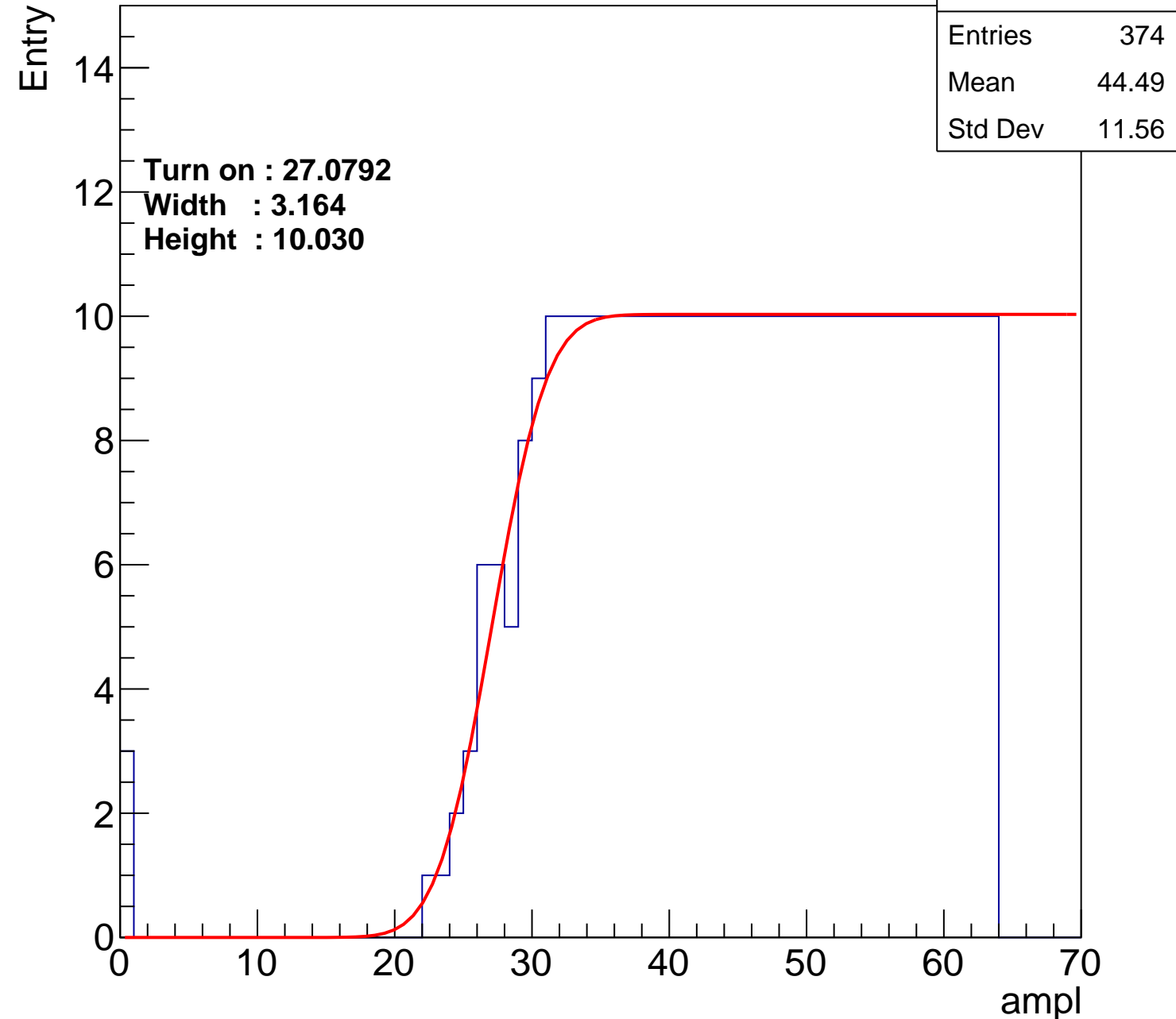
Width : 3.164

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch51

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.1770

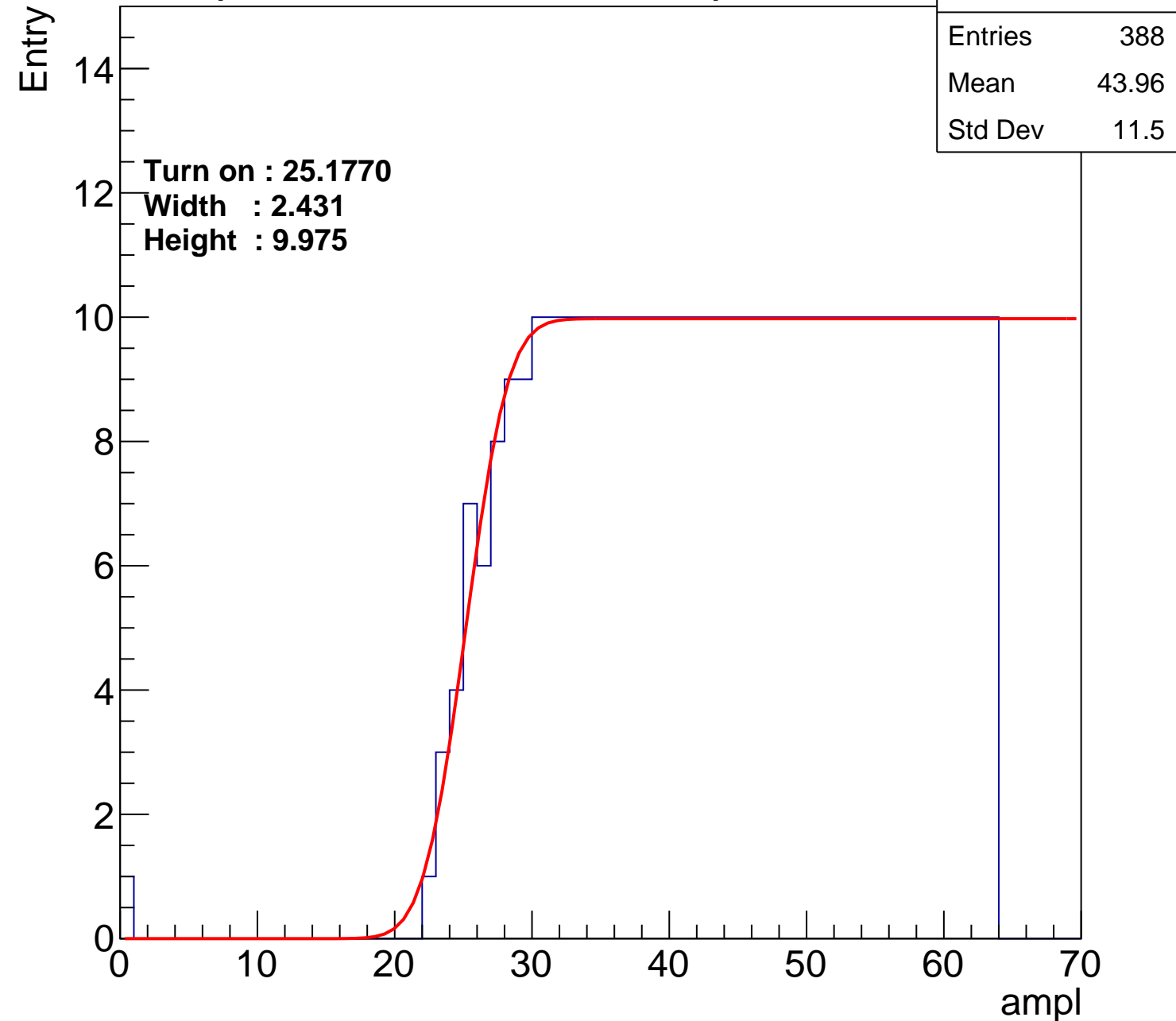
Width : 2.431

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch52

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.6431

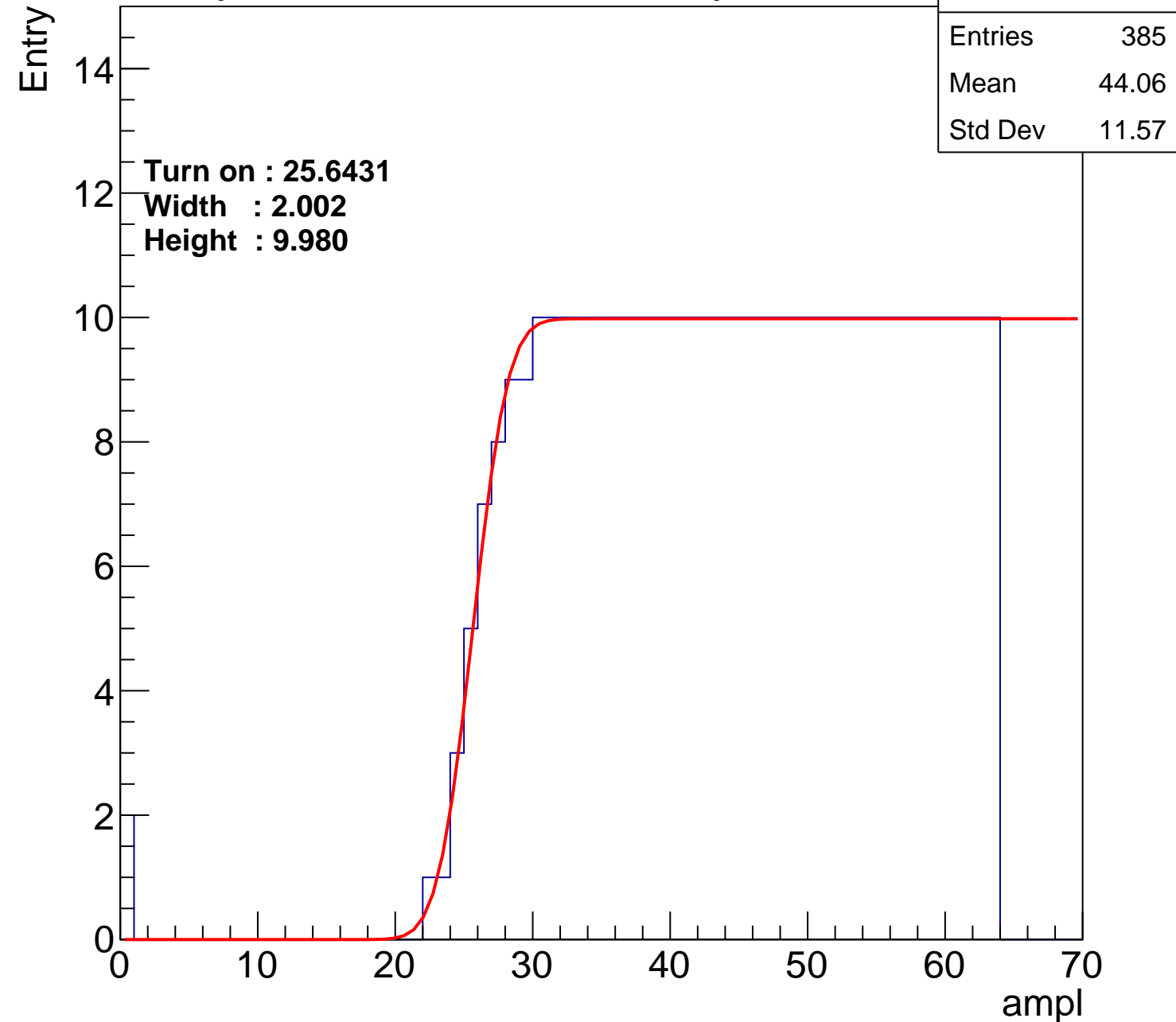
Width : 2.002

Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch53

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.1
Std Dev	11.55

**Turn on : 25.7987**

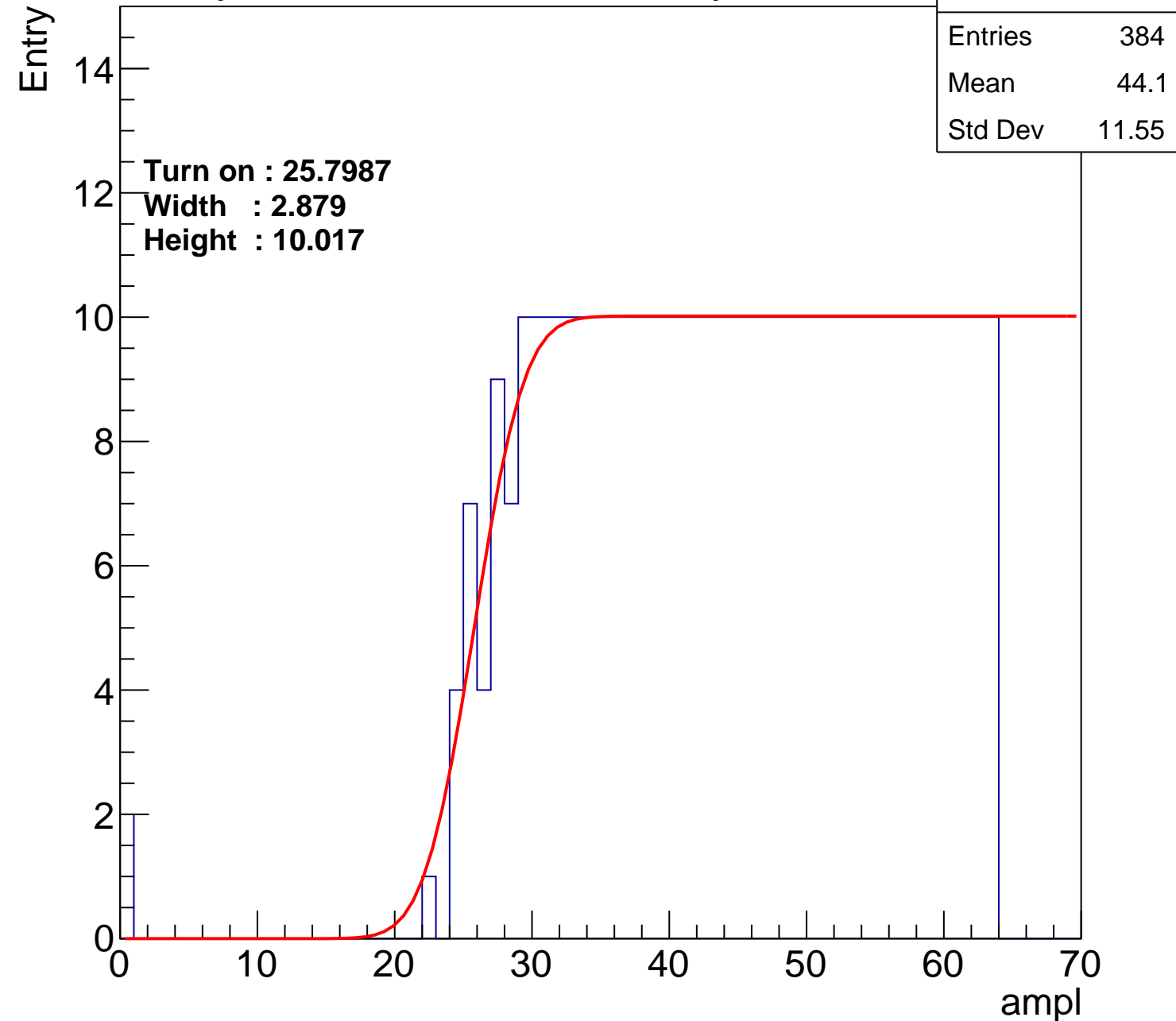
**Width : 2.879**

**Height : 10.017**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch54

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.38
Std Dev	11.74

Turn on : 27.0941

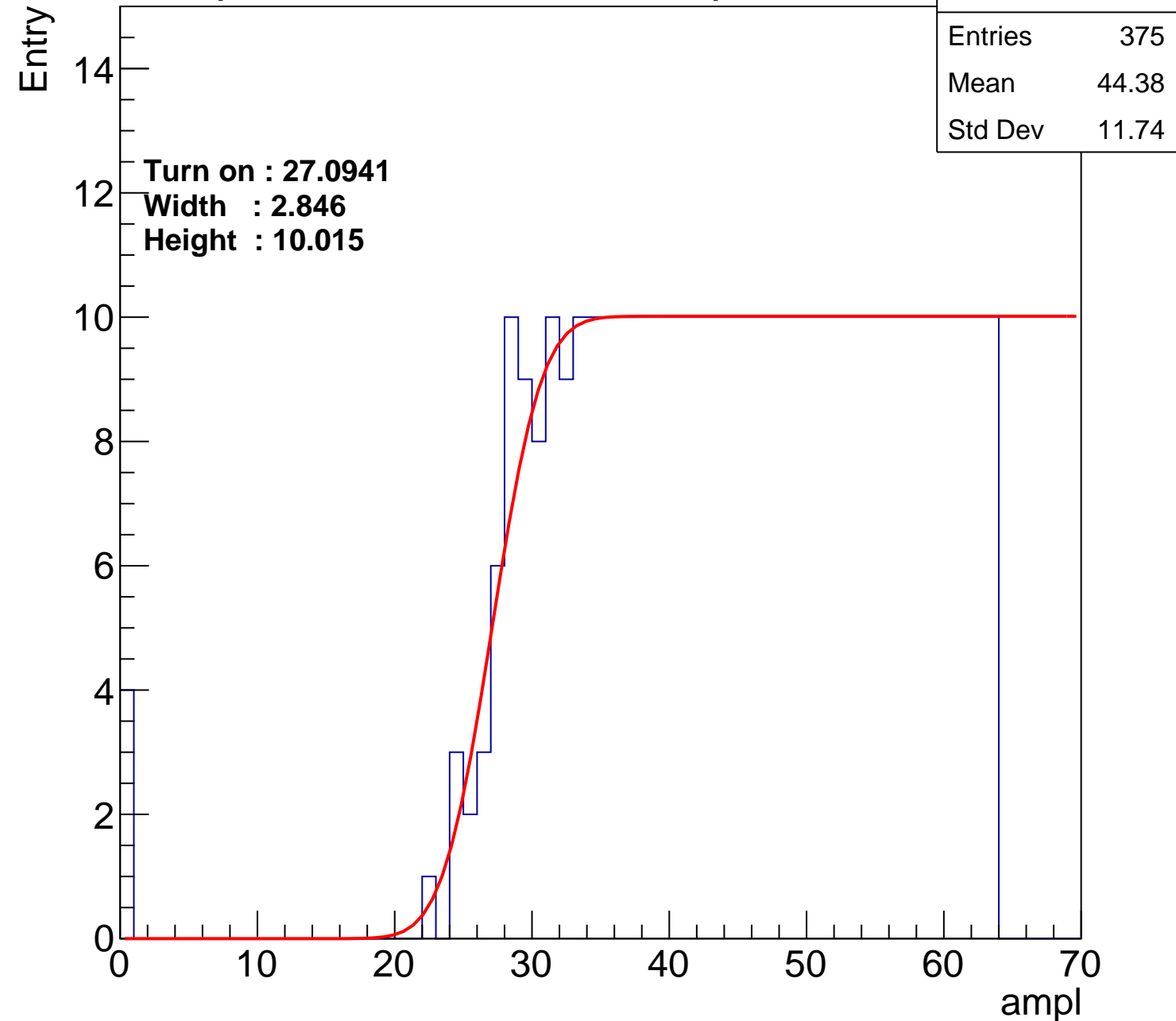
Width : 2.846

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch55

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.18
Std Dev	11.66

Turn on : 25.7251

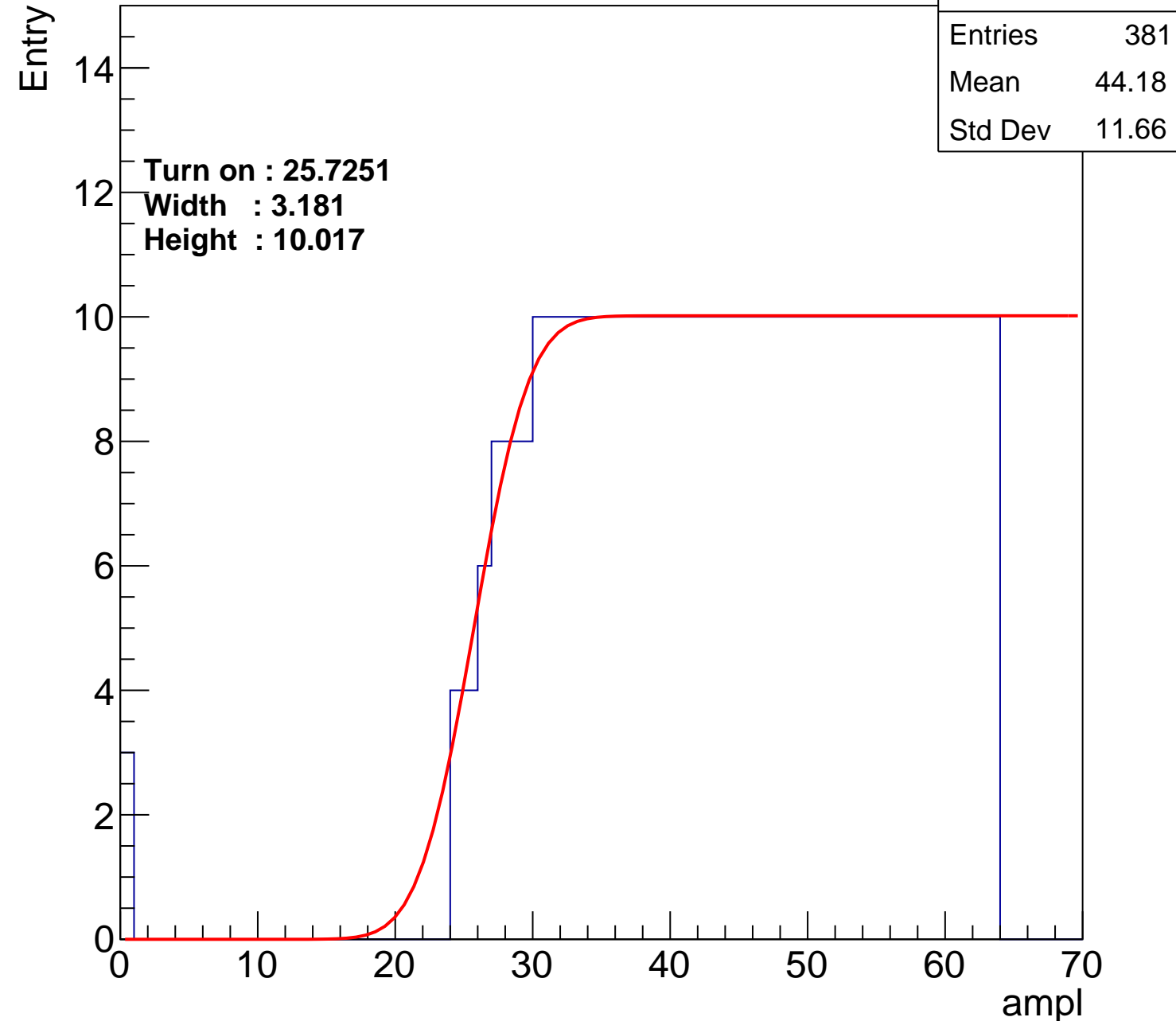
Width : 3.181

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch56

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.6601

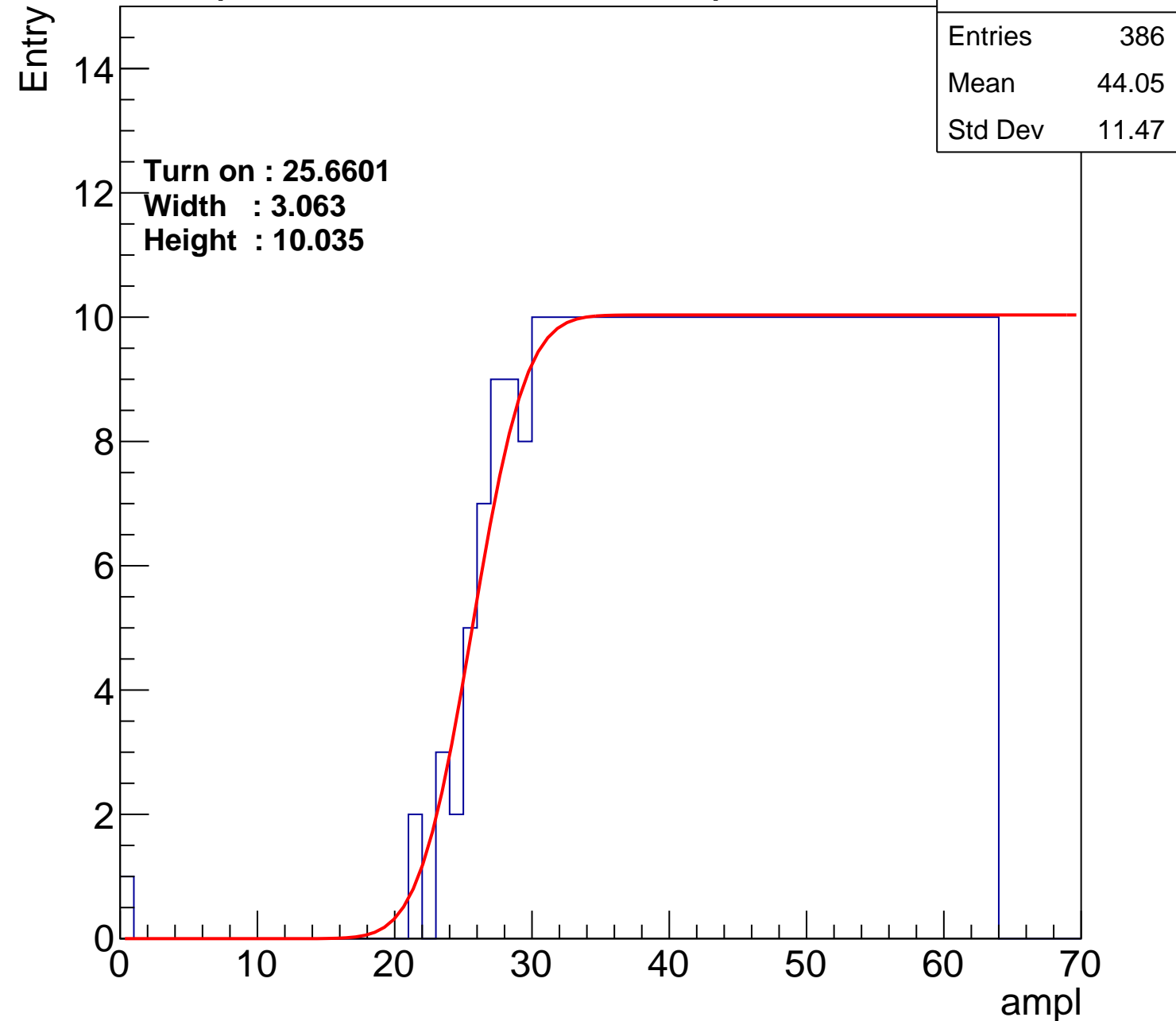
Width : 3.063

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch57

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.26
Std Dev	12.29

Turn on : 24.9173

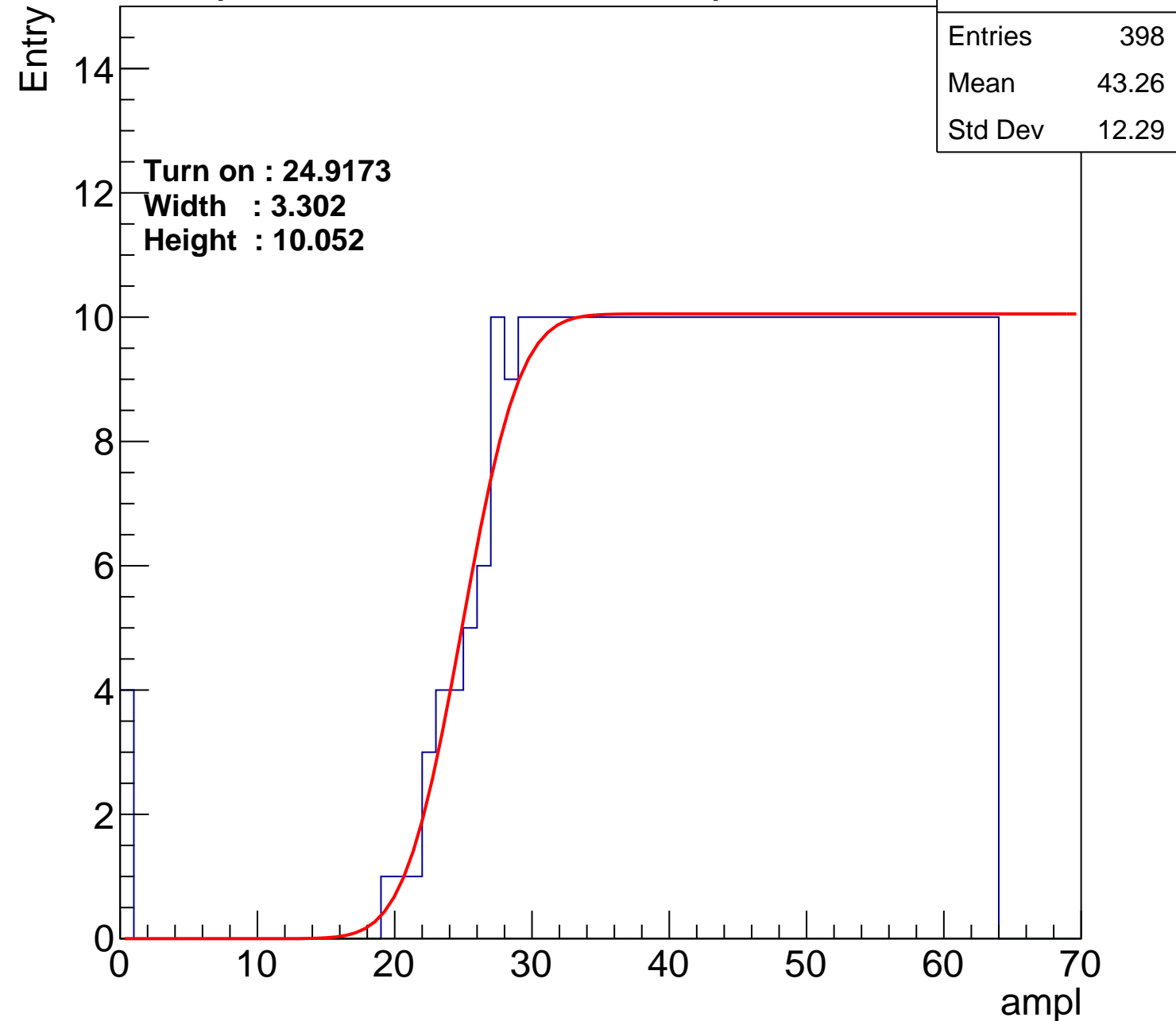
Width : 3.302

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch58

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.68
Std Dev	11.7

Turn on : 24.6933

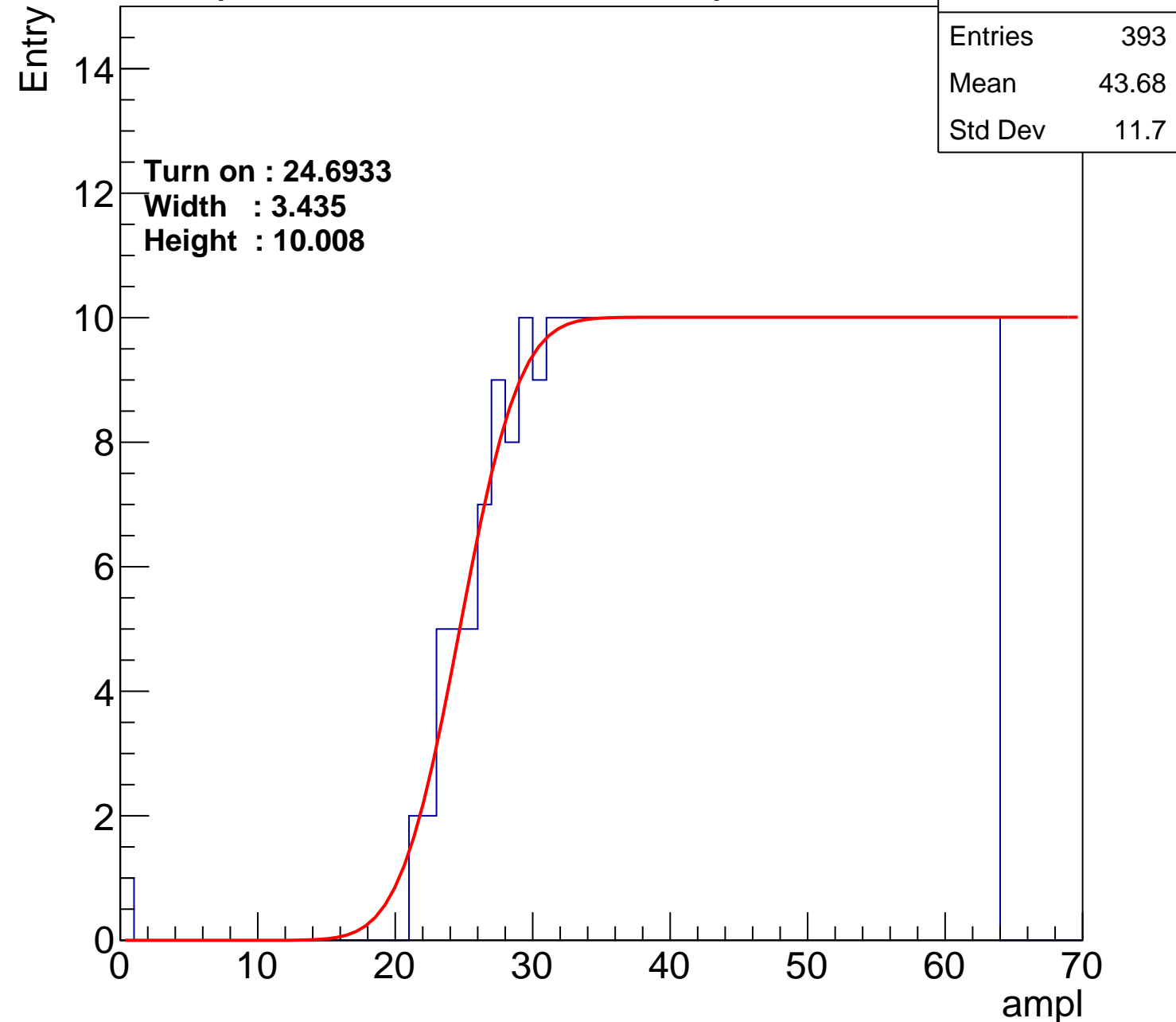
Width : 3.435

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch59

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.75
Std Dev	11.63

Turn on : 25.2790

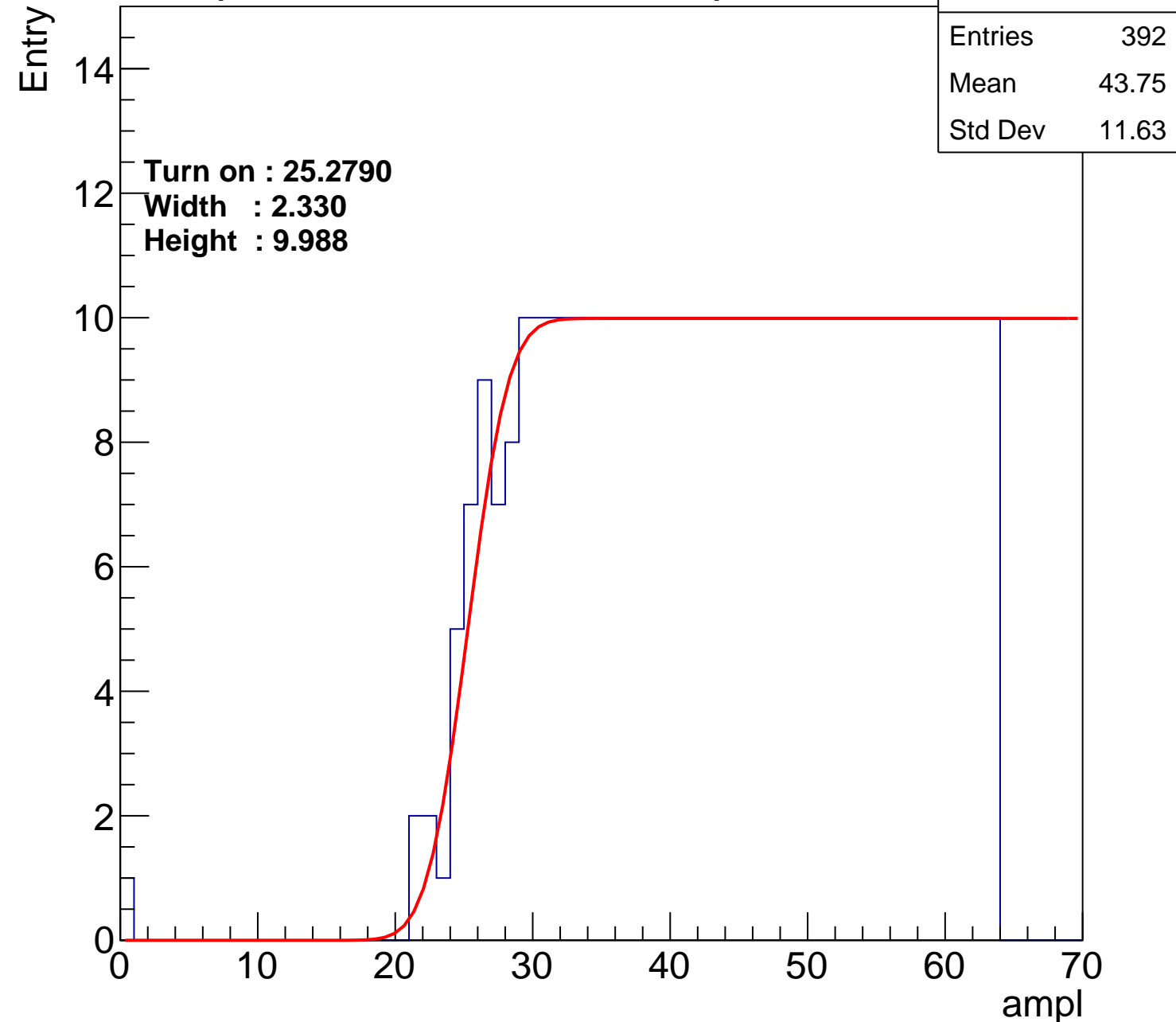
Width : 2.330

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch60

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.16
Std Dev	11.4

Turn on : 26.0597

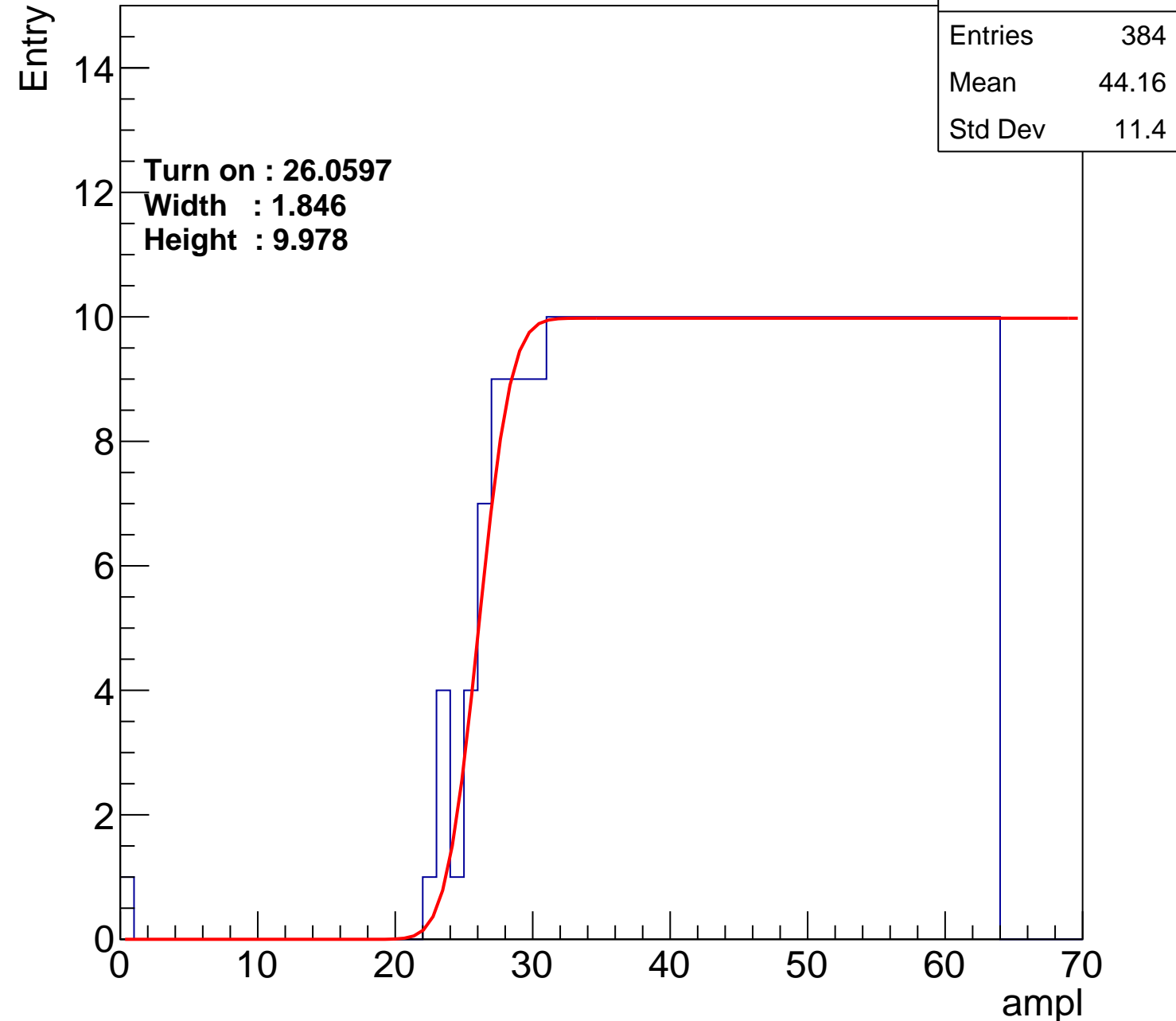
Width : 1.846

Height : 9.978

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch61

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	398
Mean	43.42
Std Dev	11.91

Turn on : 23.7743

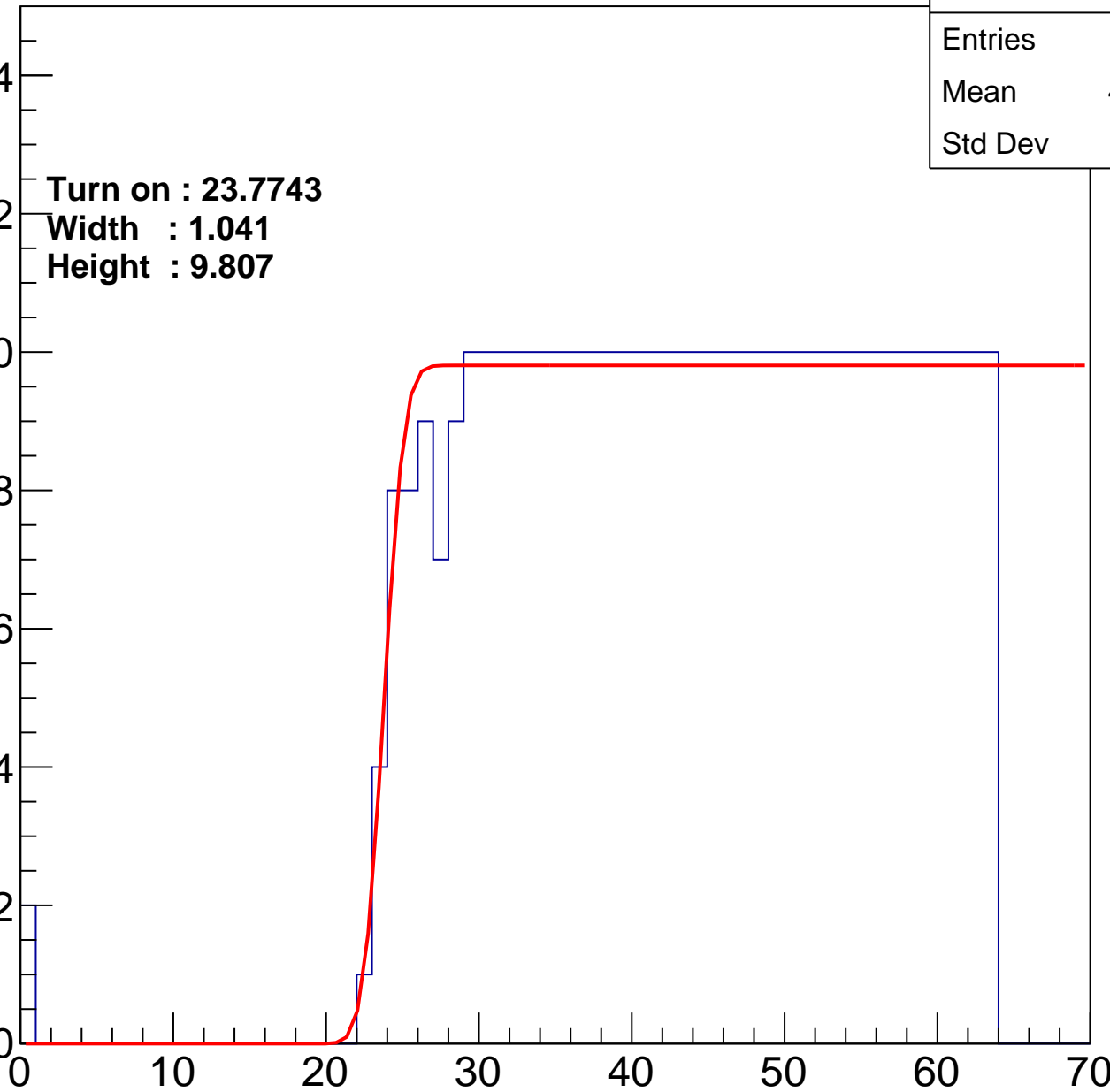
Width : 1.041

Height : 9.807

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch62

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.57
Std Dev	11.95

Turn on : 25.3232

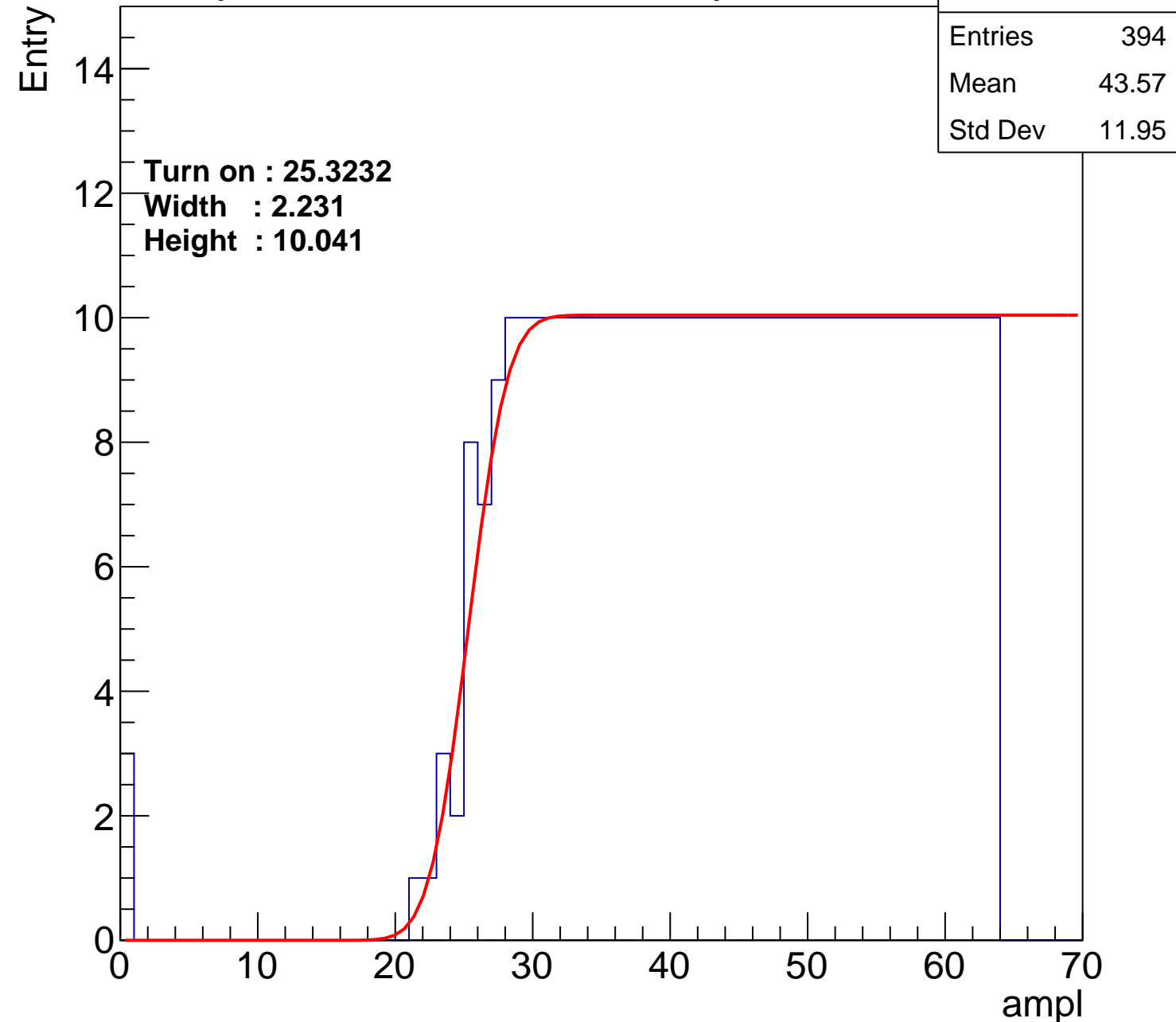
Width : 2.231

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch63

calib\_packv5\_042523\_0143.root, FC#10, port B3

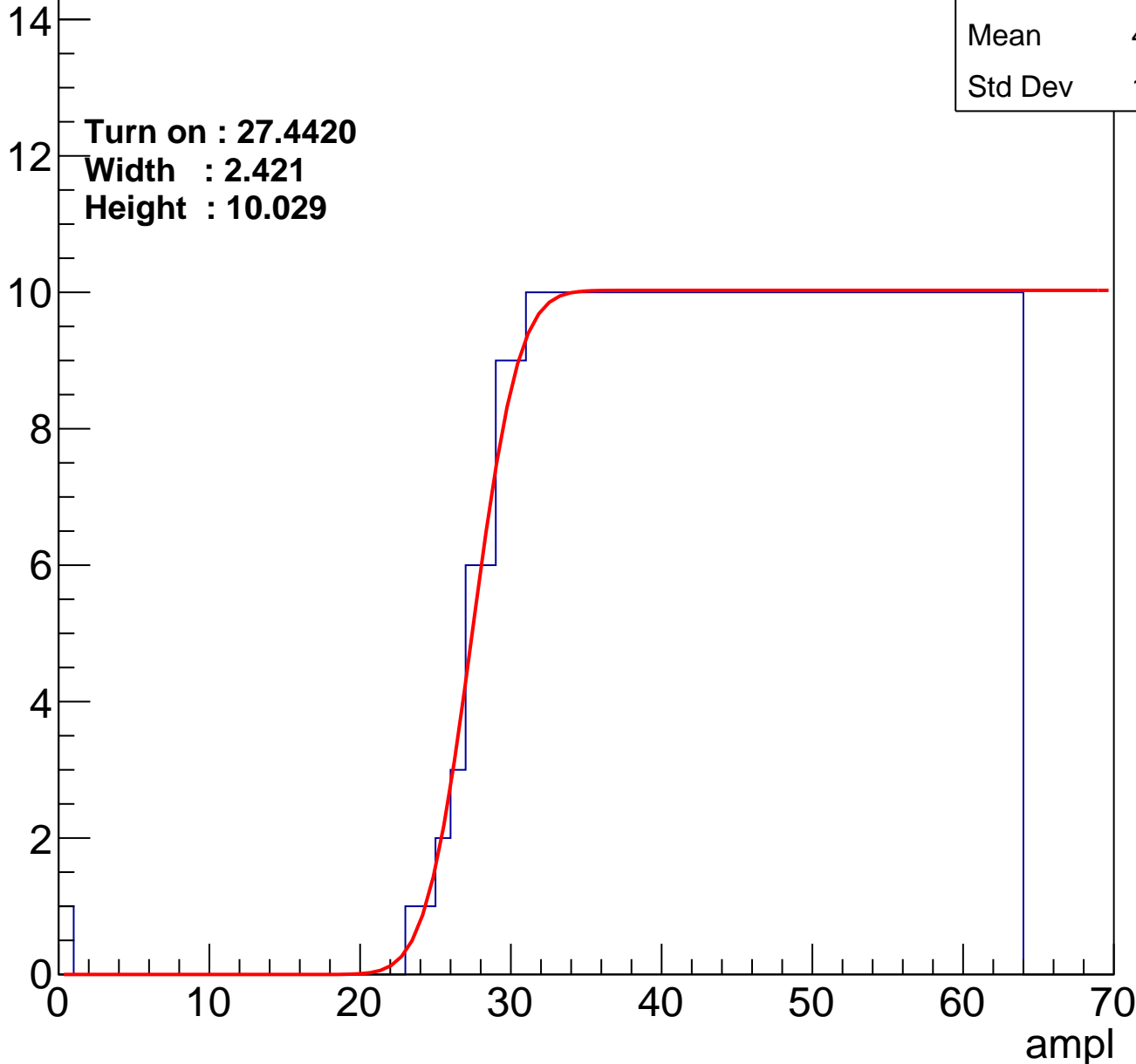
Entries	368
Mean	44.96
Std Dev	10.94

Turn on : 27.4420

Width : 2.421

Height : 10.029

Entry



# B1L002S, U6-ch64

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.4086

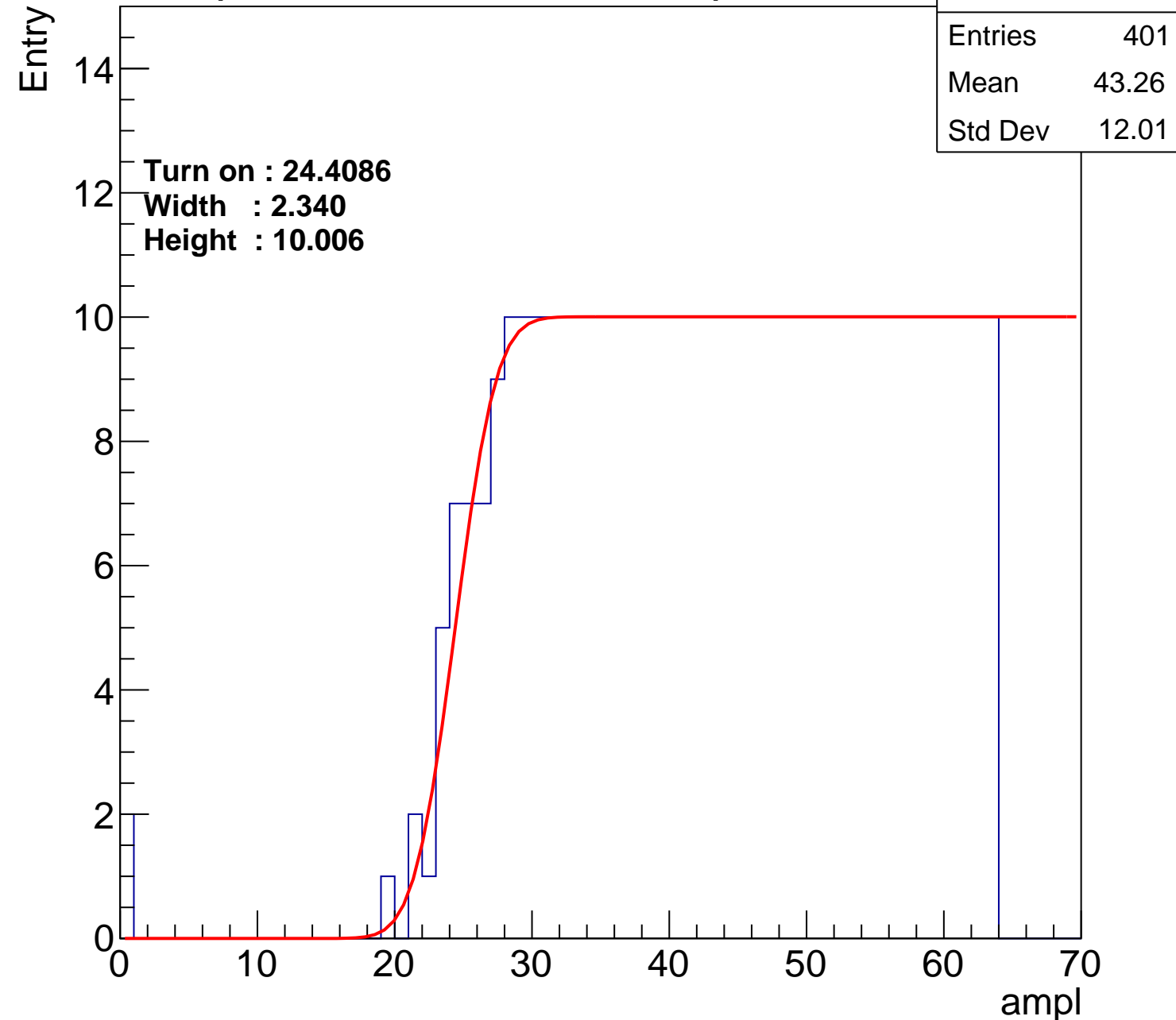
Width : 2.340

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch65

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 24.9883

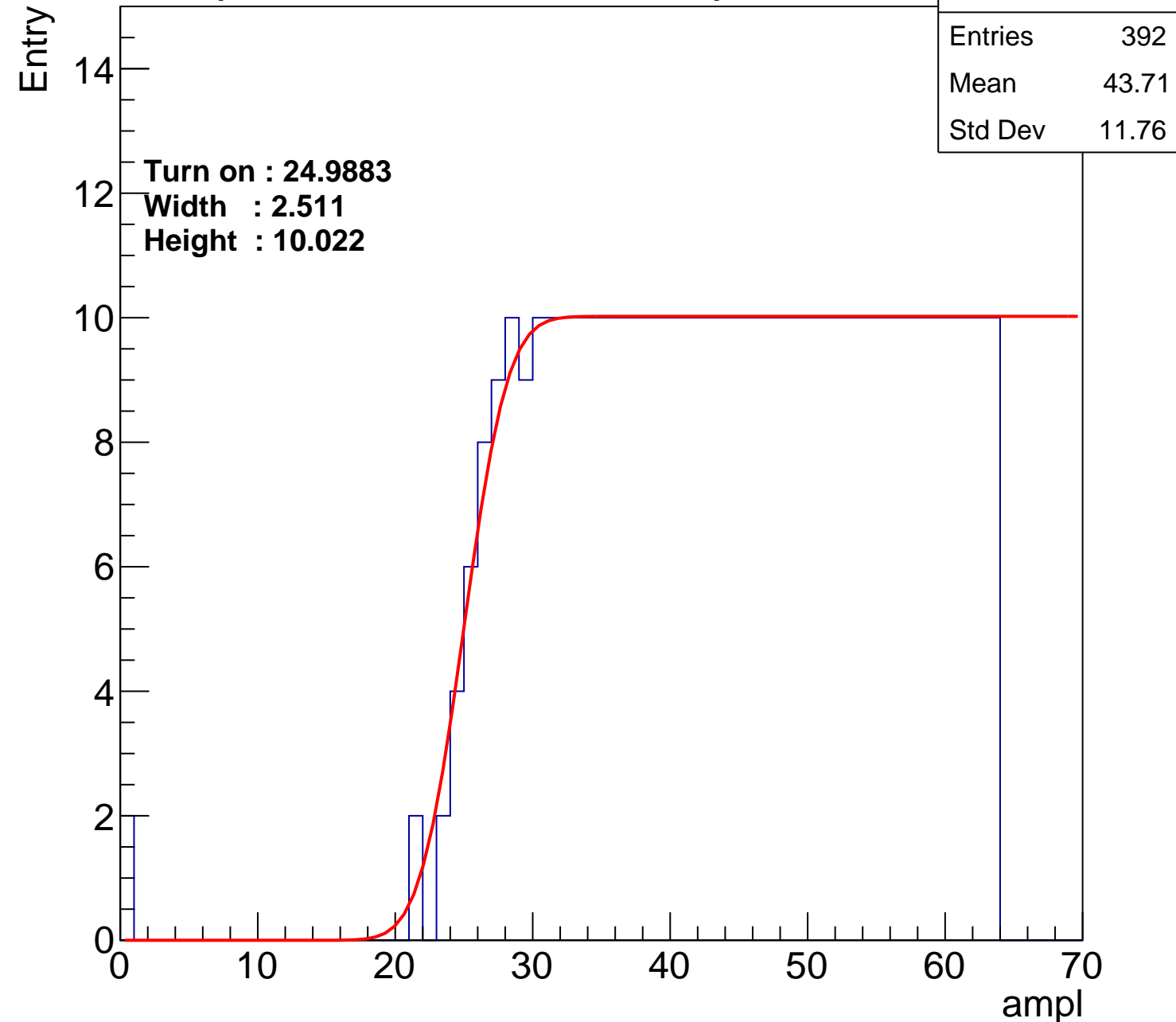
Width : 2.511

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch66

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.27

**Turn on : 26.8359**

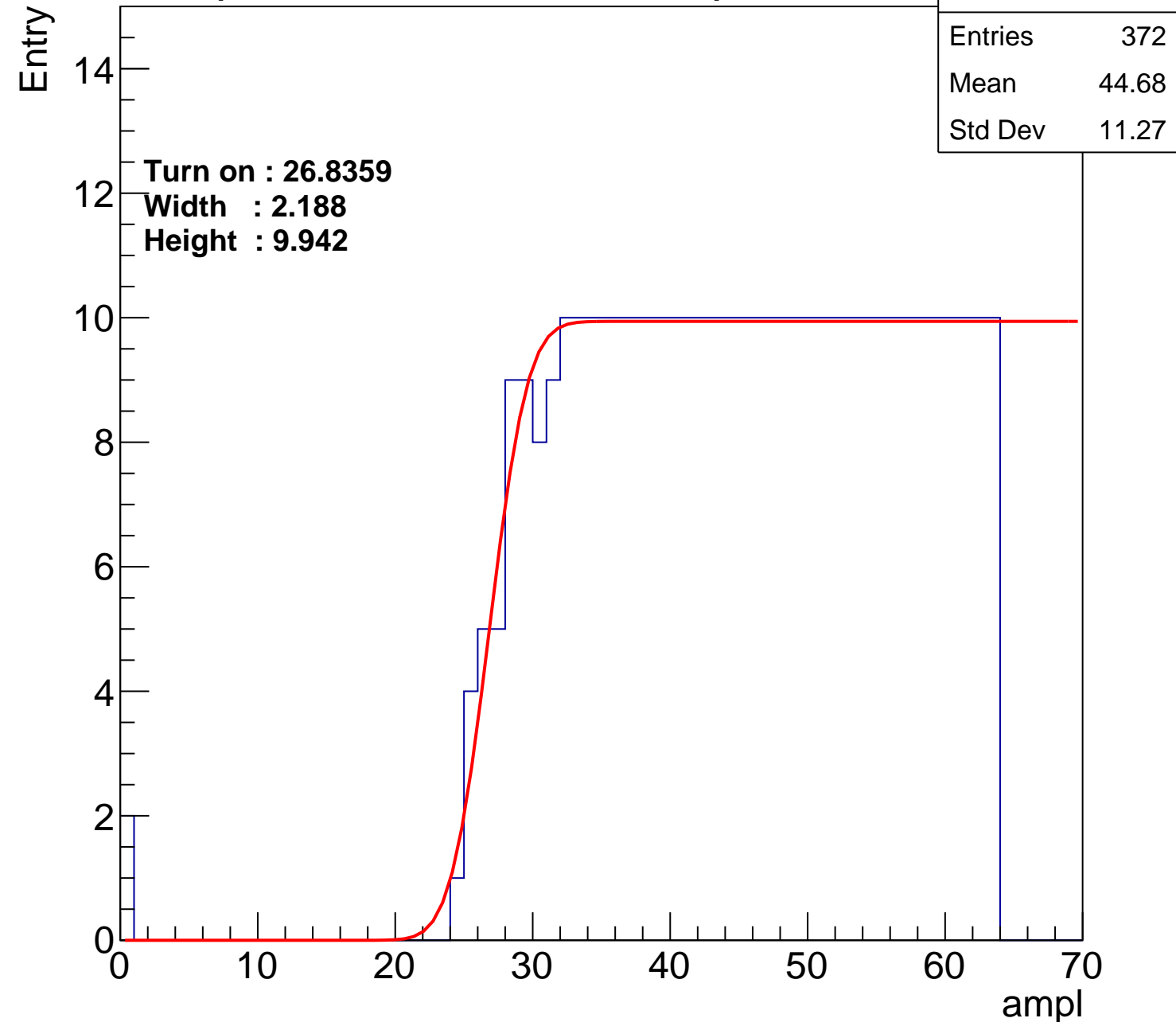
**Width : 2.188**

**Height : 9.942**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch67

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.74
Std Dev	11.29

Turn on : 27.4992

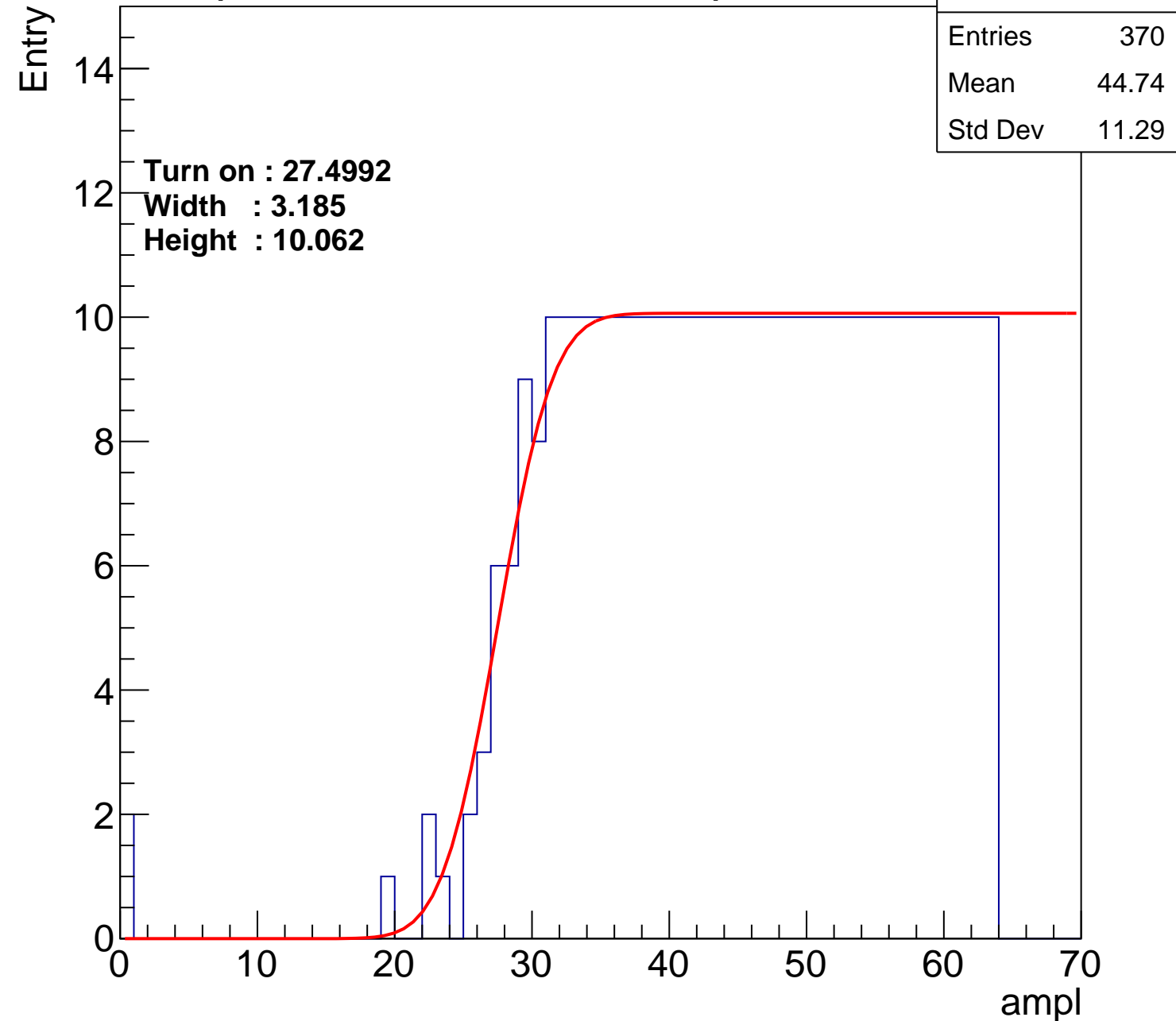
Width : 3.185

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch68

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	416
Mean	42.27
Std Dev	13.04

Turn on : 23.2866

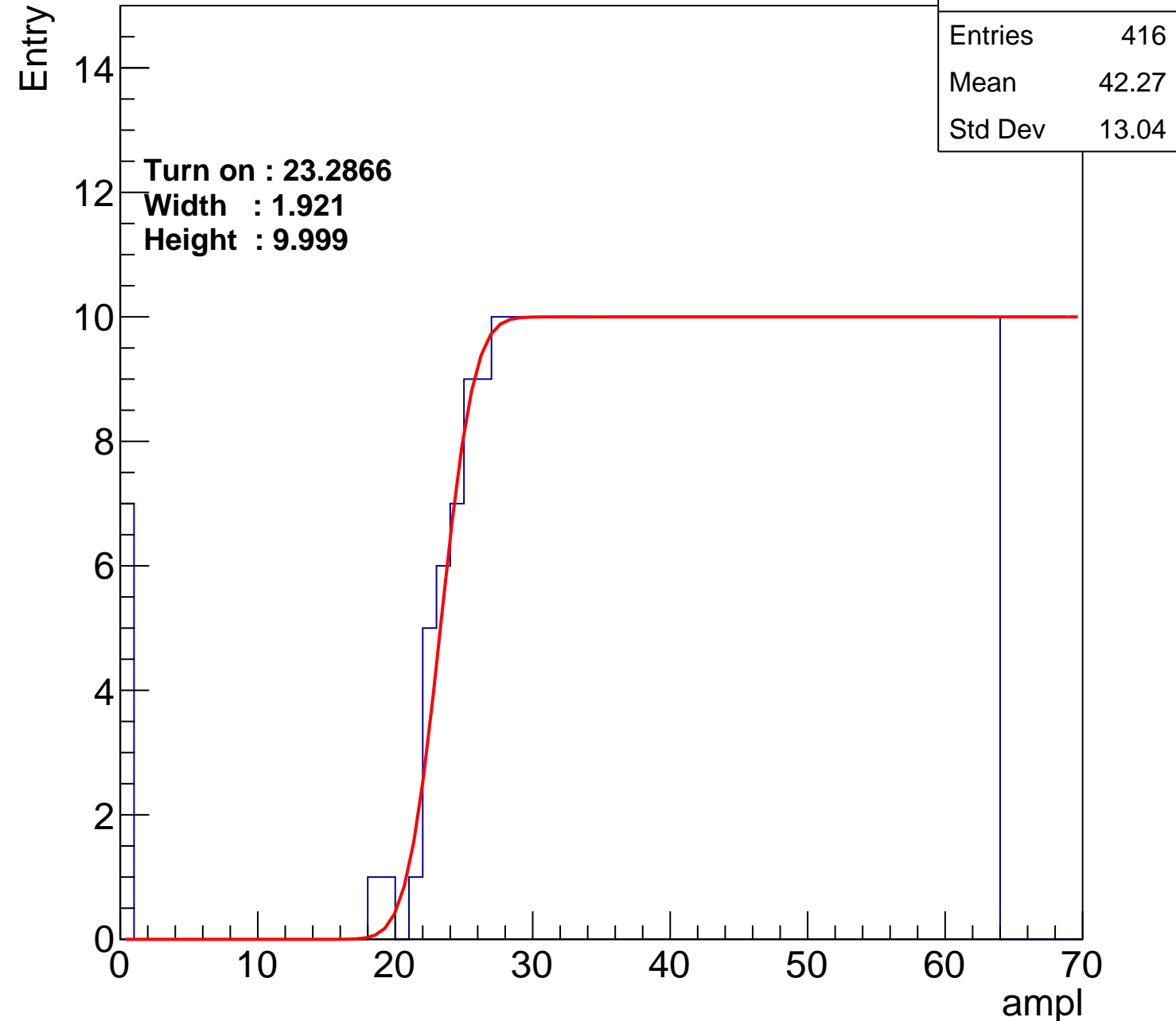
Width : 1.921

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch69

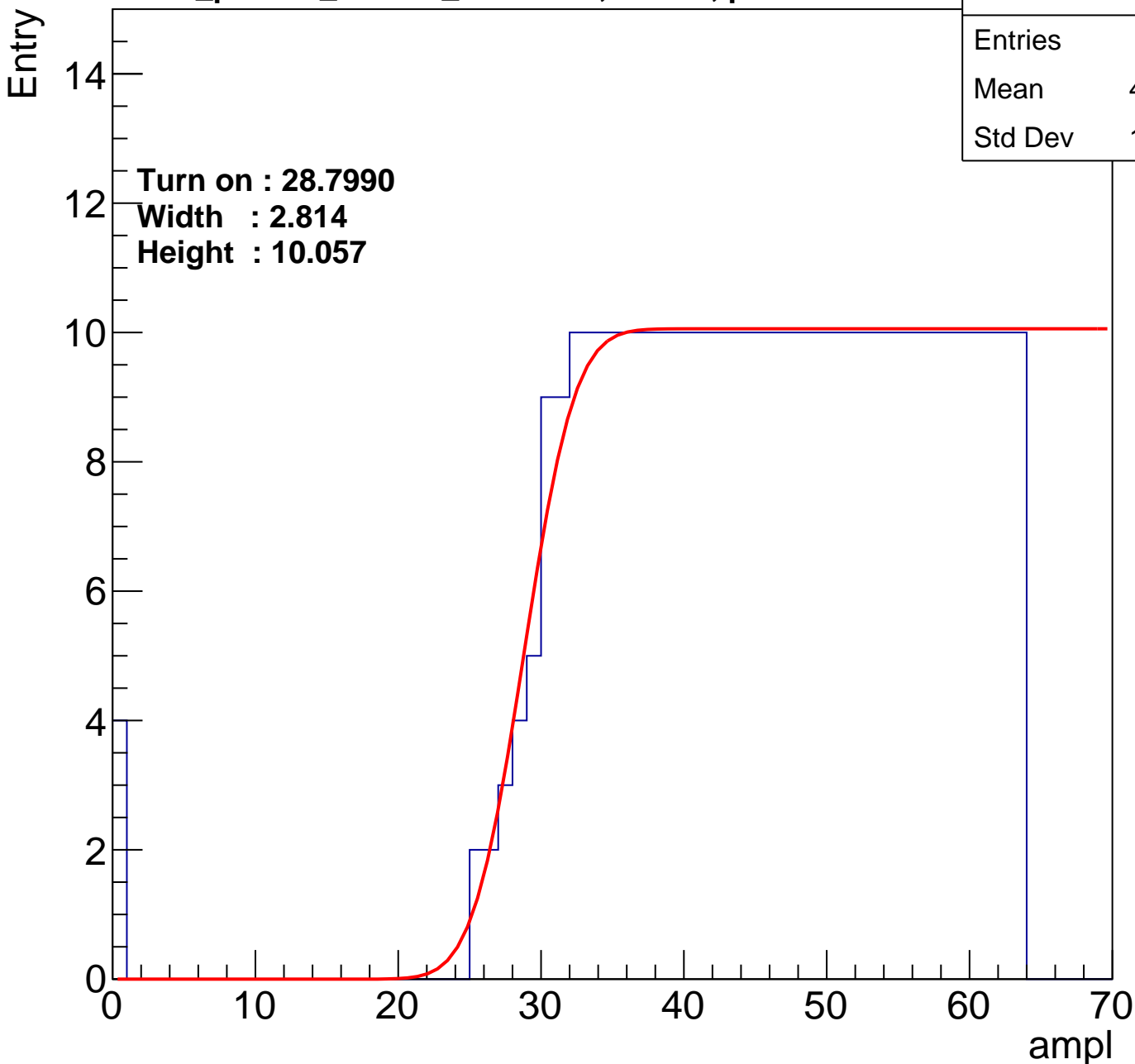
**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Entries	358
Mean	45.22
Std Dev	11.35

**Turn on : 28.7990**

**Width : 2.814**

**Height : 10.057**



# B1L002S, U6-ch70

calib\_packv5\_042523\_0143.root, FC#10, port B3

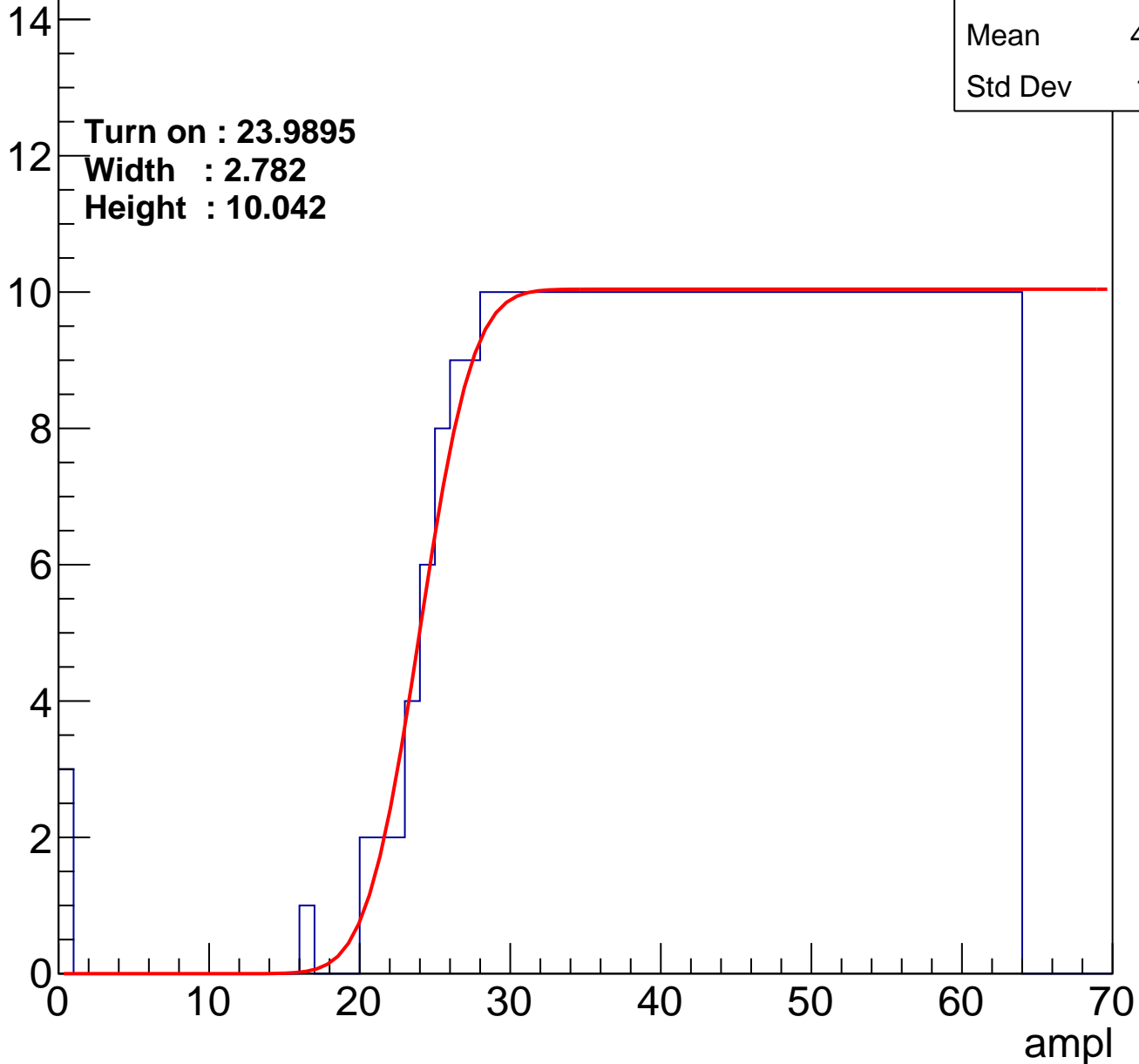
Entries	406
Mean	42.94
Std Dev	12.31

Turn on : 23.9895

Width : 2.782

Height : 10.042

Entry





# B1L002S, U6-ch71

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.7468

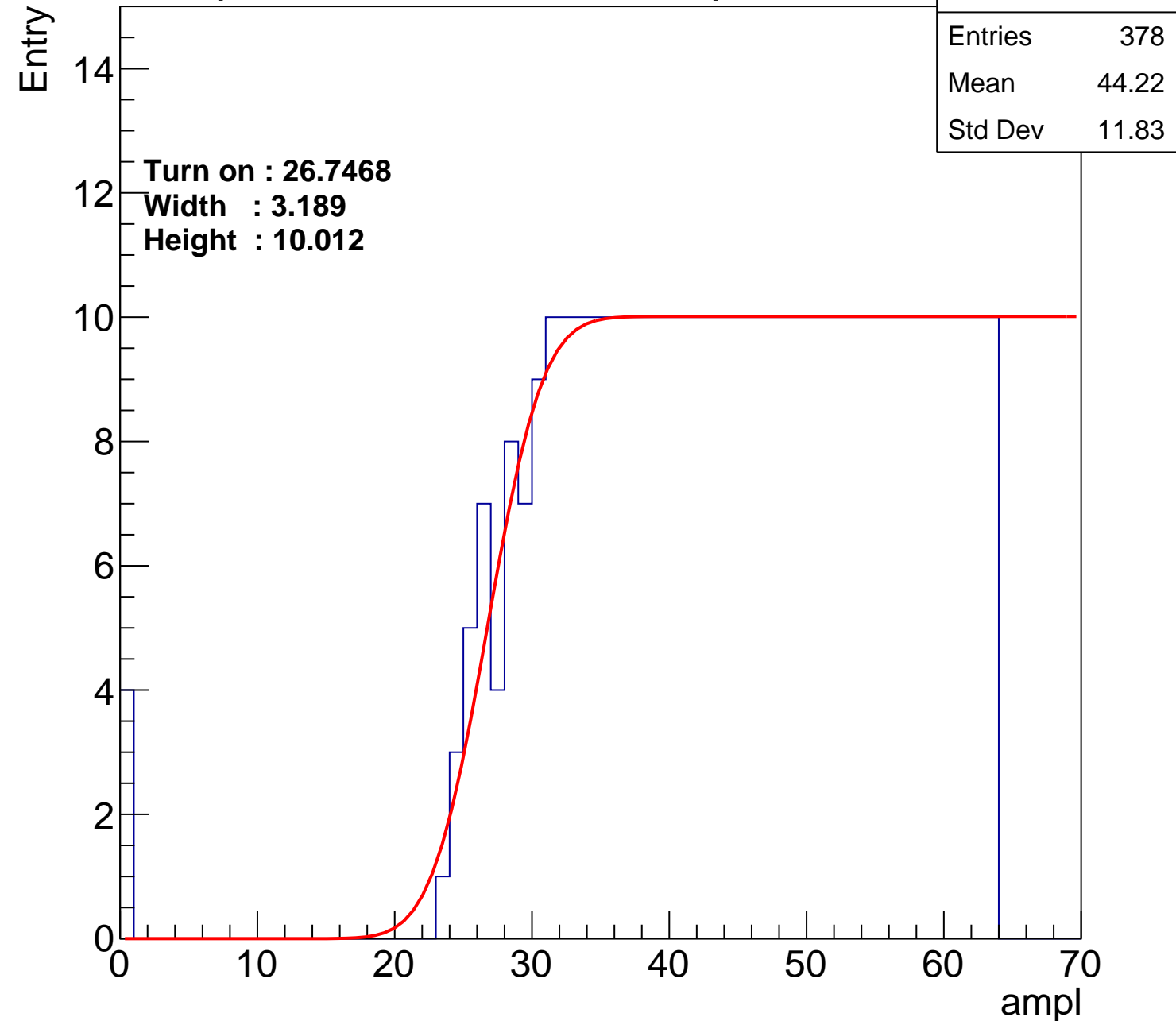
Width : 3.189

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch72

calib\_packv5\_042523\_0143.root, FC#10, port B3

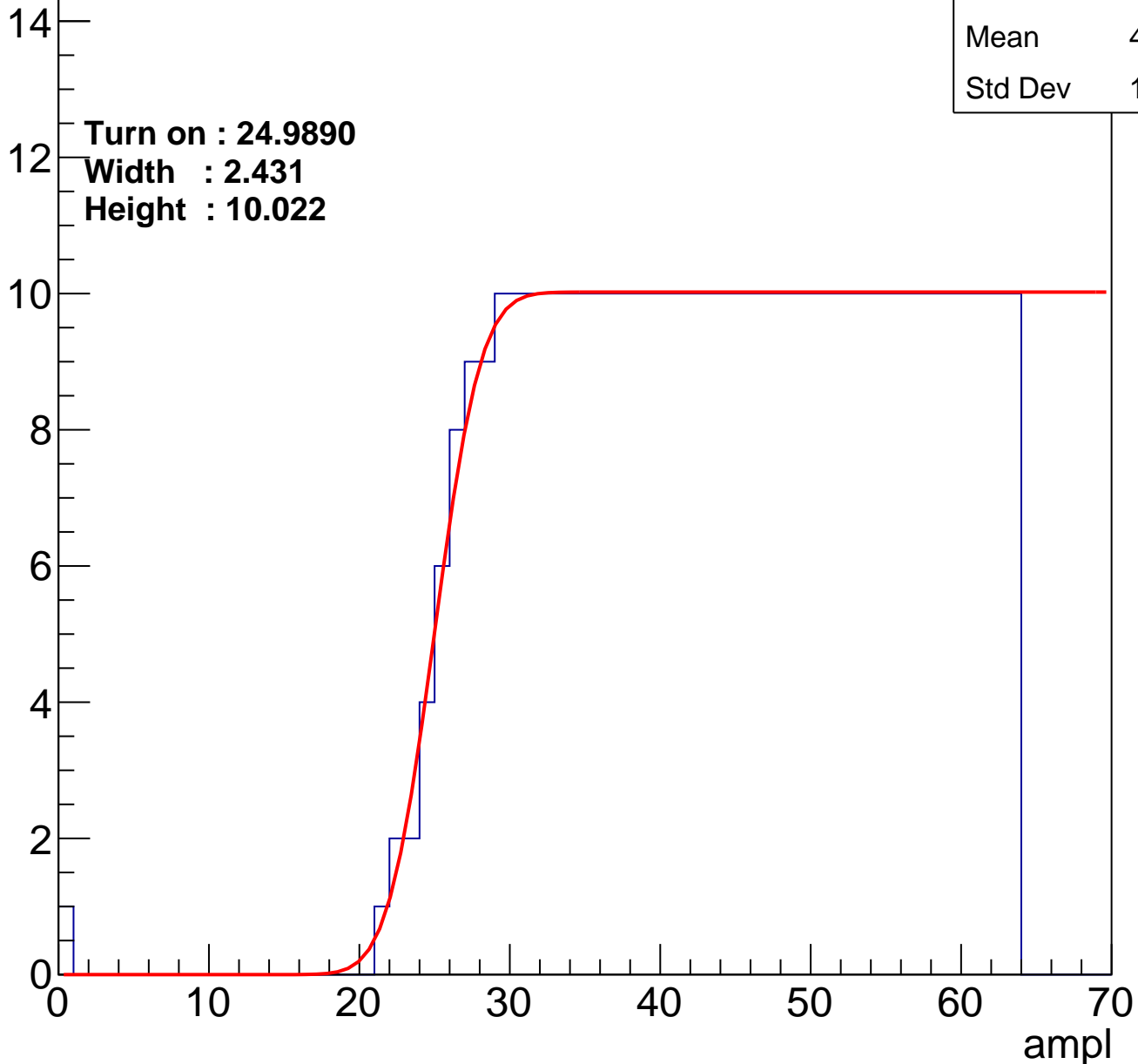
Entries	392
Mean	43.78
Std Dev	11.59

Turn on : 24.9890

Width : 2.431

Height : 10.022

Entry



# B1L002S, U6-ch73

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.45
Std Dev	11.68

**Turn on : 27.3085**

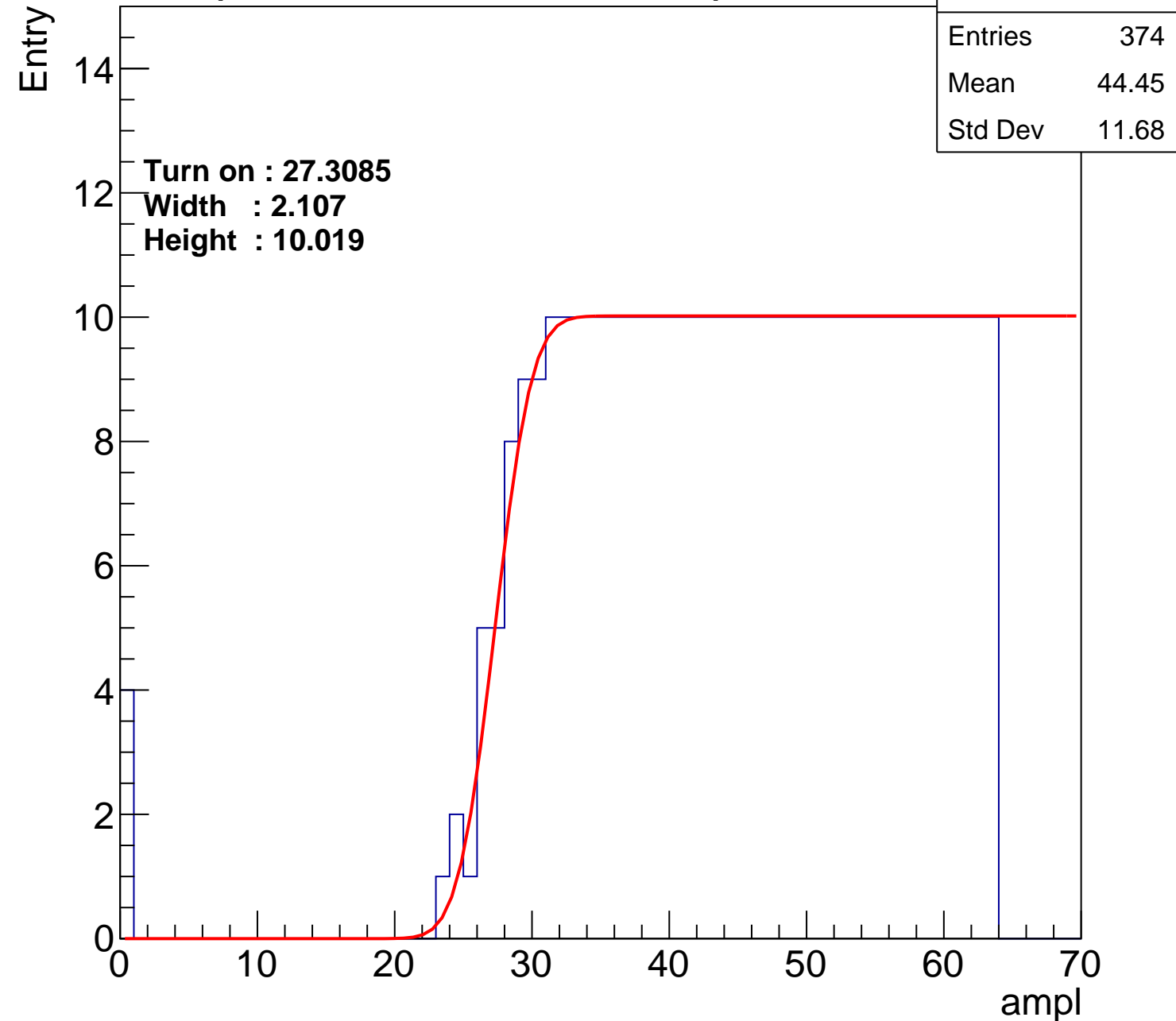
**Width : 2.107**

**Height : 10.019**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch74

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.45
Std Dev	12.03

Turn on : 24.7355

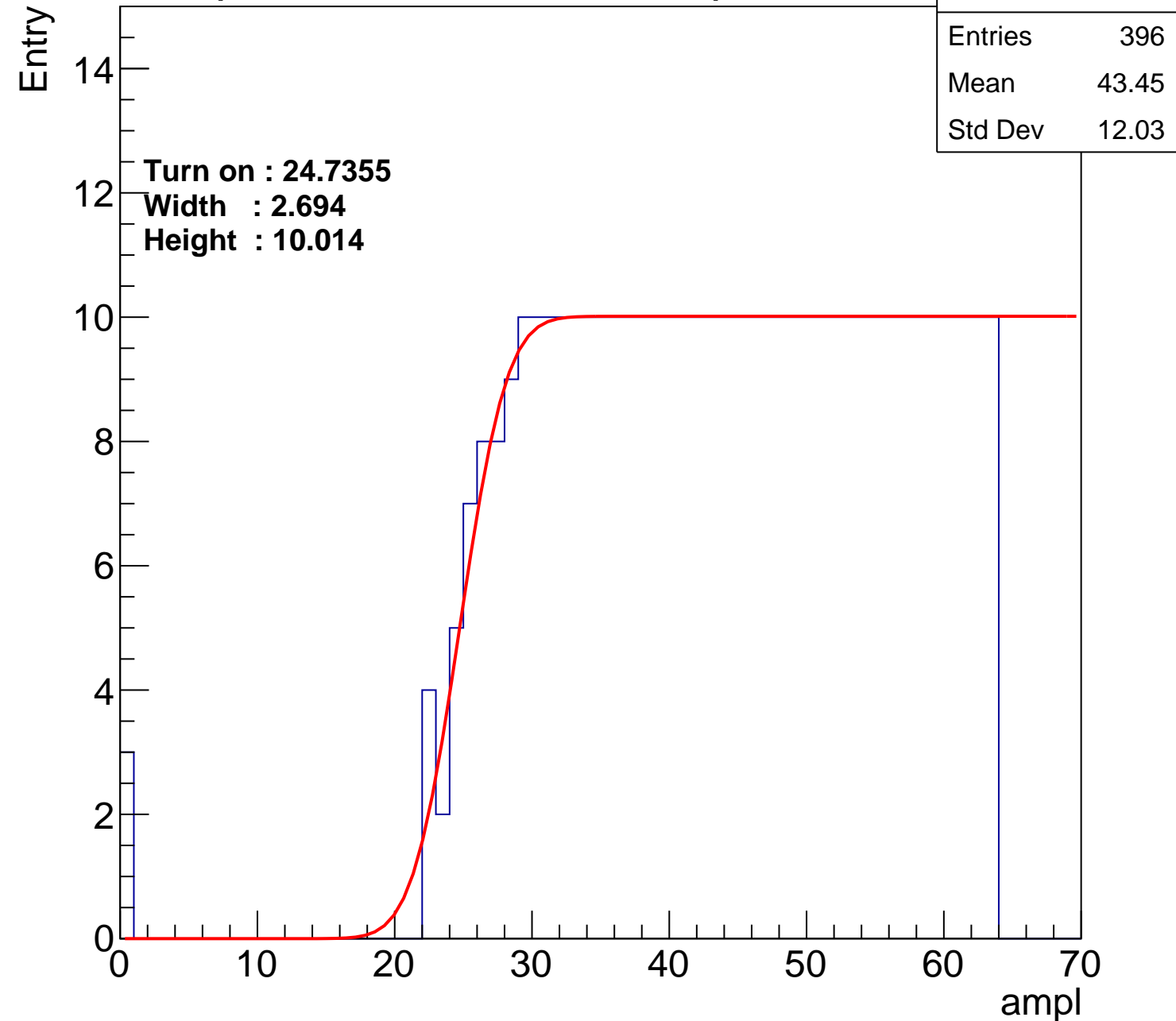
Width : 2.694

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch75

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.3621

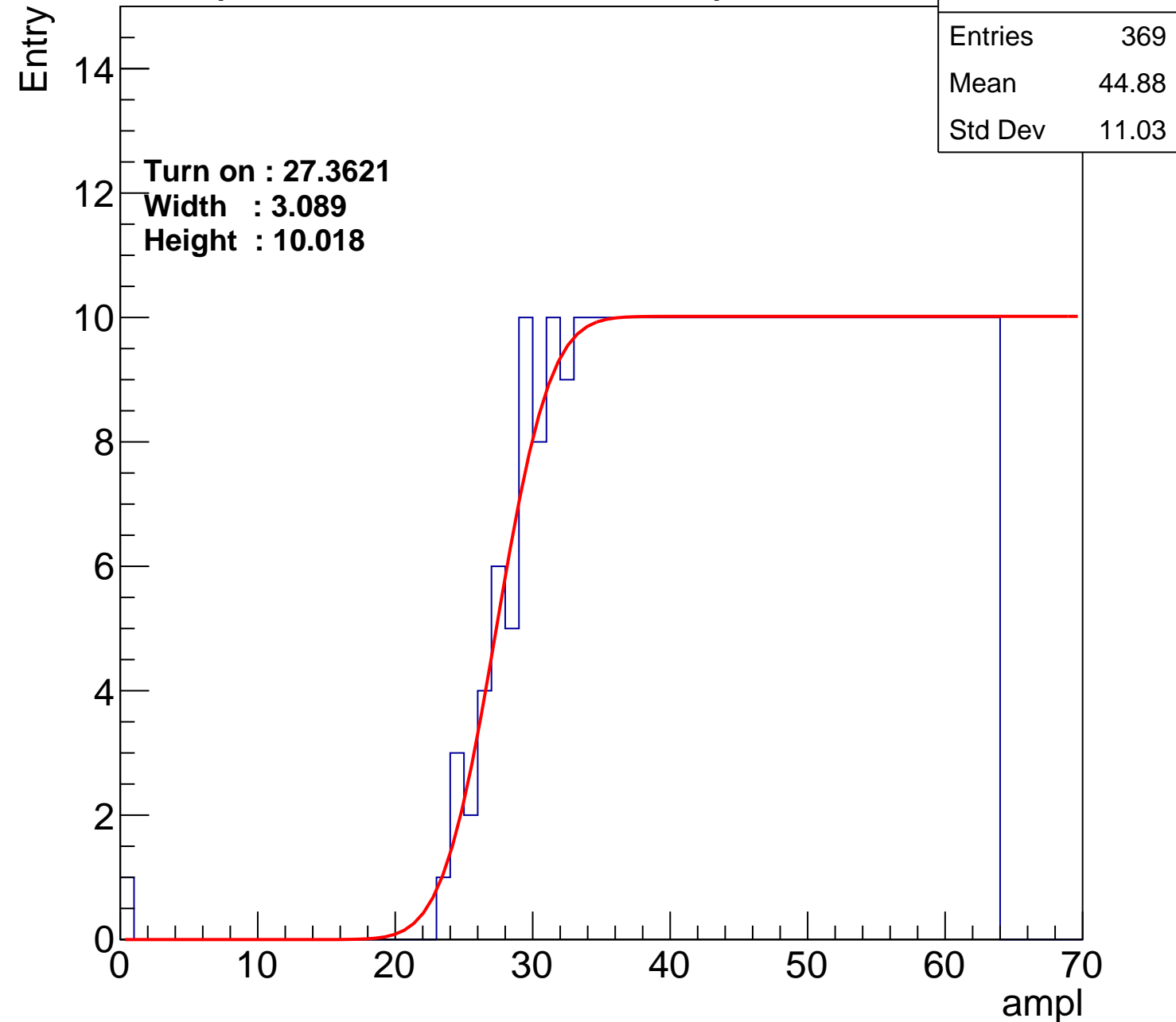
Width : 3.089

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch76

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.73
Std Dev	11.61

Turn on : 24.8202

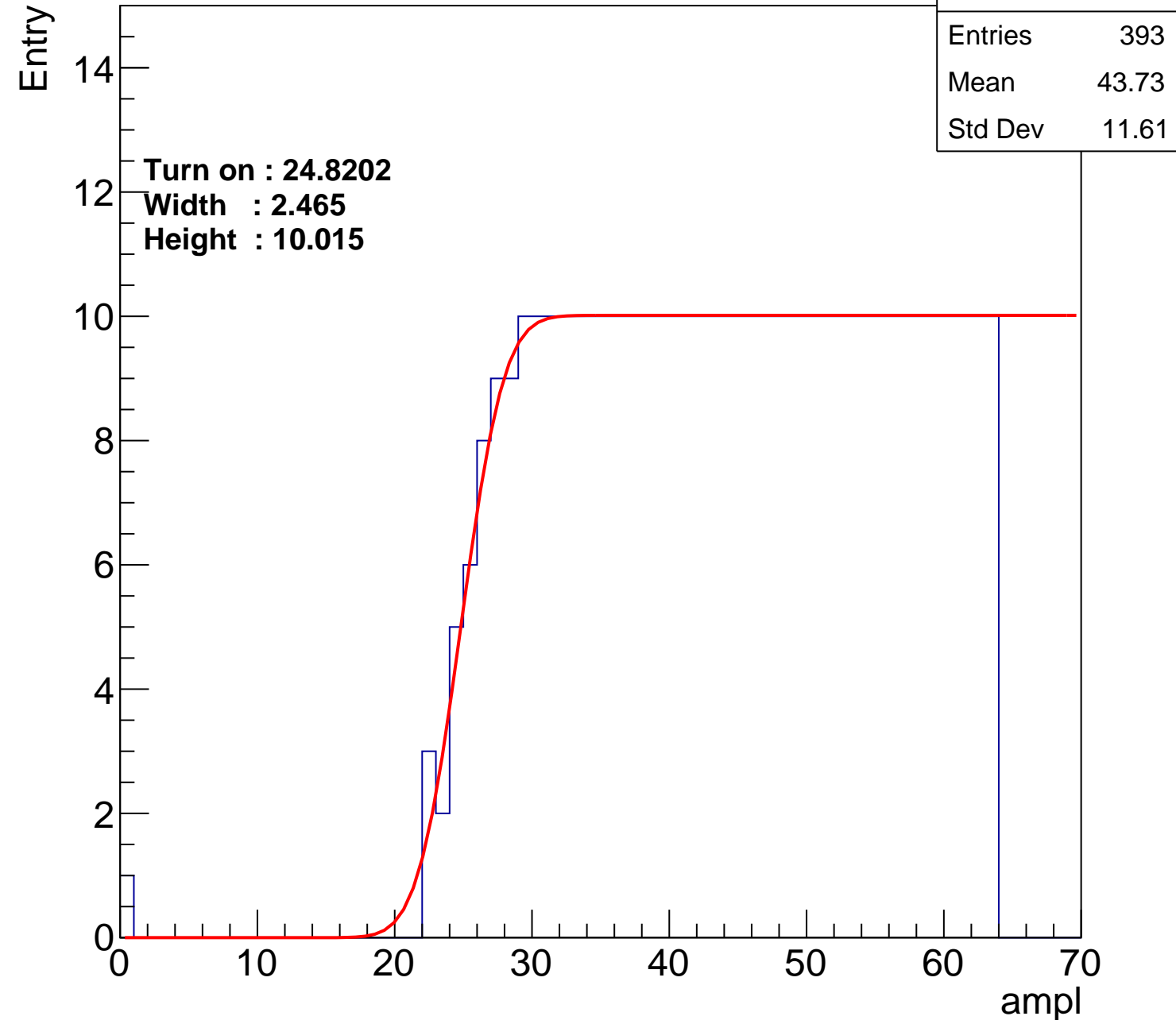
Width : 2.465

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch77

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.56
Std Dev	11.72

Turn on : 25.1084

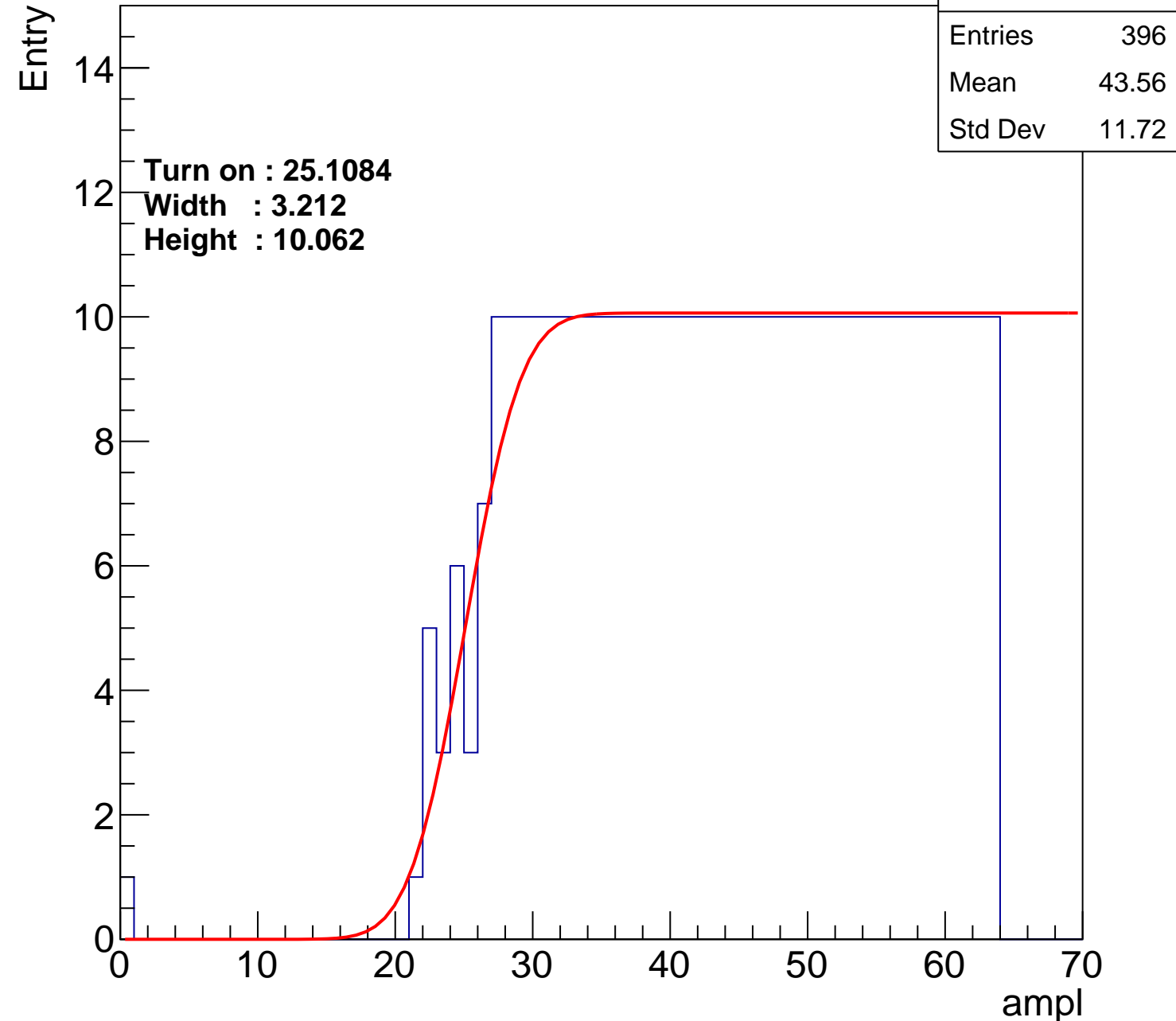
Width : 3.212

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch78

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 27.1246

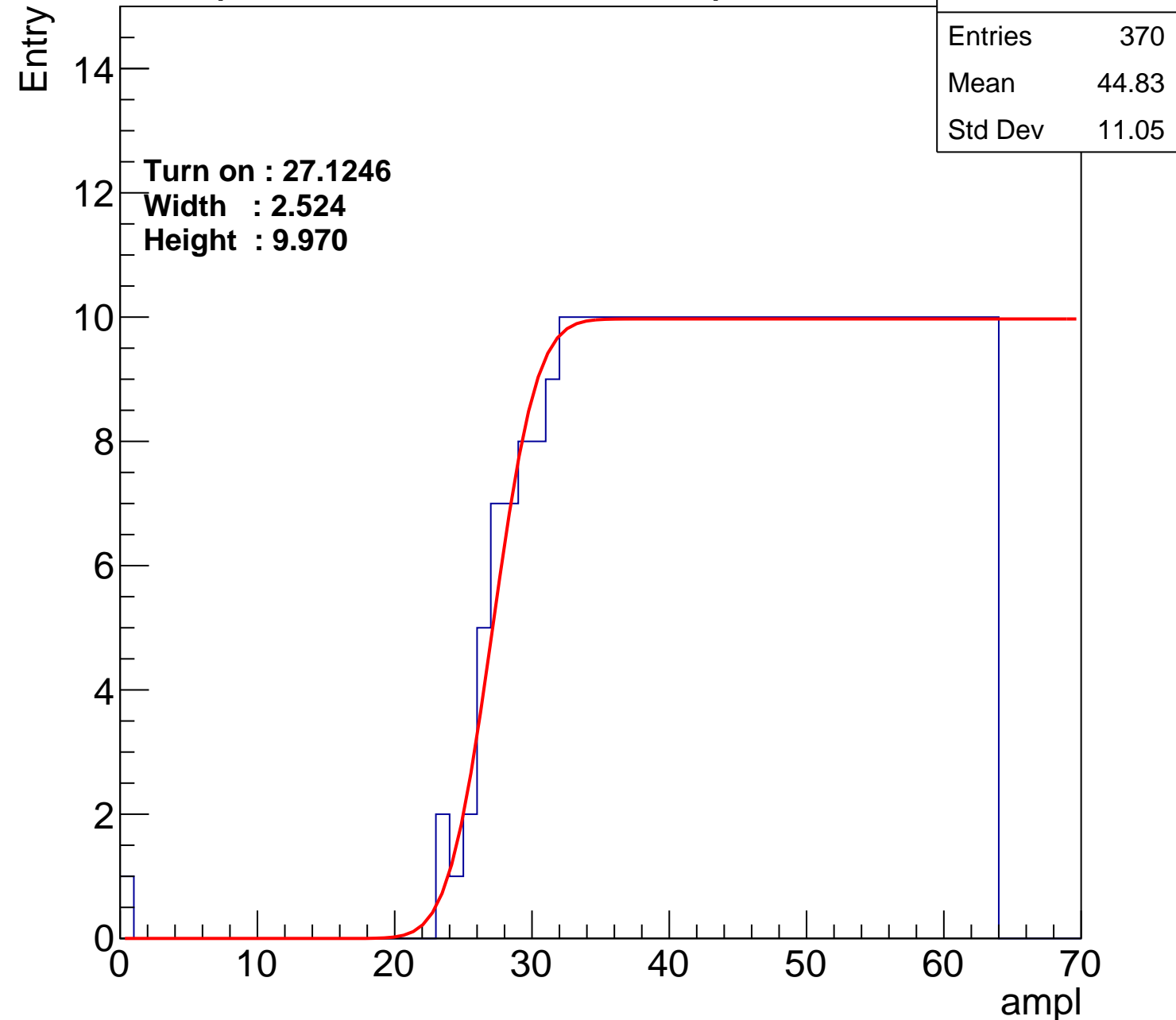
Width : 2.524

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch79

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.7984

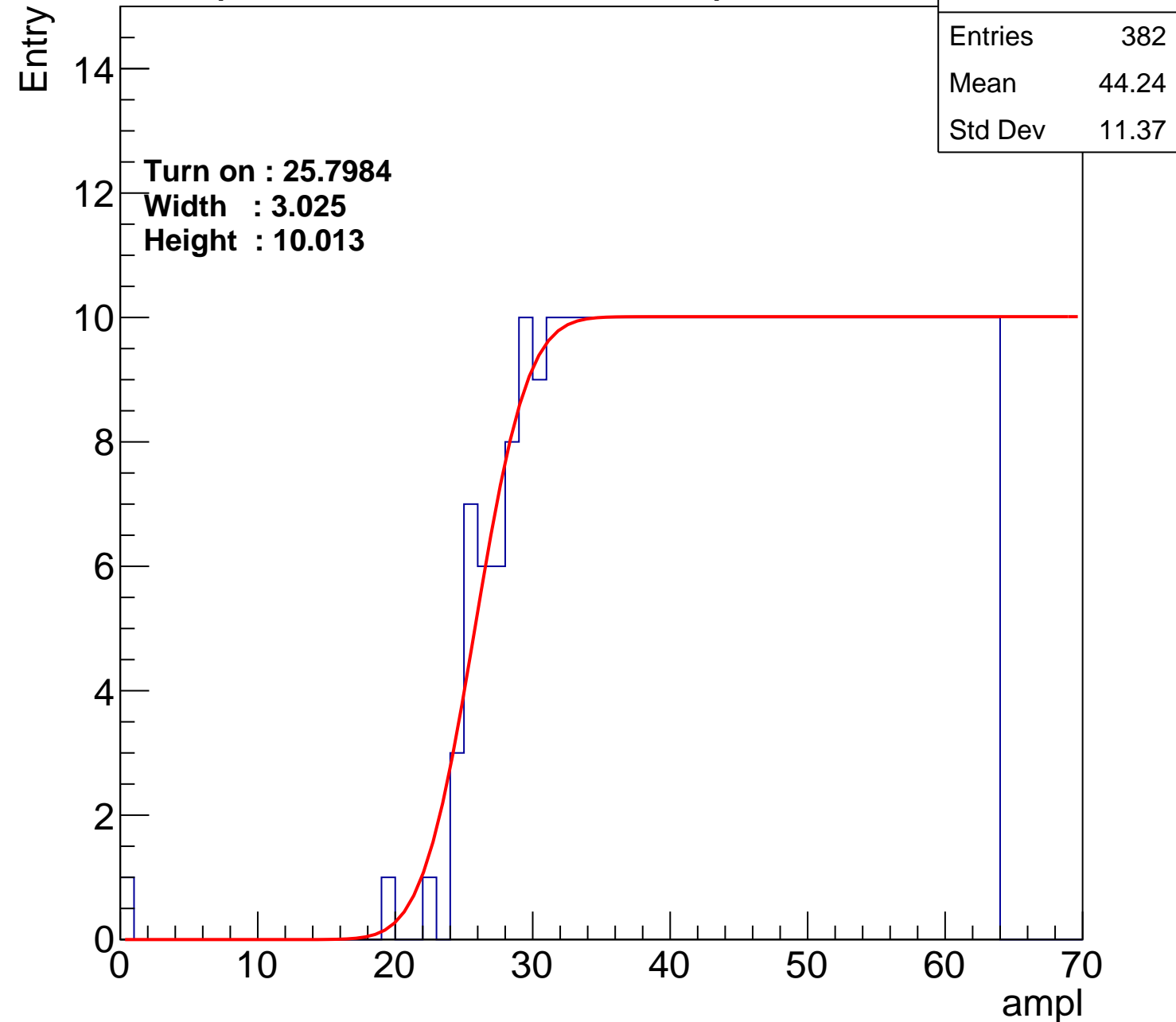
Width : 3.025

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch80

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	394
Mean	43.62
Std Dev	11.8

Turn on : 25.0837

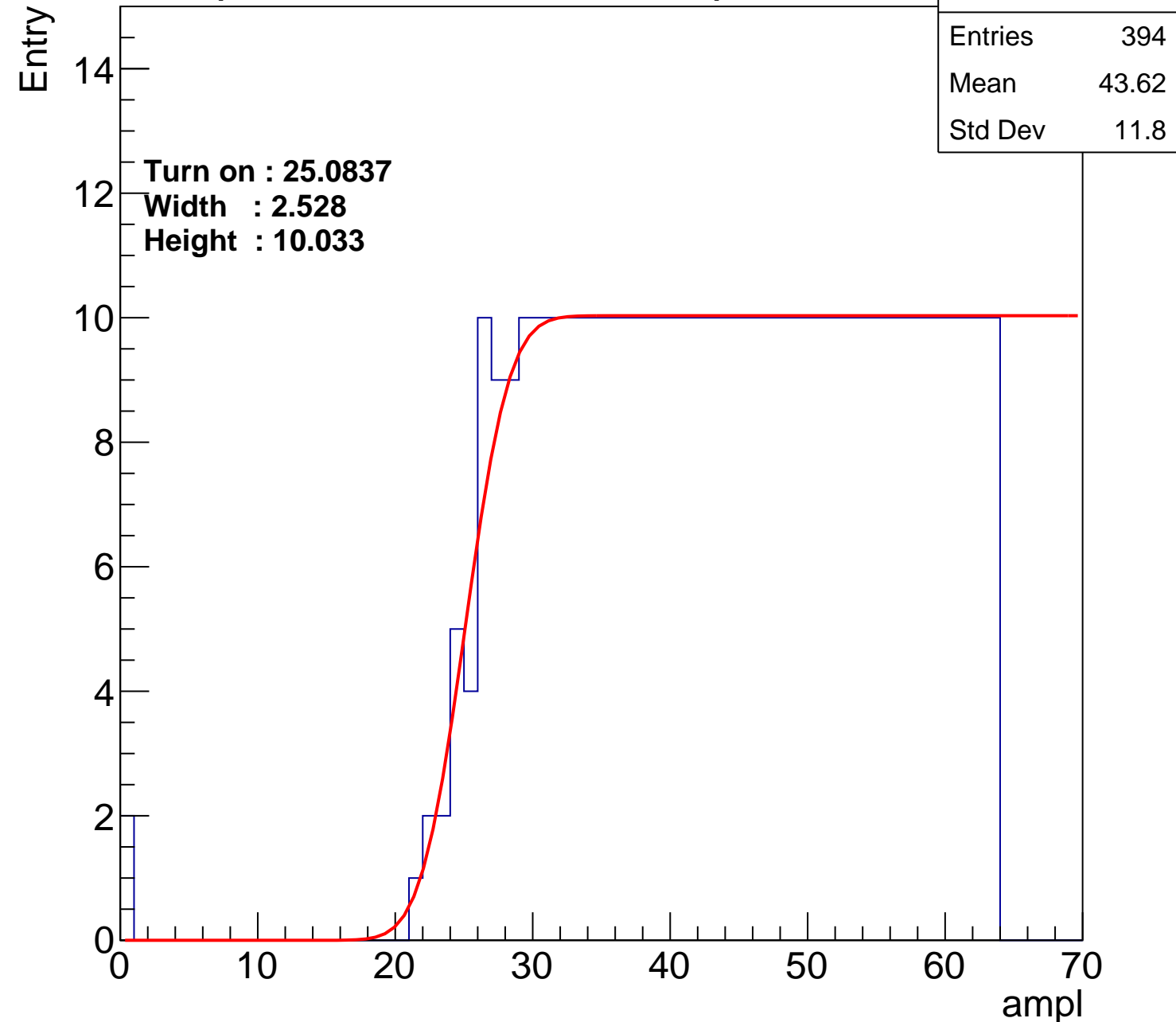
Width : 2.528

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch81

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 25.8572

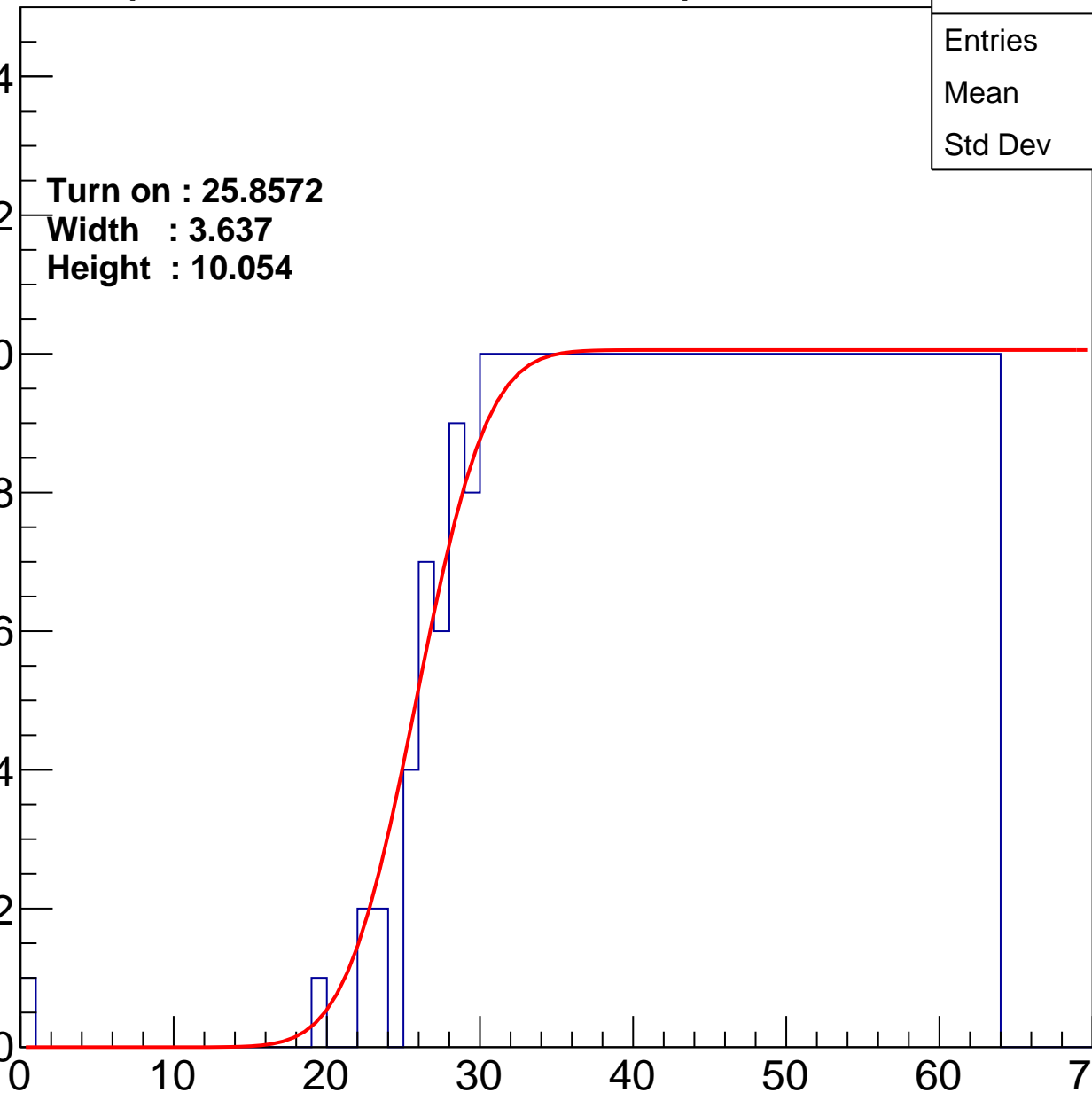
Width : 3.637

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch82

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.7554

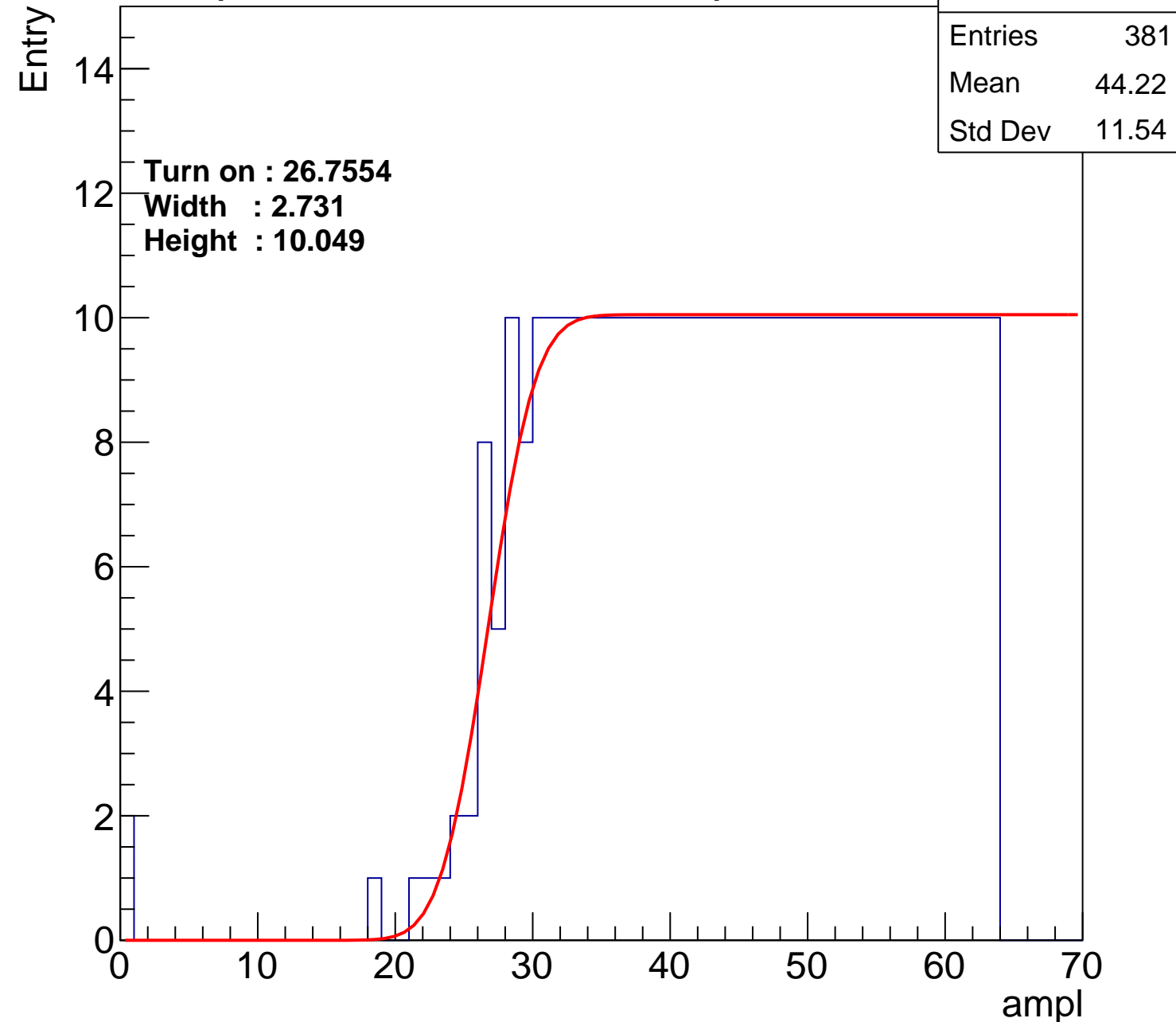
Width : 2.731

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch83

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.3372

Width : 2.504

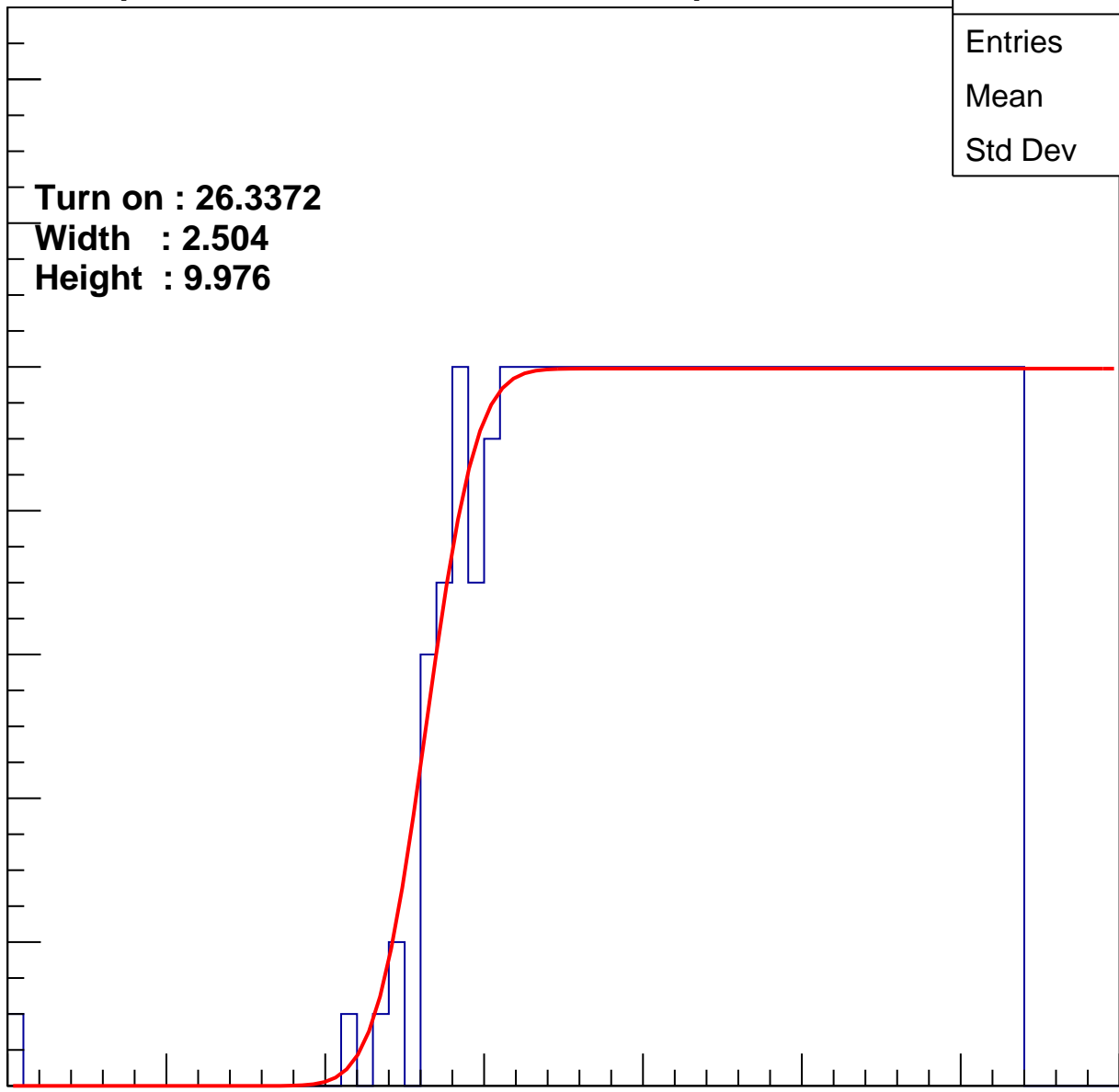
Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U6-ch84

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	405
Mean	42.88
Std Dev	12.57

Turn on : 24.4651

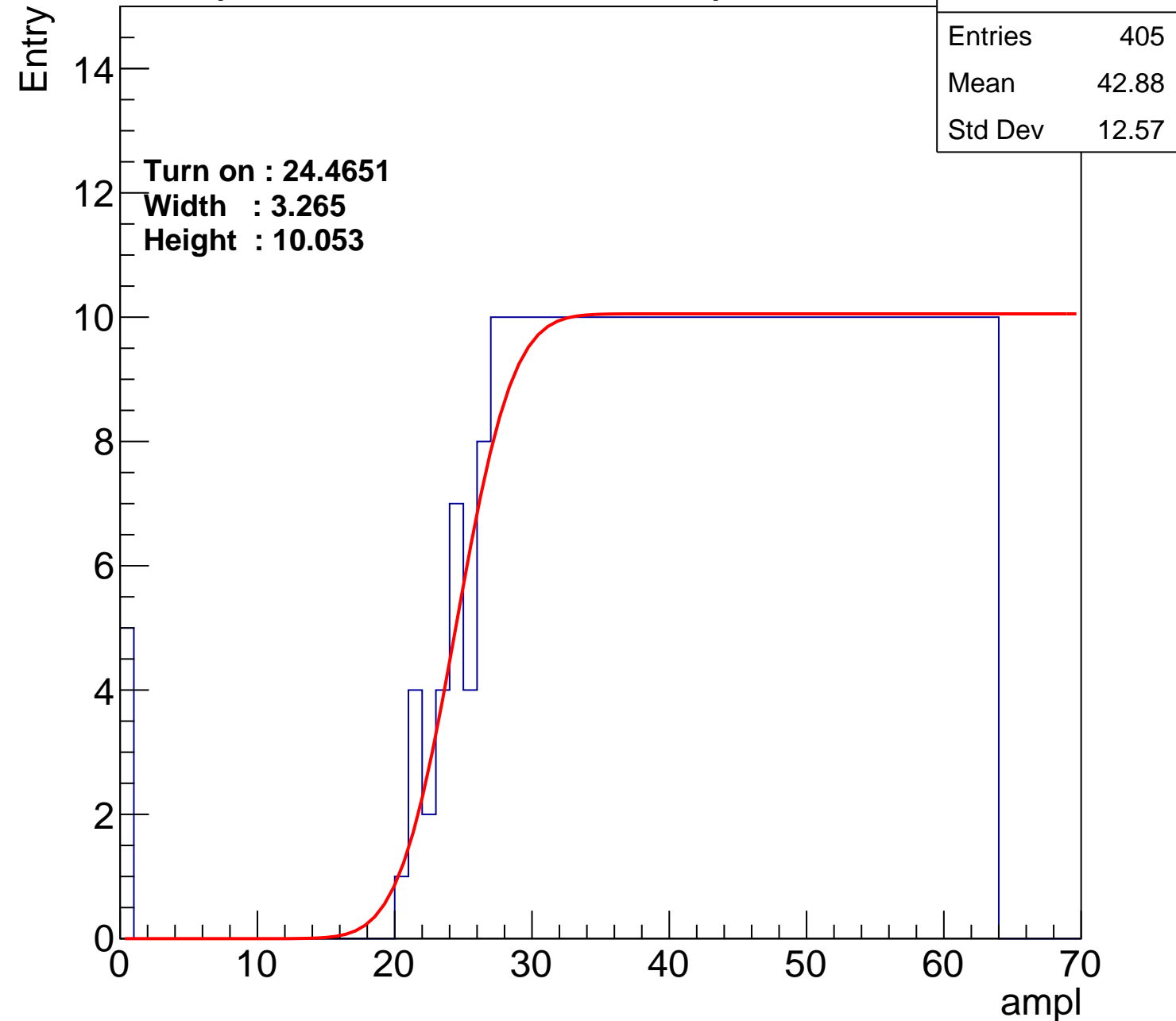
Width : 3.265

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch85

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	373
Mean	44.58
Std Dev	11.47

Turn on : 27.4232

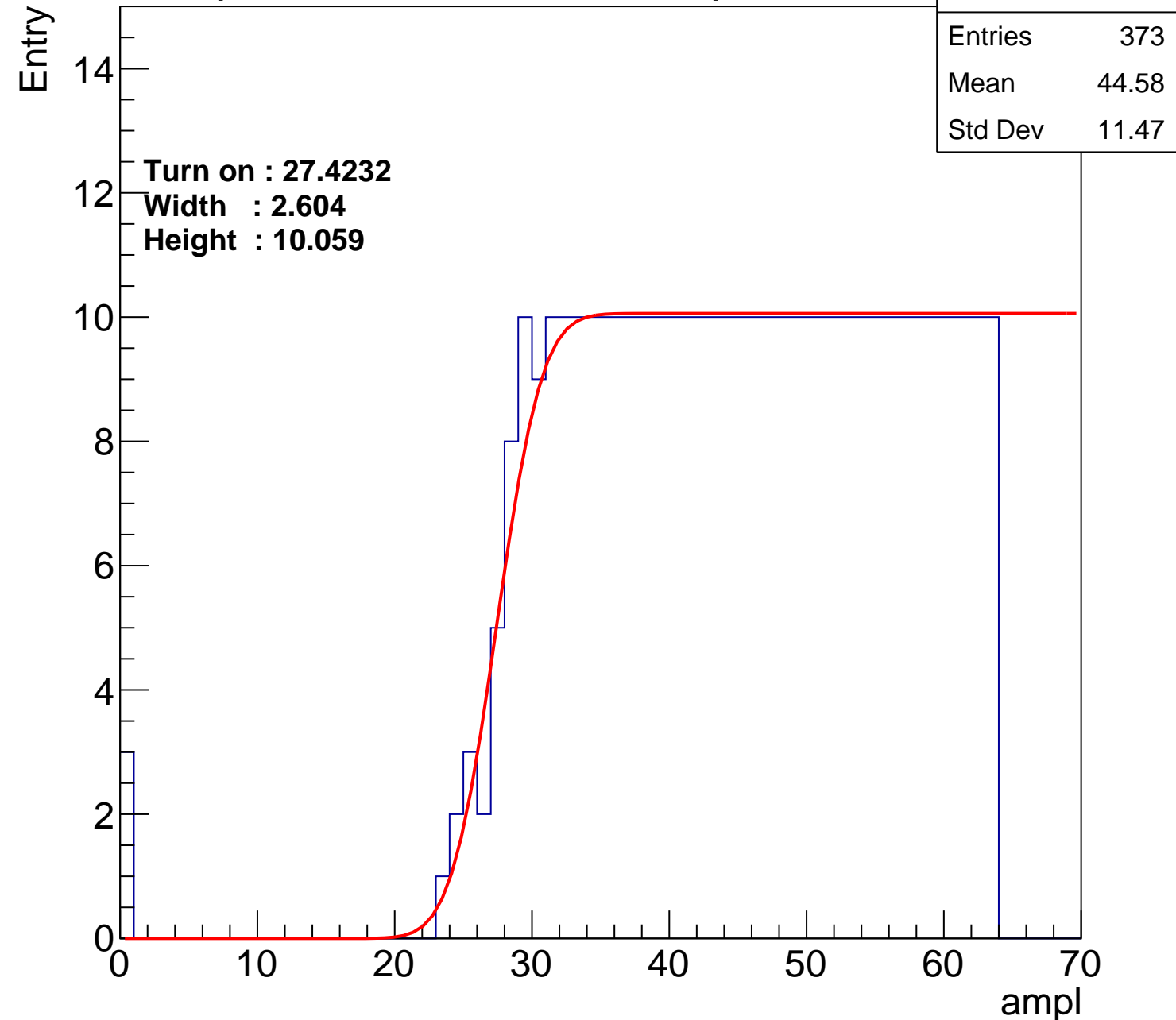
Width : 2.604

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch86

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	376
Mean	44.58
Std Dev	11.13

Turn on : 26.7604

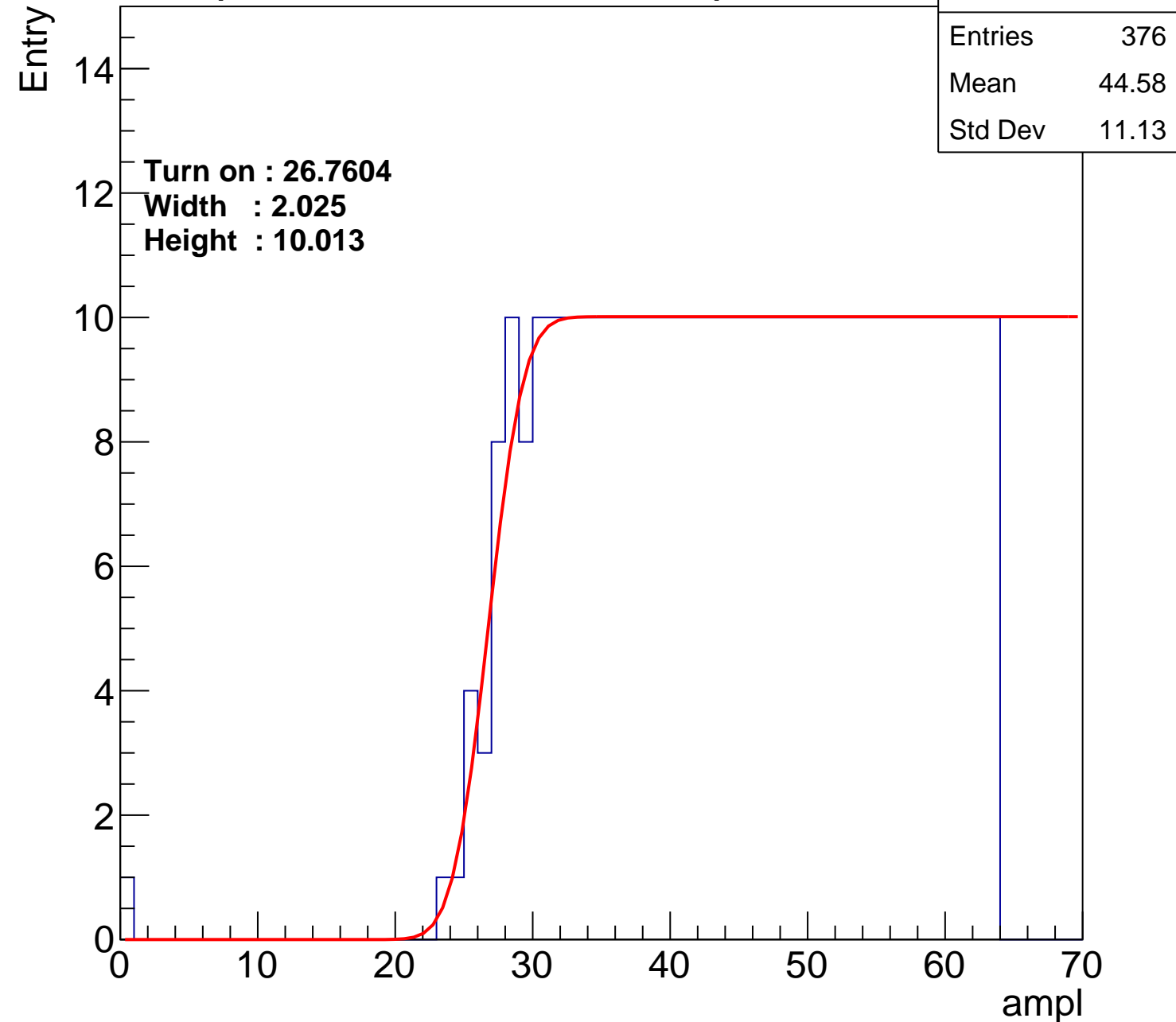
Width : 2.025

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch87

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	365
Mean	44.96
Std Dev	11.19

Turn on : 27.7004

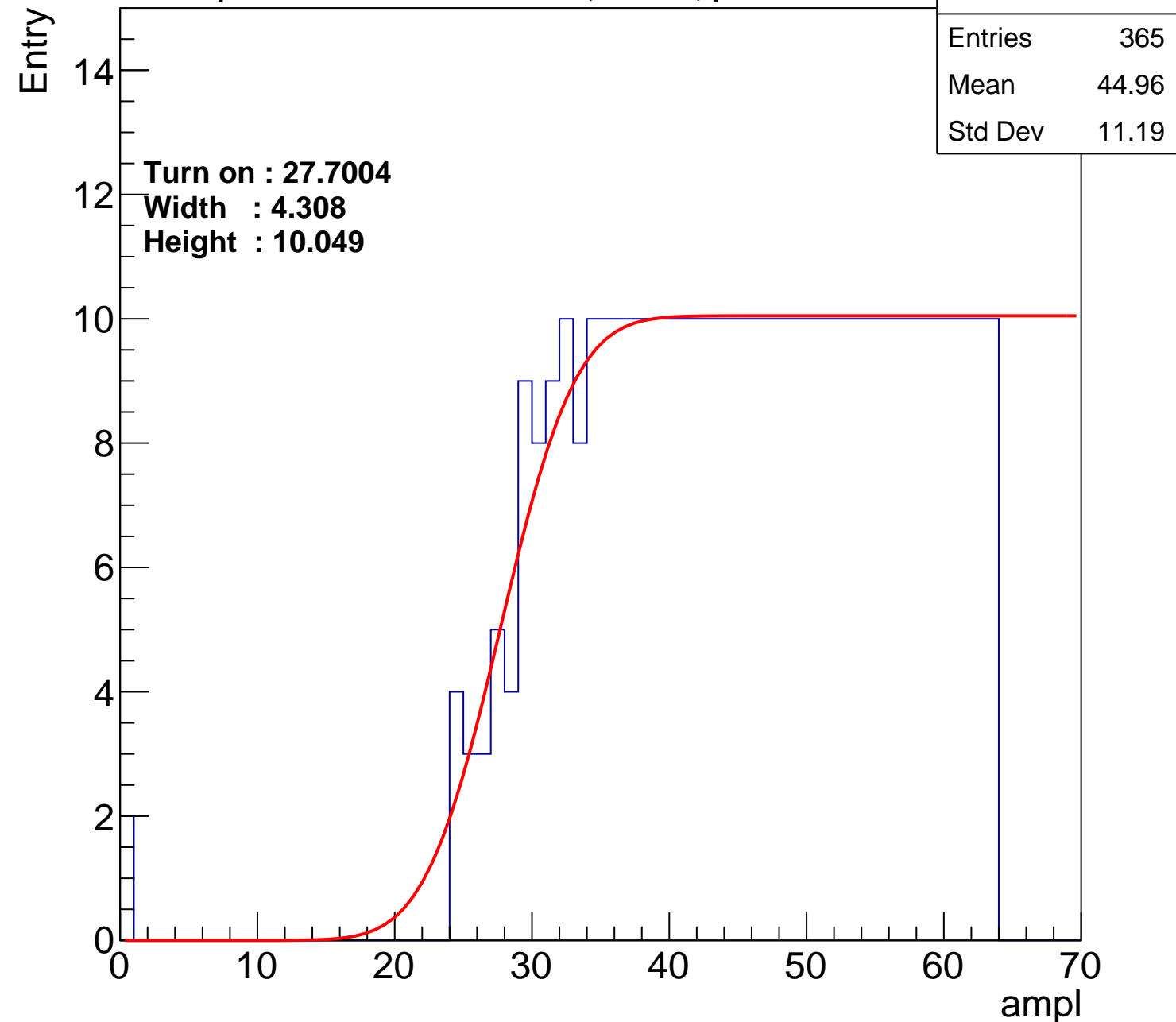
Width : 4.308

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch88

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.06
Std Dev	11.87

Turn on : 25.9783

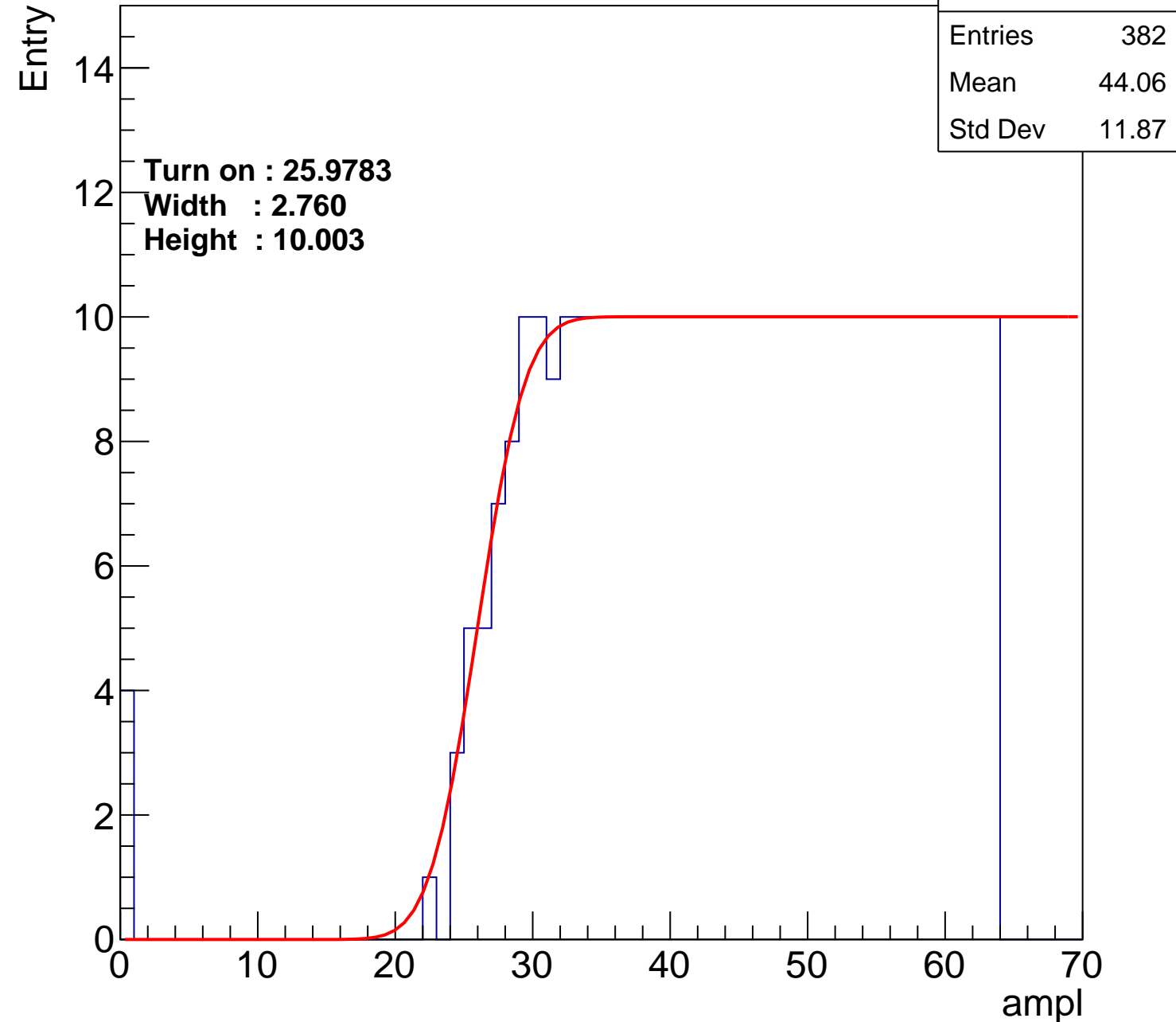
Width : 2.760

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch89

calib\_packv5\_042523\_0143.root, FC#10, port B3

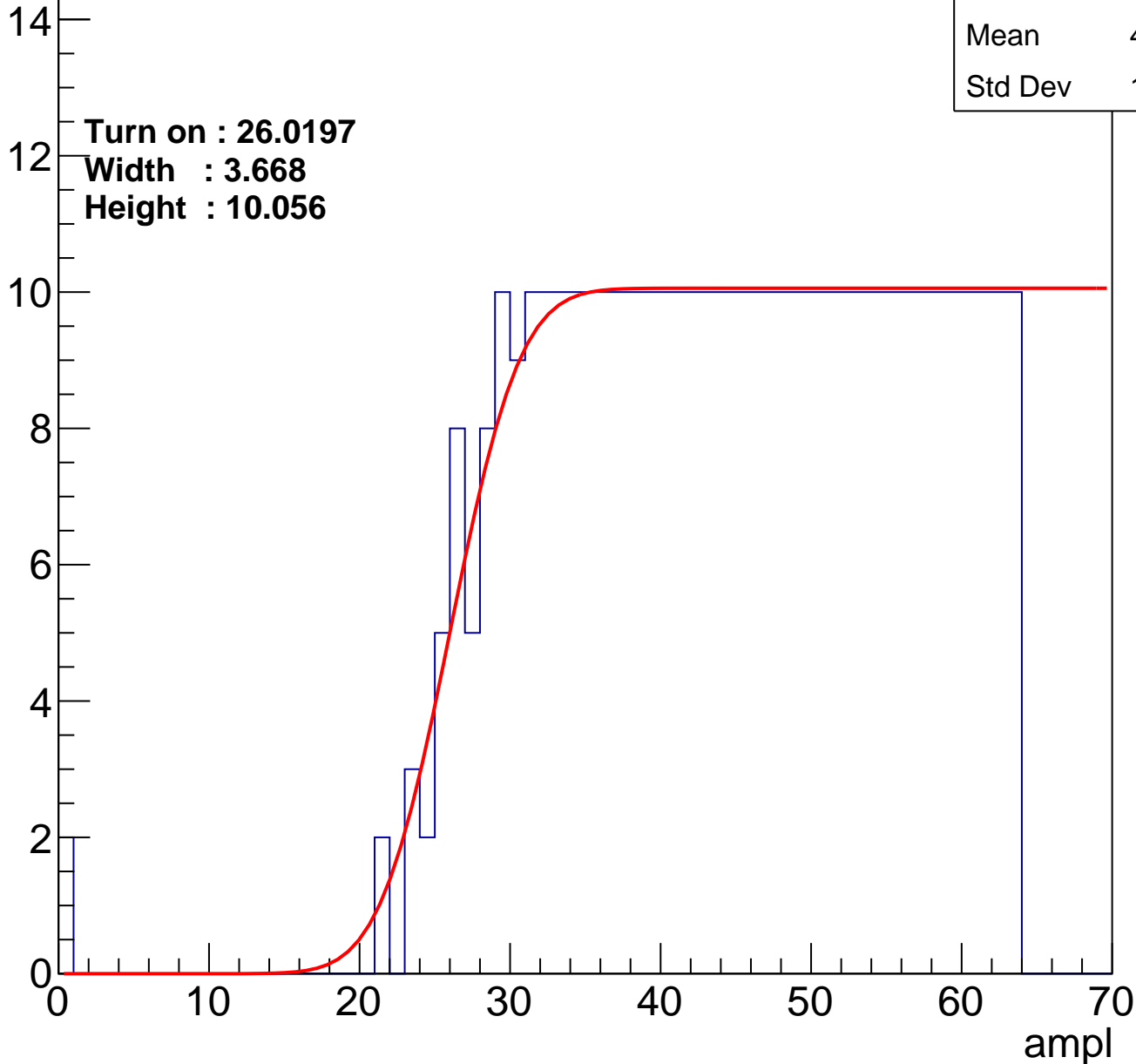
Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 26.0197

Width : 3.668

Height : 10.056

Entry



# B1L002S, U6-ch90

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	378
Mean	44.46
Std Dev	11.24

Turn on : 26.5385

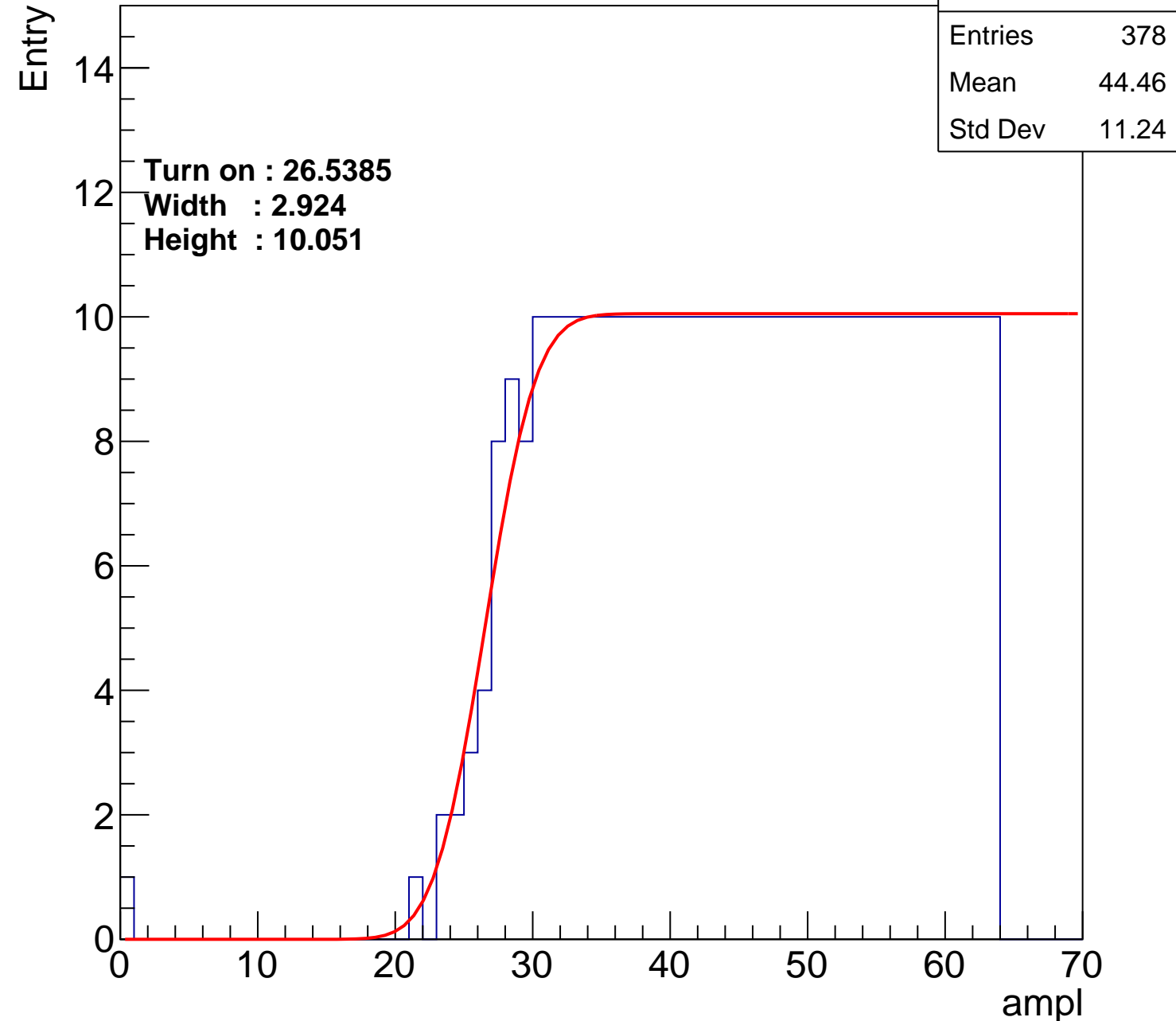
Width : 2.924

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch91

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	409
Mean	42.95
Std Dev	12.03

Turn on : 23.4965

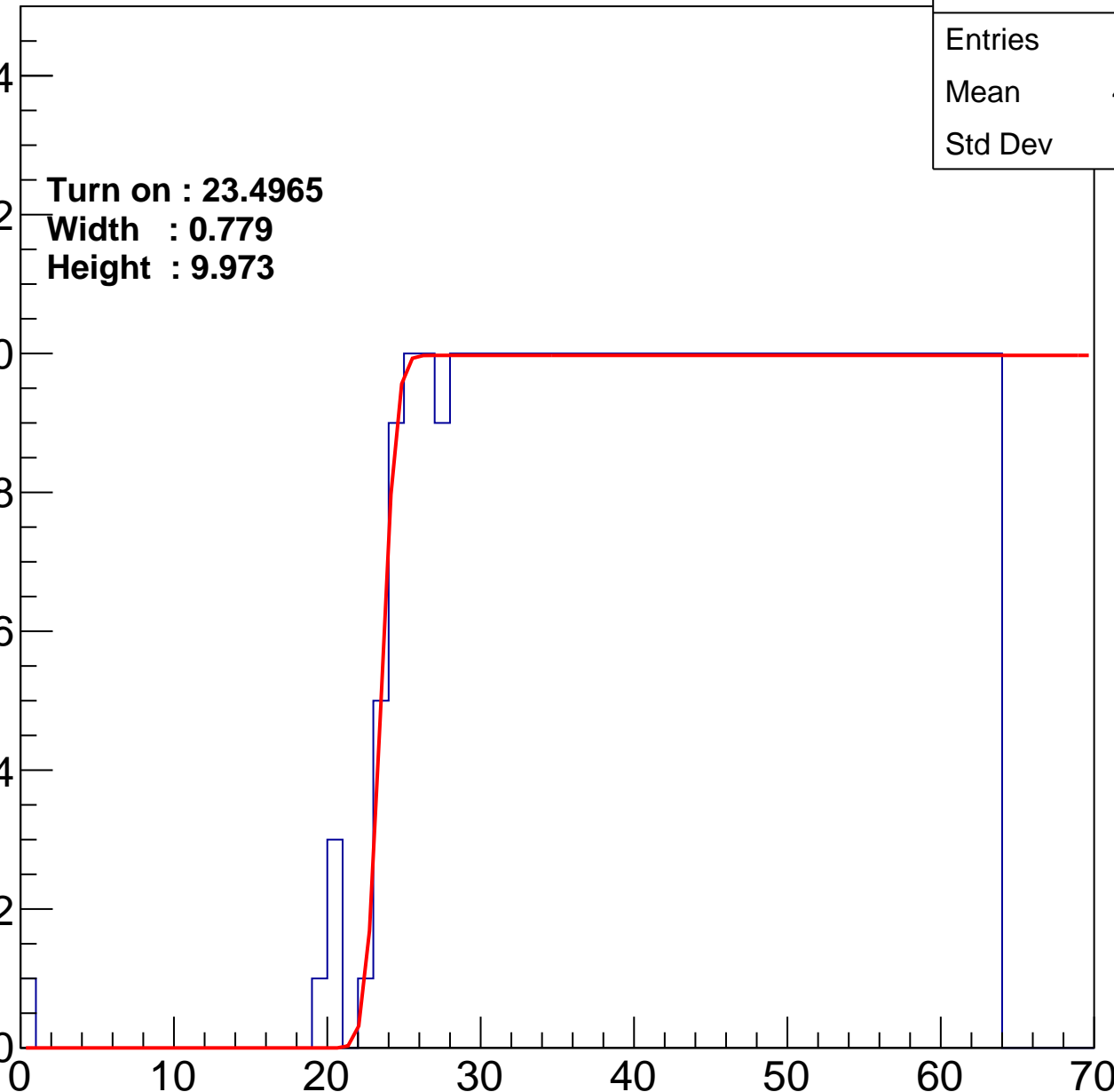
Width : 0.779

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch92

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.68
Std Dev	11.92

Turn on : 24.5789

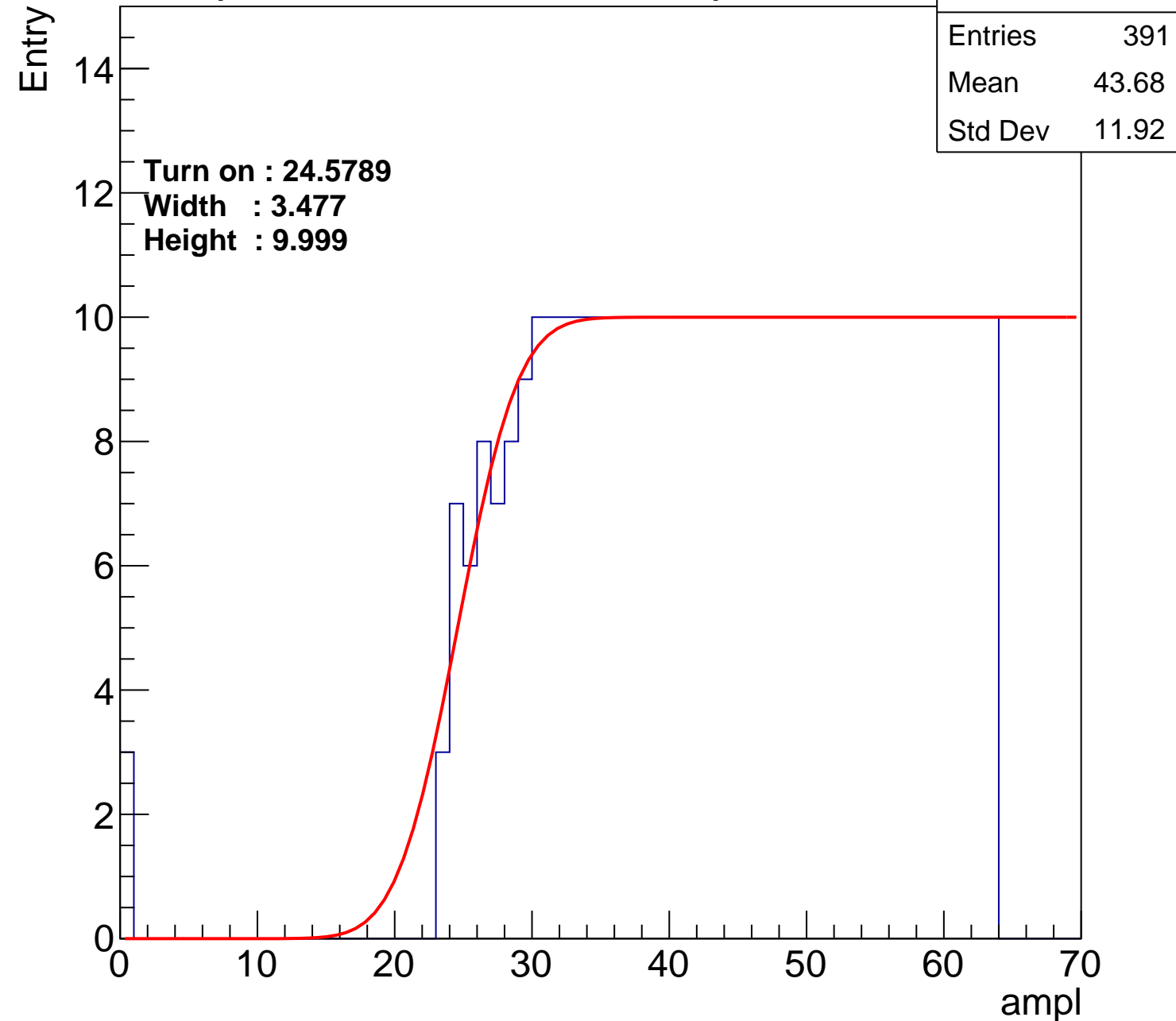
Width : 3.477

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch93

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.63
Std Dev	11.11

Turn on : 26.6146

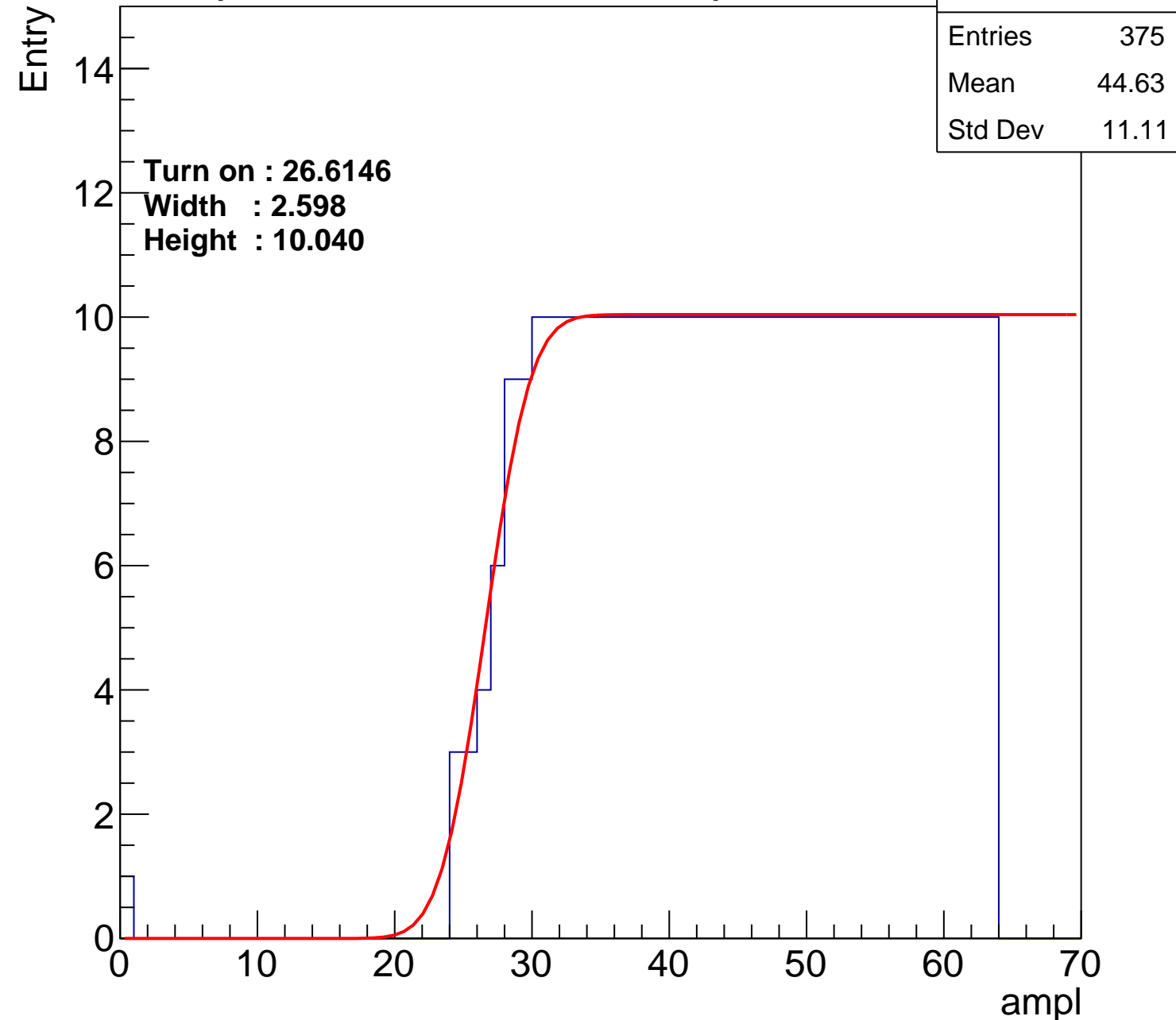
Width : 2.598

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch94

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	410
Mean	42.8
Std Dev	12.26

Turn on : 23.1163

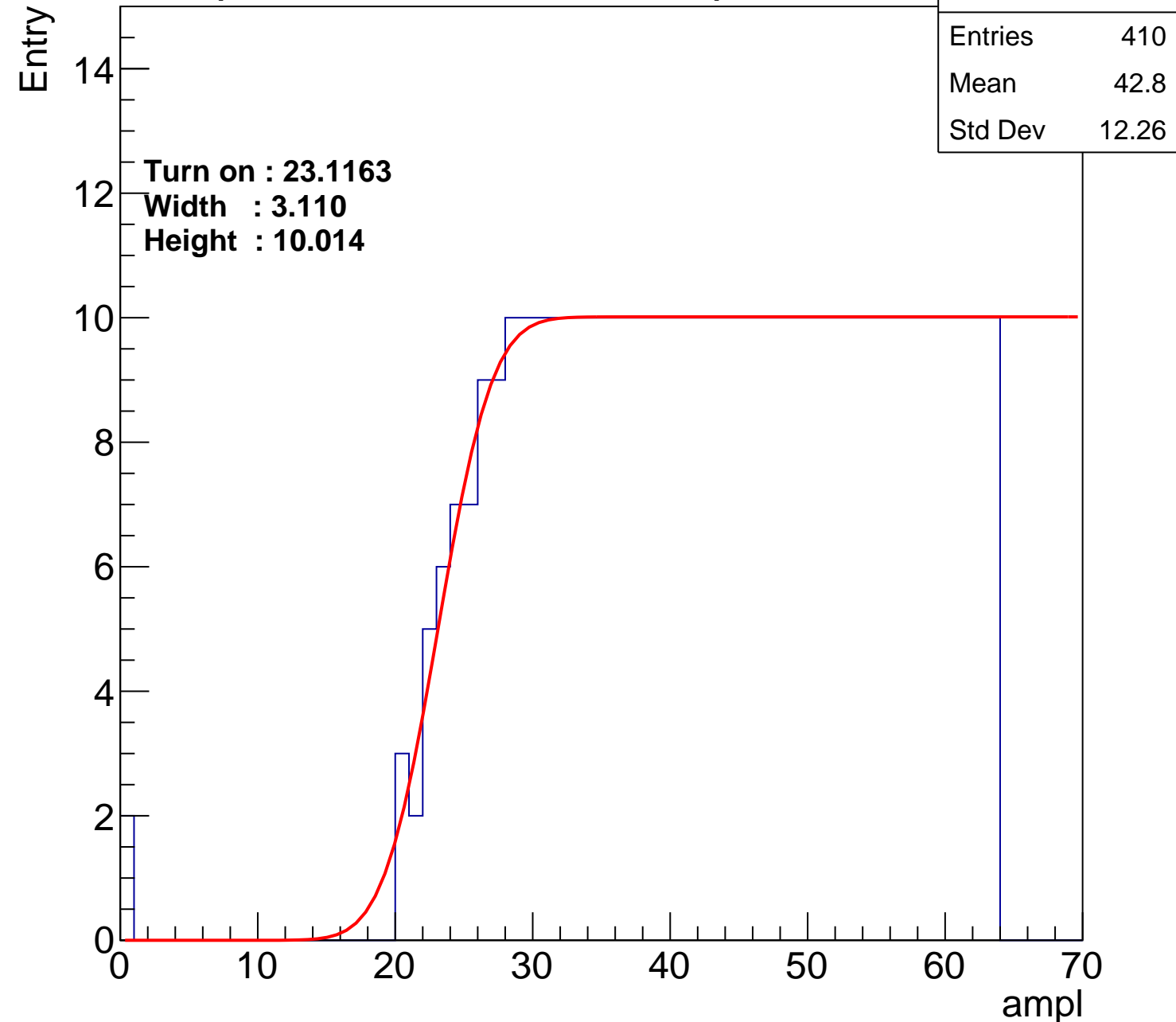
Width : 3.110

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch95

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 26.3373

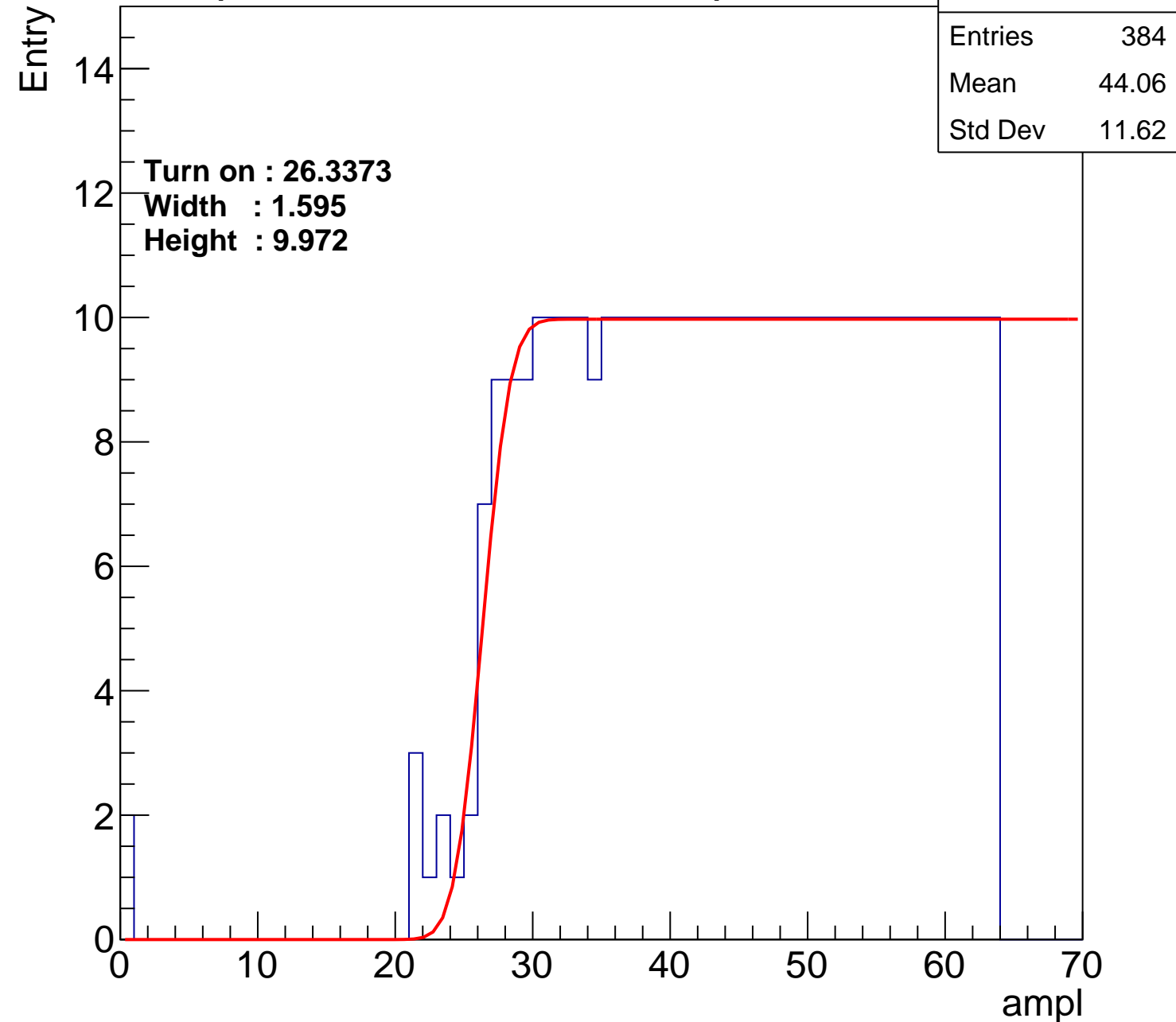
Width : 1.595

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch96

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 27.2273

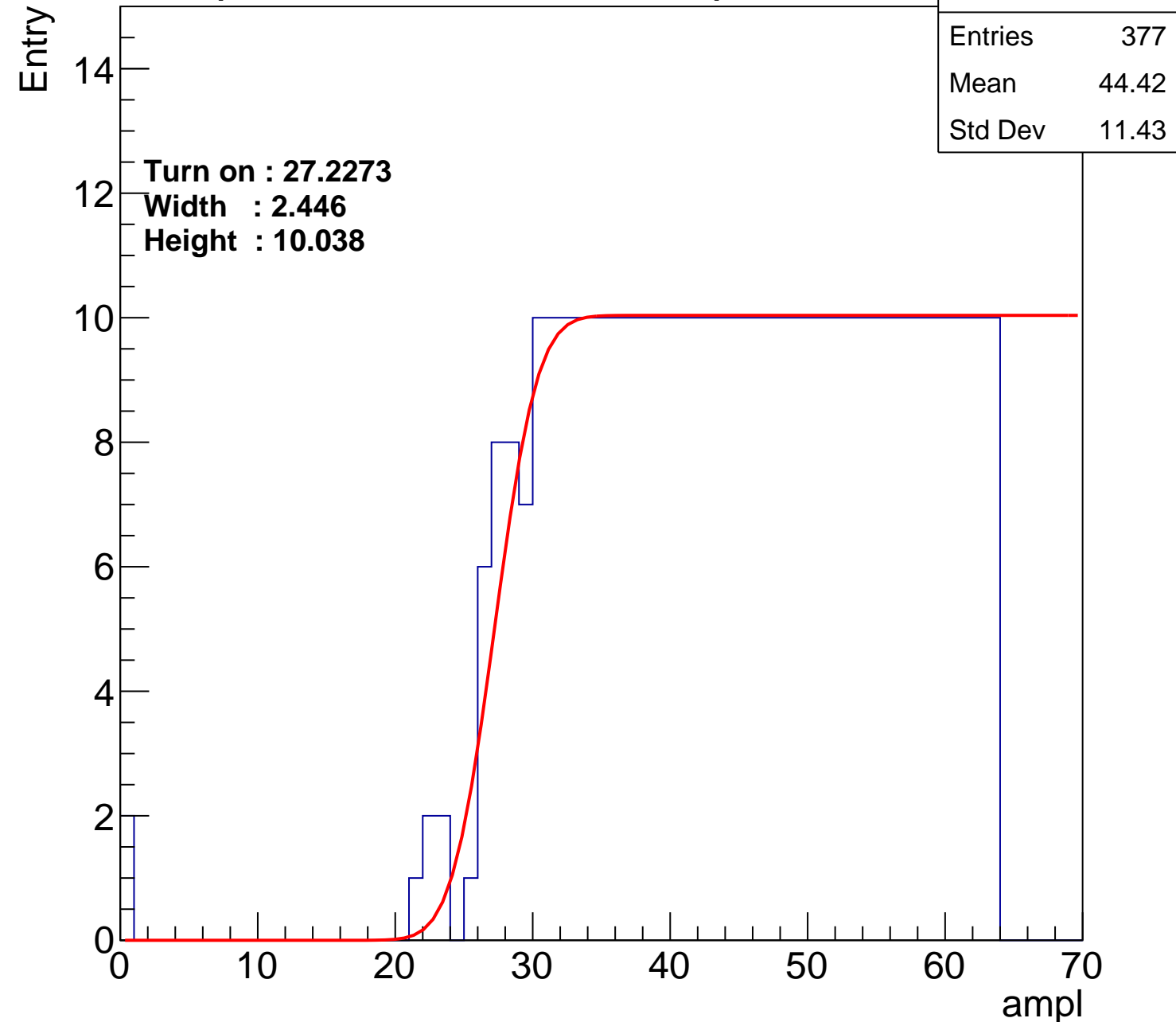
Width : 2.446

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch97

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.42
Std Dev	12.08

**Turn on : 24.8524**

**Width : 2.618**

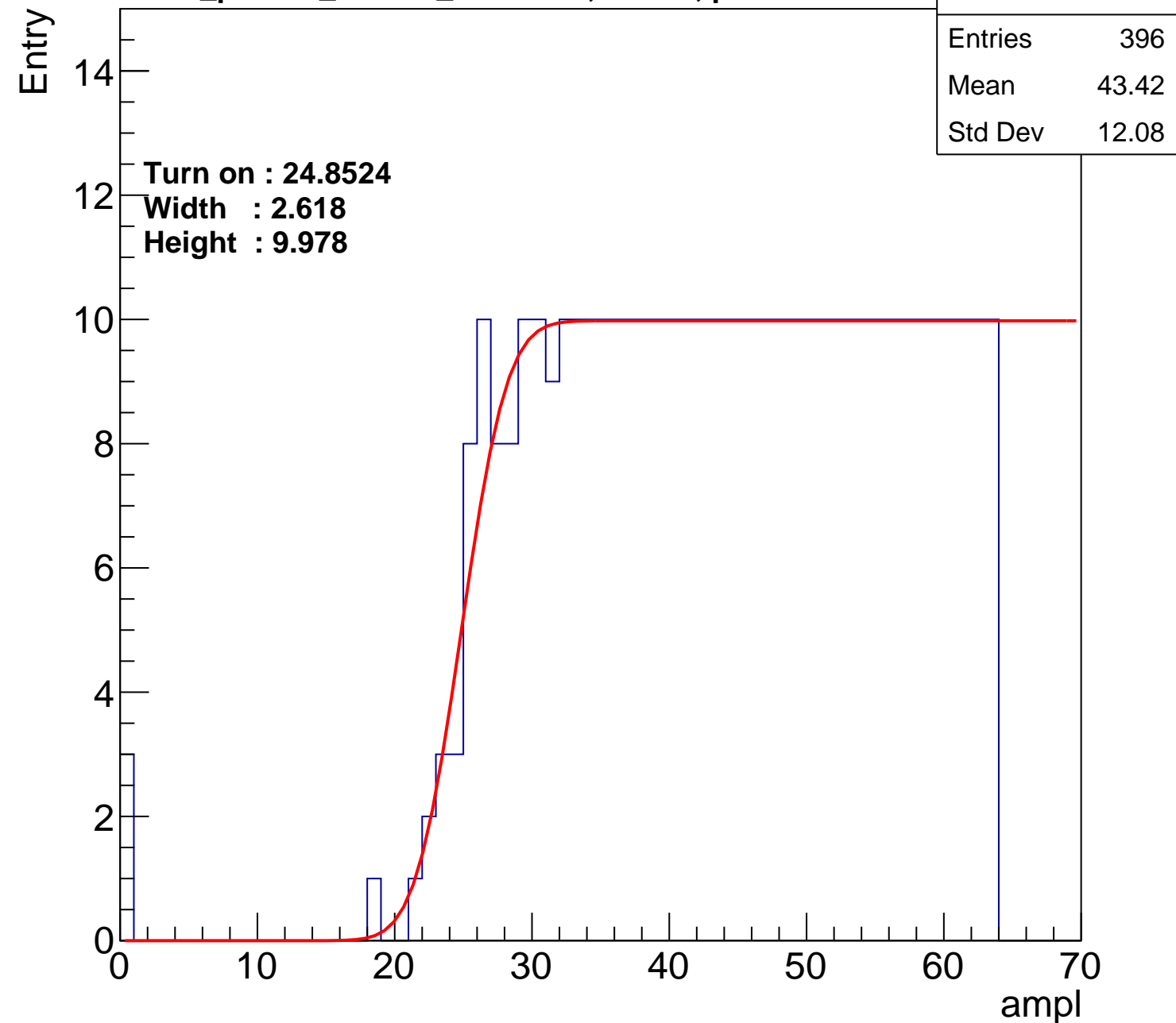
**Height : 9.978**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U6-ch98

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	385
Mean	44.13
Std Dev	11.38

**Turn on : 25.9710**

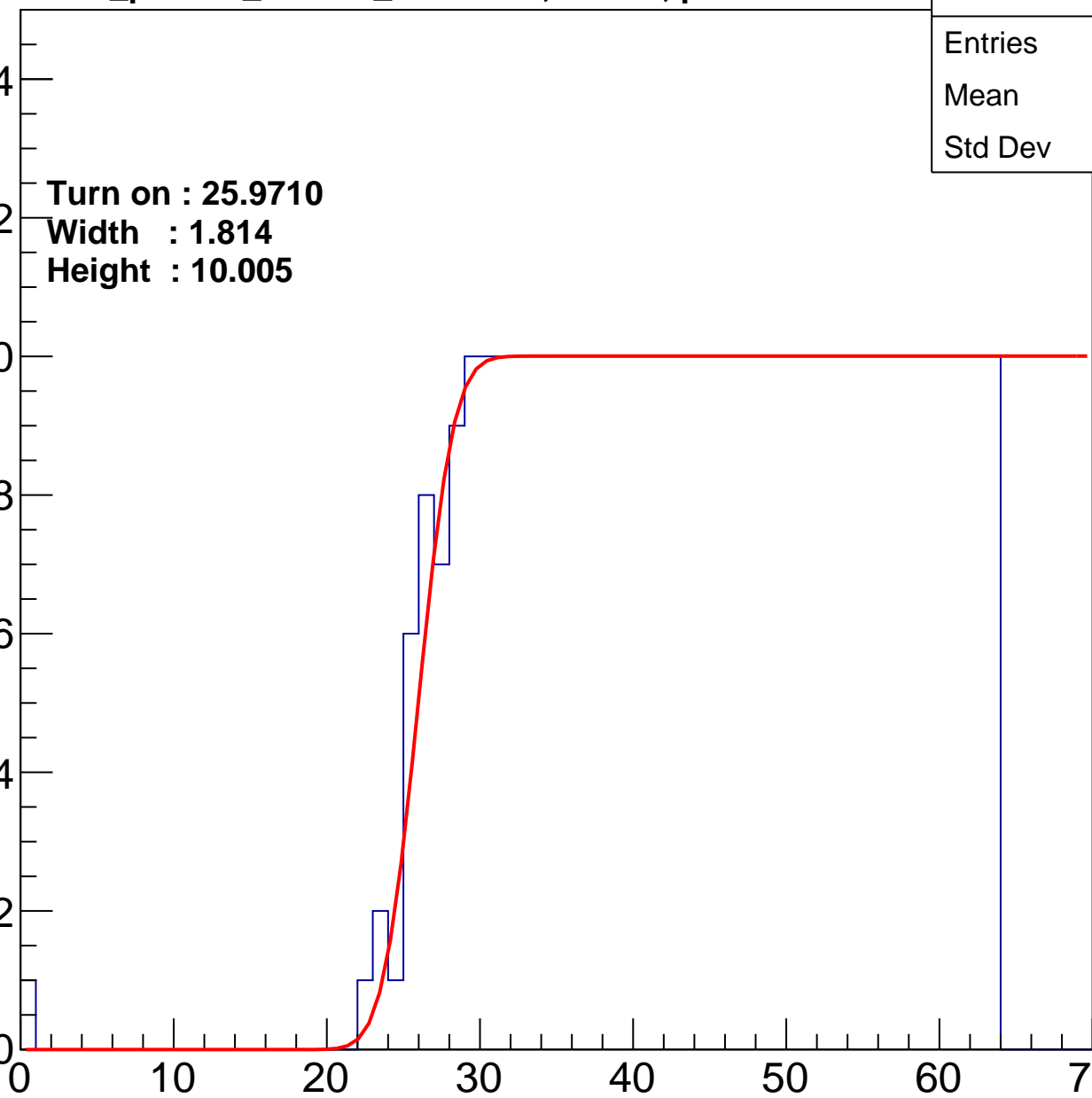
**Width : 1.814**

**Height : 10.005**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch99

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	377
Mean	44.32
Std Dev	11.74

**Turn on : 26.8937**

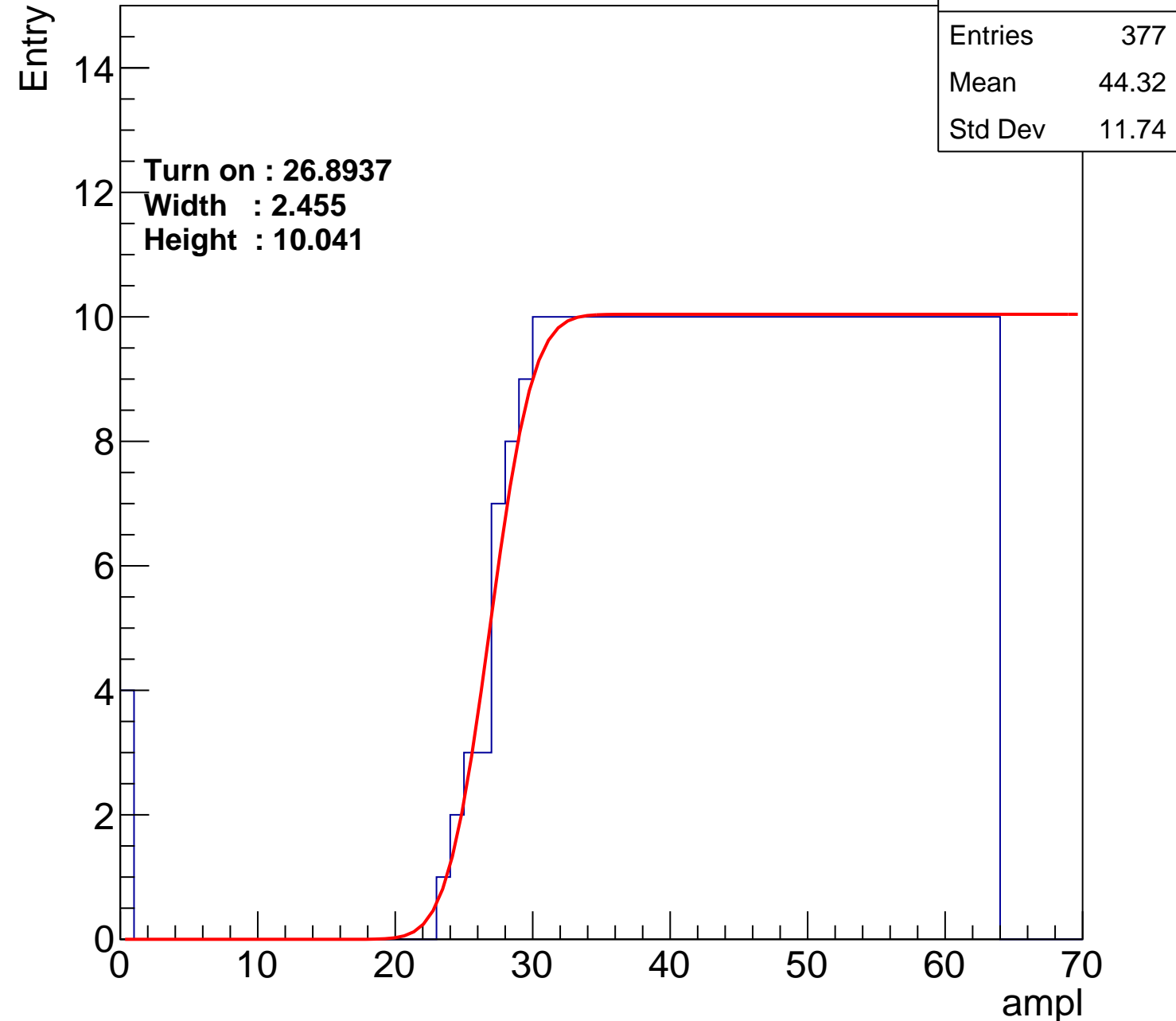
**Width : 2.455**

**Height : 10.041**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch100

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.0052

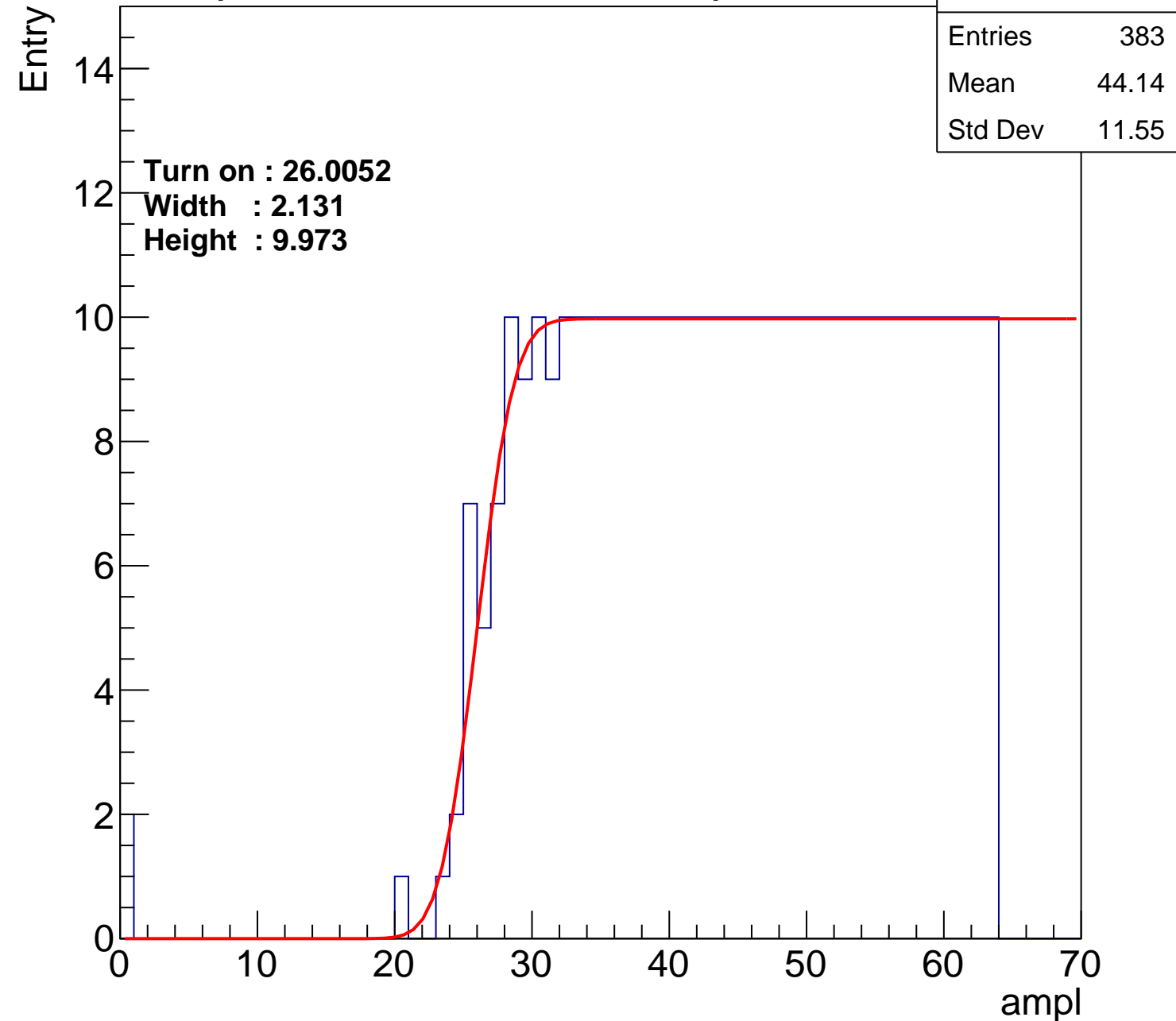
Width : 2.131

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch101

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.04
Std Dev	11.74

Turn on : 26.0473

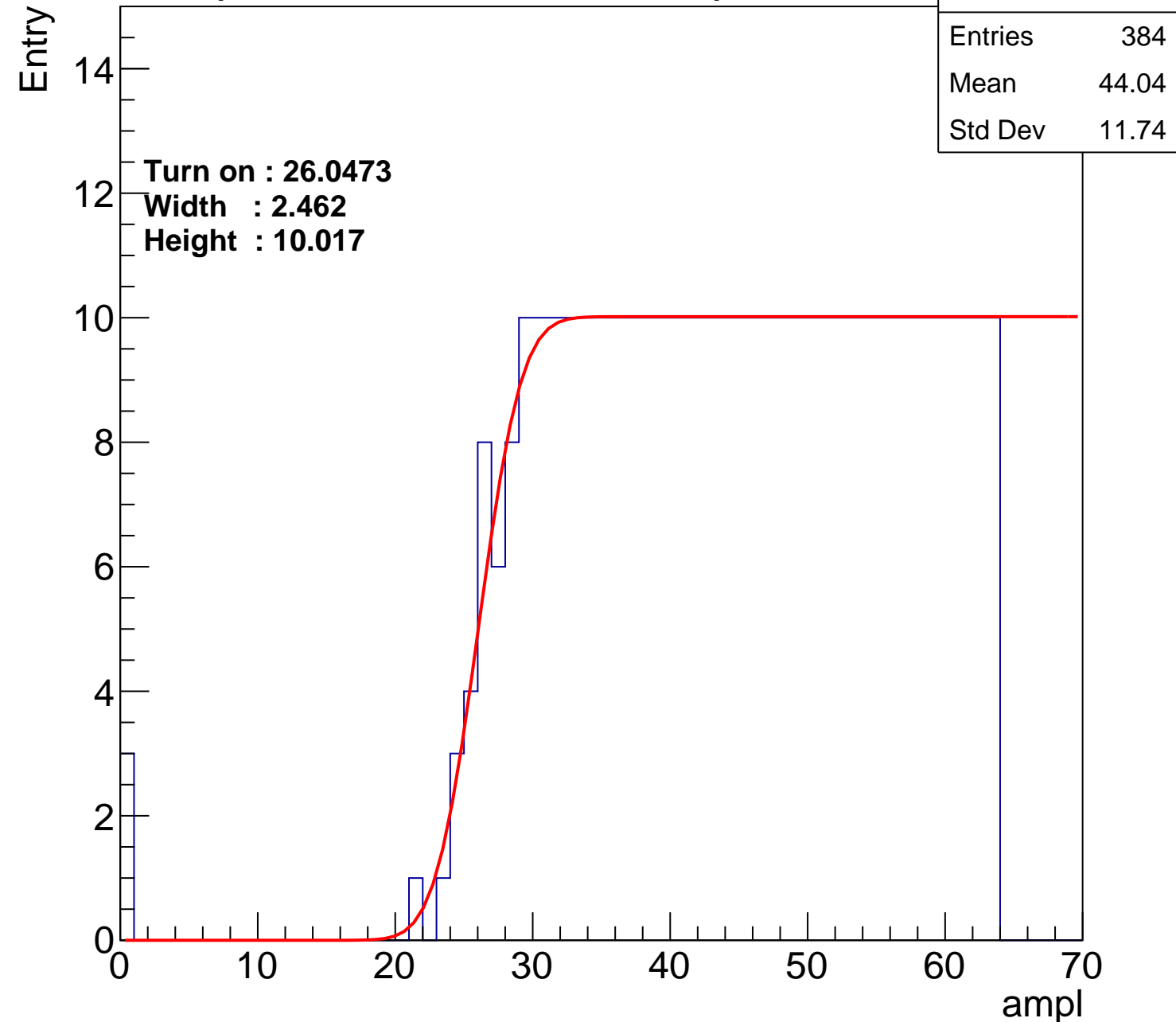
Width : 2.462

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch102

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	396
Mean	43.46
Std Dev	11.95

Turn on : 24.8467

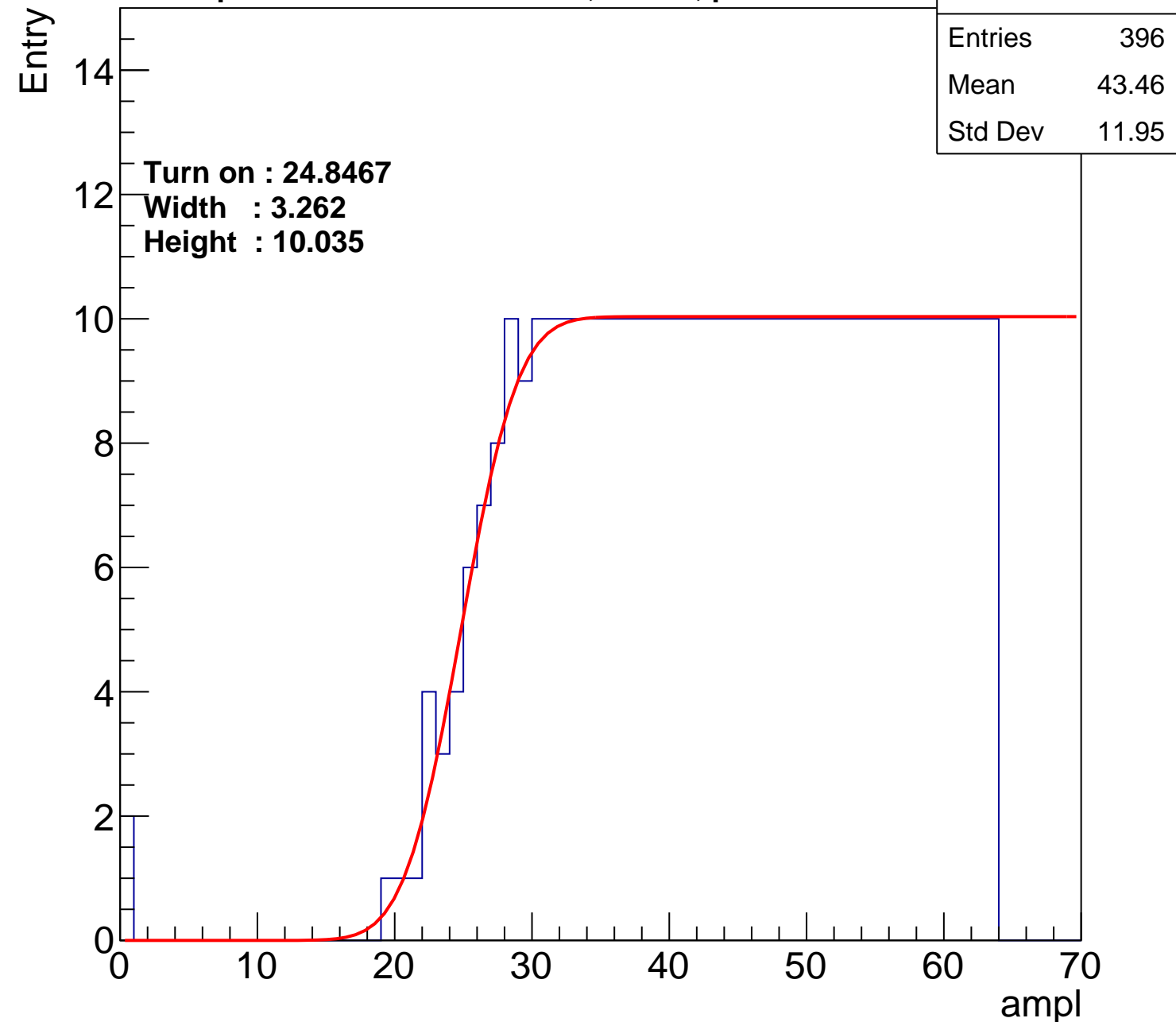
Width : 3.262

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch103

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	393
Mean	43.59
Std Dev	11.97

Turn on : 25.0440

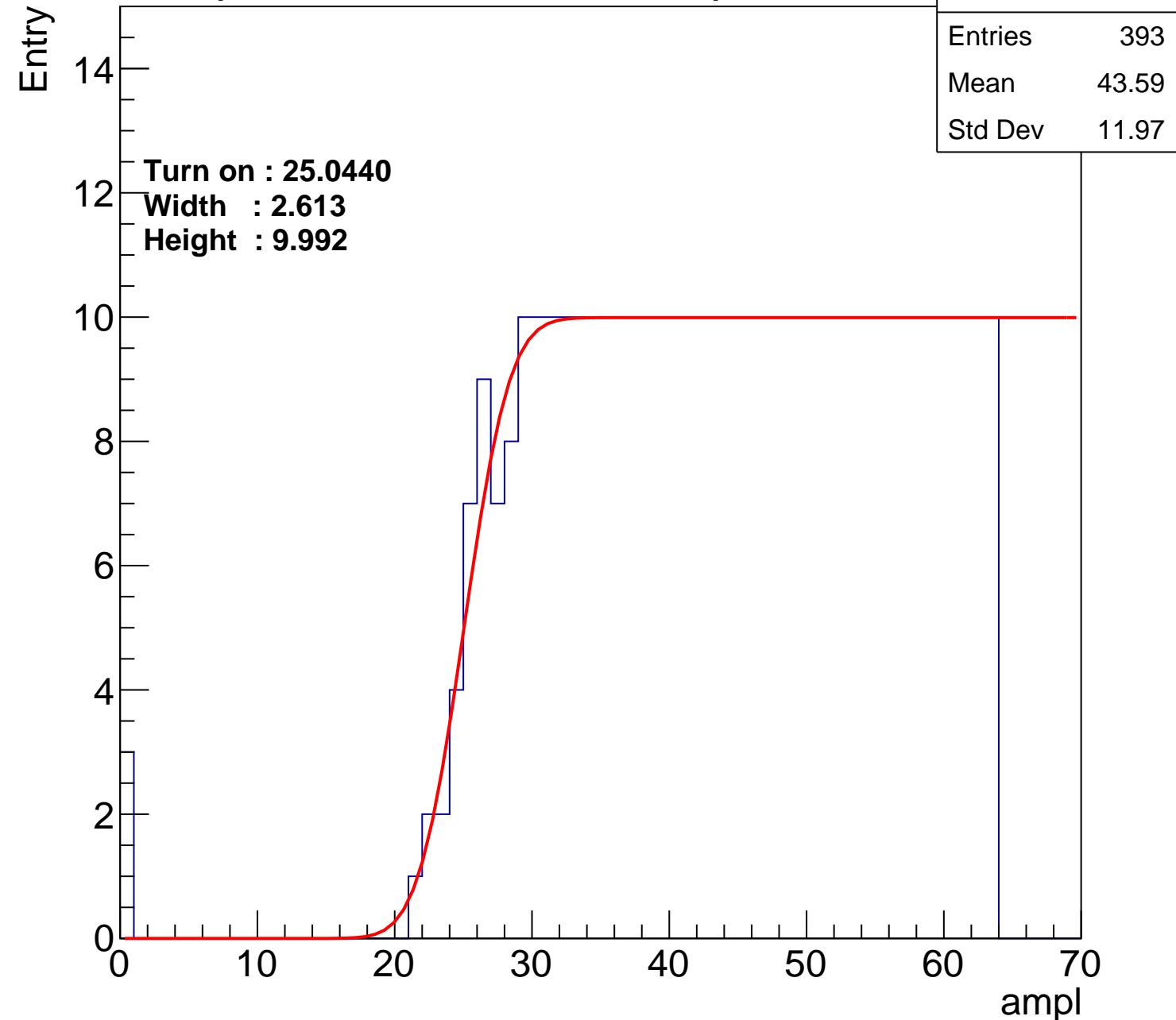
Width : 2.613

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch104

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.5717

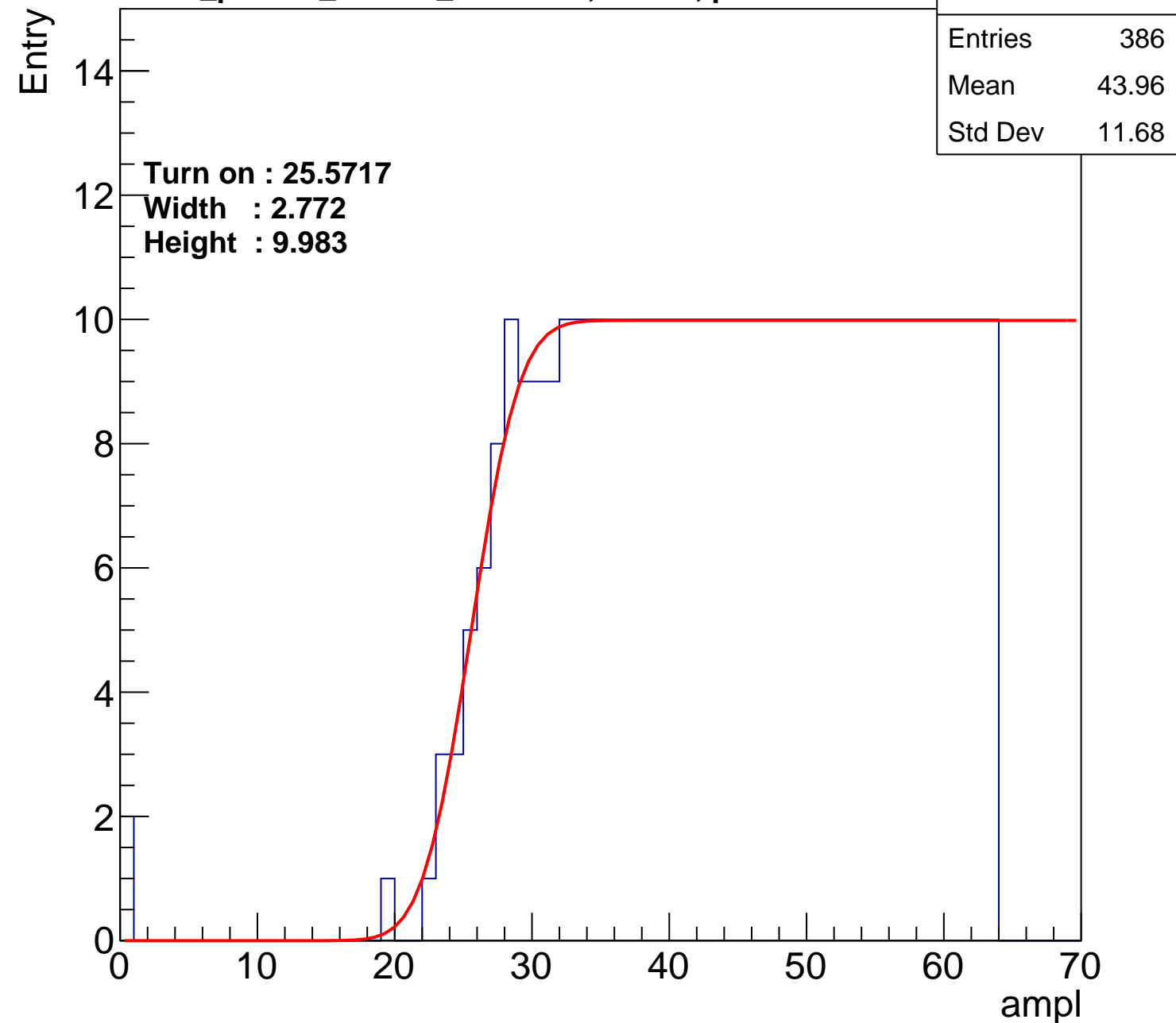
Width : 2.772

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch105

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 26.6466

Width : 3.248

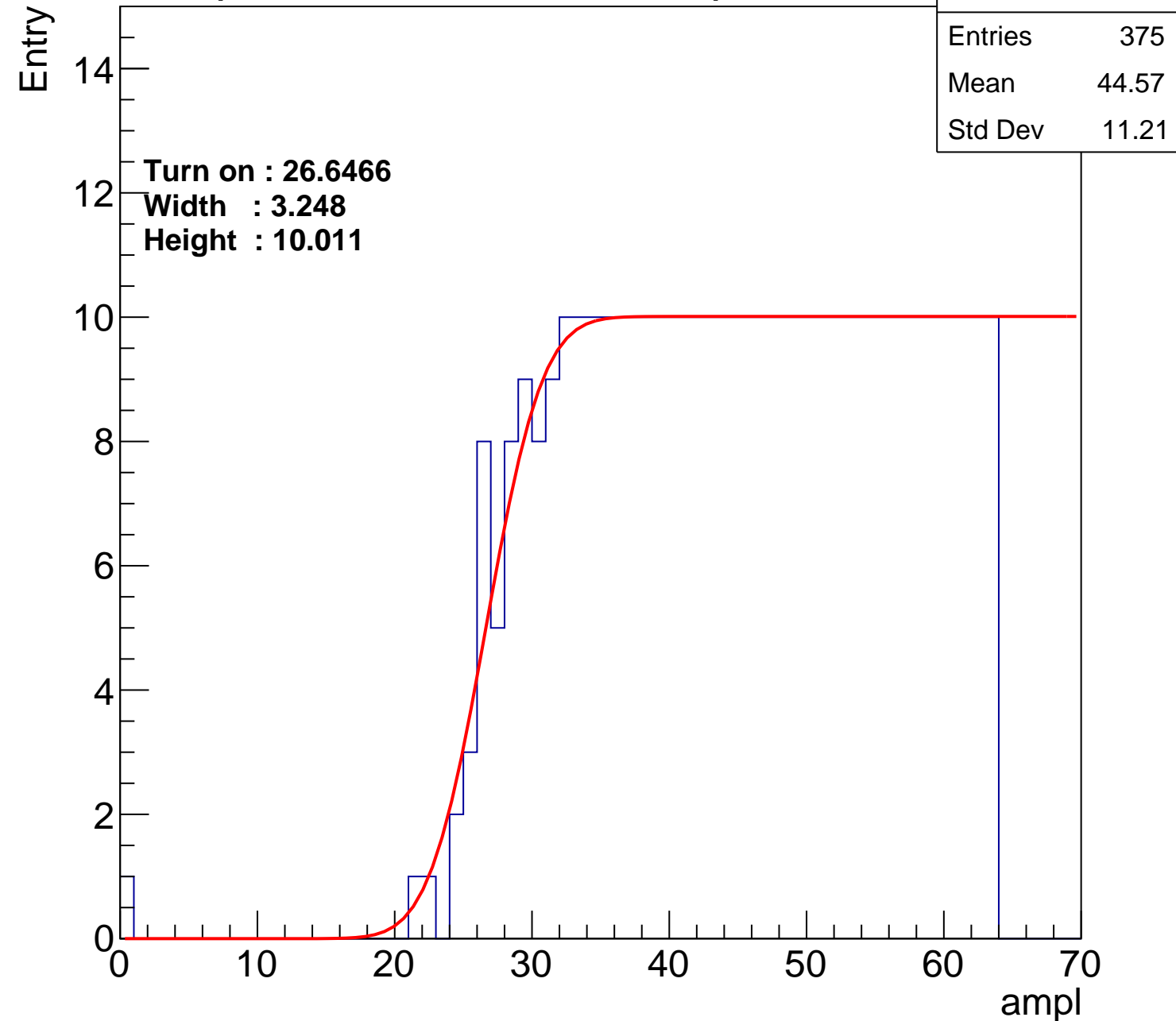
Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U6-ch106

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	406
Mean	42.95
Std Dev	12.3

Turn on : 23.7935

Width : 2.005

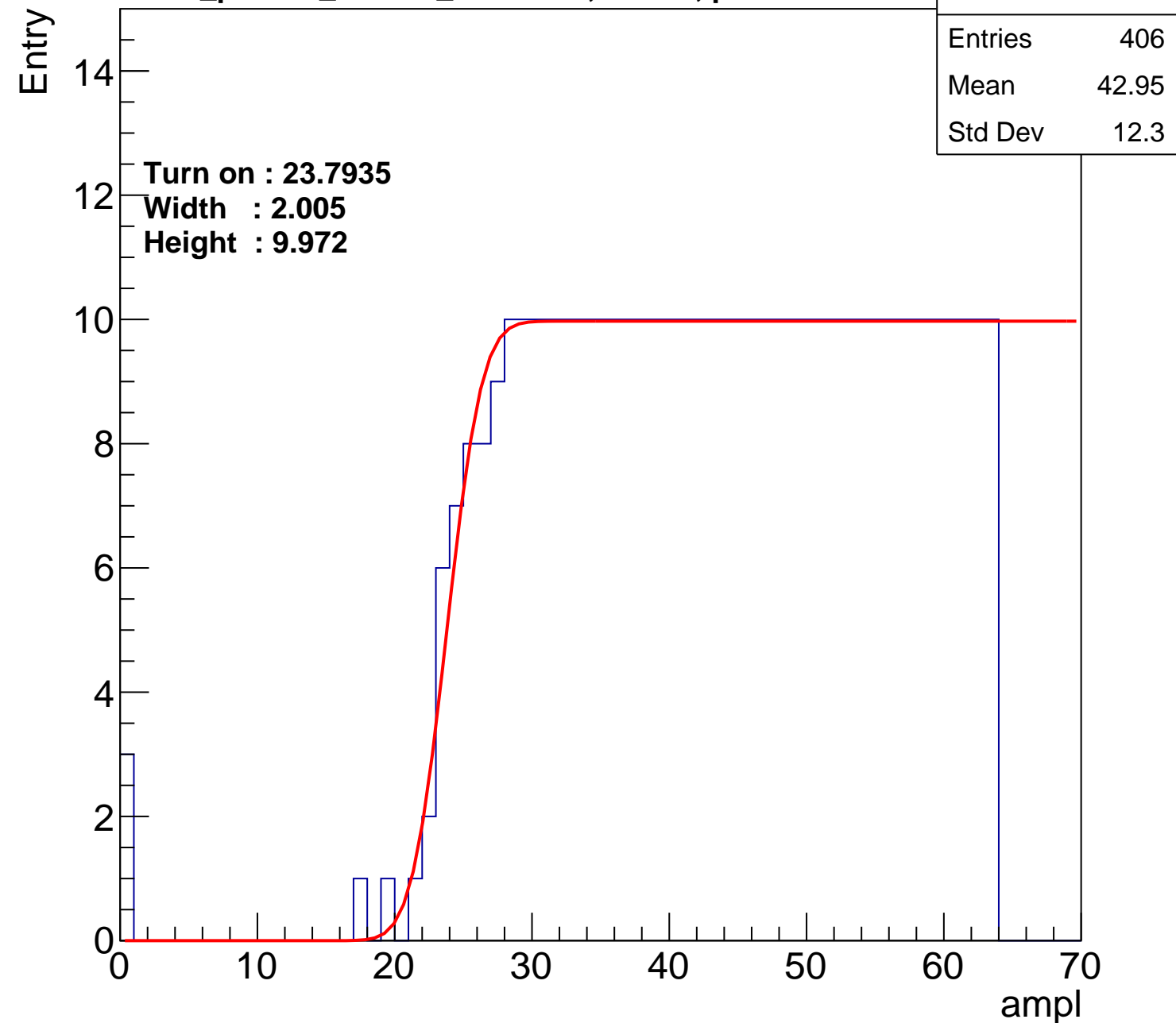
Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U6-ch107

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.5796

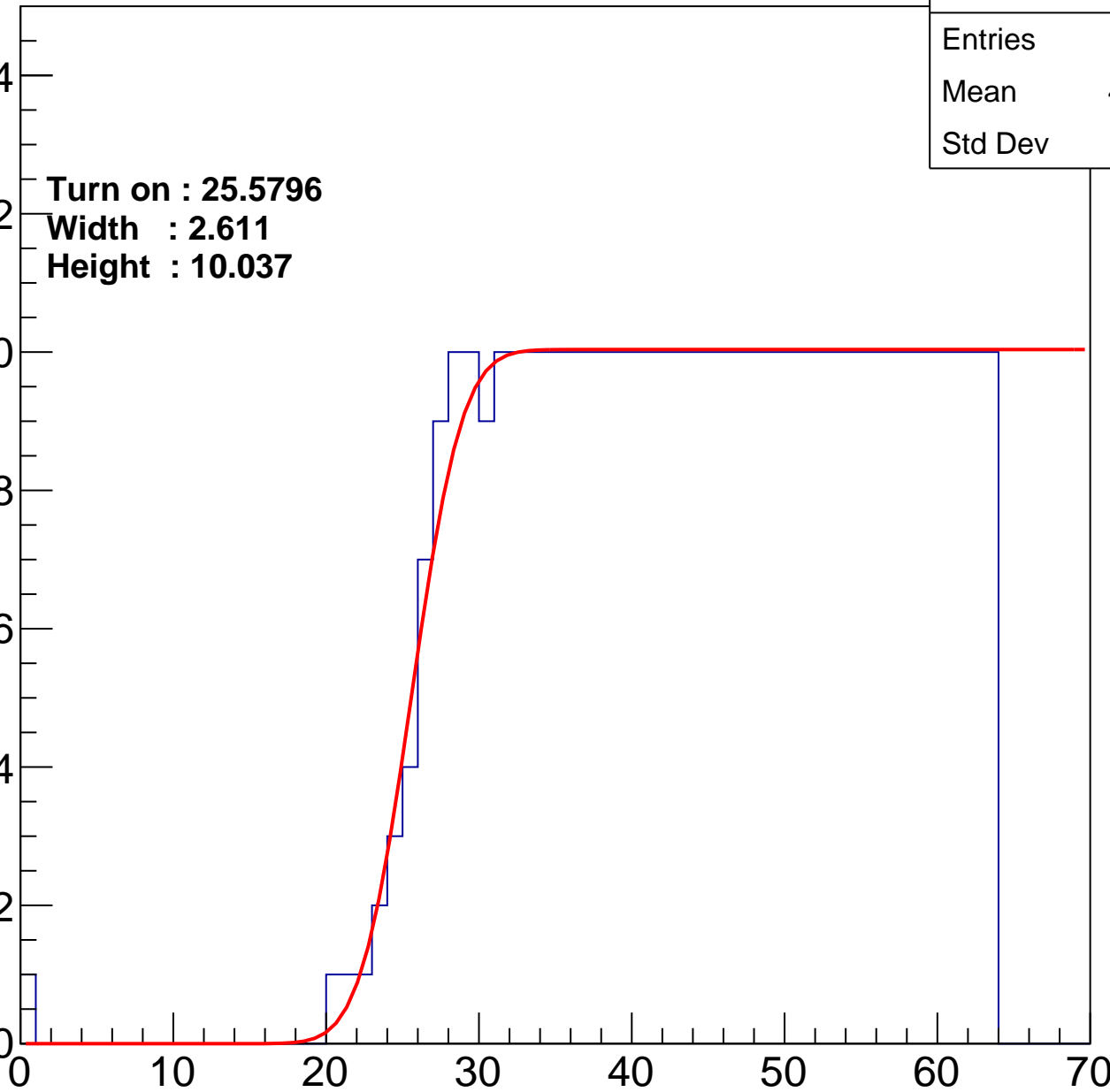
Width : 2.611

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch108

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.09
Std Dev	12.01

Turn on : 26.5234

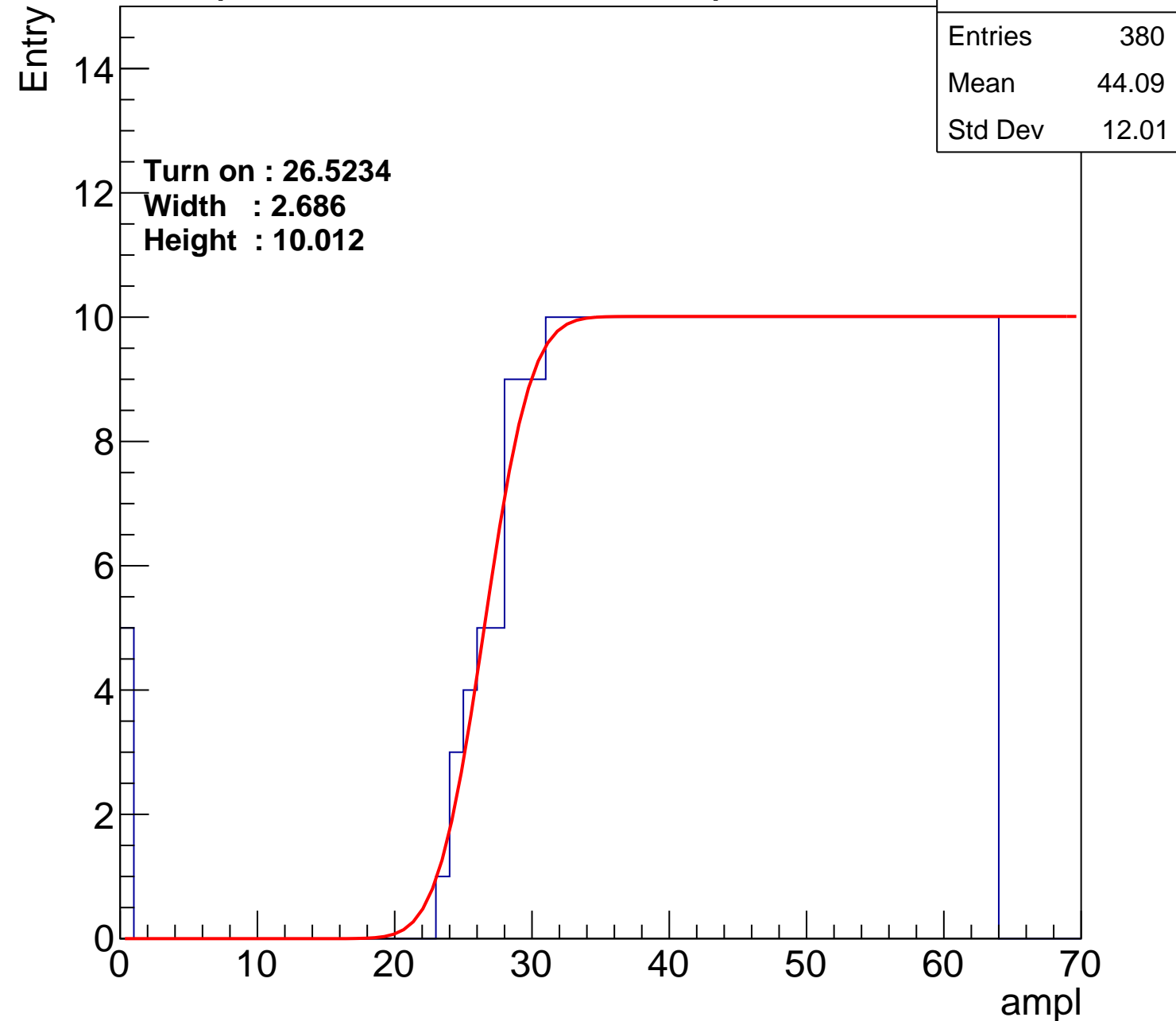
Width : 2.686

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch109

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	391
Mean	43.83
Std Dev	11.55

Turn on : 25.1534

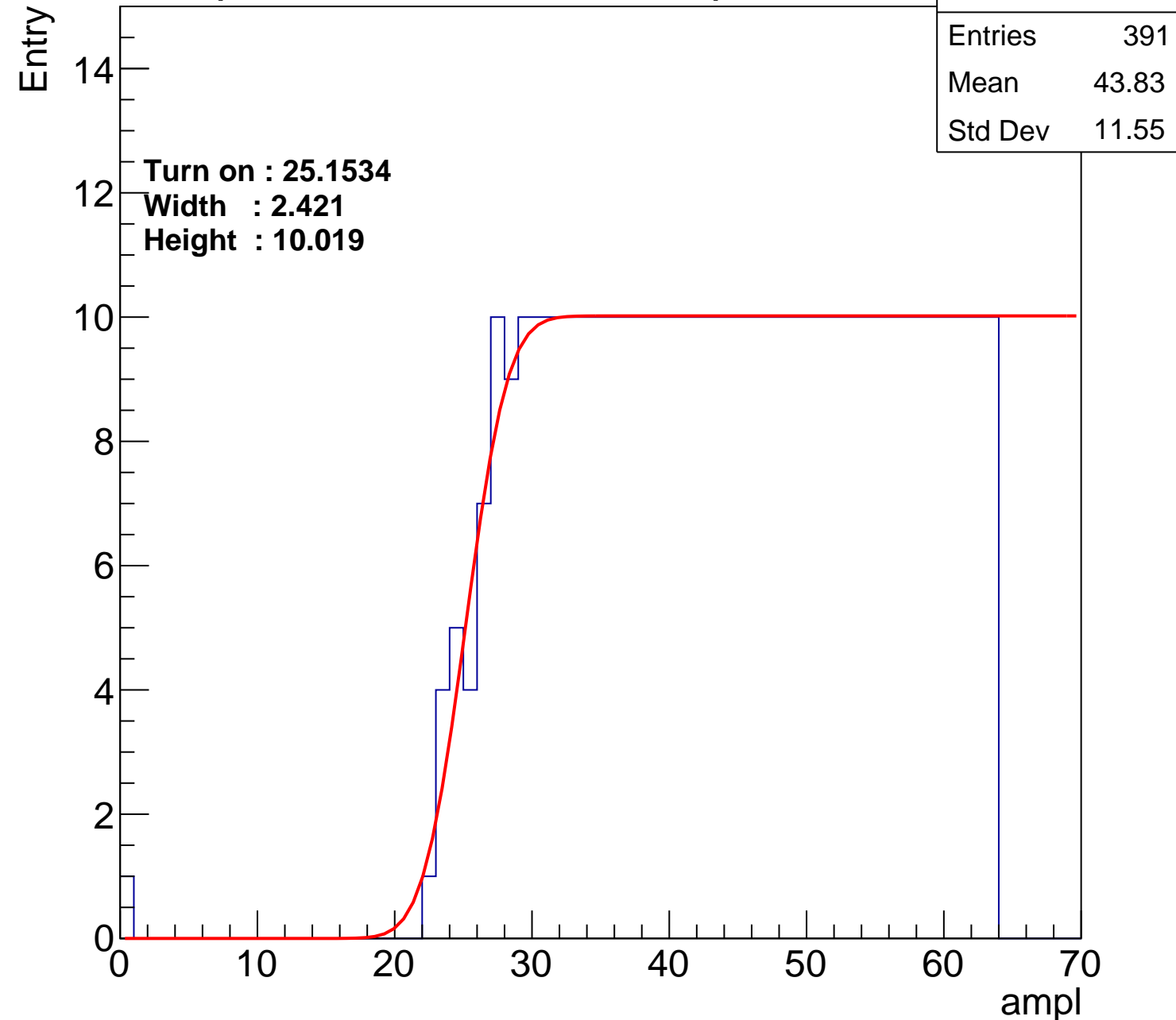
Width : 2.421

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch110

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.26
Std Dev	11.4

Turn on : 26.4758

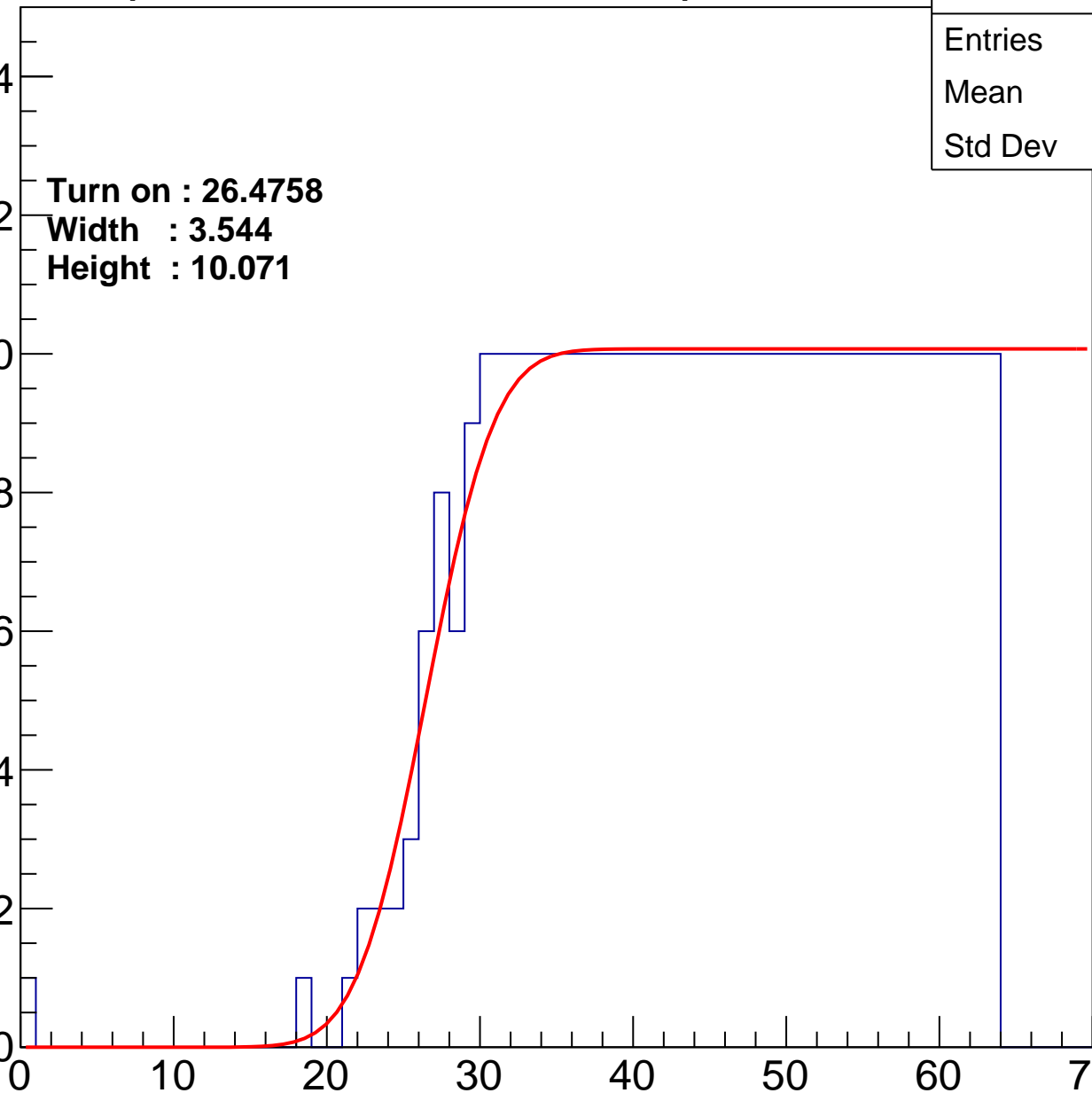
Width : 3.544

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch111

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	371
Mean	44.8
Std Dev	11.05

Turn on : 27.2374

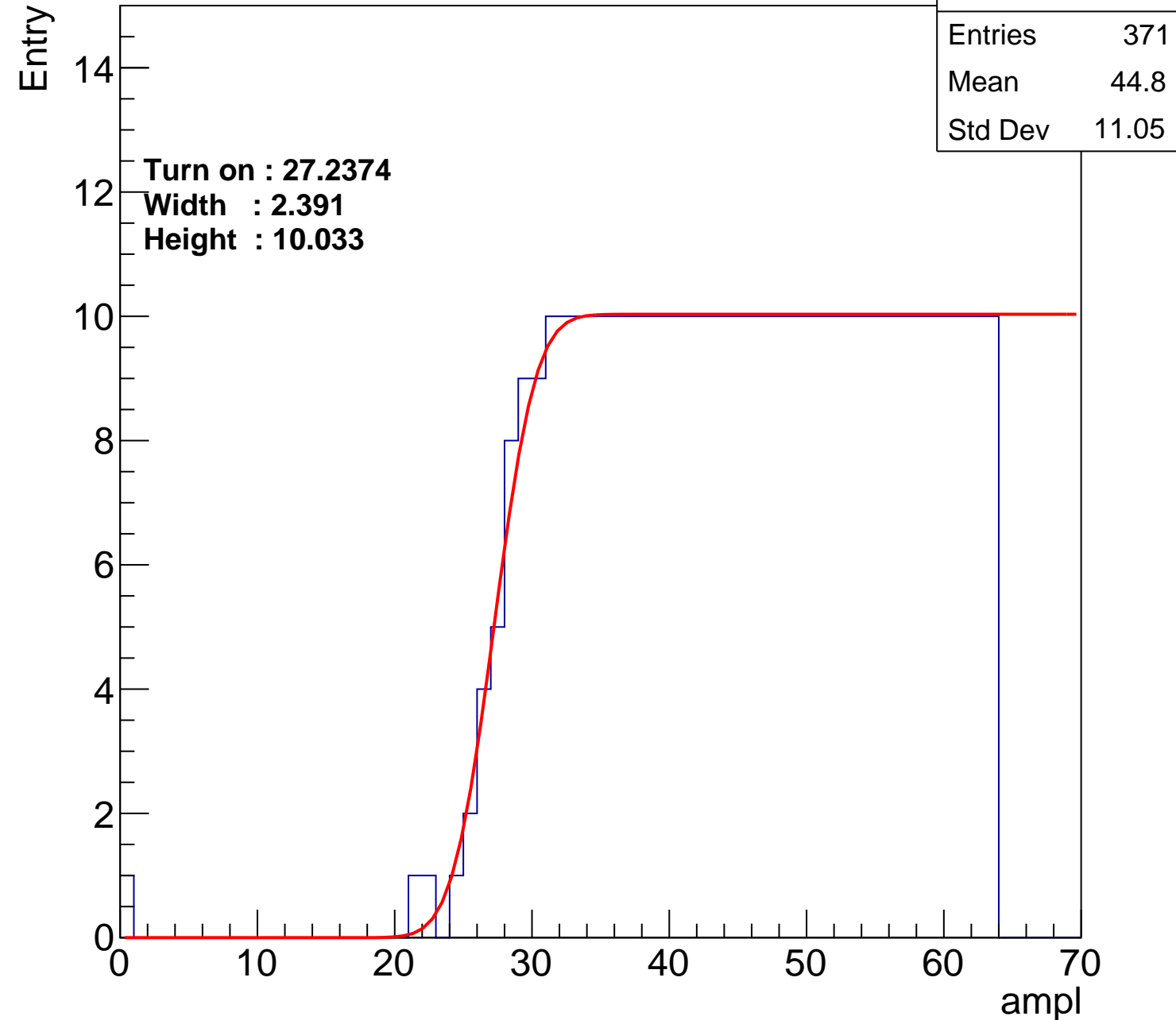
Width : 2.391

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch112

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 27.4085

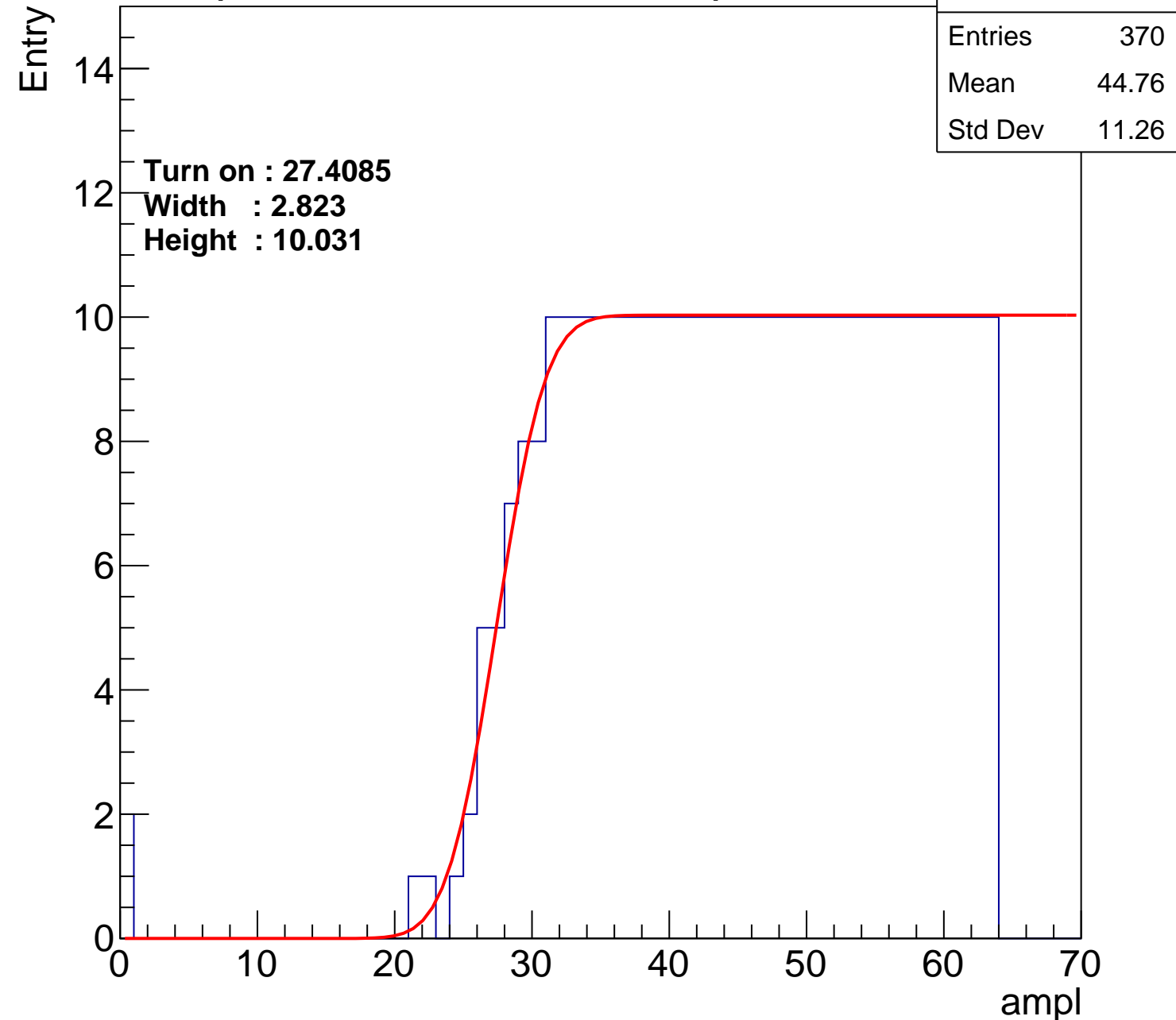
Width : 2.823

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch113

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.1767

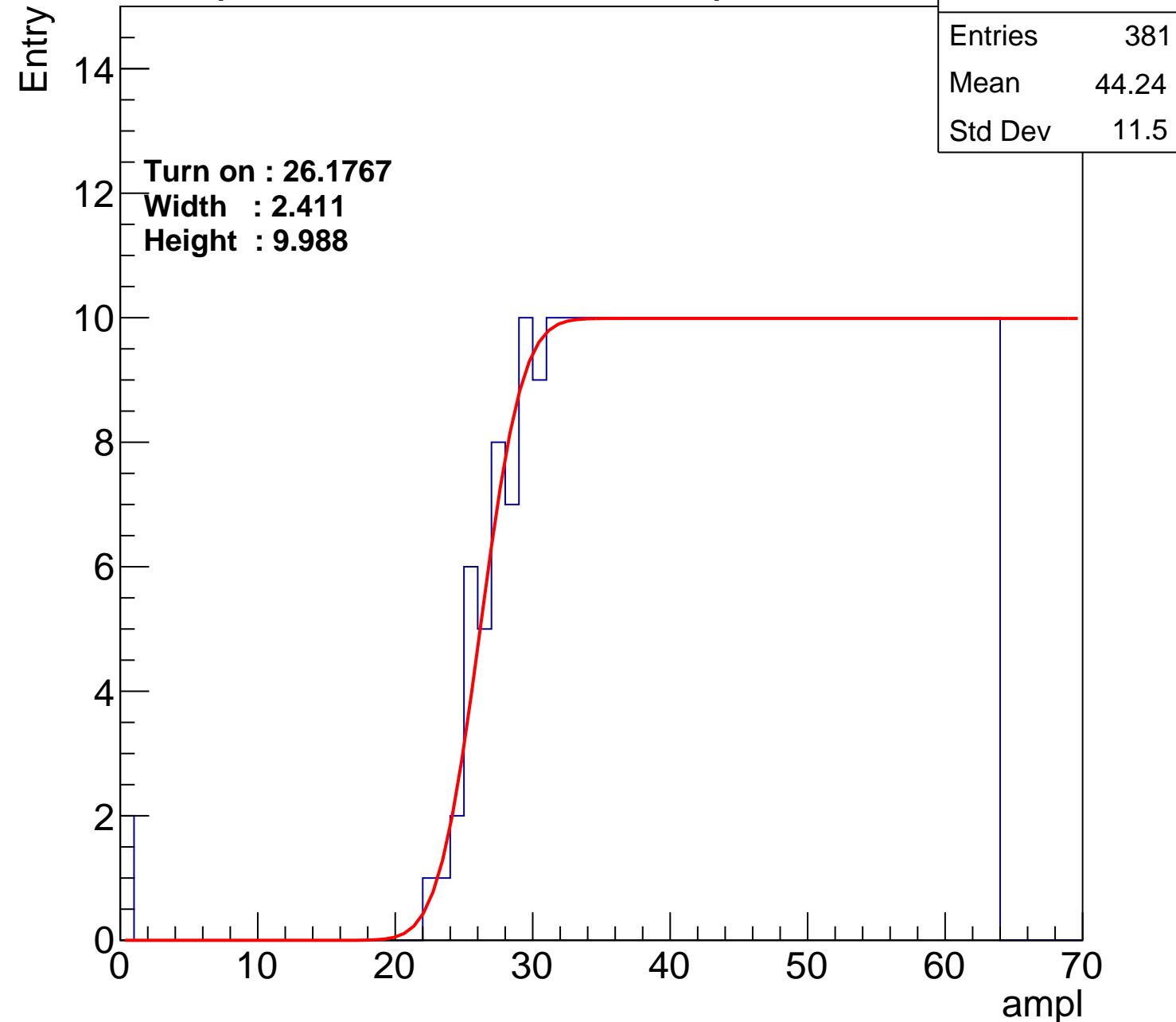
Width : 2.411

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch114

calib\_packv5\_042523\_0143.root, FC#10, port B3

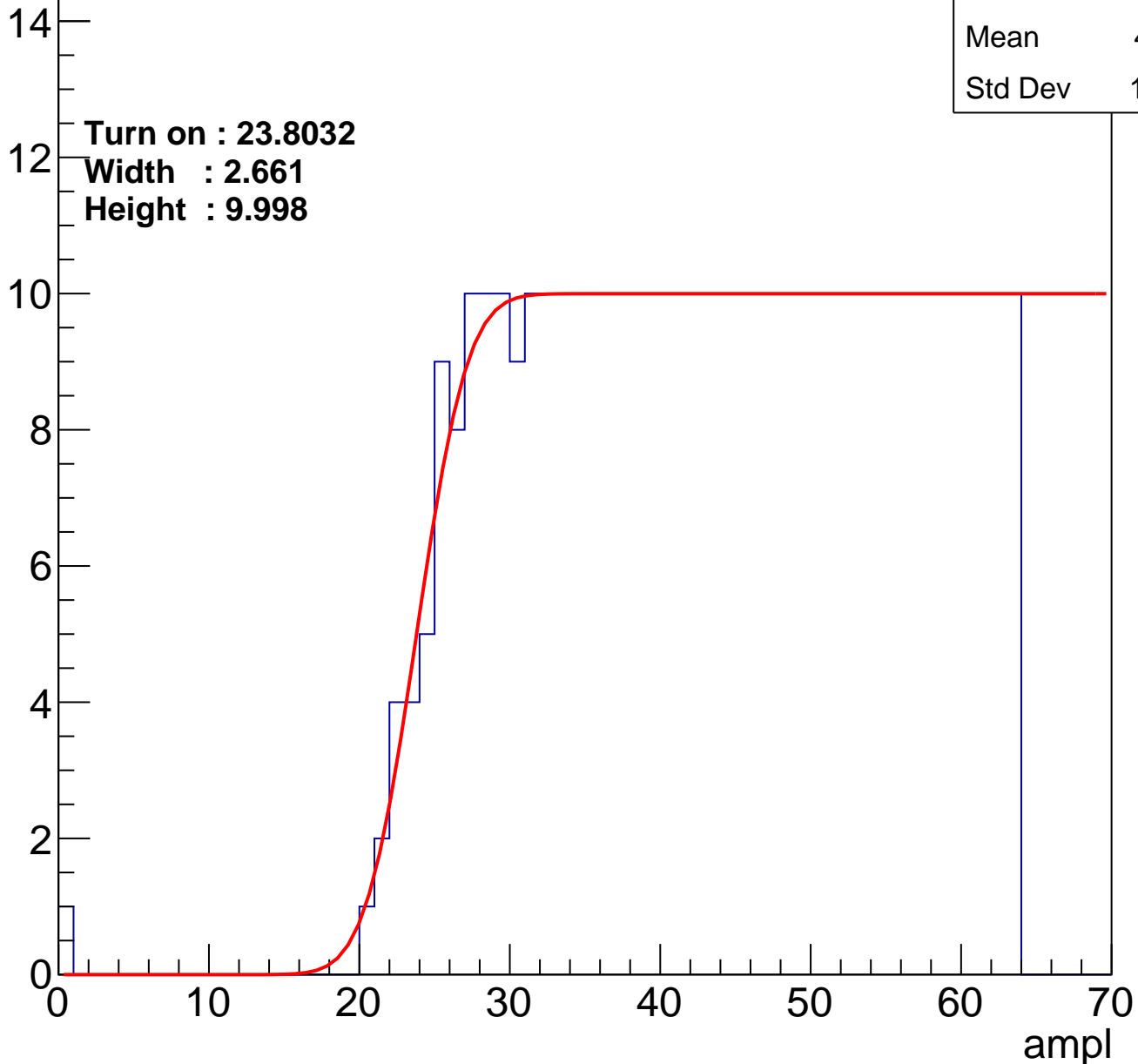
Entries	403
Mean	43.21
Std Dev	11.92

Turn on : 23.8032

Width : 2.661

Height : 9.998

Entry



# B1L002S, U6-ch115

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	368
Mean	44.87
Std Dev	11.11

Turn on : 27.2754

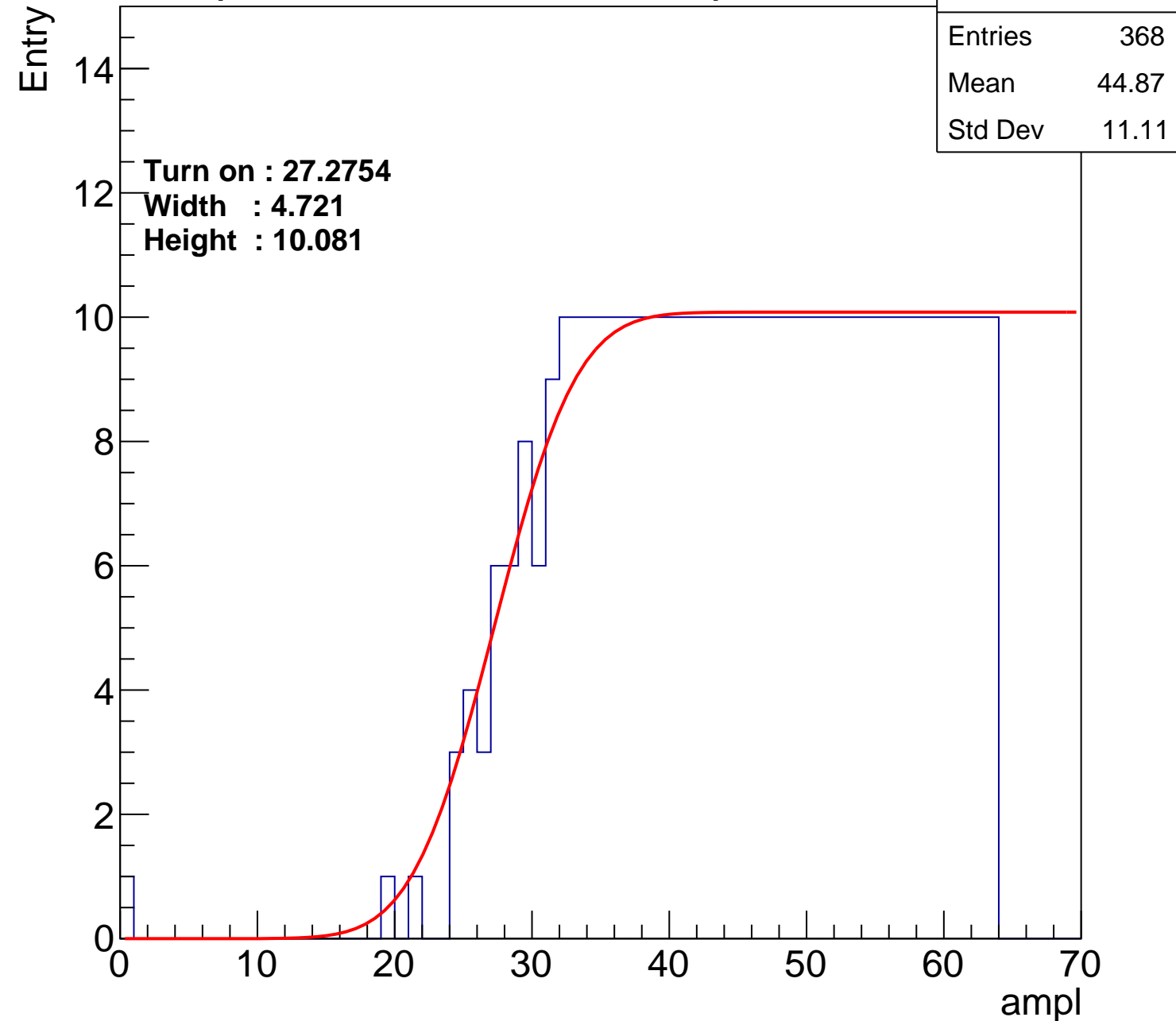
Width : 4.721

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch116

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	382
Mean	44.23
Std Dev	11.38

Turn on : 25.3528

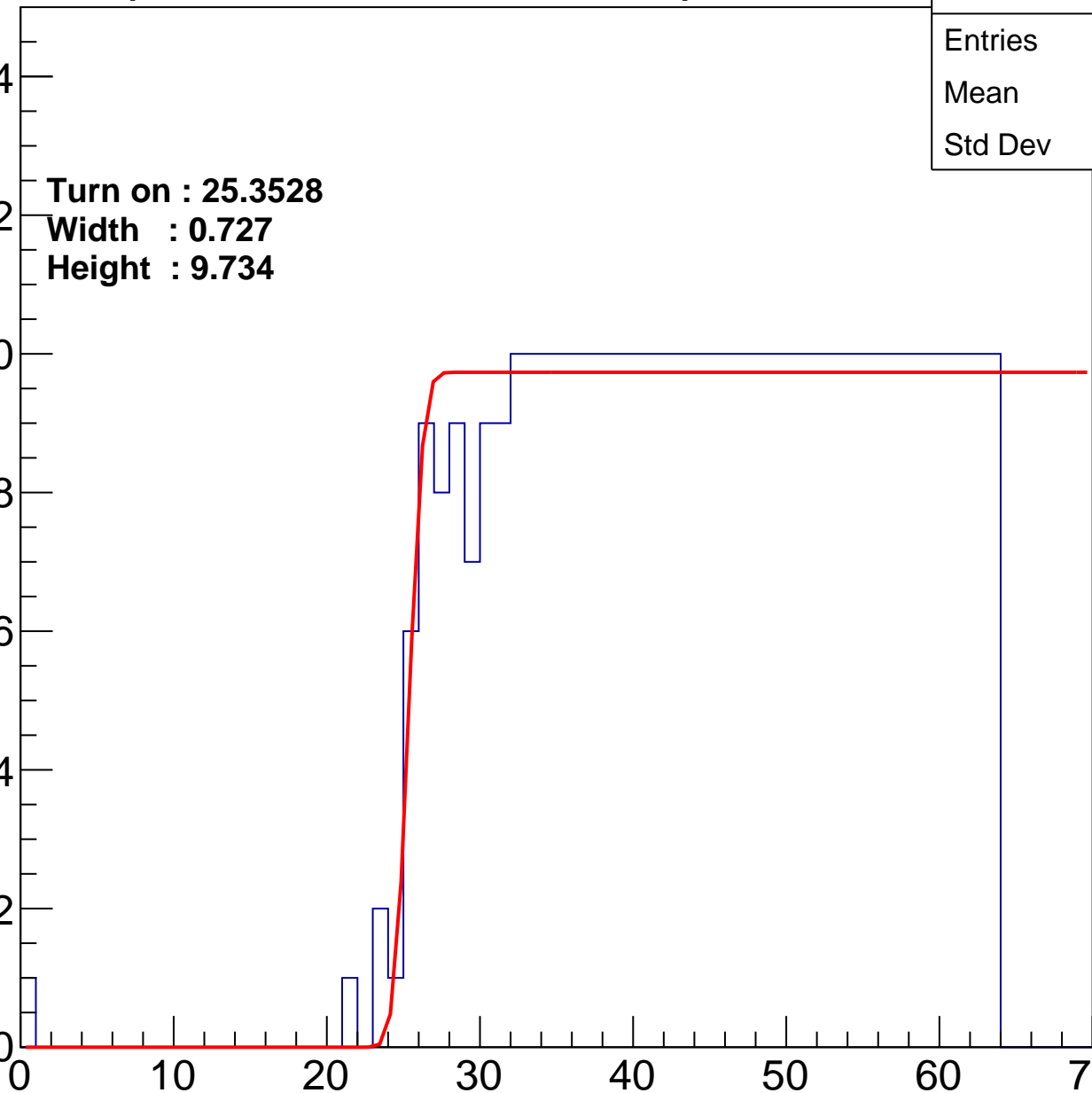
Width : 0.727

Height : 9.734

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch117

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.9919

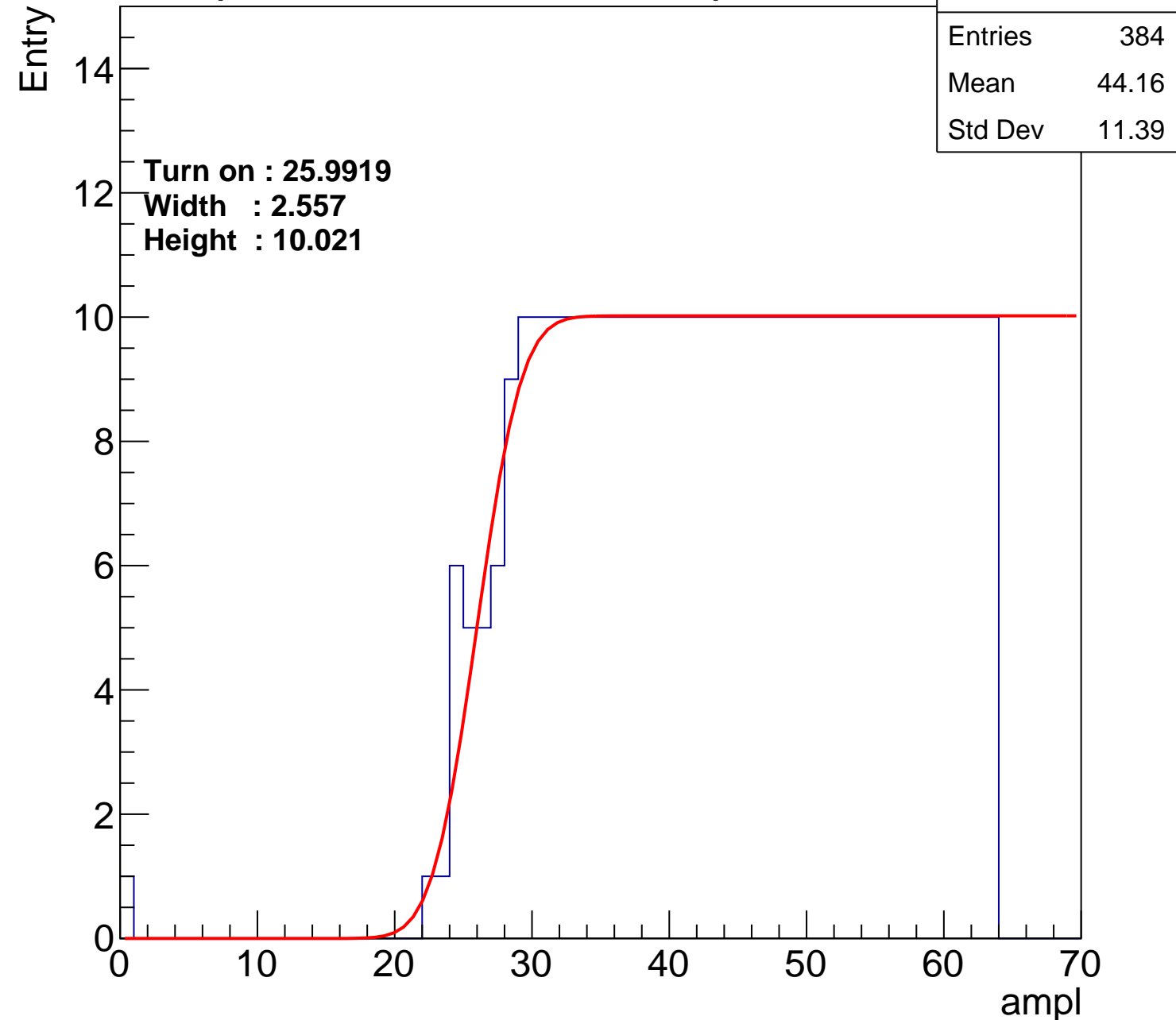
Width : 2.557

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch118

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.1347

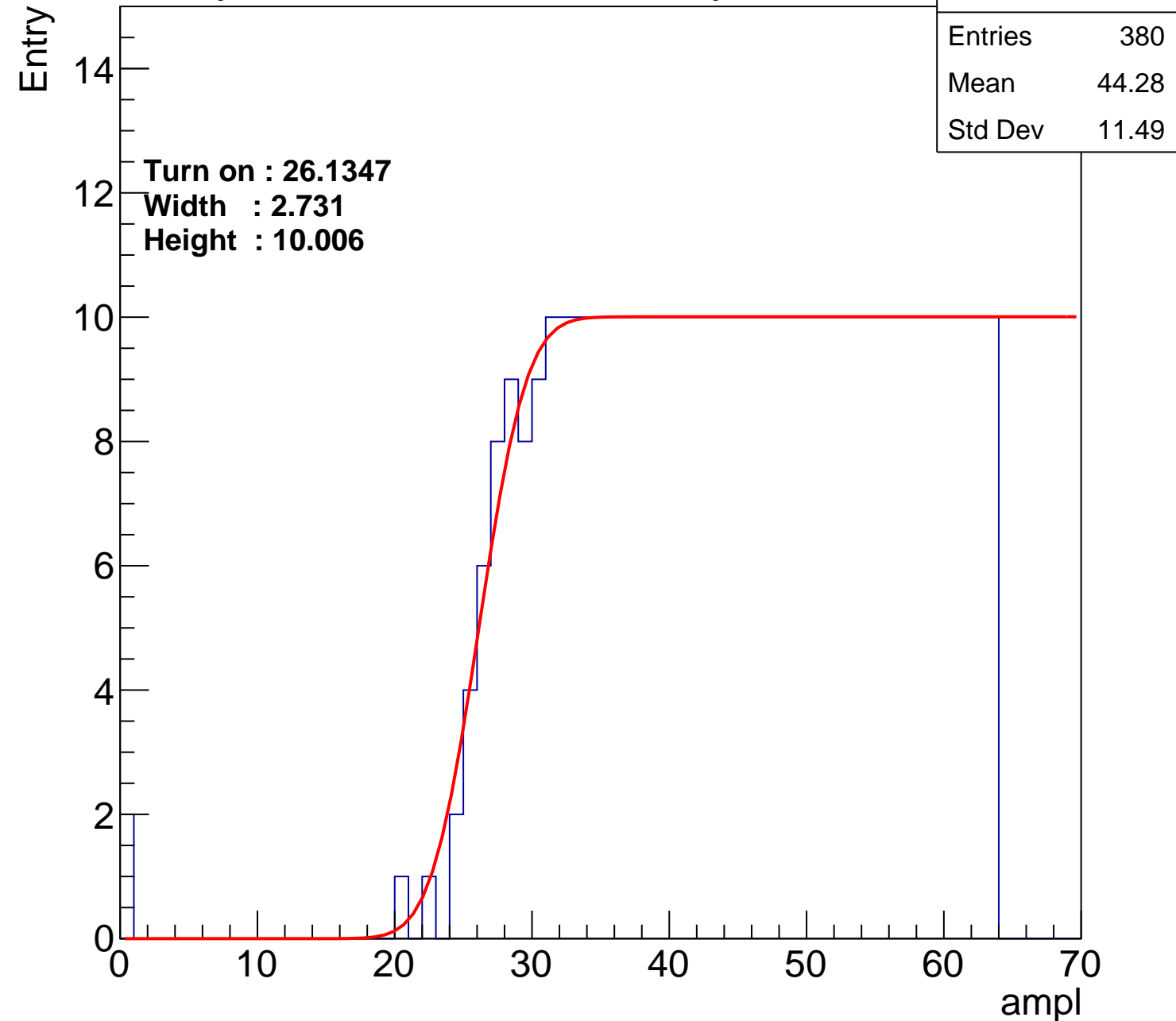
Width : 2.731

Height : 10.006

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch119

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	380
Mean	44.14
Std Dev	11.86

Turn on : 26.5770

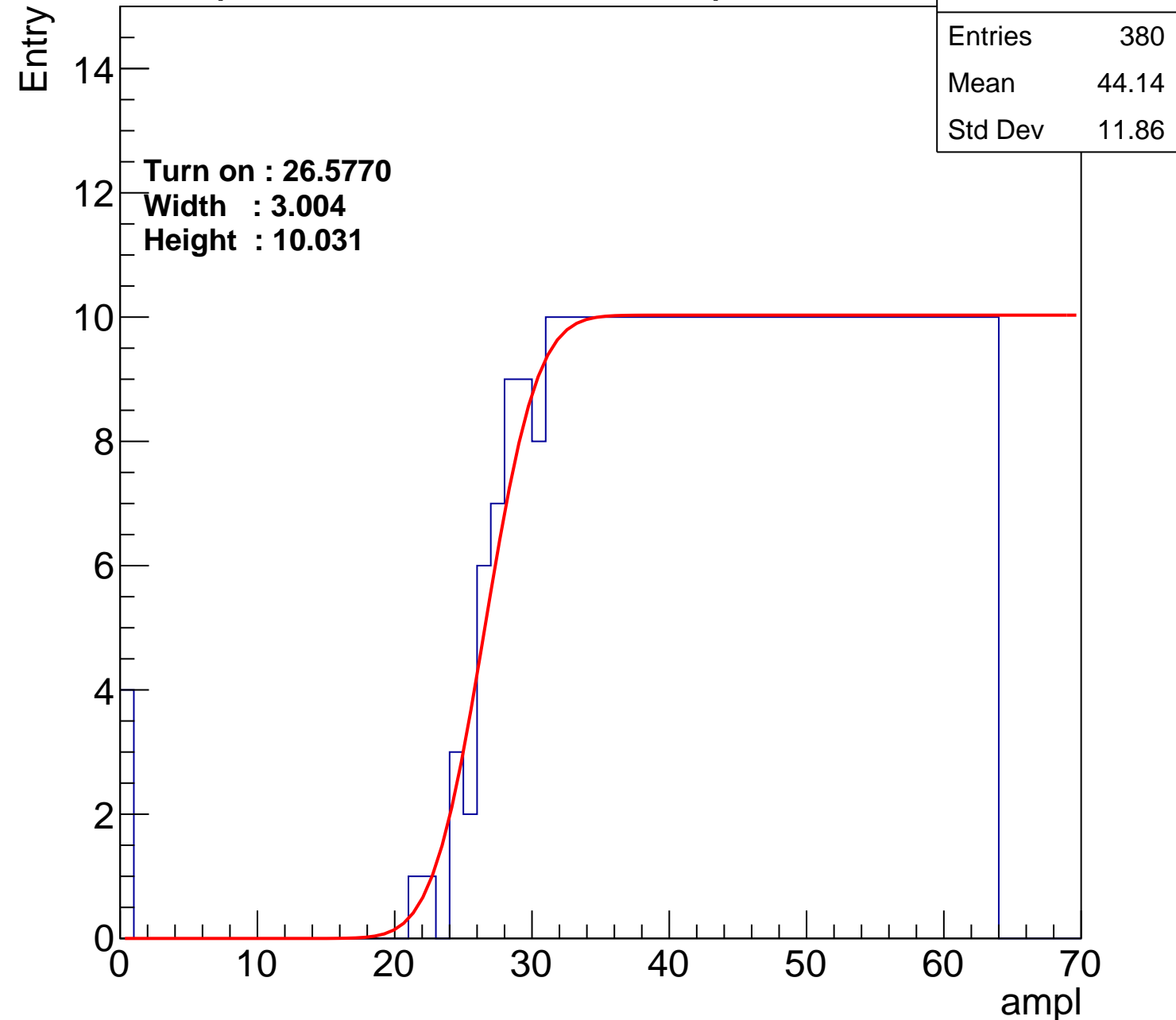
Width : 3.004

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch120

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 25.8400

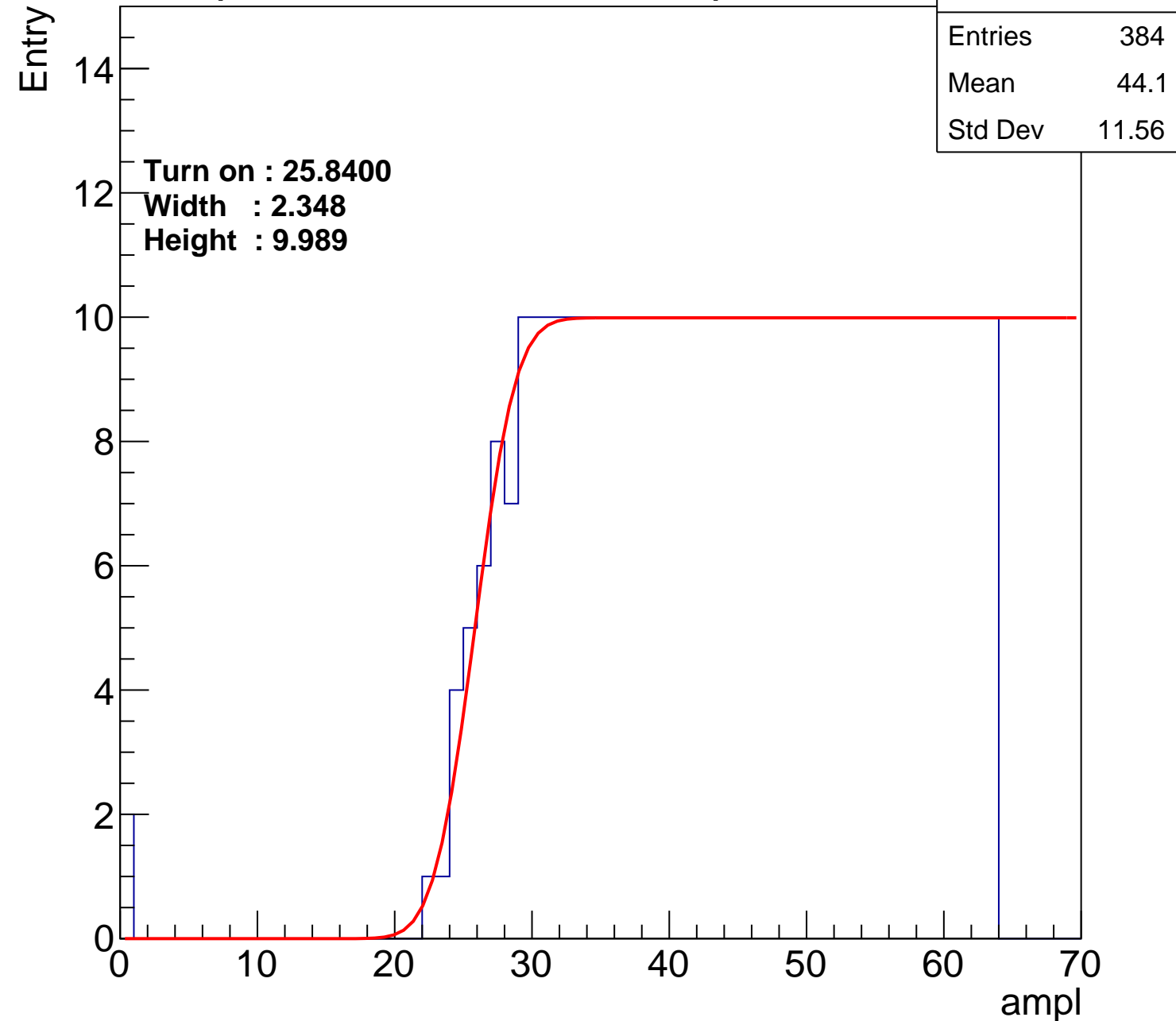
Width : 2.348

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch121

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.9125

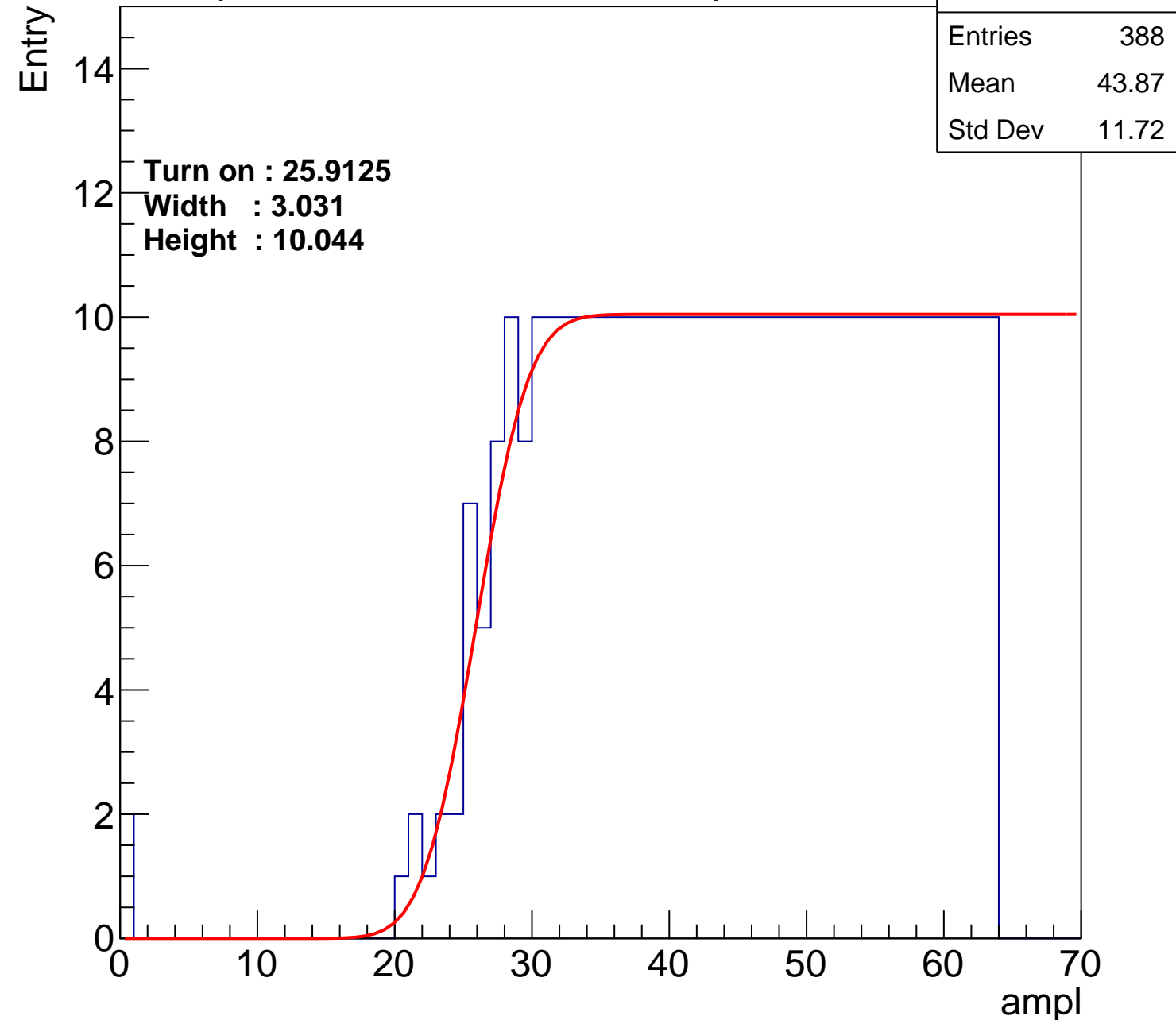
Width : 3.031

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch122

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.88
Std Dev	11.95

Turn on : 26.2252

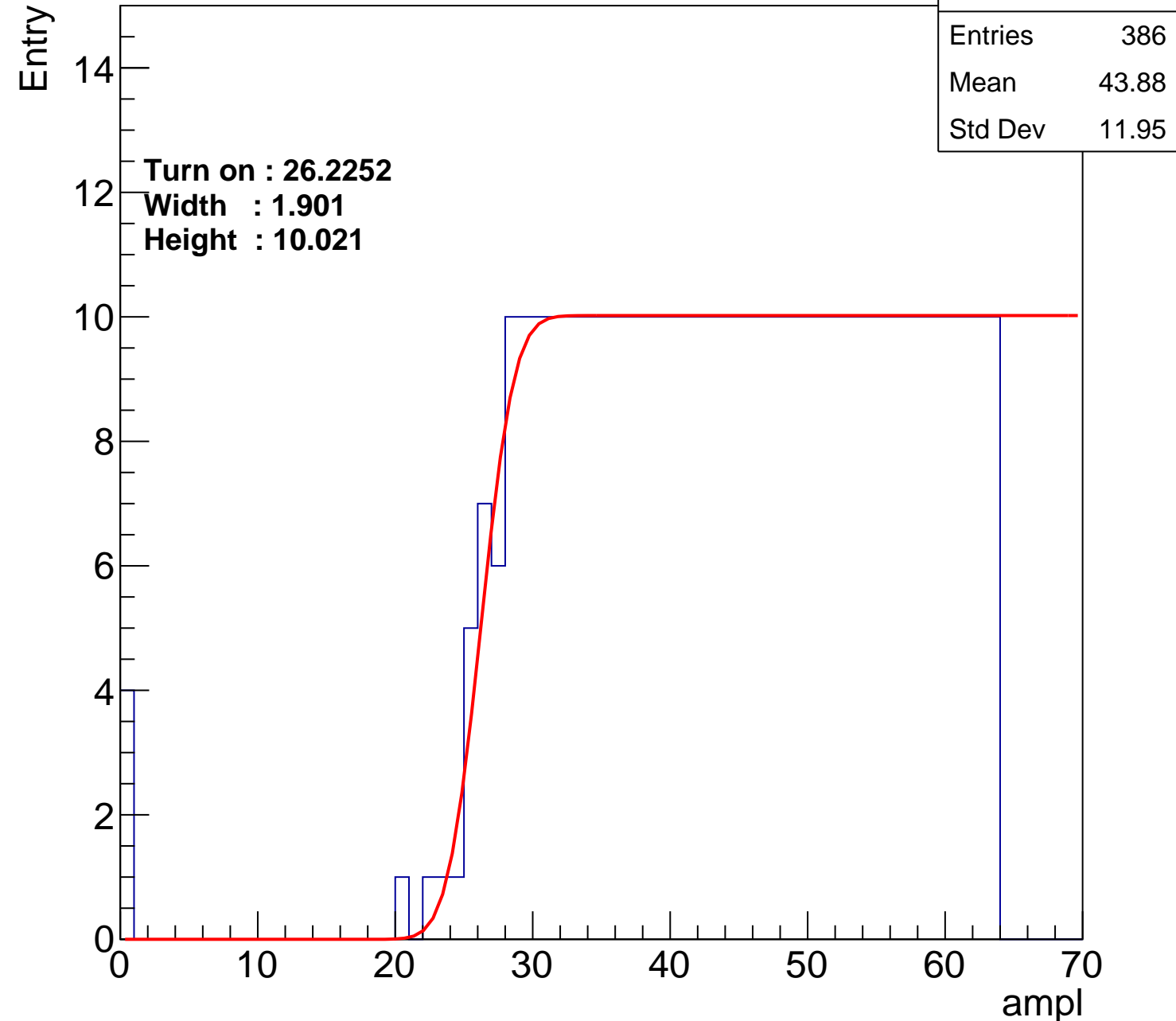
Width : 1.901

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch123

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 25.8583

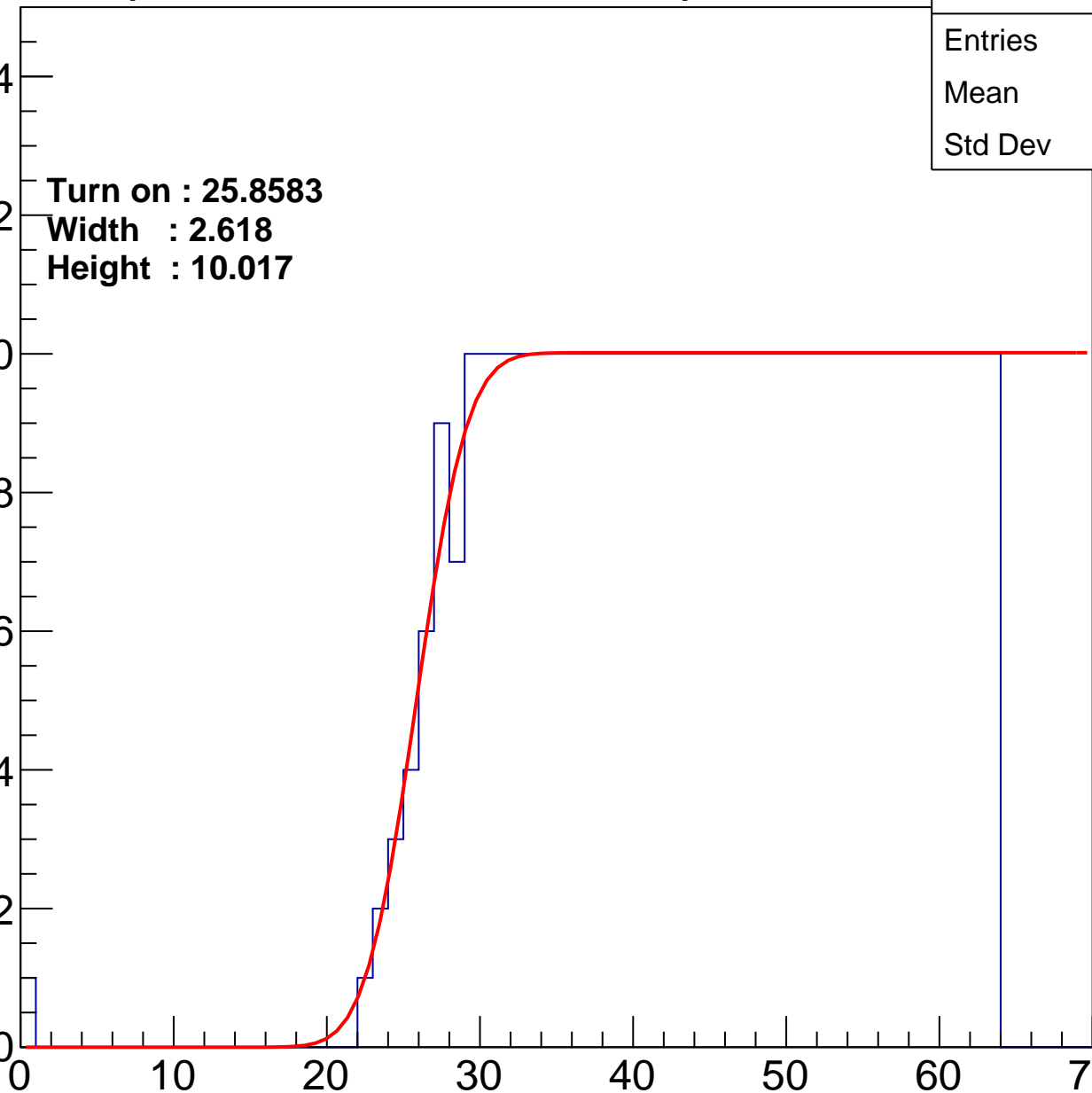
Width : 2.618

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch124

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.35
Std Dev	11.35

Turn on : 26.4005

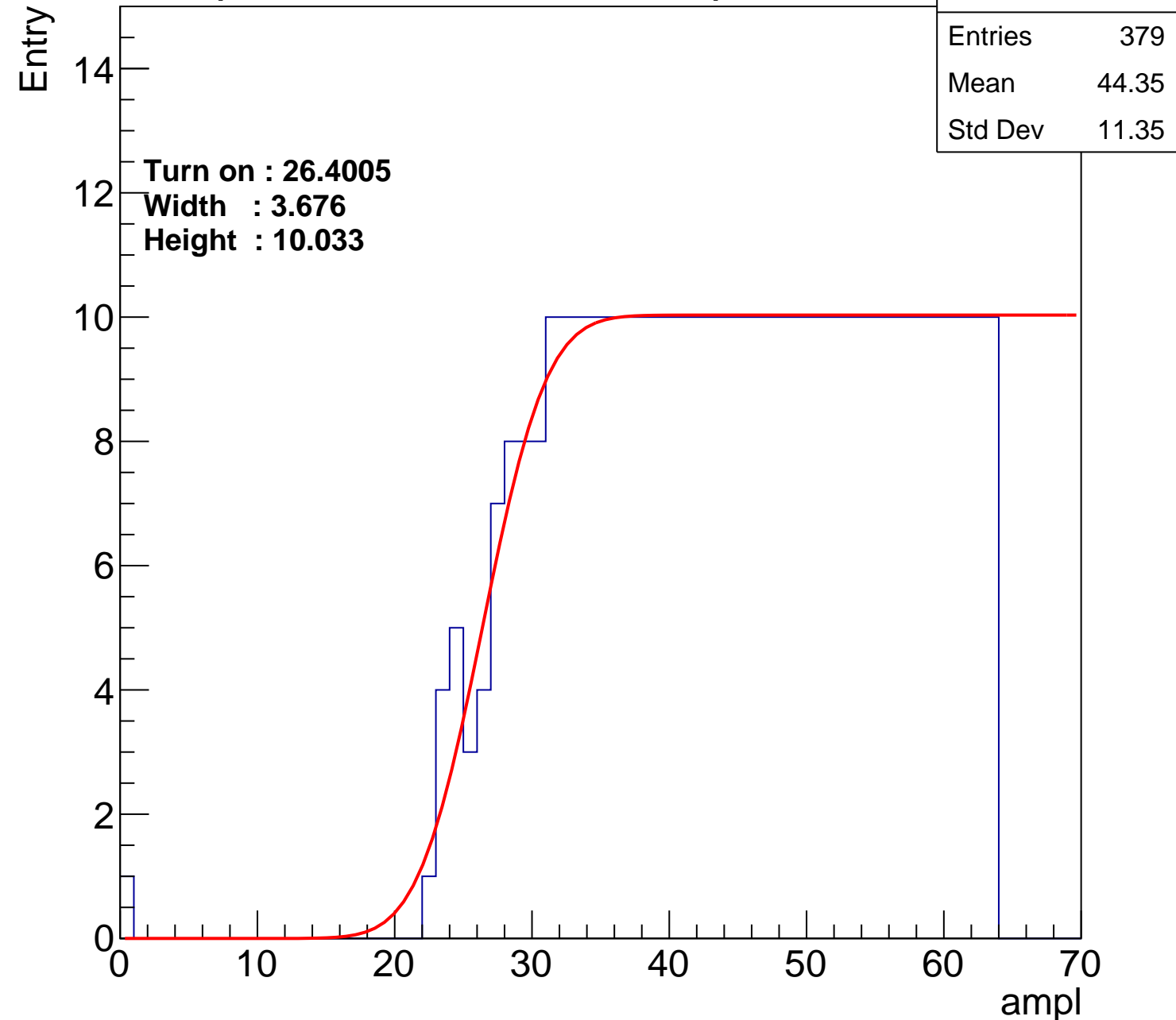
Width : 3.676

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch125

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	379
Mean	44.22
Std Dev	11.79

Turn on : 26.6837

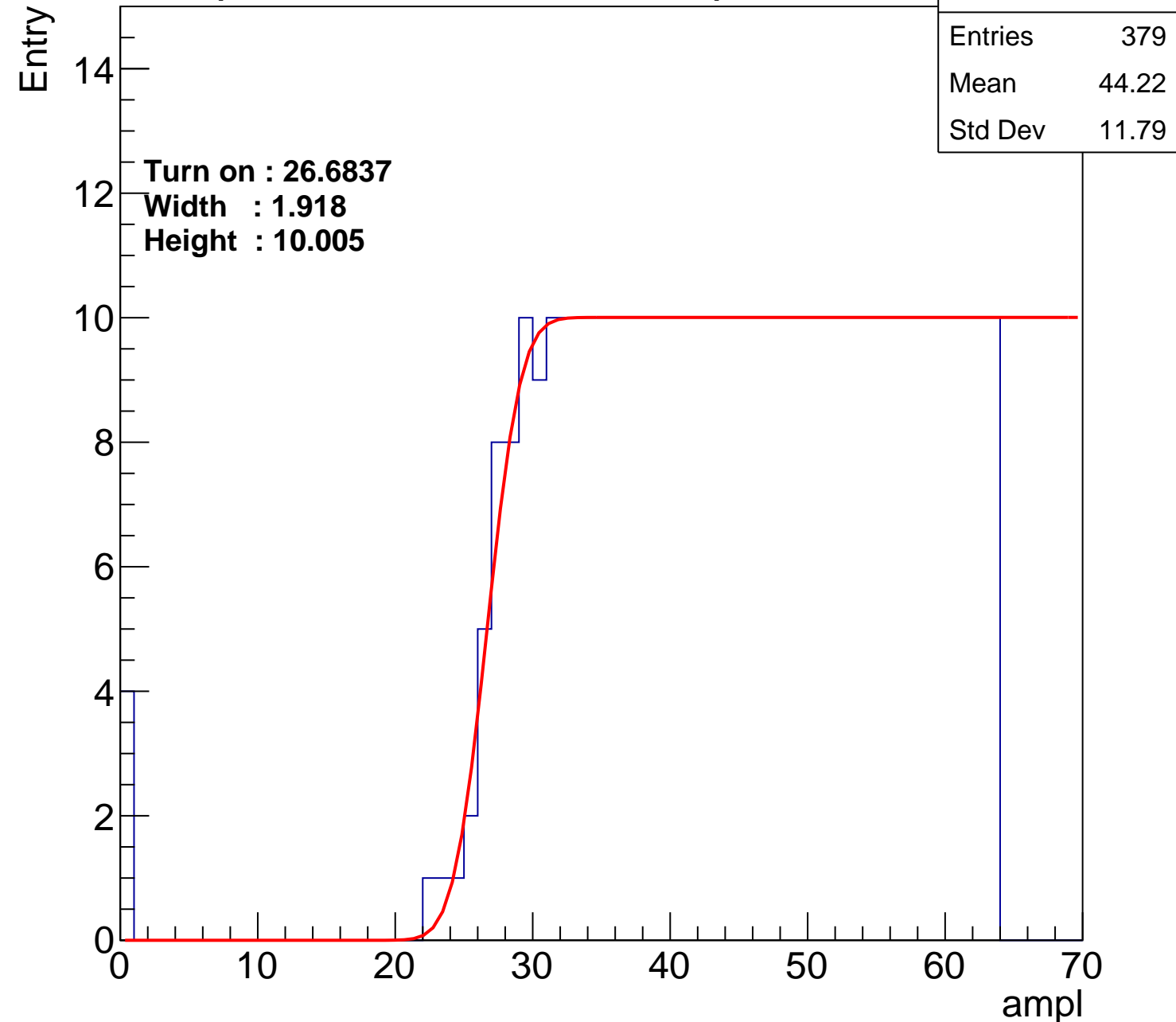
Width : 1.918

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch126

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.7297

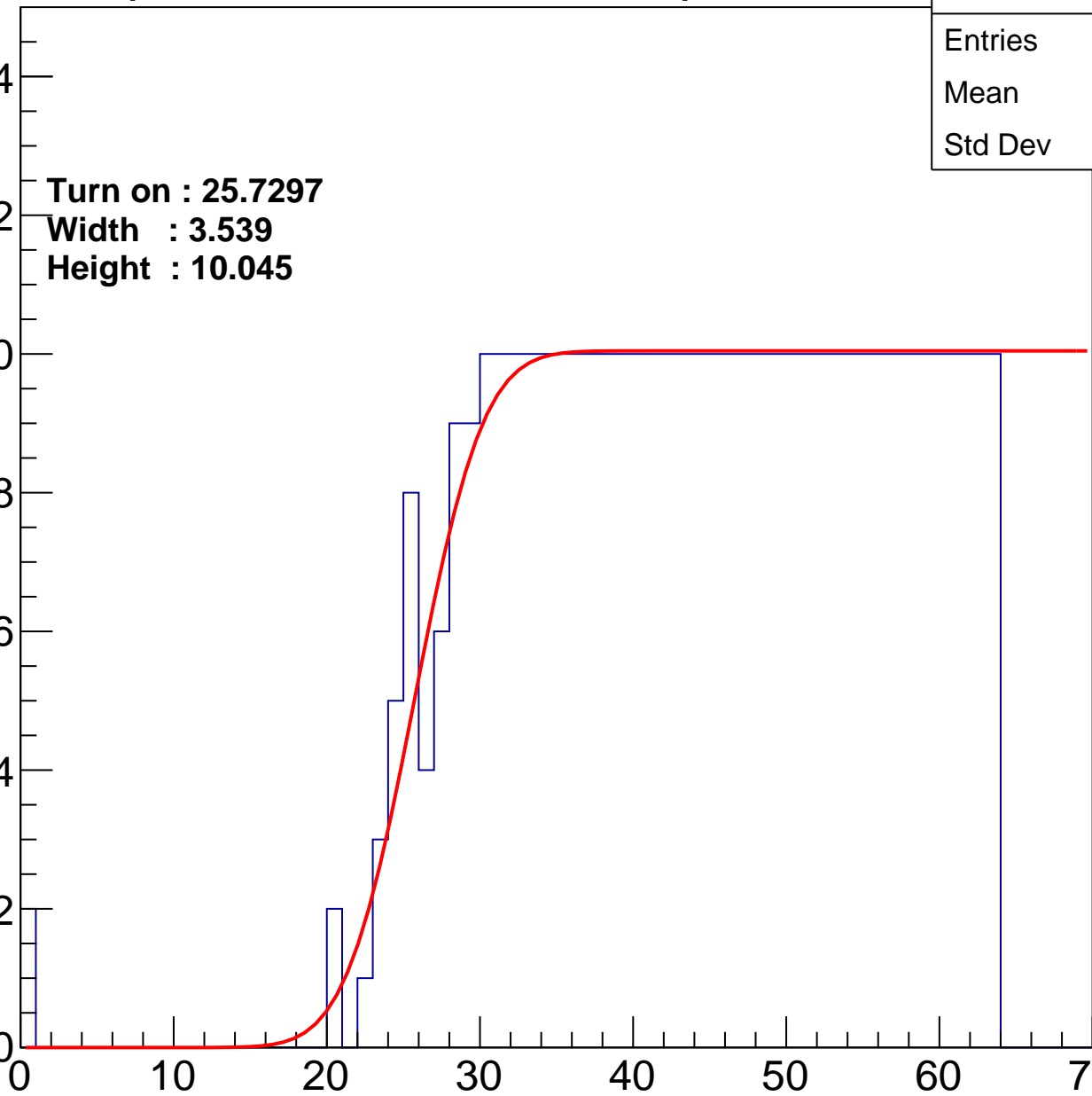
Width : 3.539

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L002S, U6-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.1859

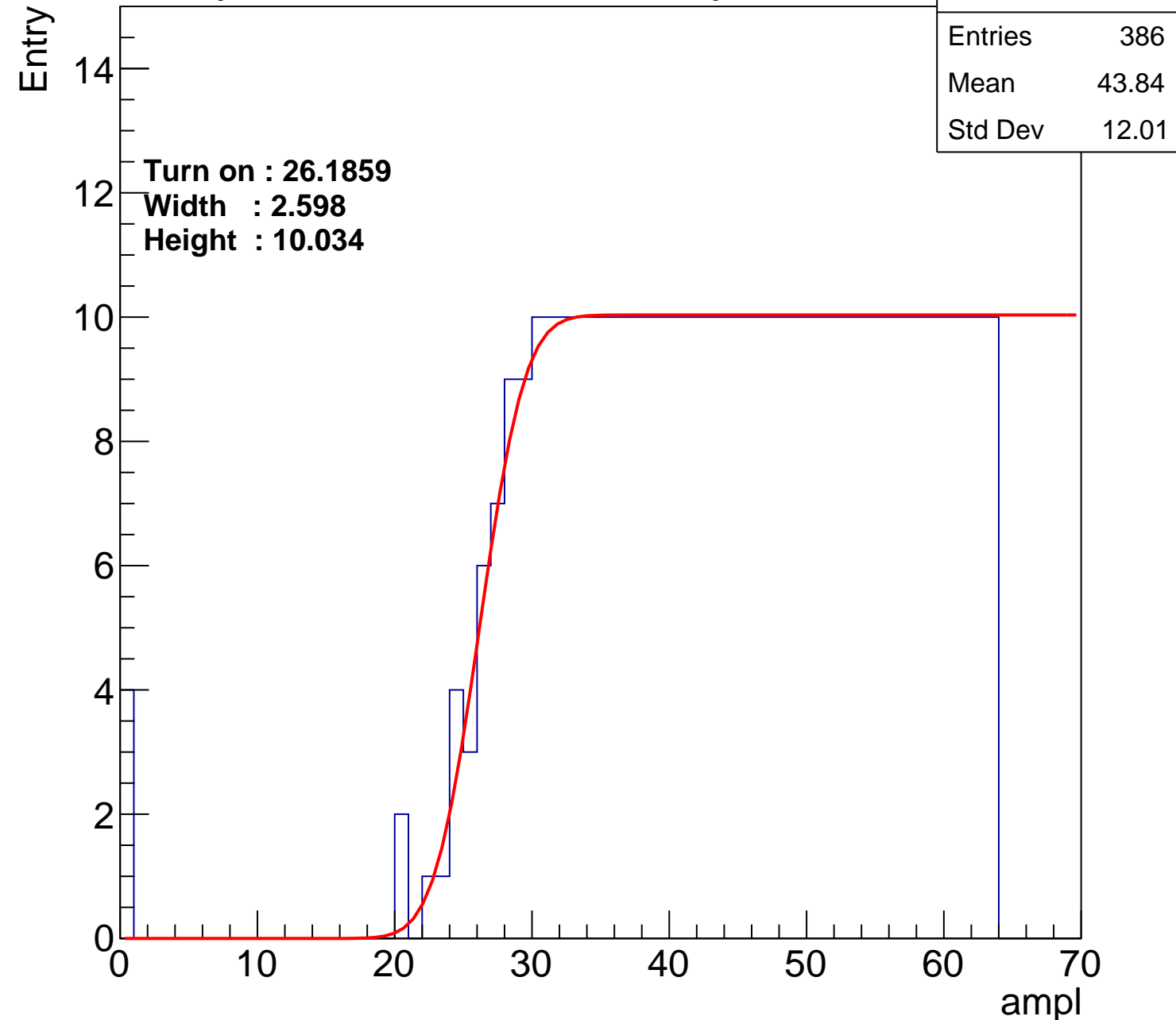
Width : 2.598

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L002S, U6-ch127

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.1859

Width : 2.598

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

