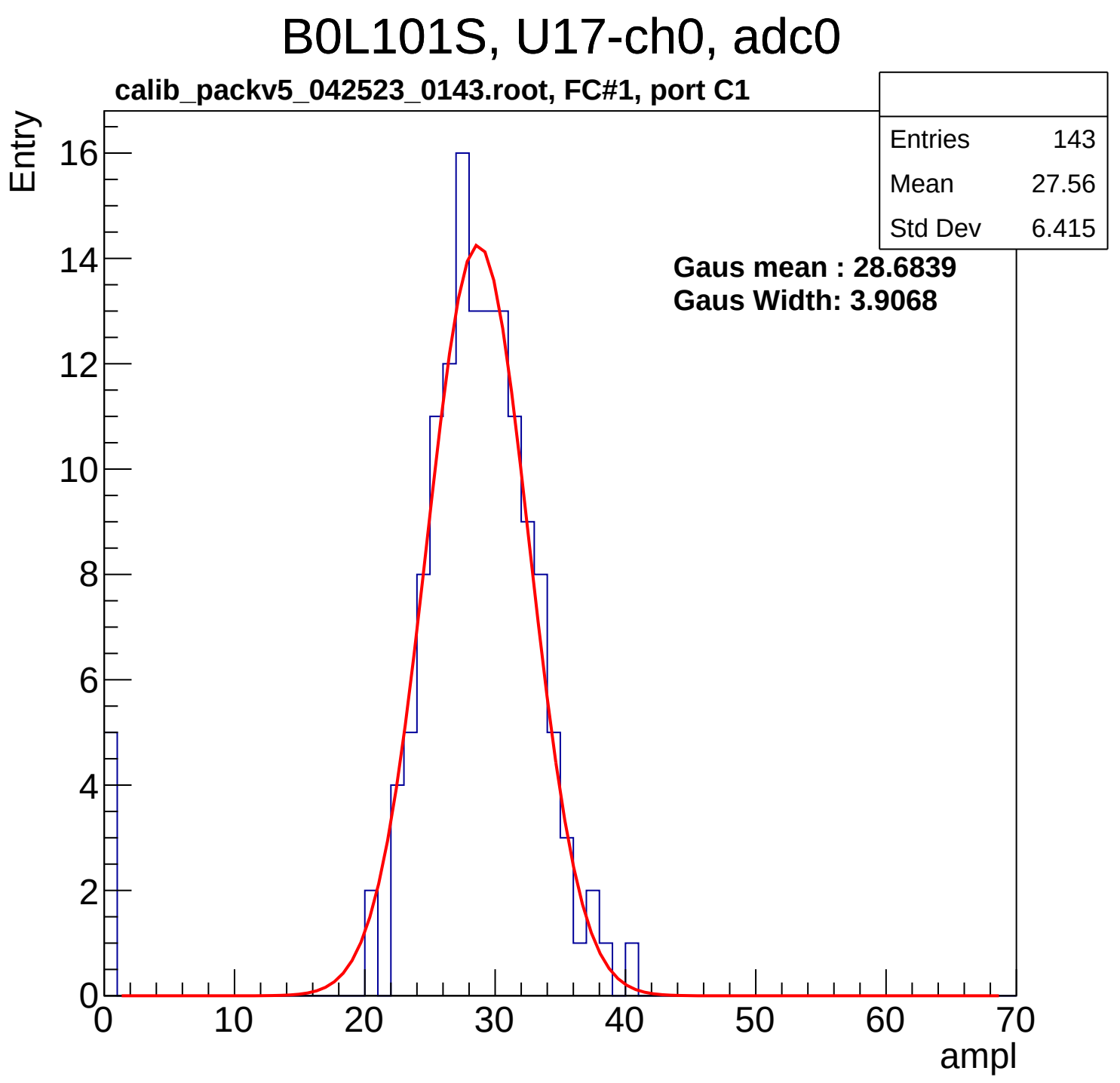
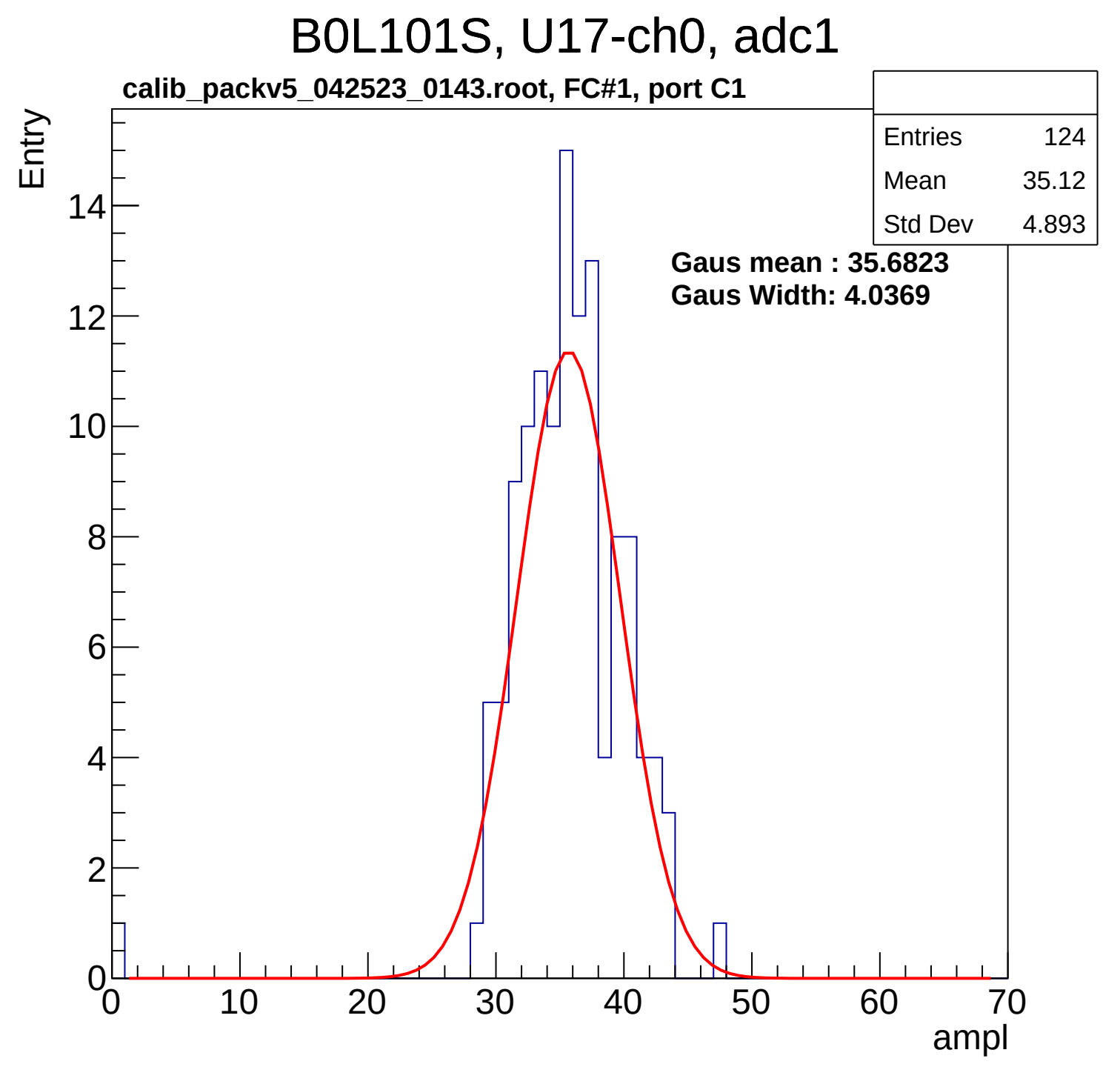
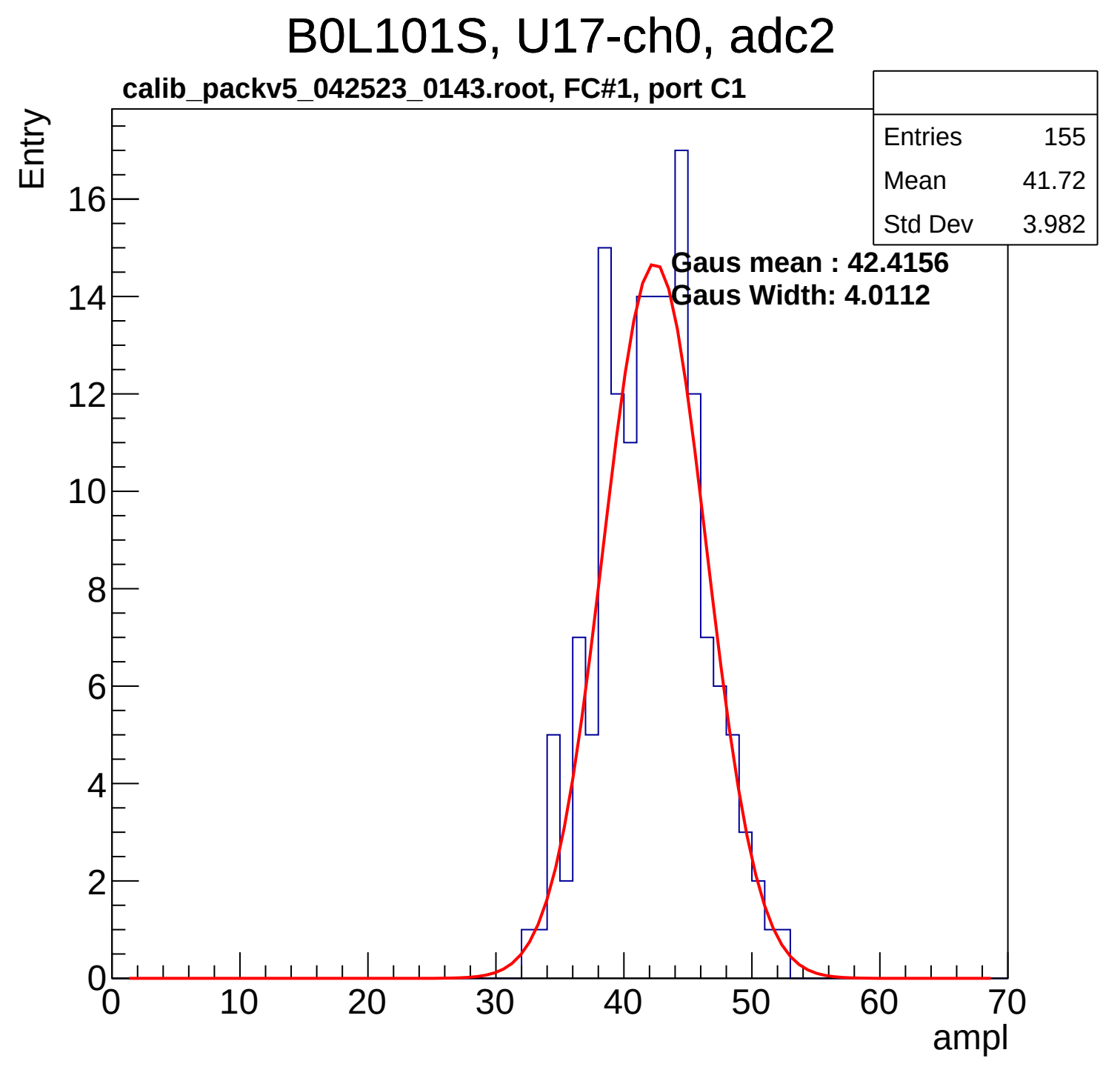
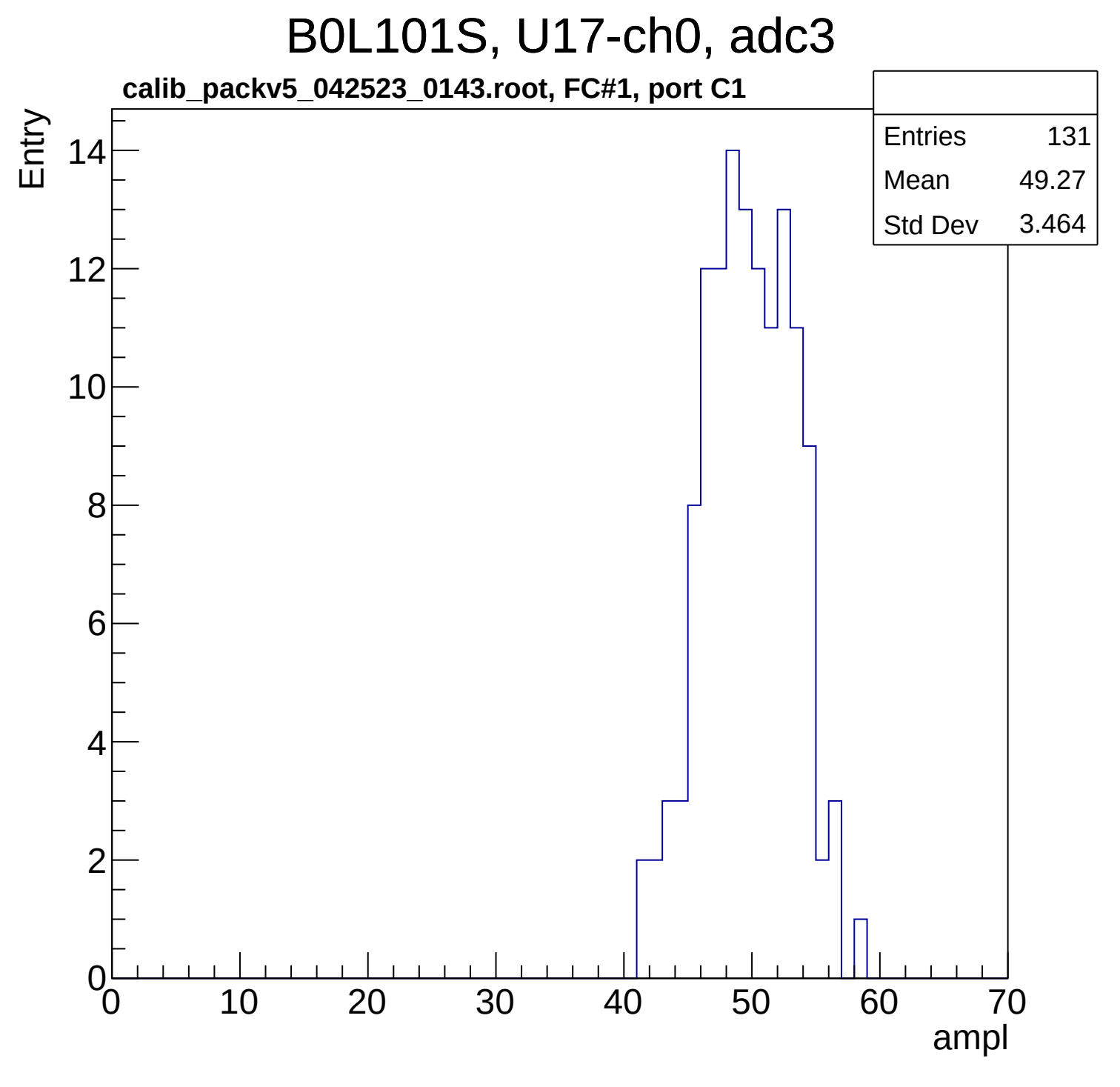
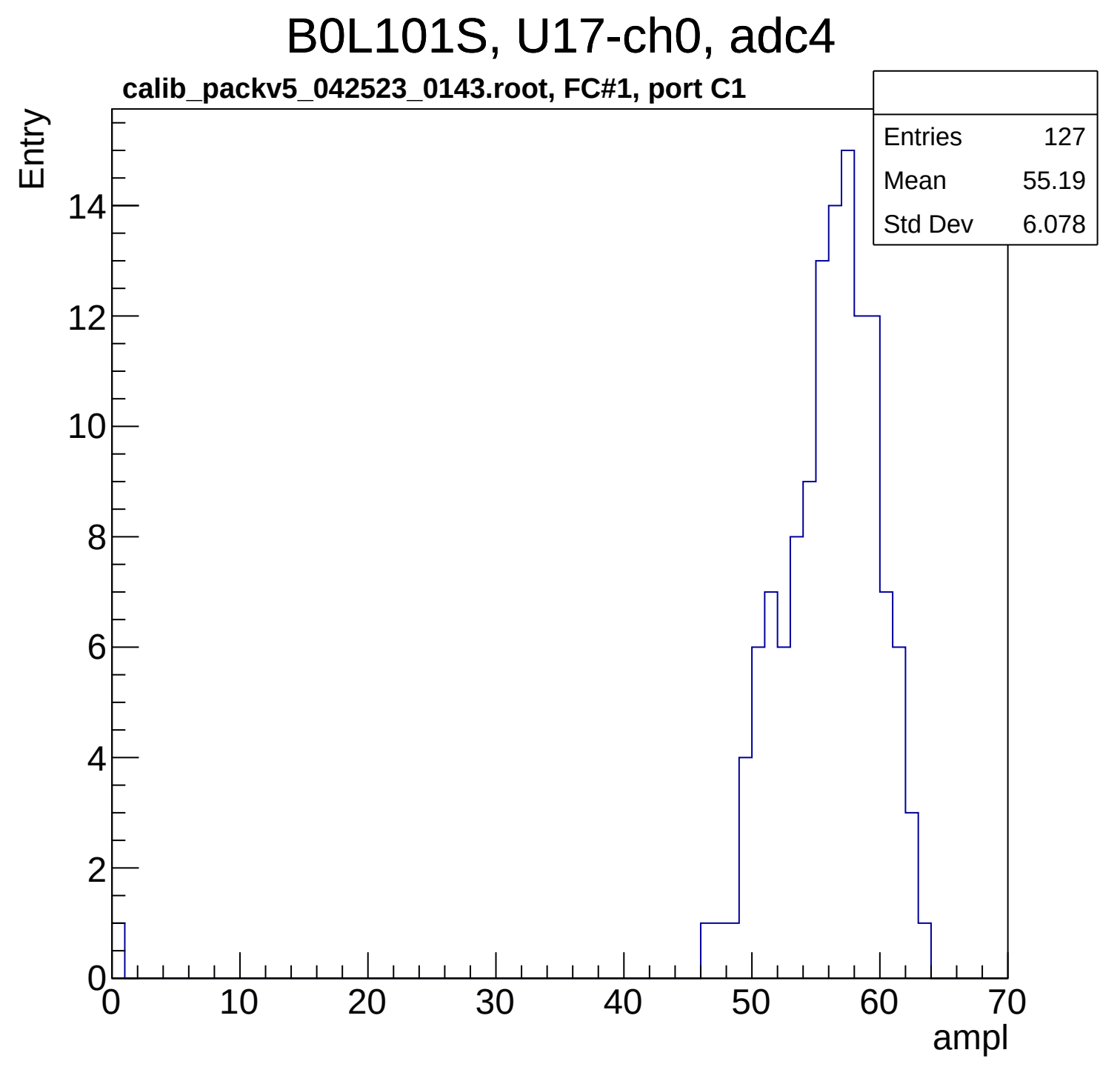
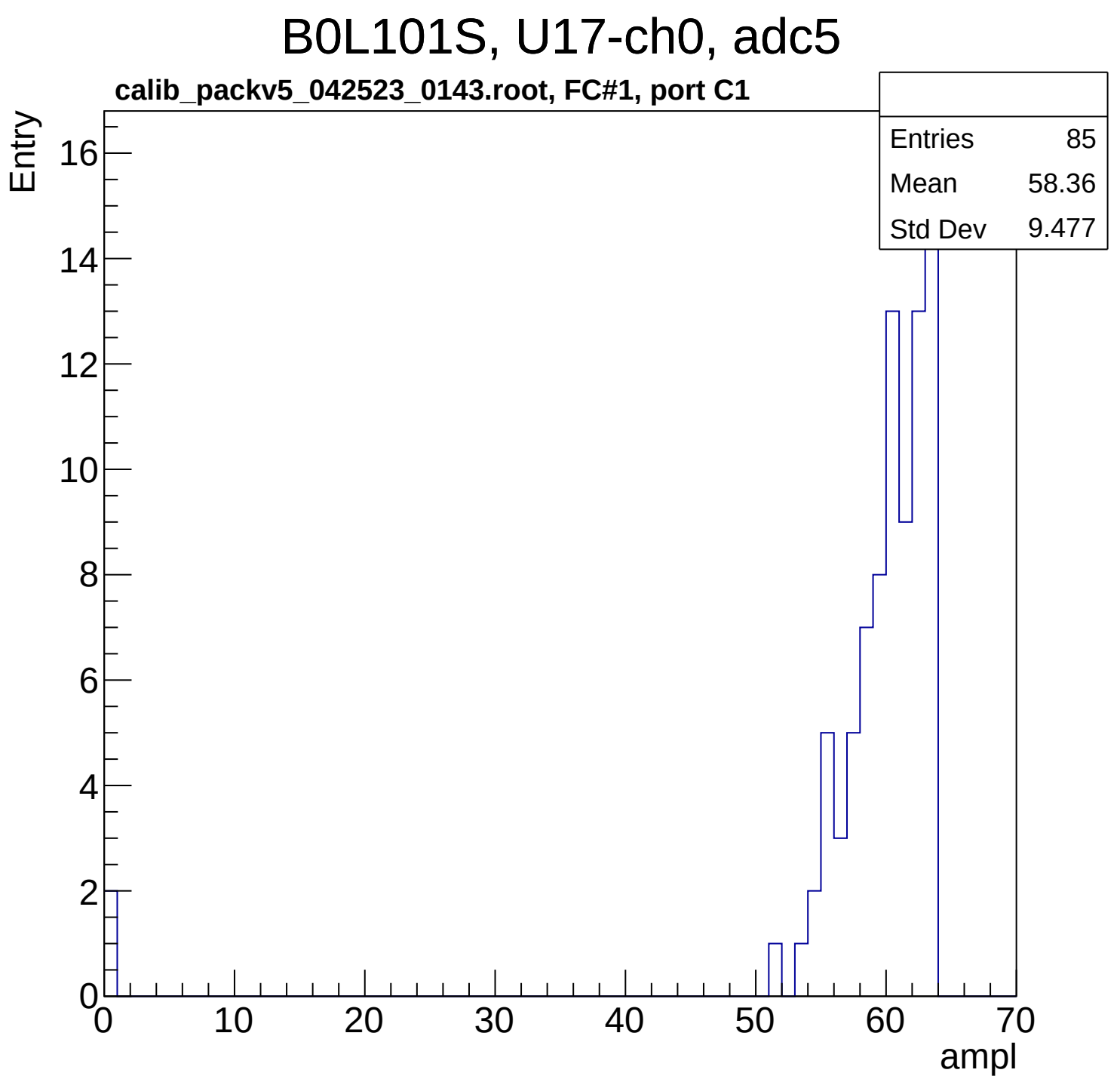

B0L101S, U17-ch0, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 27.56
Std Dev 6.415
Gaus mean : 28.6839
Gaus Width: 3.9068
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

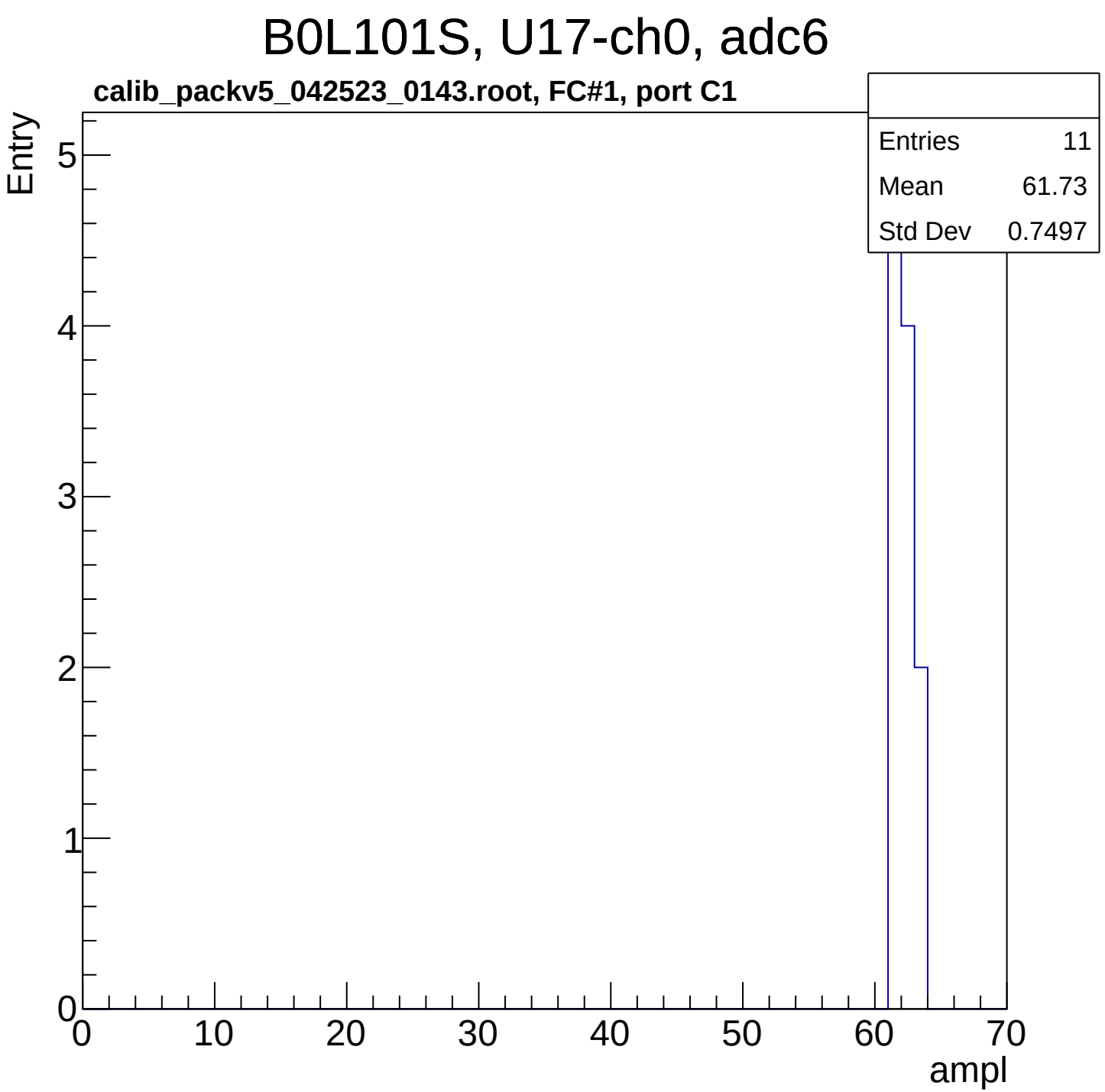

B0L101S, U17-ch0, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 35.12
Std Dev 4.893
Gaus mean : 35.6823
Gaus Width: 4.0369
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

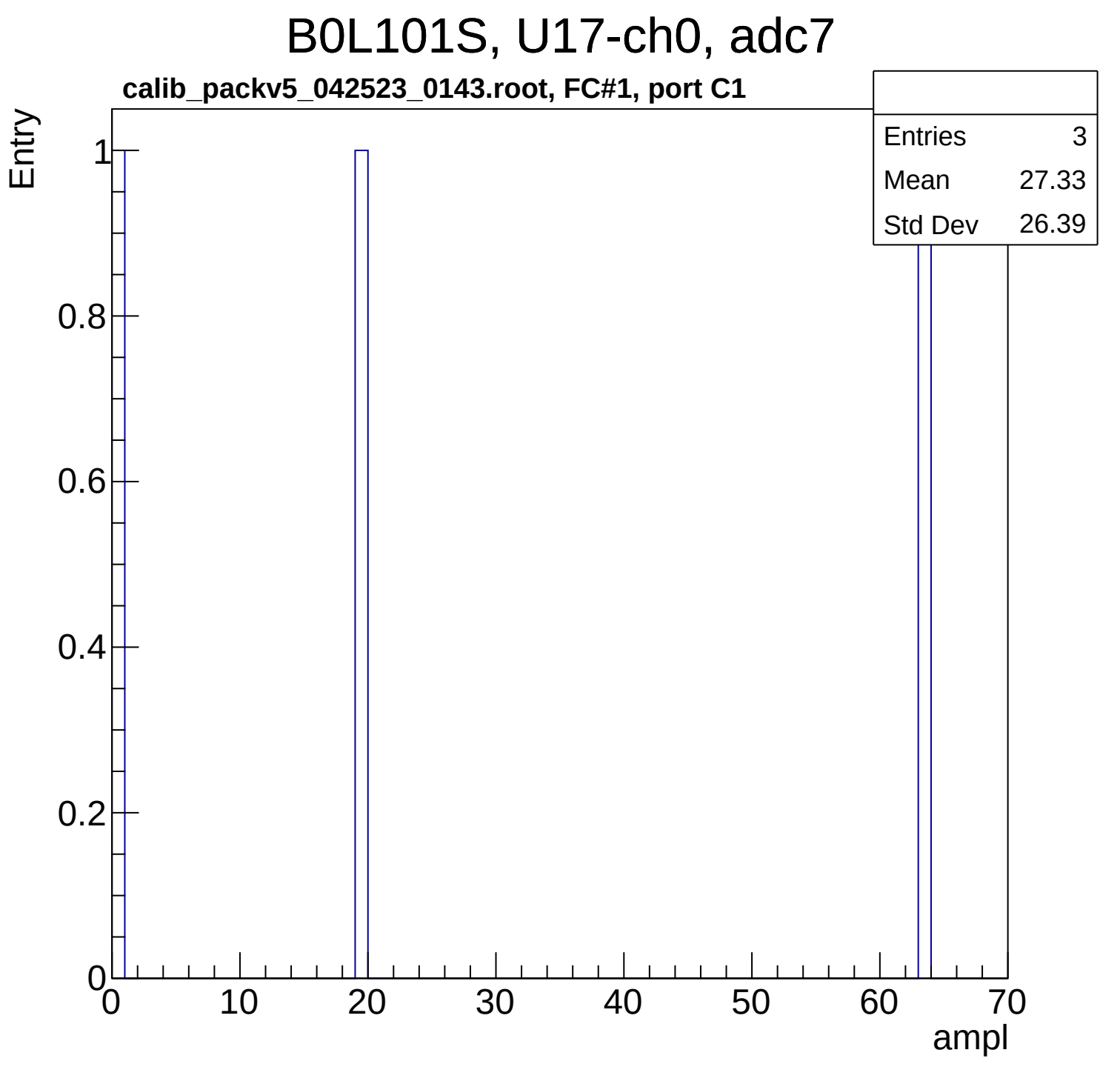

B0L101S, U17-ch0, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 41.72
Std Dev 3.982
Gaus mean : 42.4156
Gaus Width: 4.0112
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

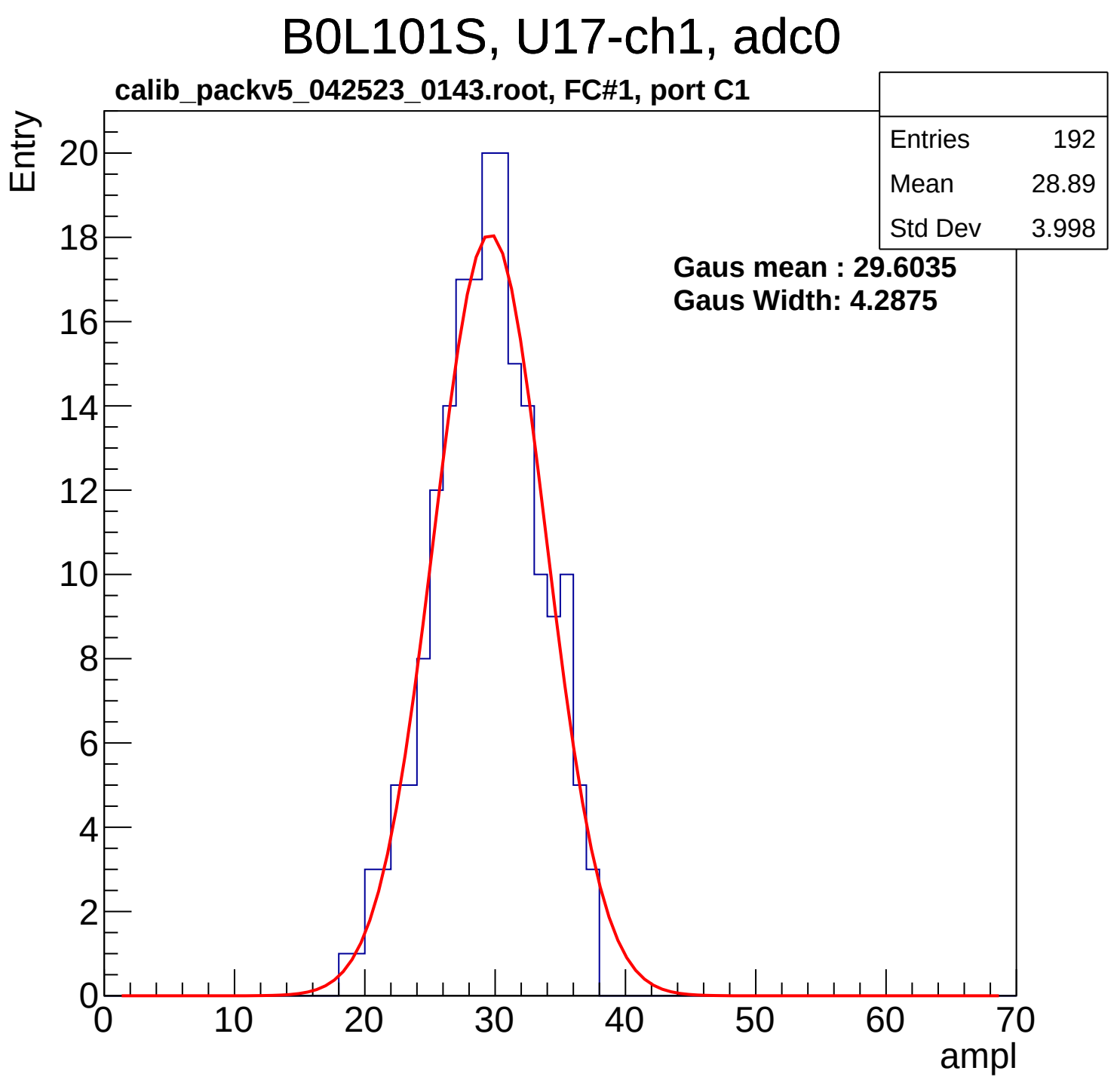

B0L101S, U17-ch0, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 49.27
Std Dev 3.464
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

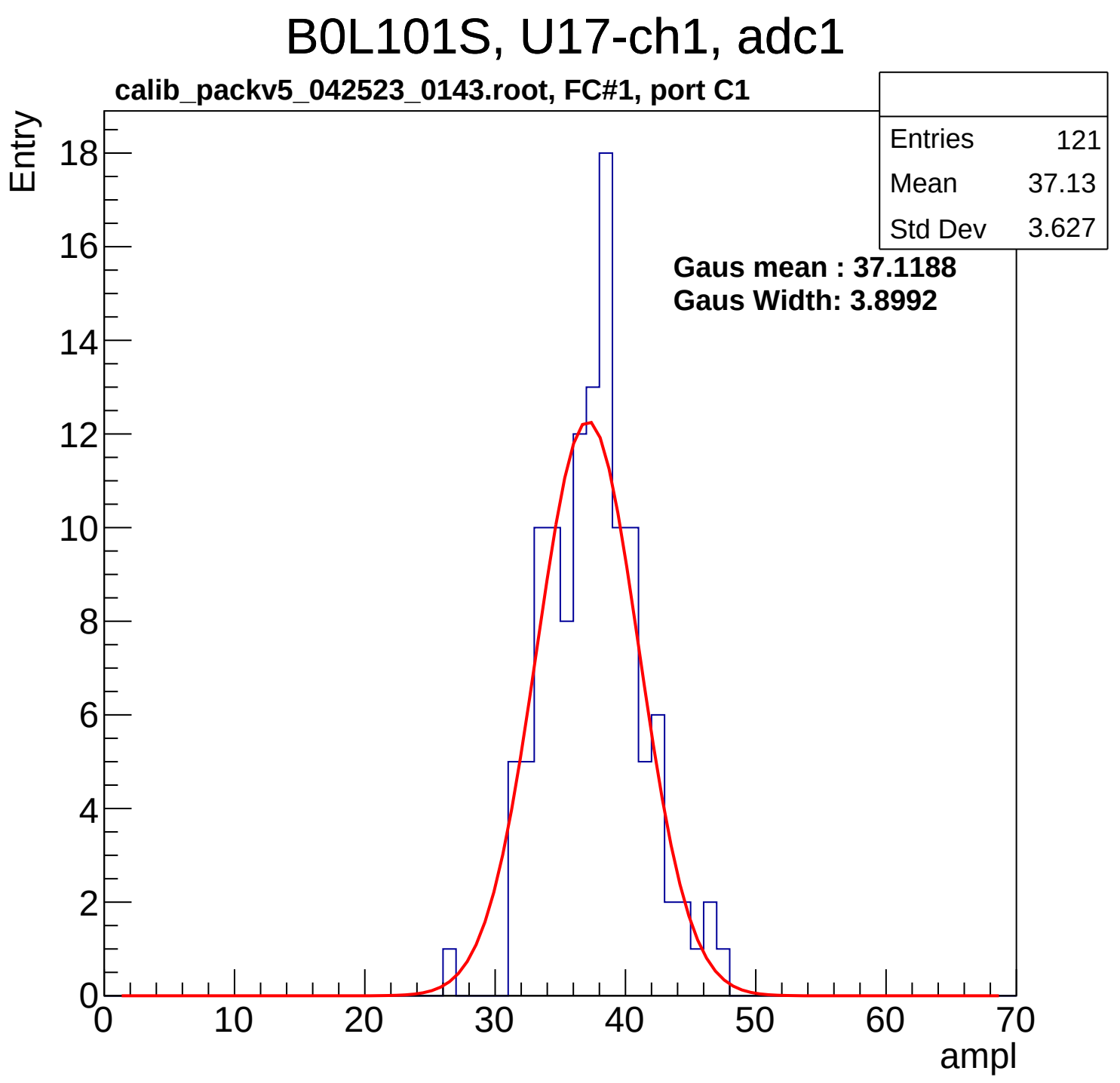

B0L101S, U17-ch0, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 55.19
Std Dev 6.078
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

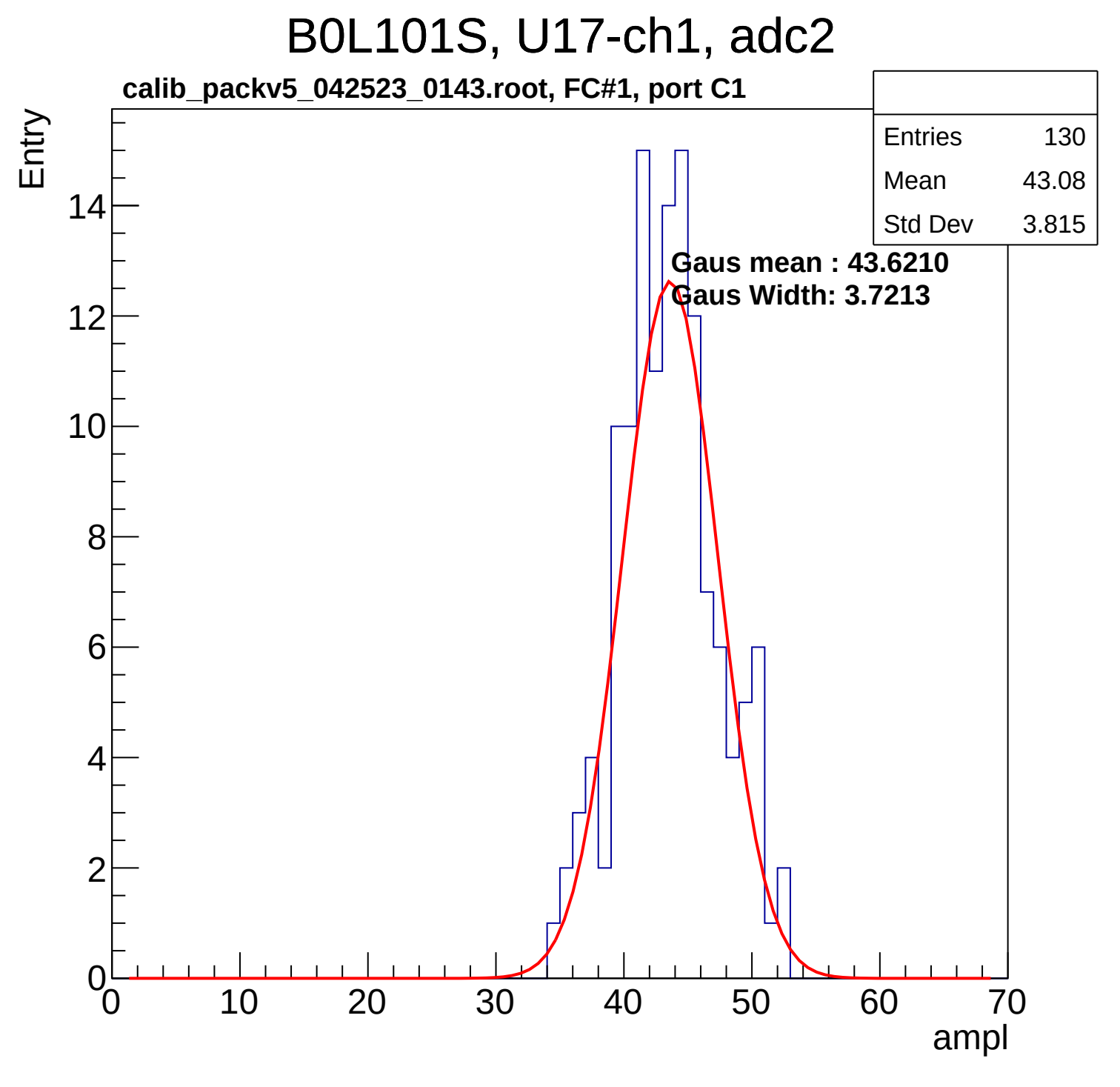

B0L101S, U17-ch0, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 85
Mean 58.36
Std Dev 9.477
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

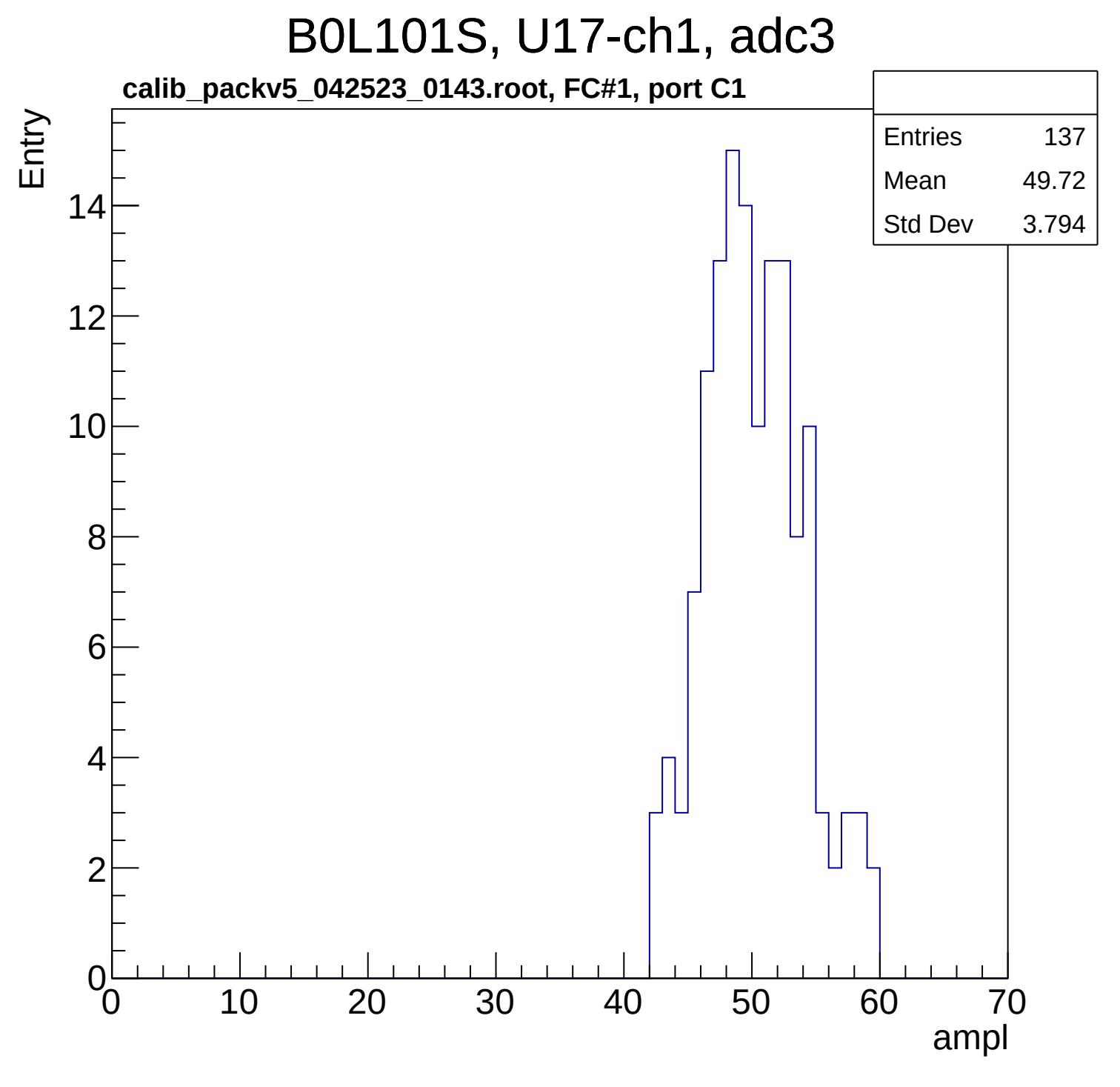

B0L101S, U17-ch0, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 61.73
Std Dev 0.7497
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

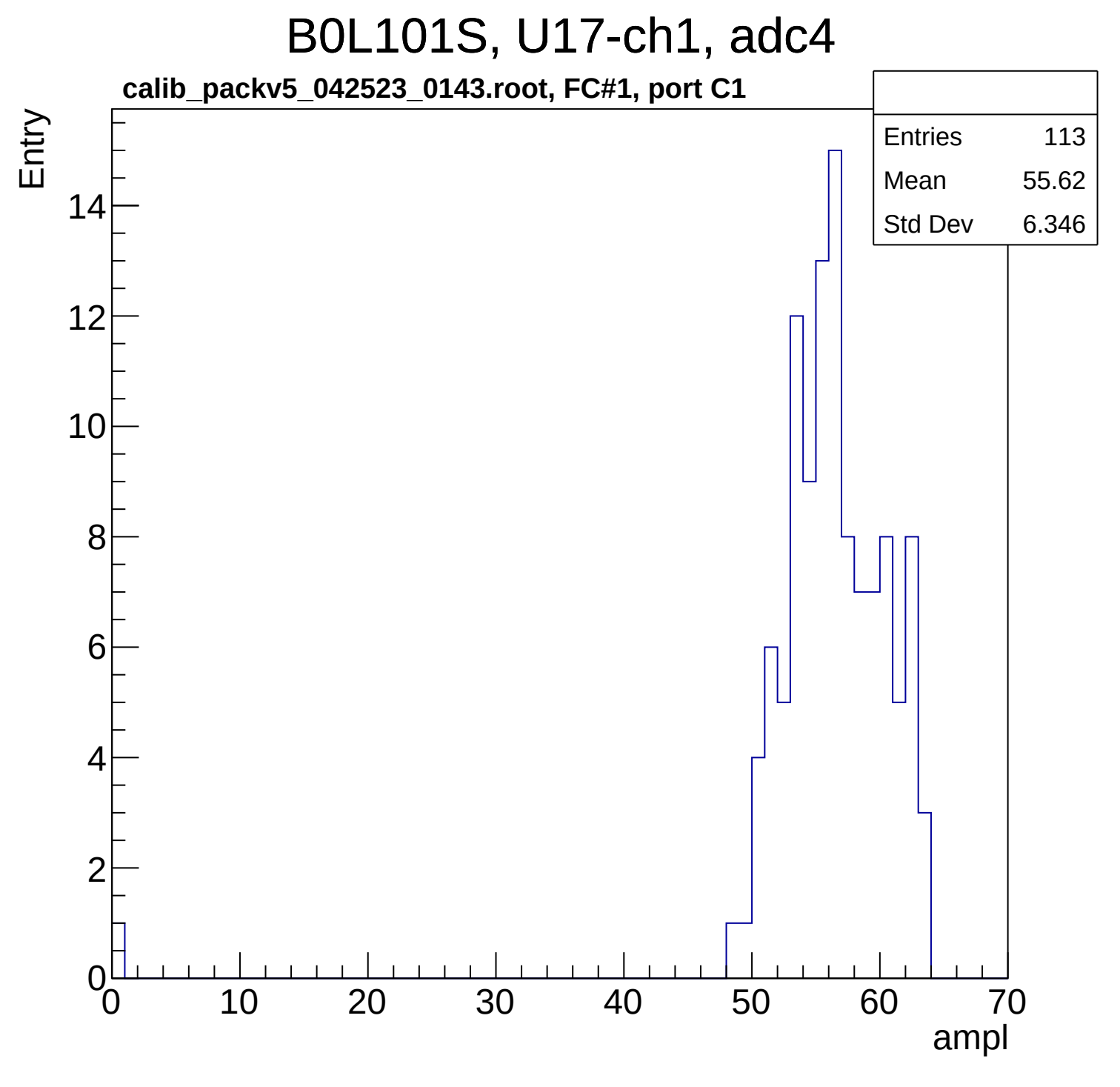

B0L101S, U17-ch0, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 27.33
Std Dev 26.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

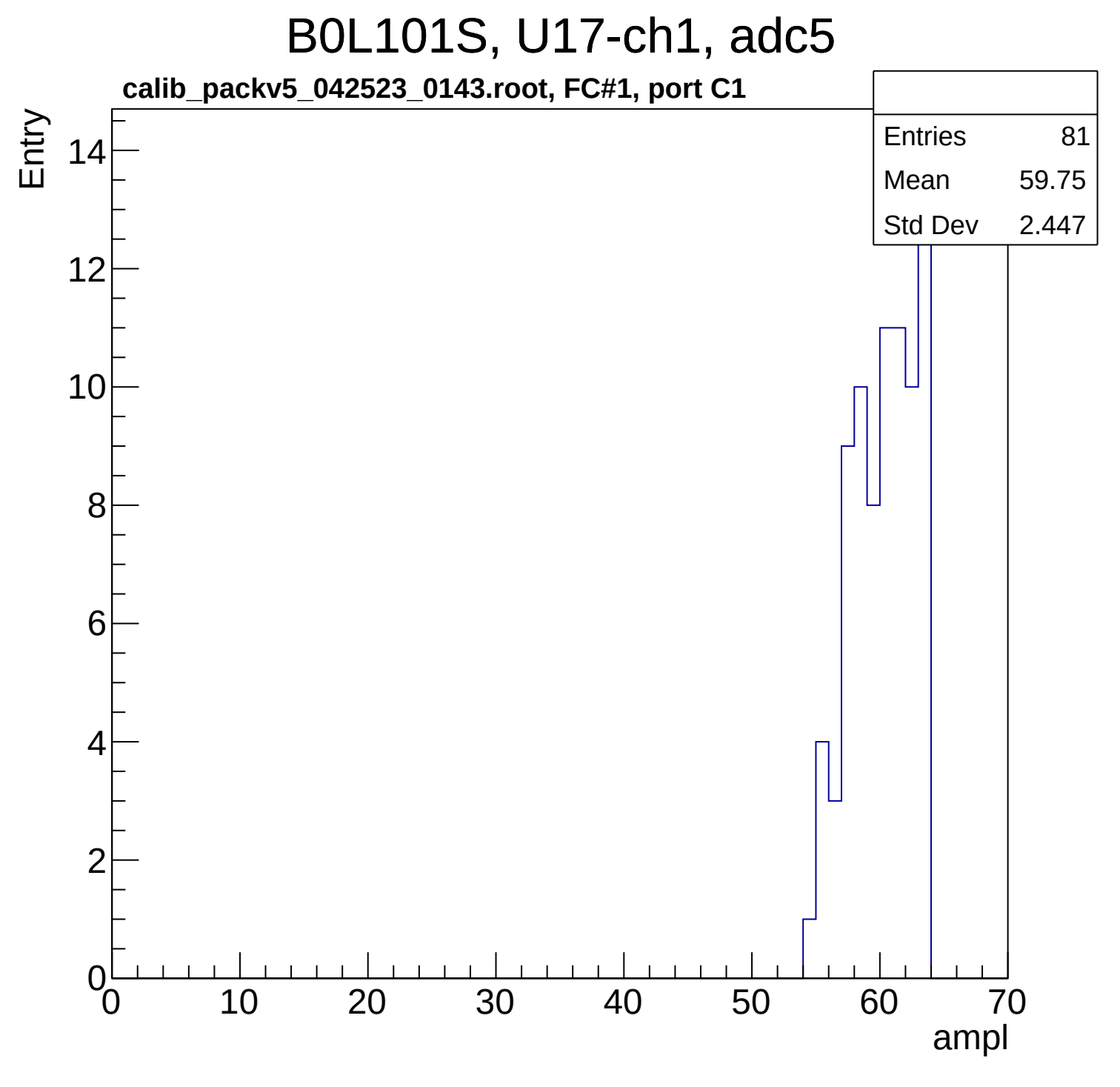

B0L101S, U17-ch1, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 192
Mean 28.89
Std Dev 3.998
Gaus mean : 29.6035
Gaus Width: 4.2875
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

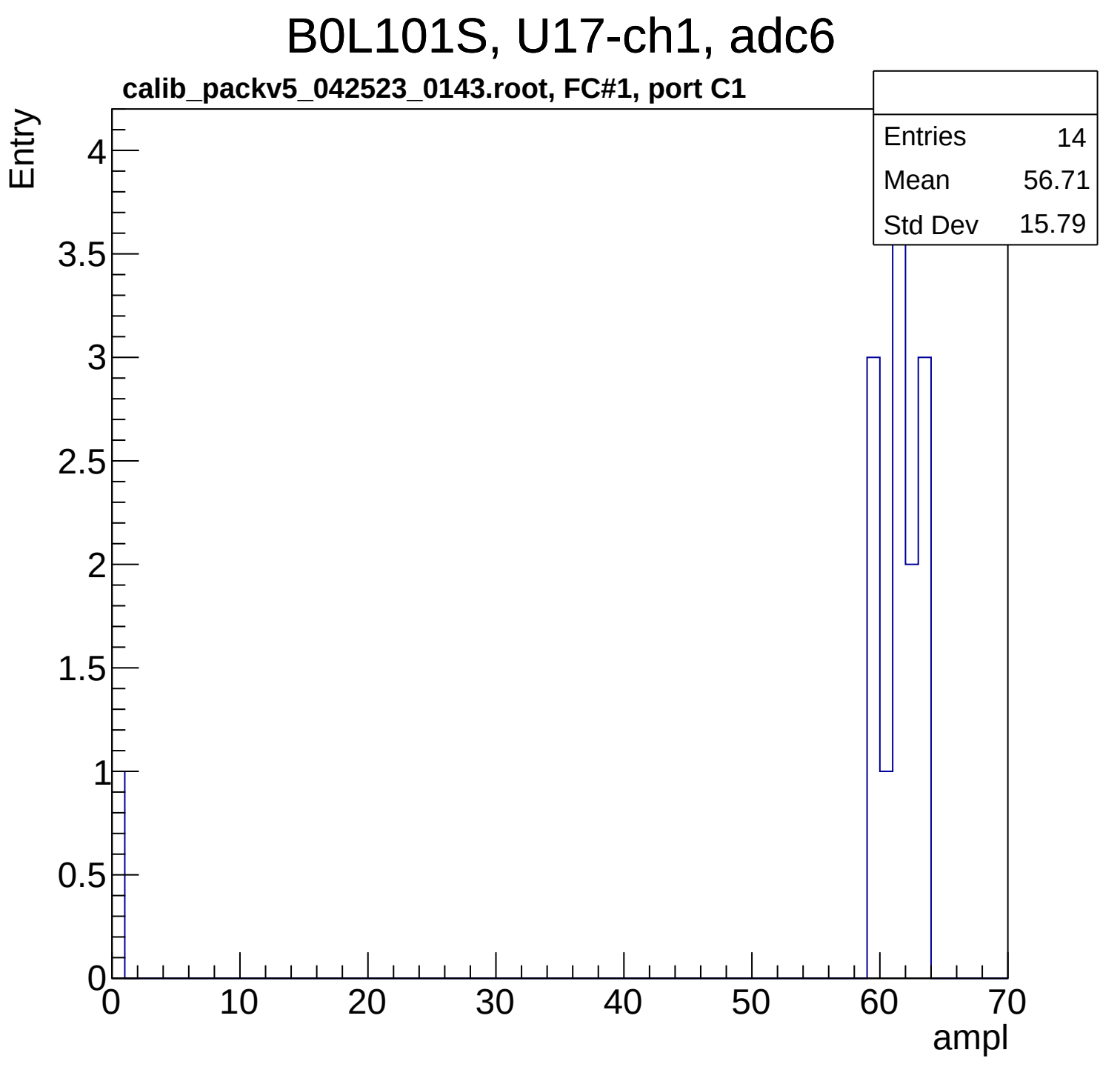

B0L101S, U17-ch1, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 37.13
Std Dev 3.627
Gaus mean : 37.1188
Gaus Width: 3.8992
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

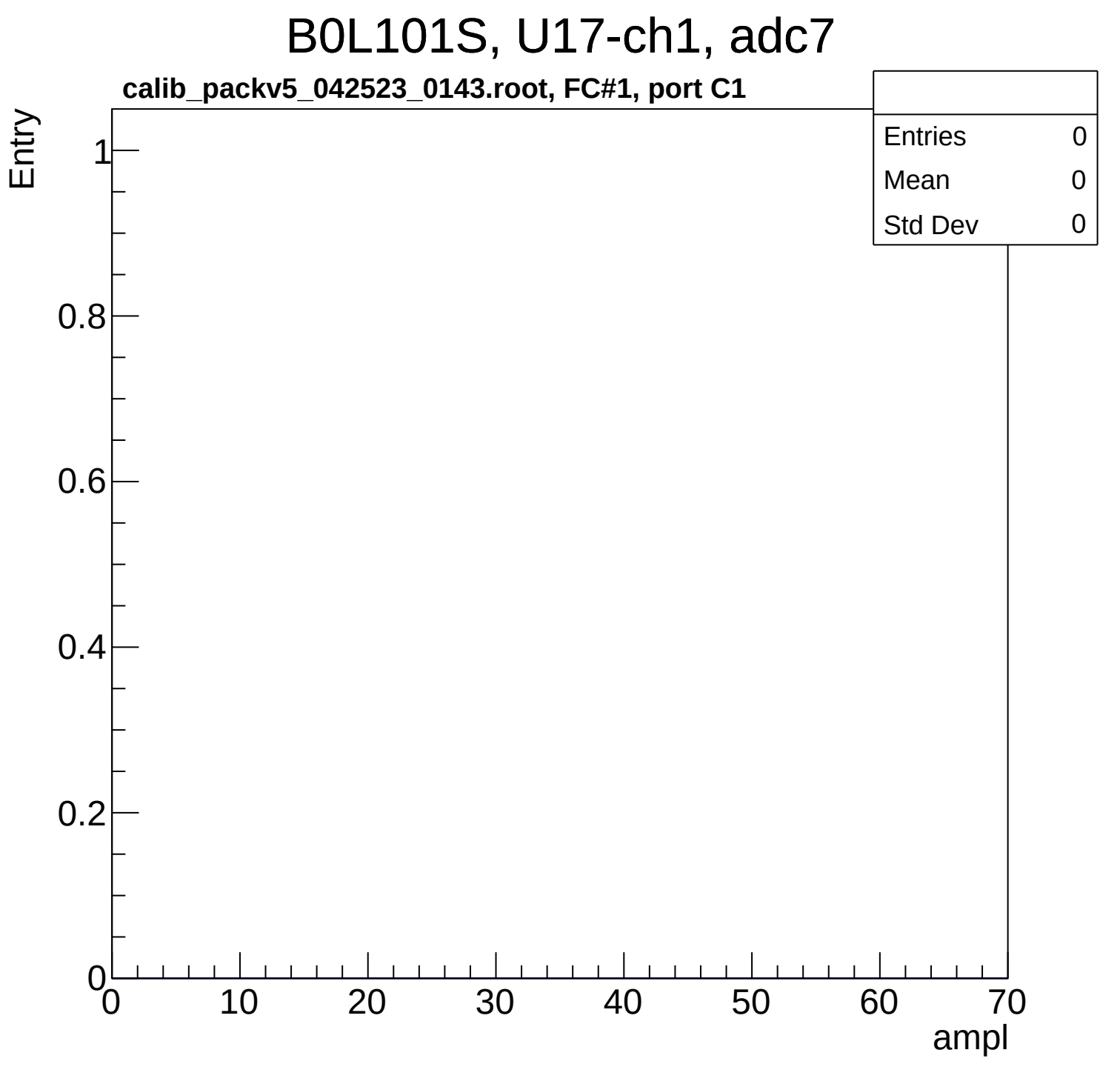

B0L101S, U17-ch1, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 43.08
Std Dev 3.815
Gaus mean : 43.6210
Gaus Width: 3.7213
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

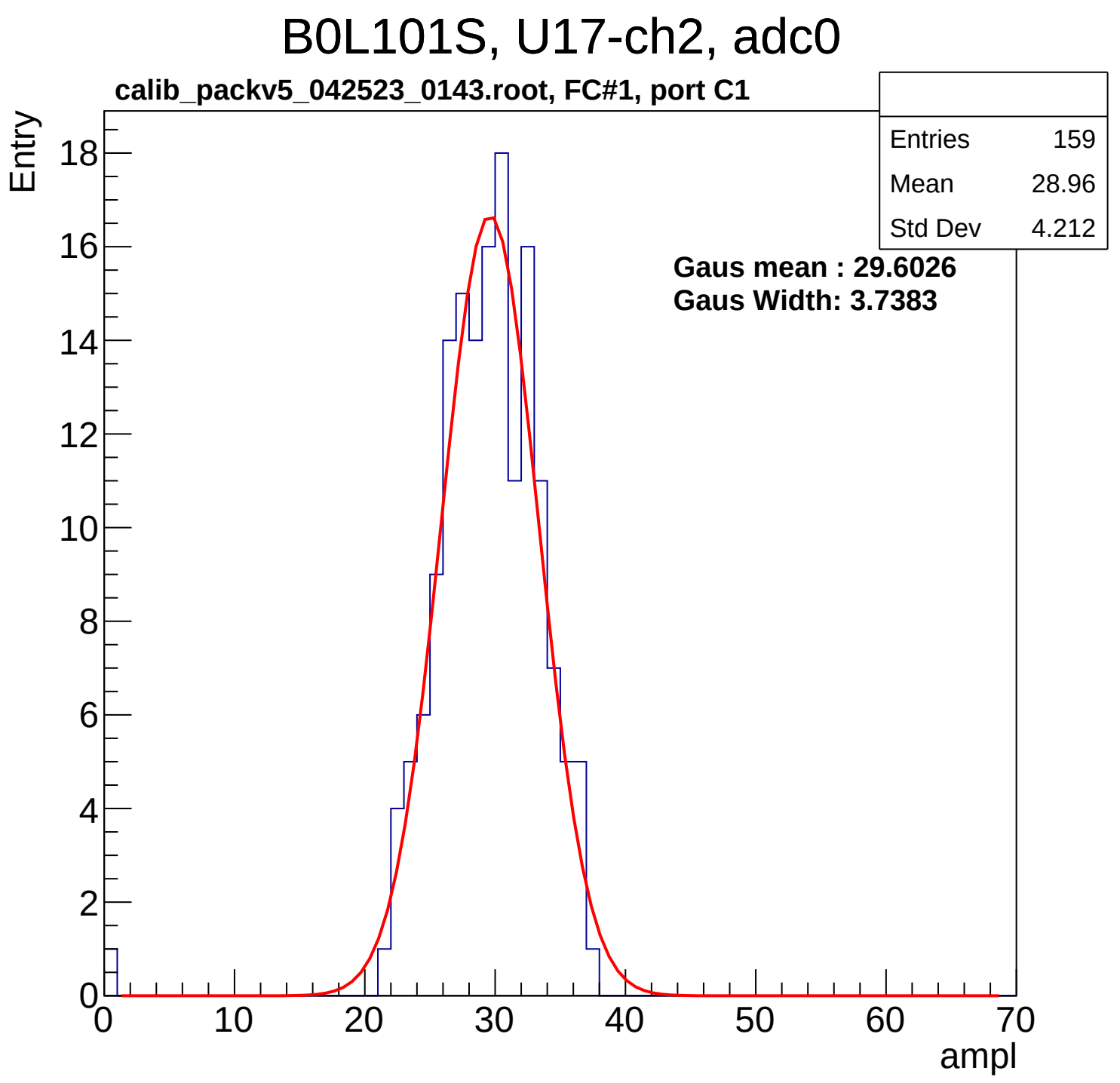

B0L101S, U17-ch1, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 49.72
Std Dev 3.794
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

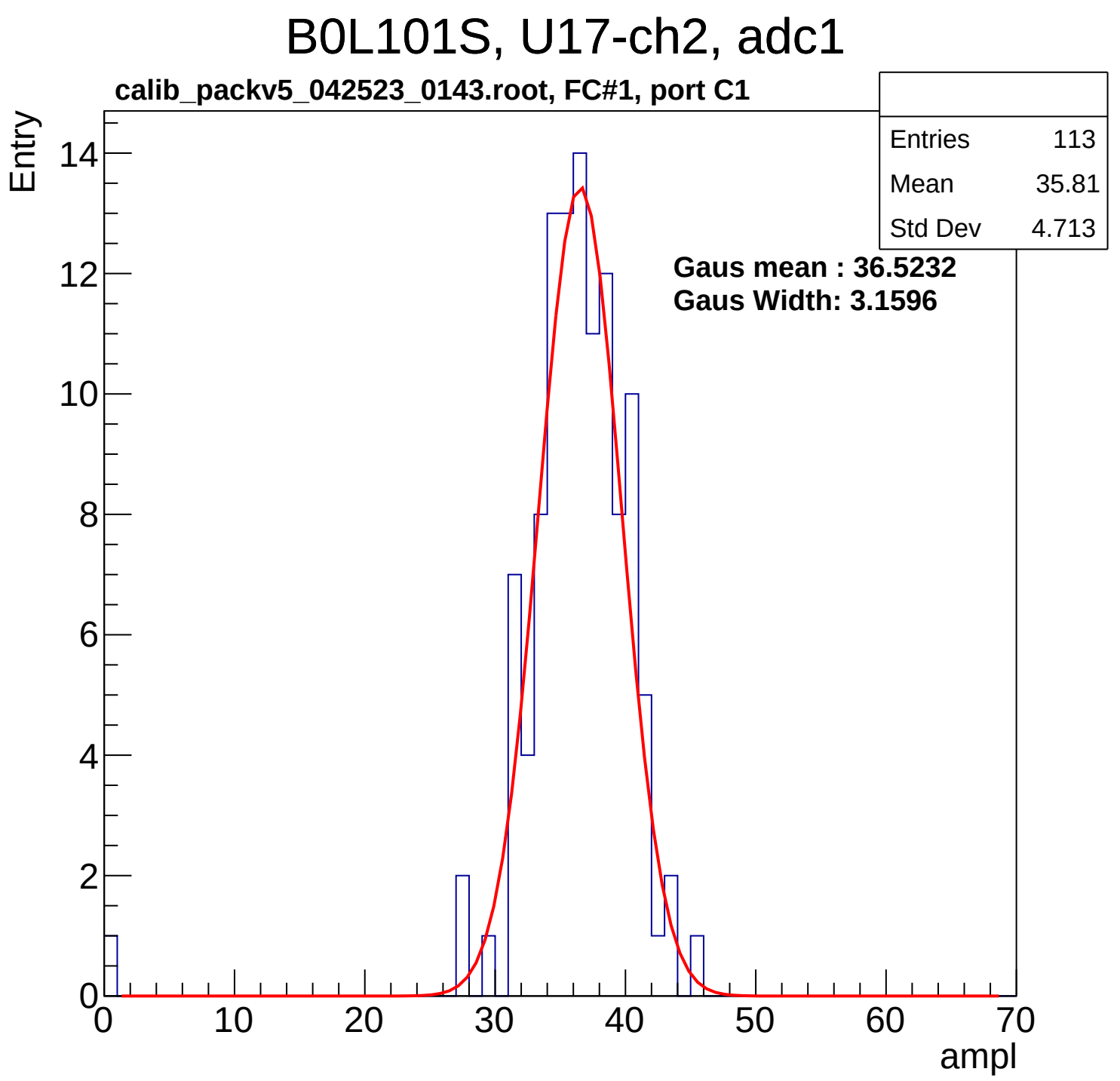

B0L101S, U17-ch1, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 55.62
Std Dev 6.346
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

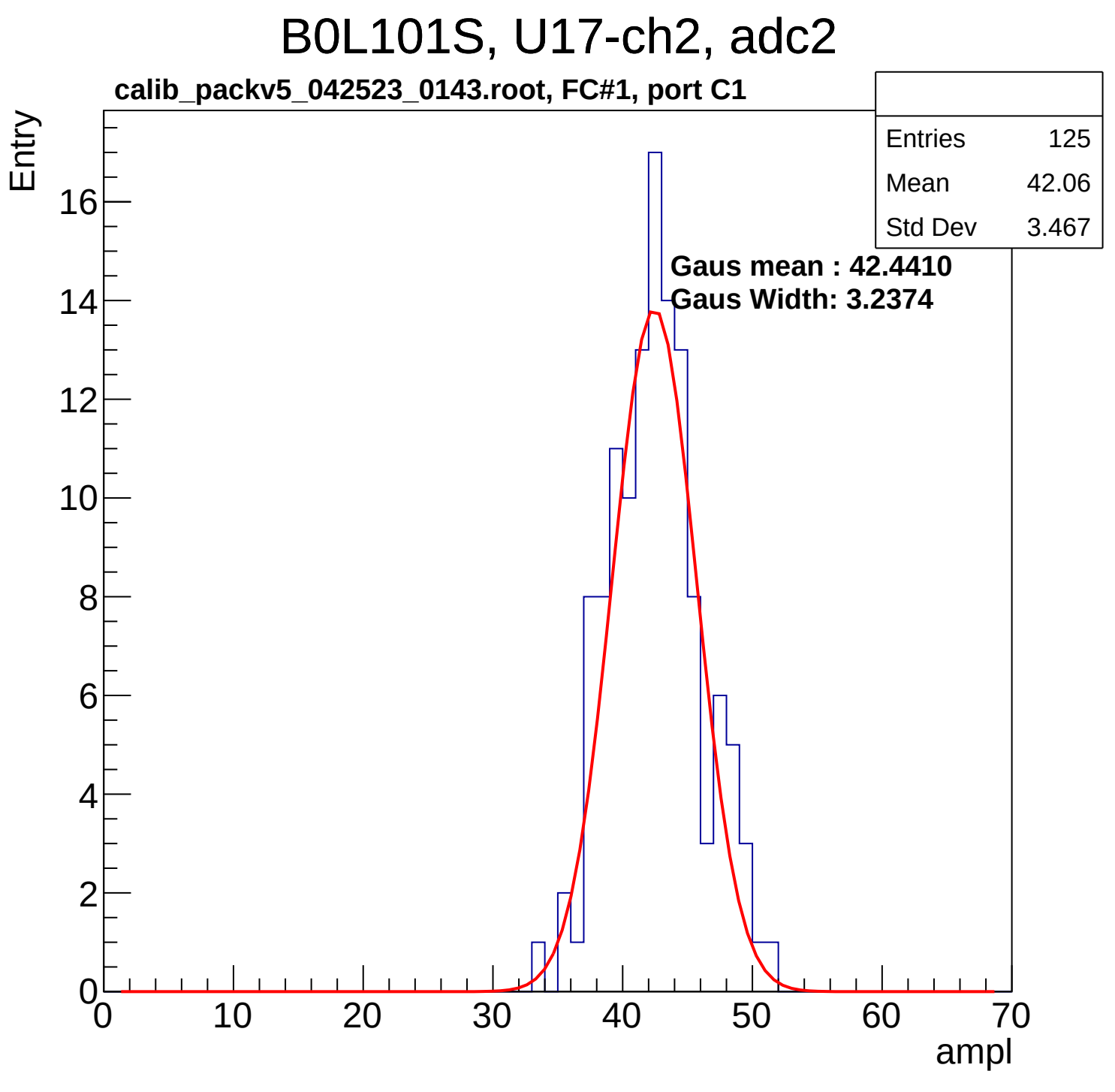

B0L101S, U17-ch1, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.75
Std Dev 2.447
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

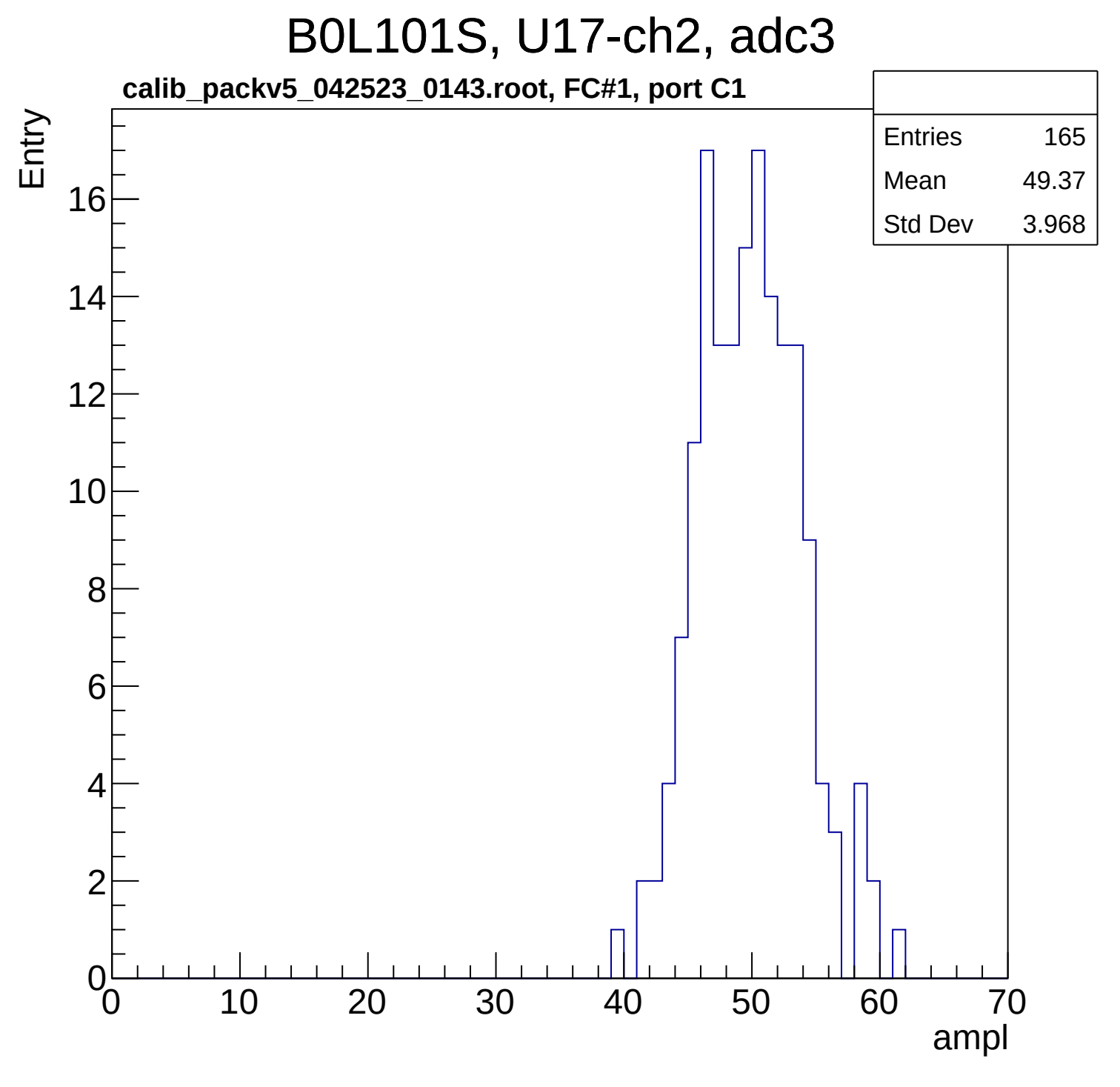

B0L101S, U17-ch1, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 56.71
Std Dev 15.79
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

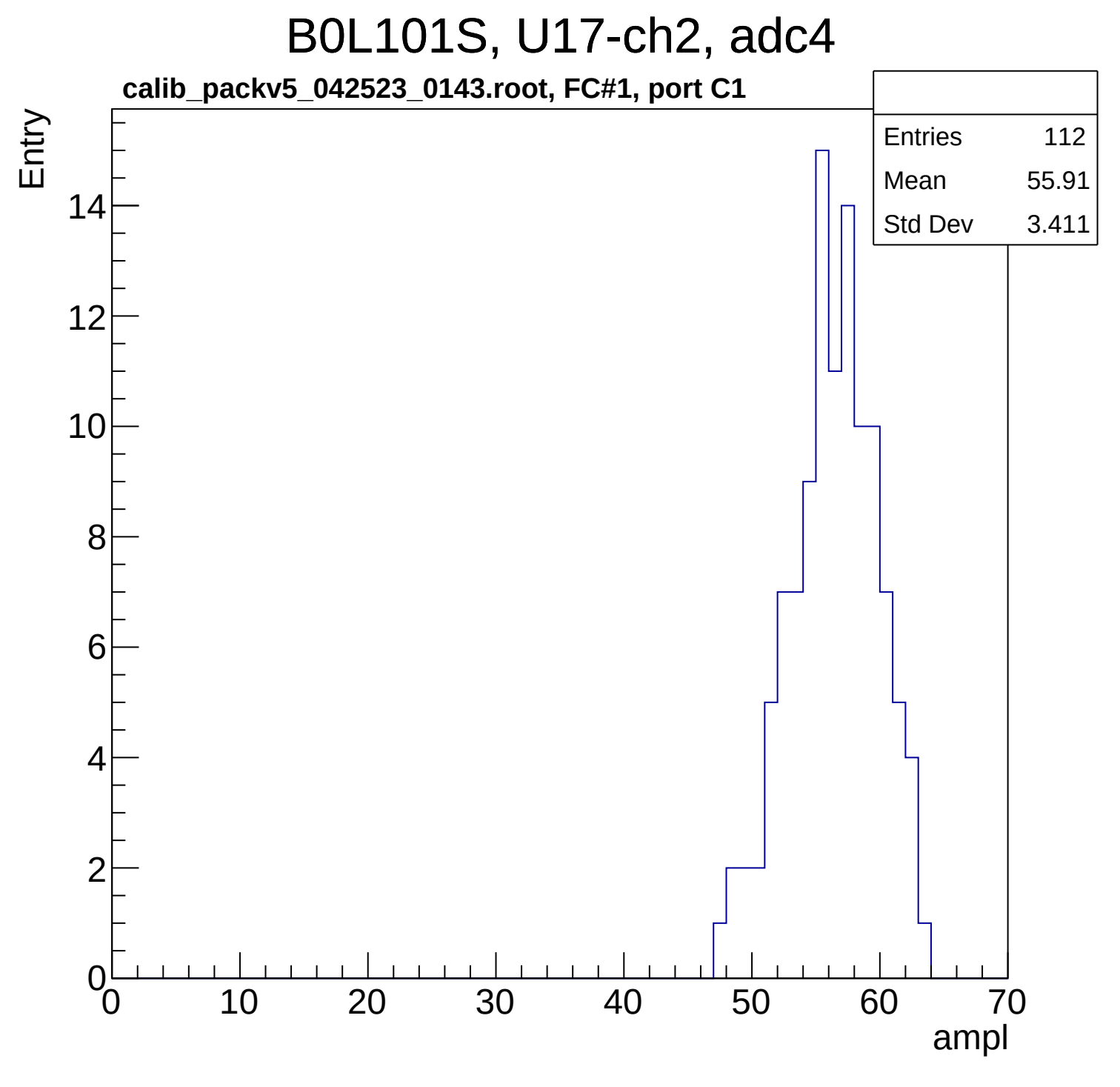

B0L101S, U17-ch1, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

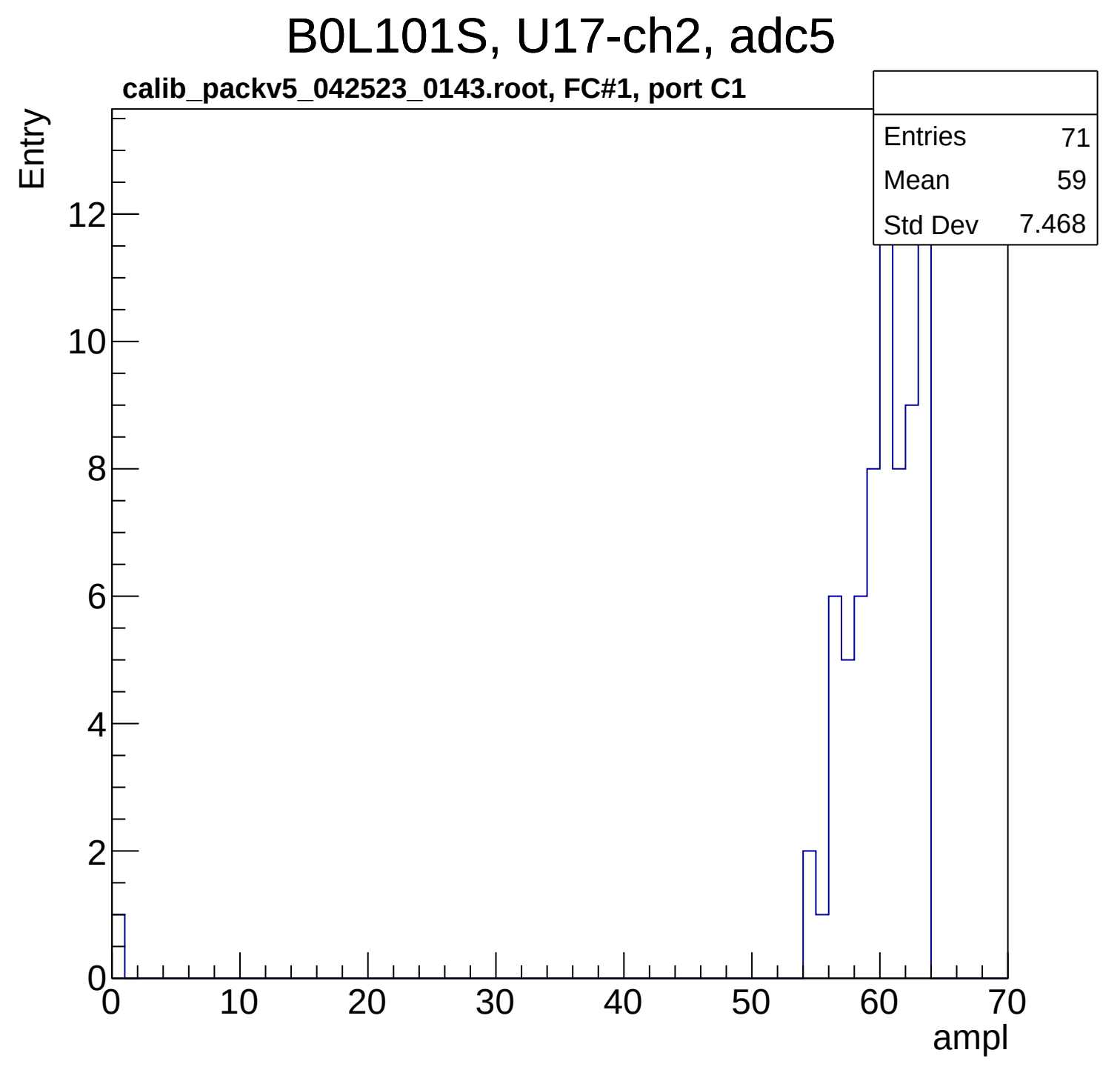

B0L101S, U17-ch2, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 28.96
Std Dev 4.212
Gaus mean : 29.6026
Gaus Width: 3.7383
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

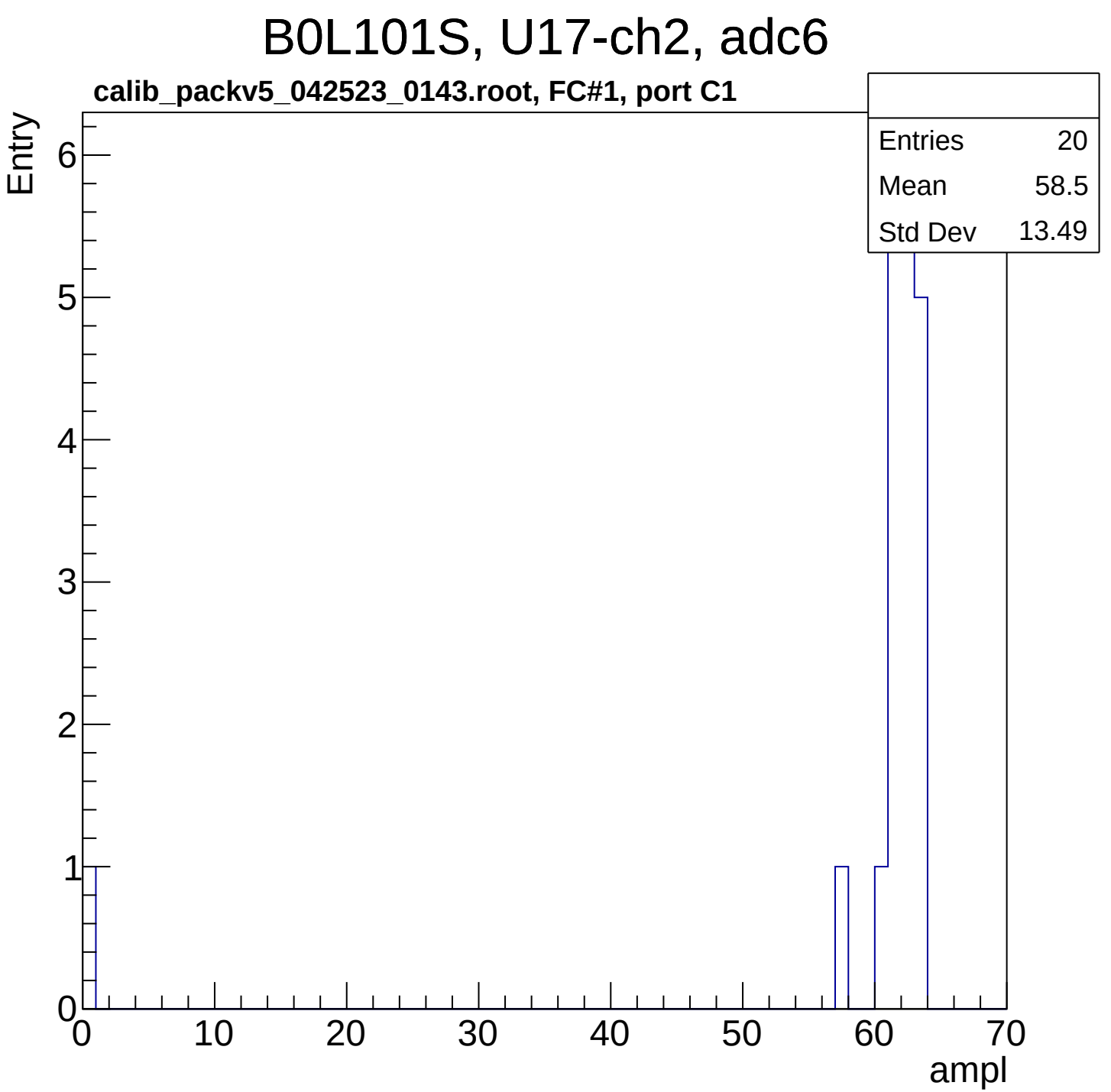

B0L101S, U17-ch2, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 35.81
Std Dev 4.713
Gaus mean : 36.5232
Gaus Width: 3.1596
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

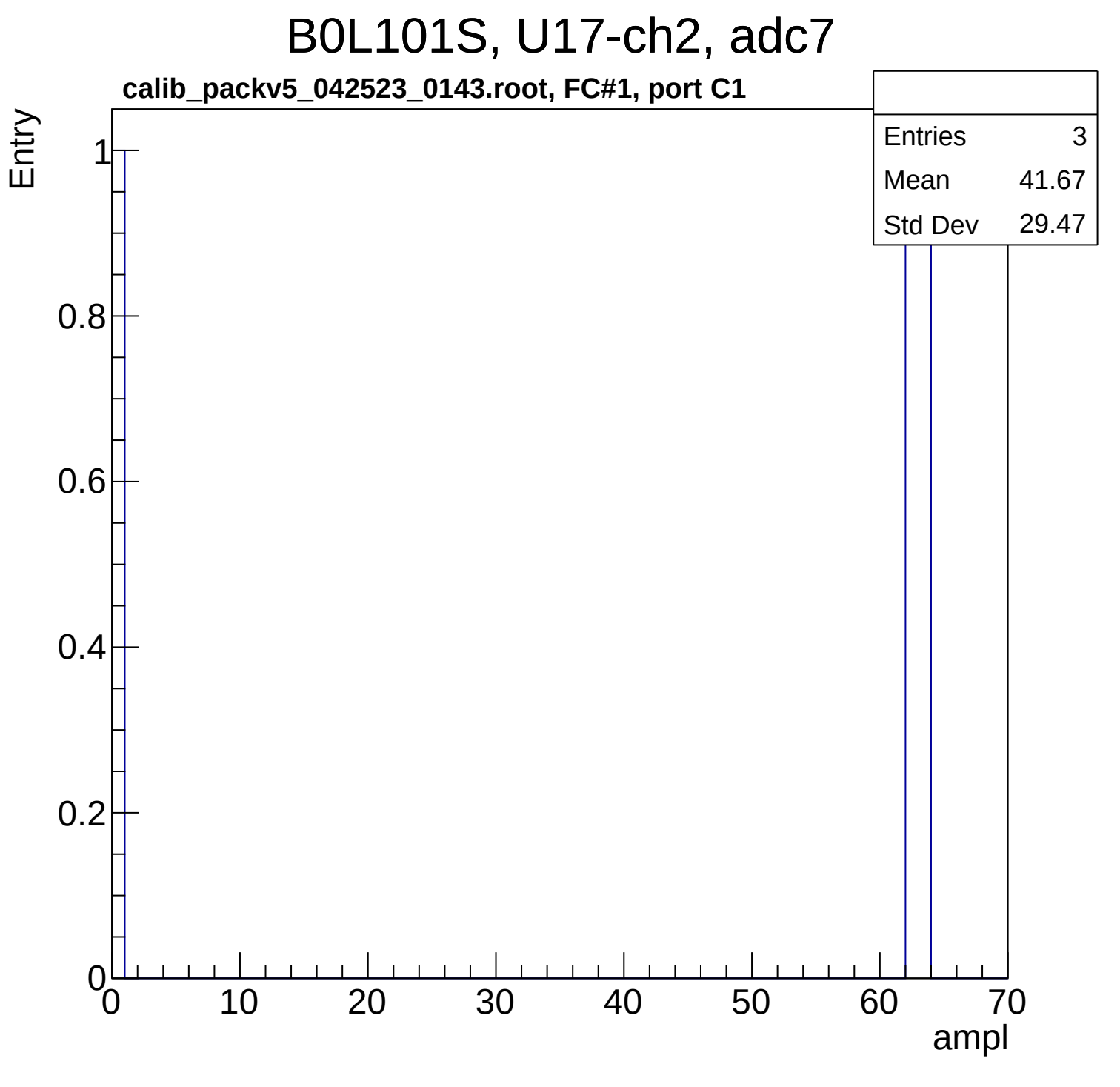

B0L101S, U17-ch2, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 42.06
Std Dev 3.467
Gaus mean : 42.4410
Gaus Width: 3.2374
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

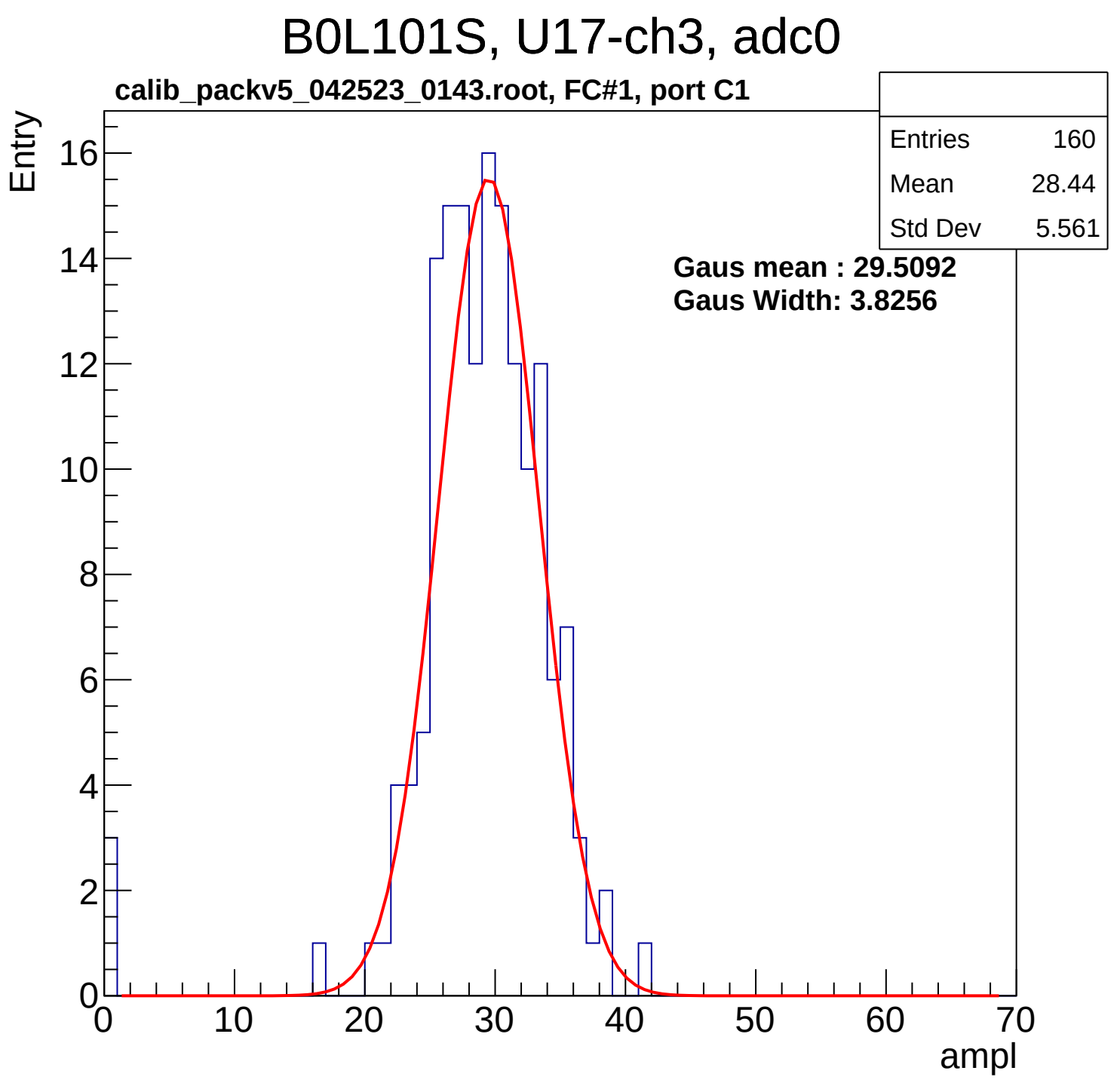

B0L101S, U17-ch2, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 165
Mean 49.37
Std Dev 3.968
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

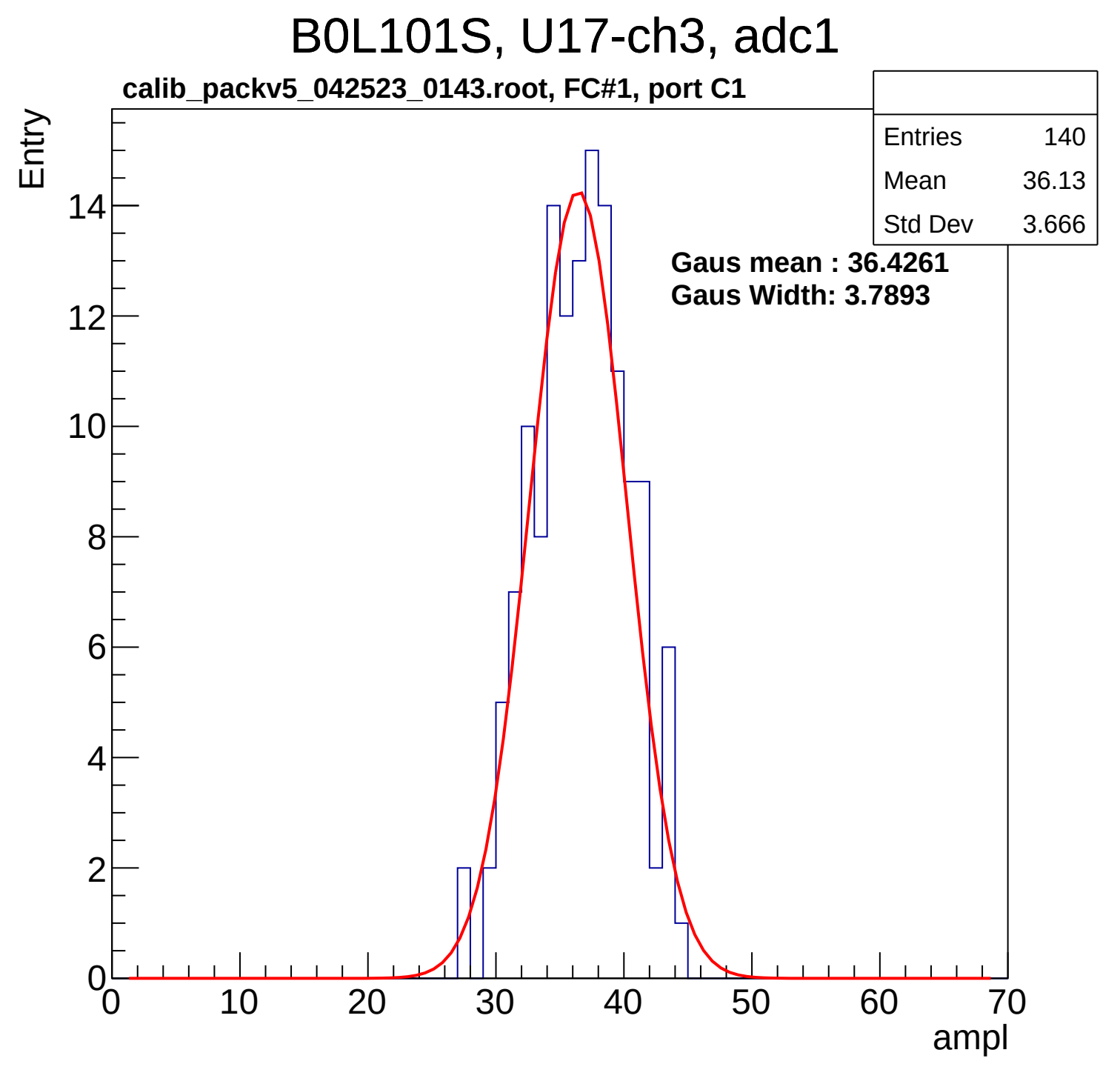

B0L101S, U17-ch2, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 55.91
Std Dev 3.411
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

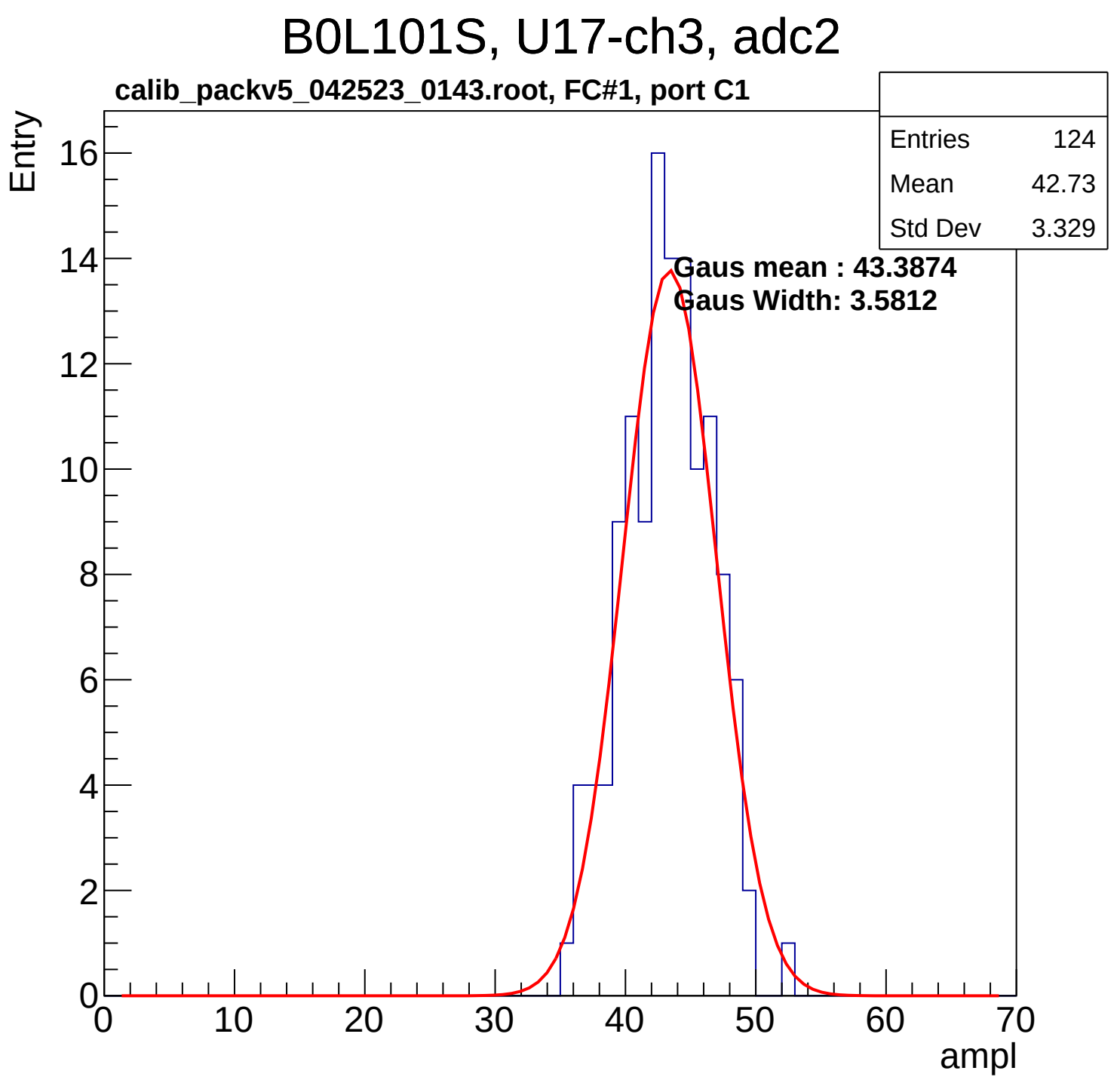

B0L101S, U17-ch2, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 71
Mean 59
Std Dev 7.468
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

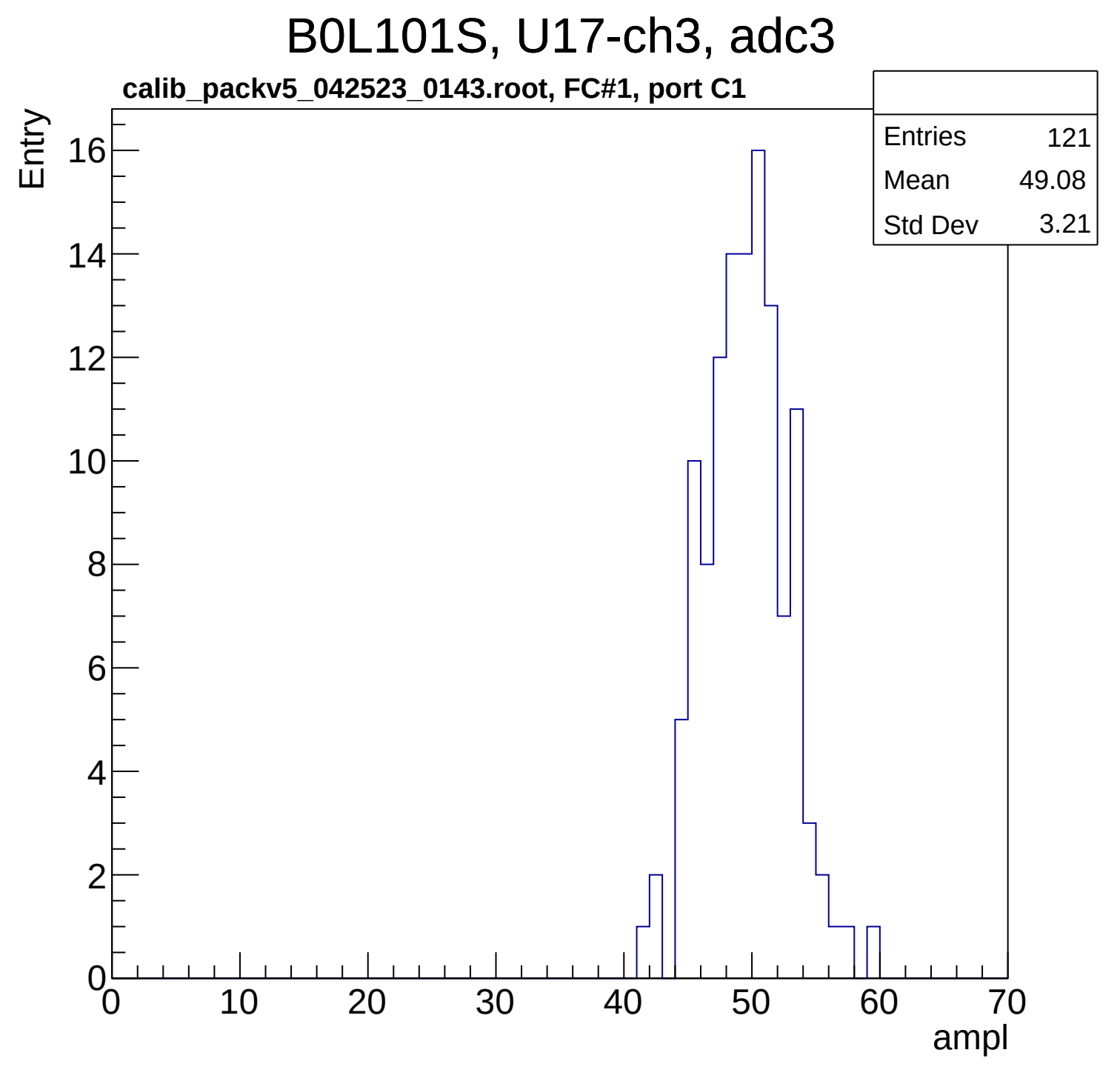

B0L101S, U17-ch2, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 20
Mean 58.5
Std Dev 13.49
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

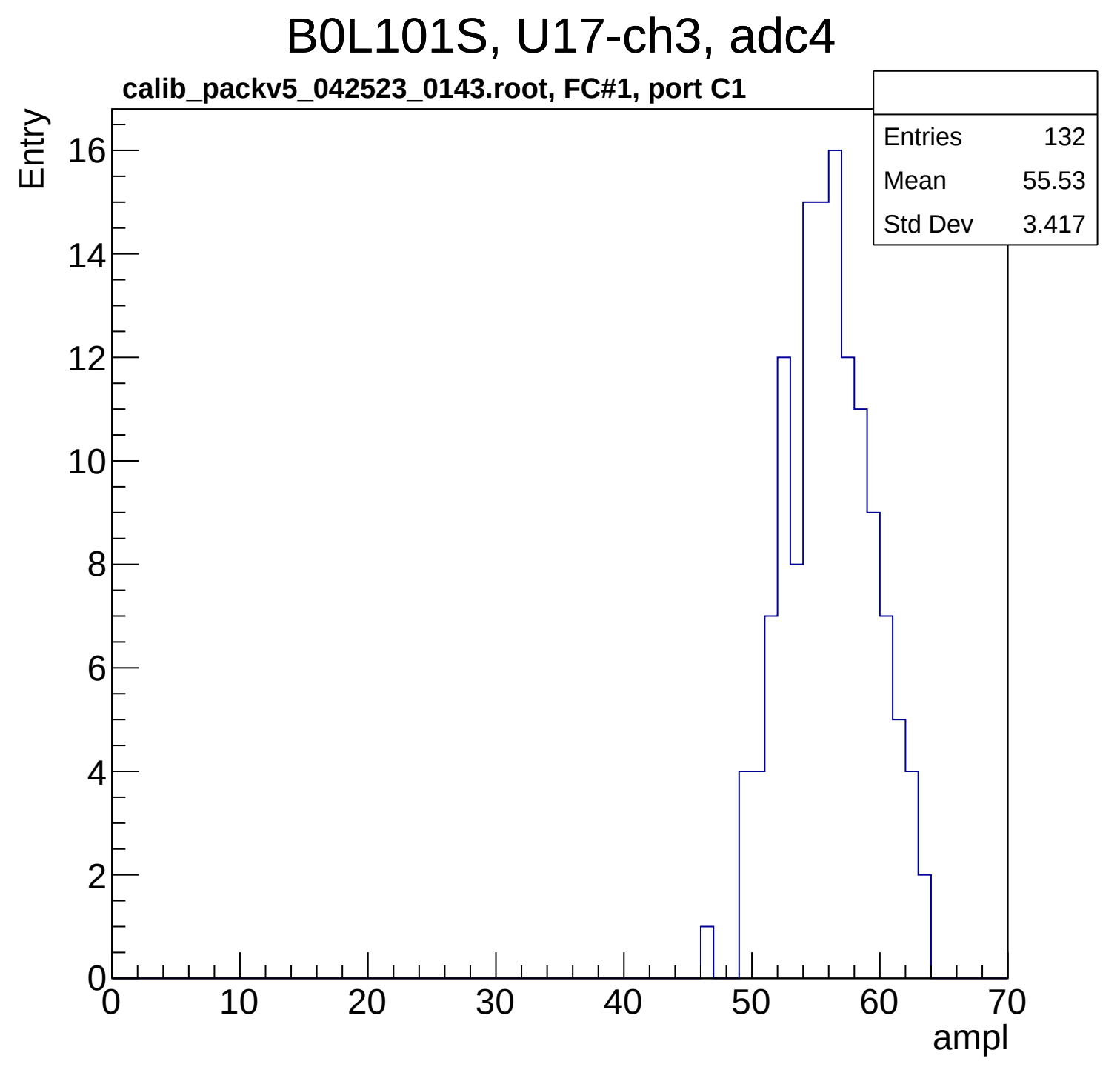

B0L101S, U17-ch2, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

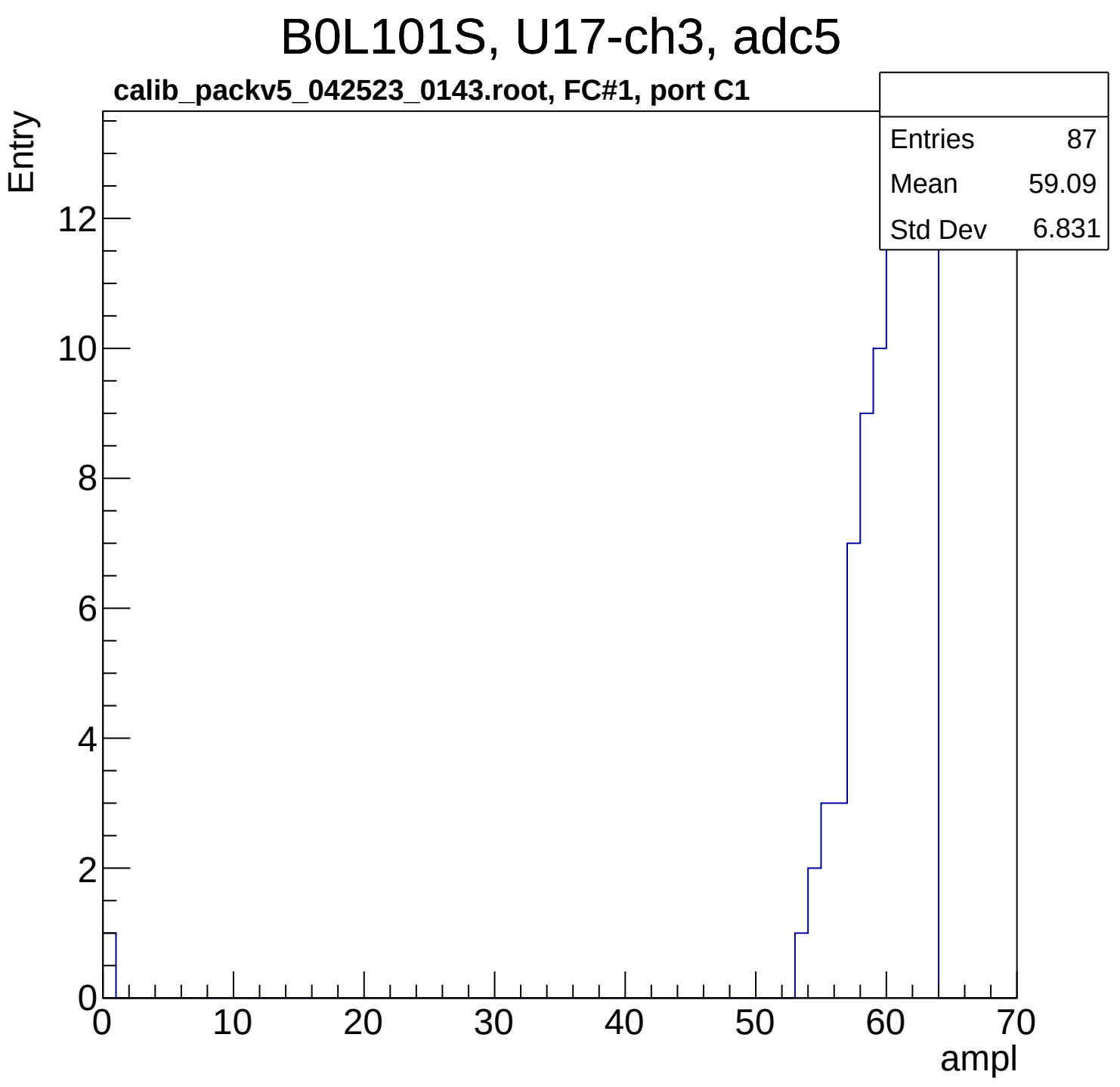

B0L101S, U17-ch3, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 160
Mean 28.44
Std Dev 5.561
Gaus mean : 29.5092
Gaus Width: 3.8256
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

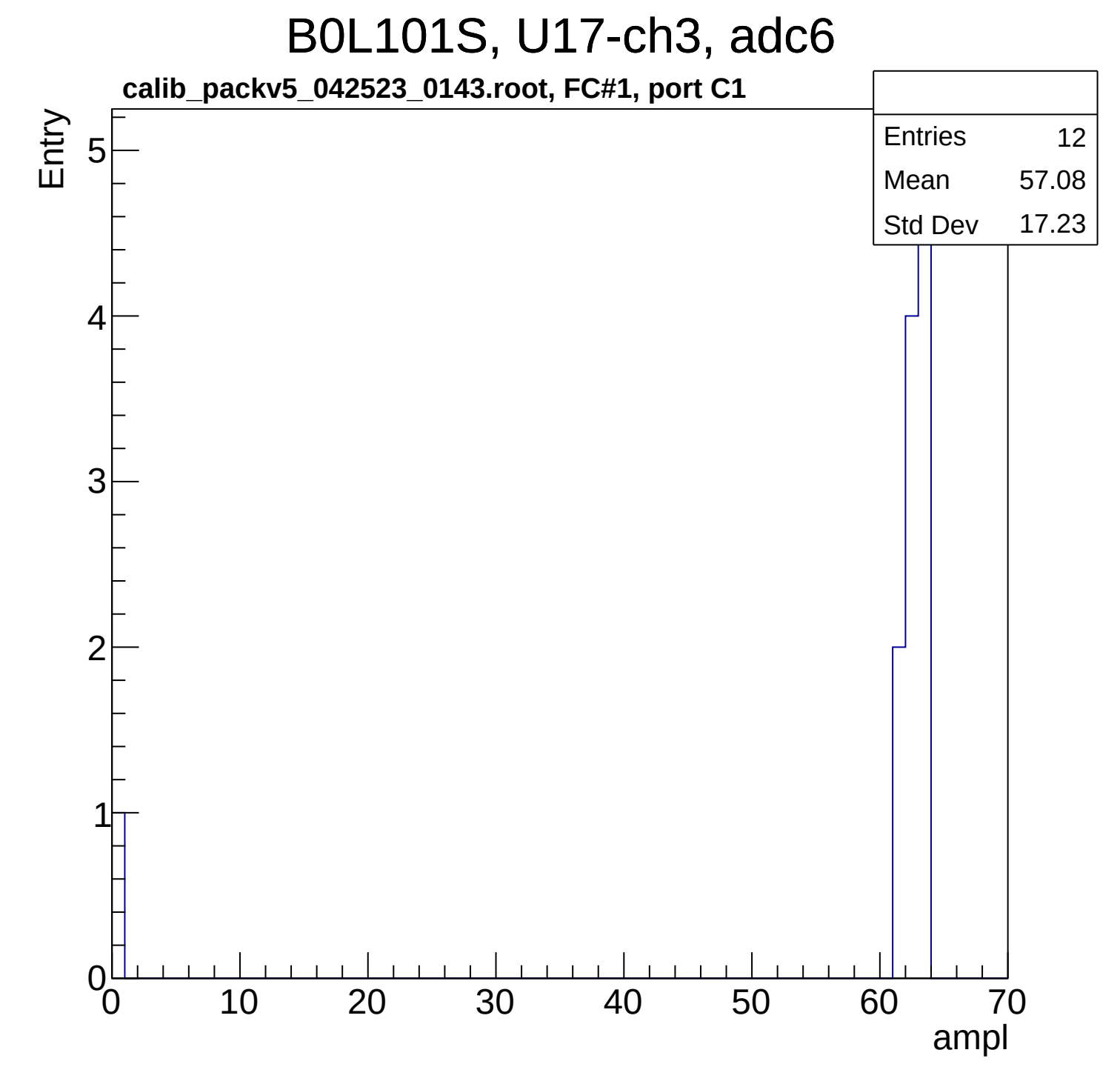

B0L101S, U17-ch3, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 36.13
Std Dev 3.666
Gaus mean : 36.4261
Gaus Width: 3.7893
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

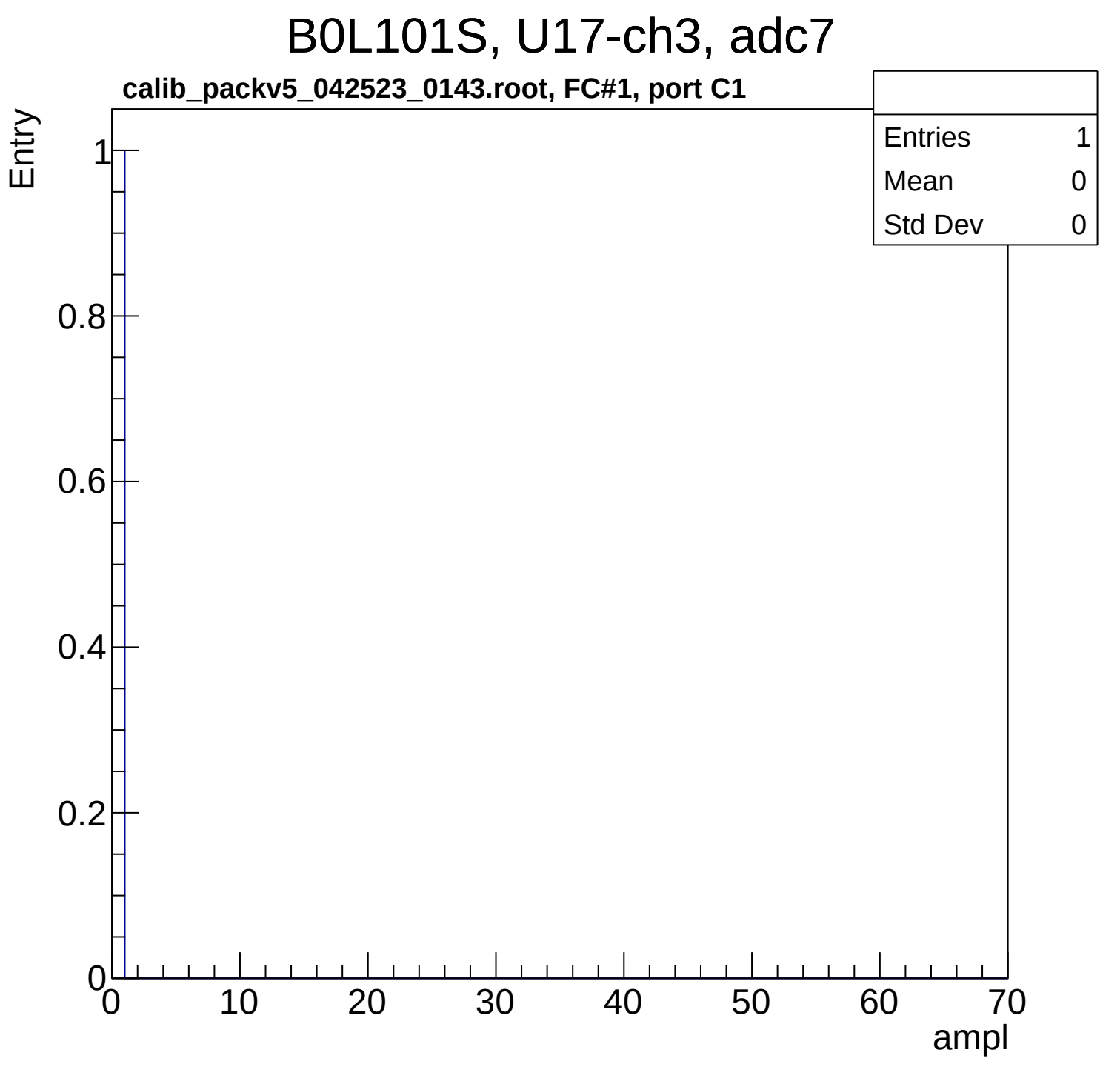

B0L101S, U17-ch3, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 42.73
Std Dev 3.329
Gaus mean : 43.3874
Gaus Width: 3.5812
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

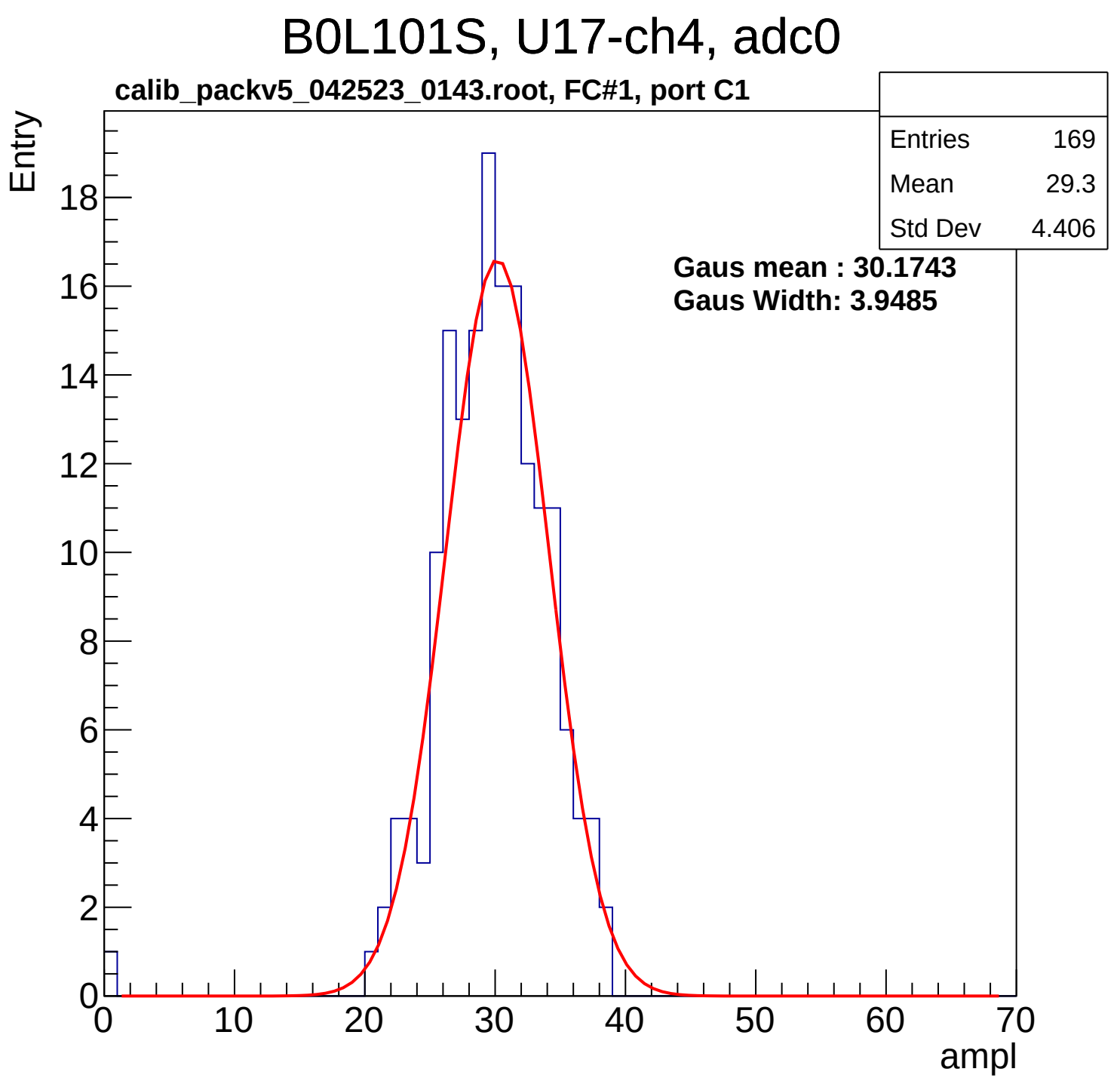

B0L101S, U17-ch3, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 49.08
Std Dev 3.21
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

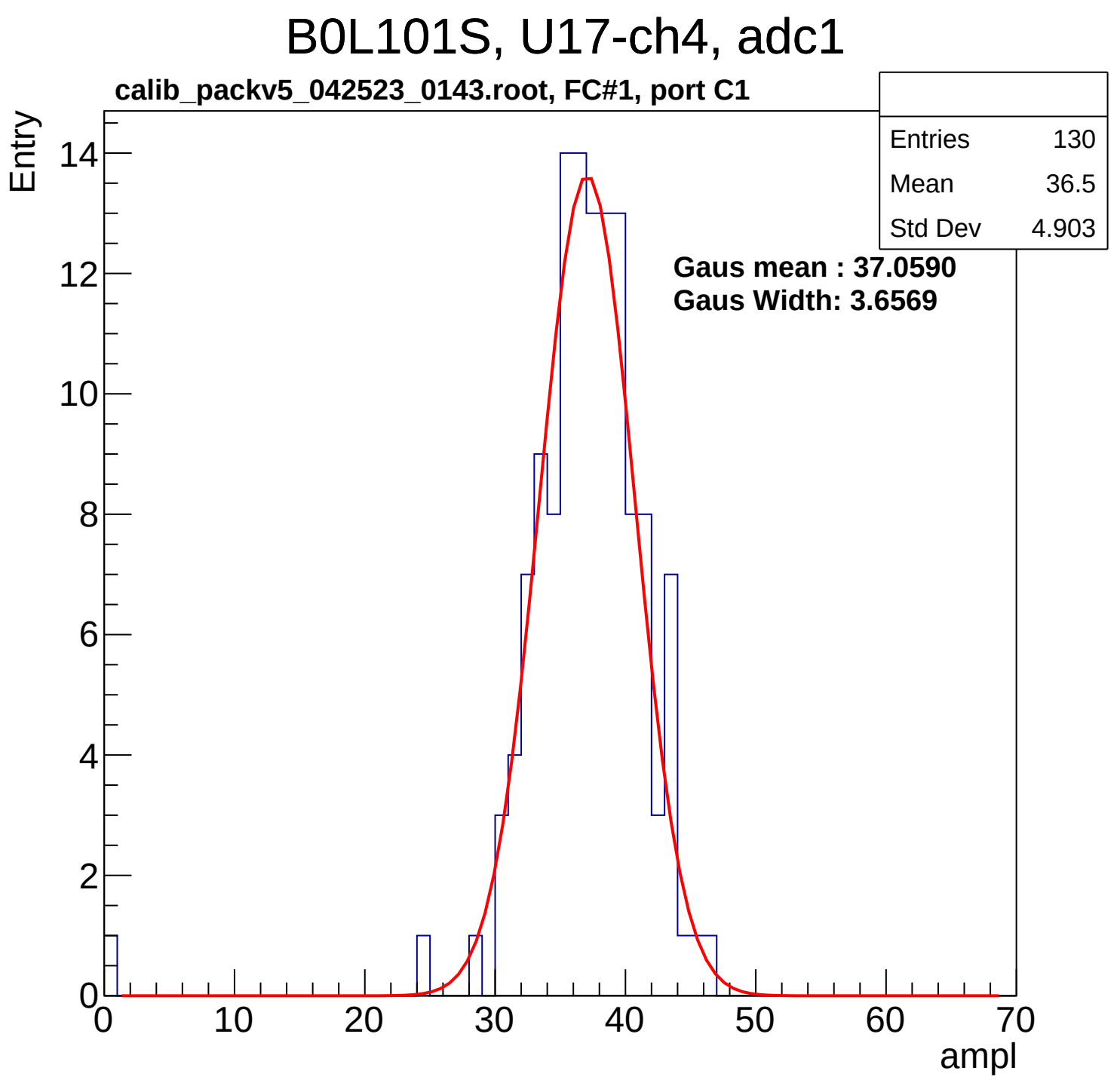

B0L101S, U17-ch3, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 55.53
Std Dev 3.417
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

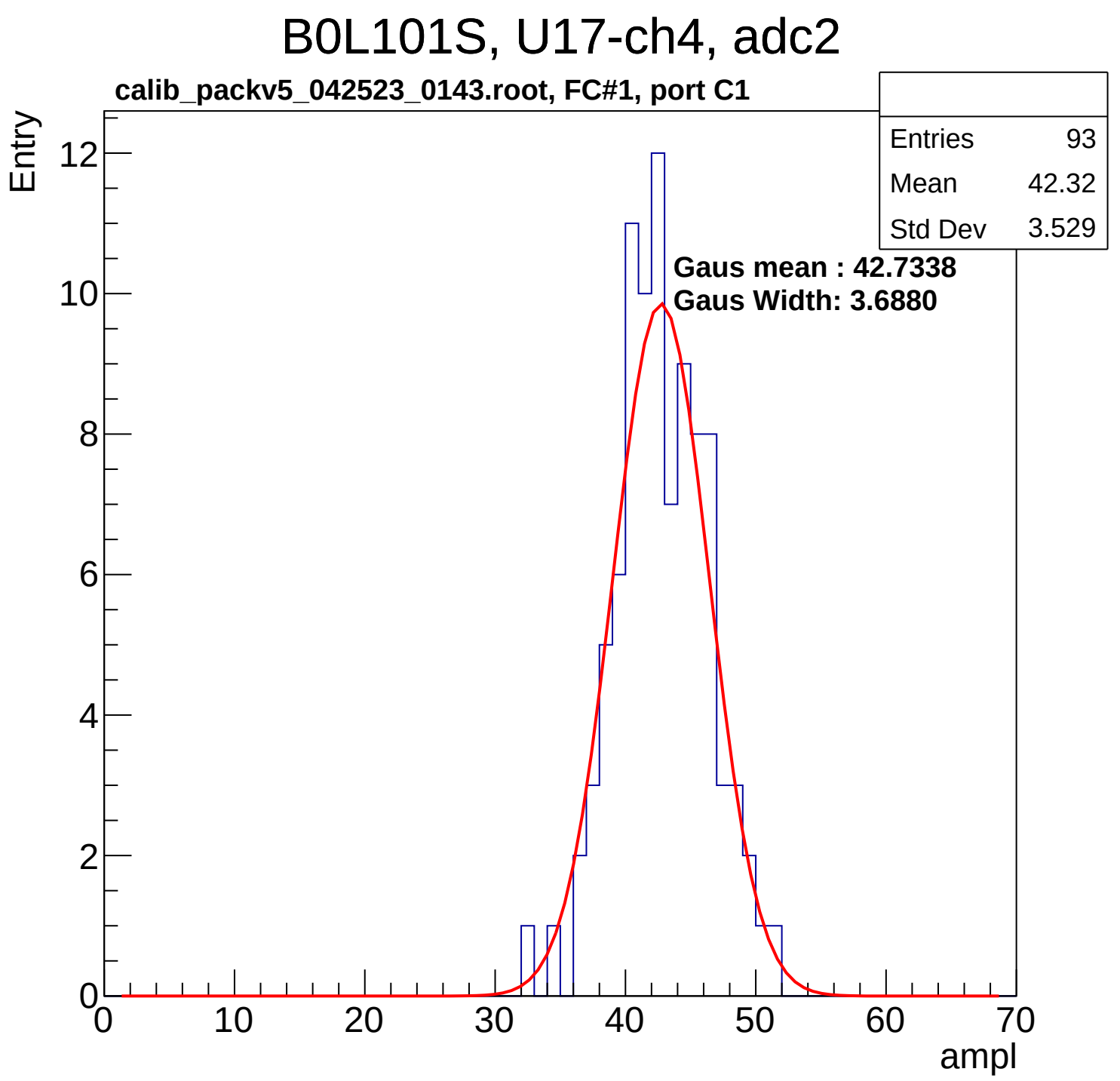

B0L101S, U17-ch3, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 59.09
Std Dev 6.831
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

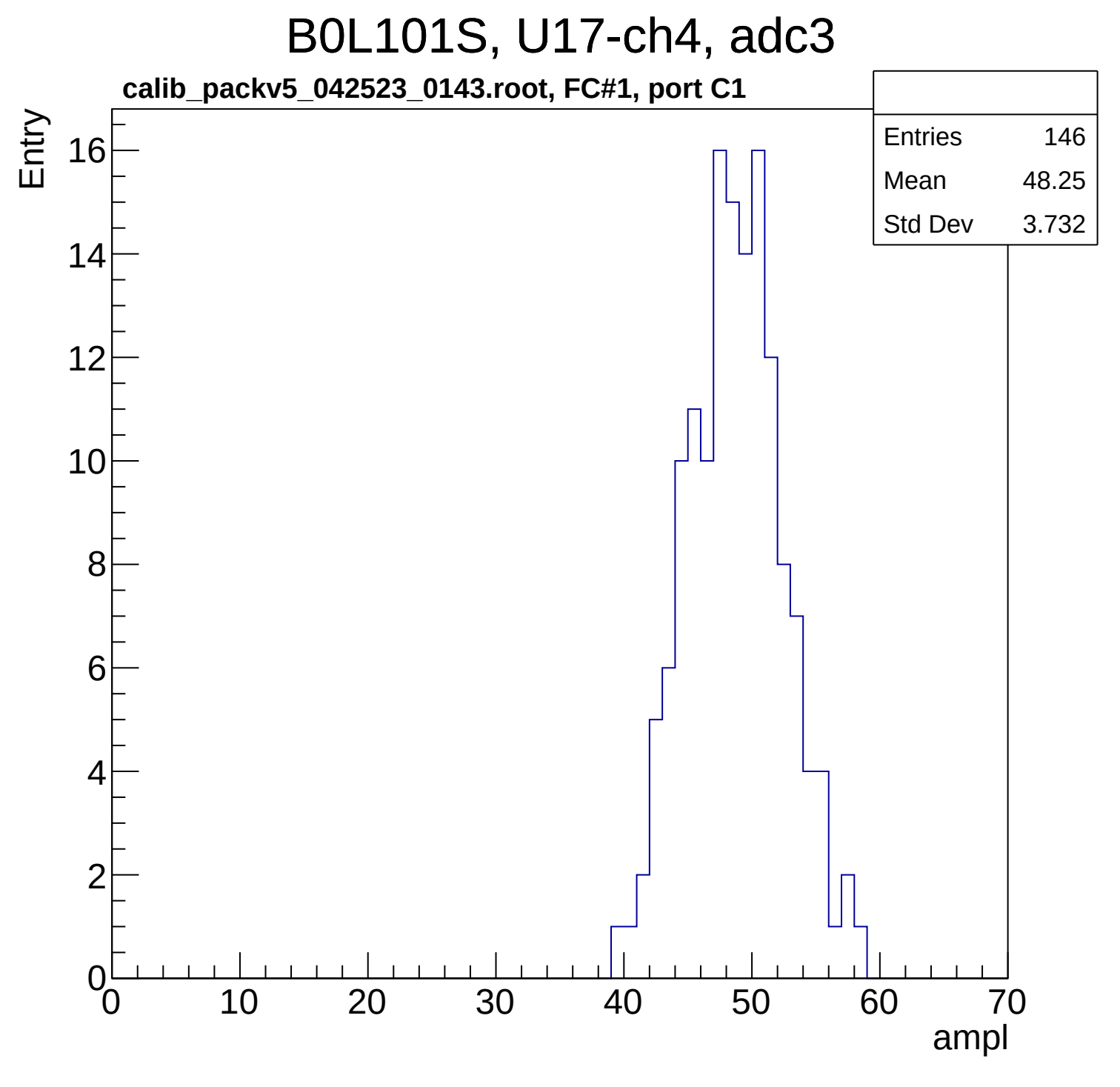

B0L101S, U17-ch3, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 57.08
Std Dev 17.23
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

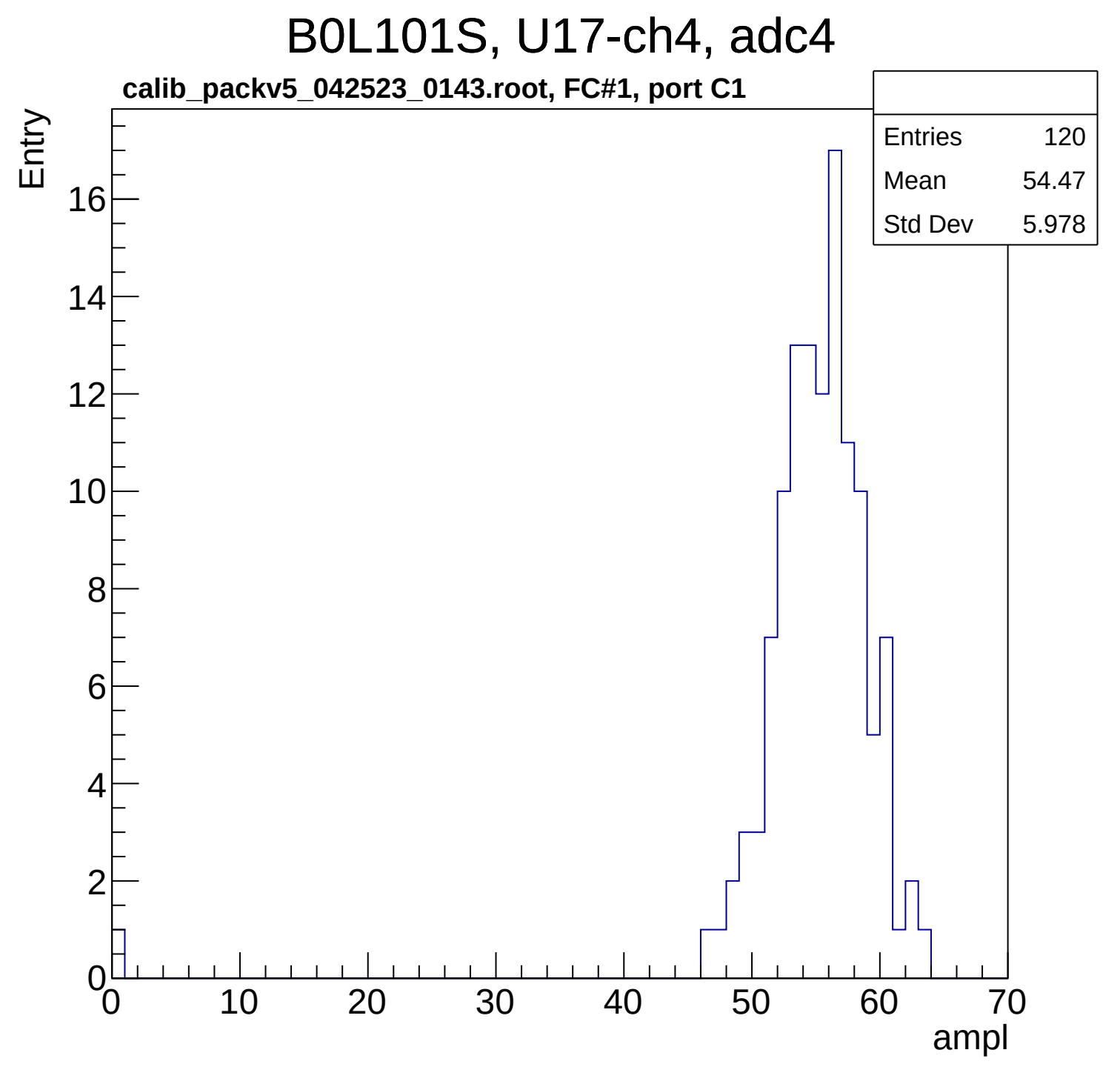

B0L101S, U17-ch3, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

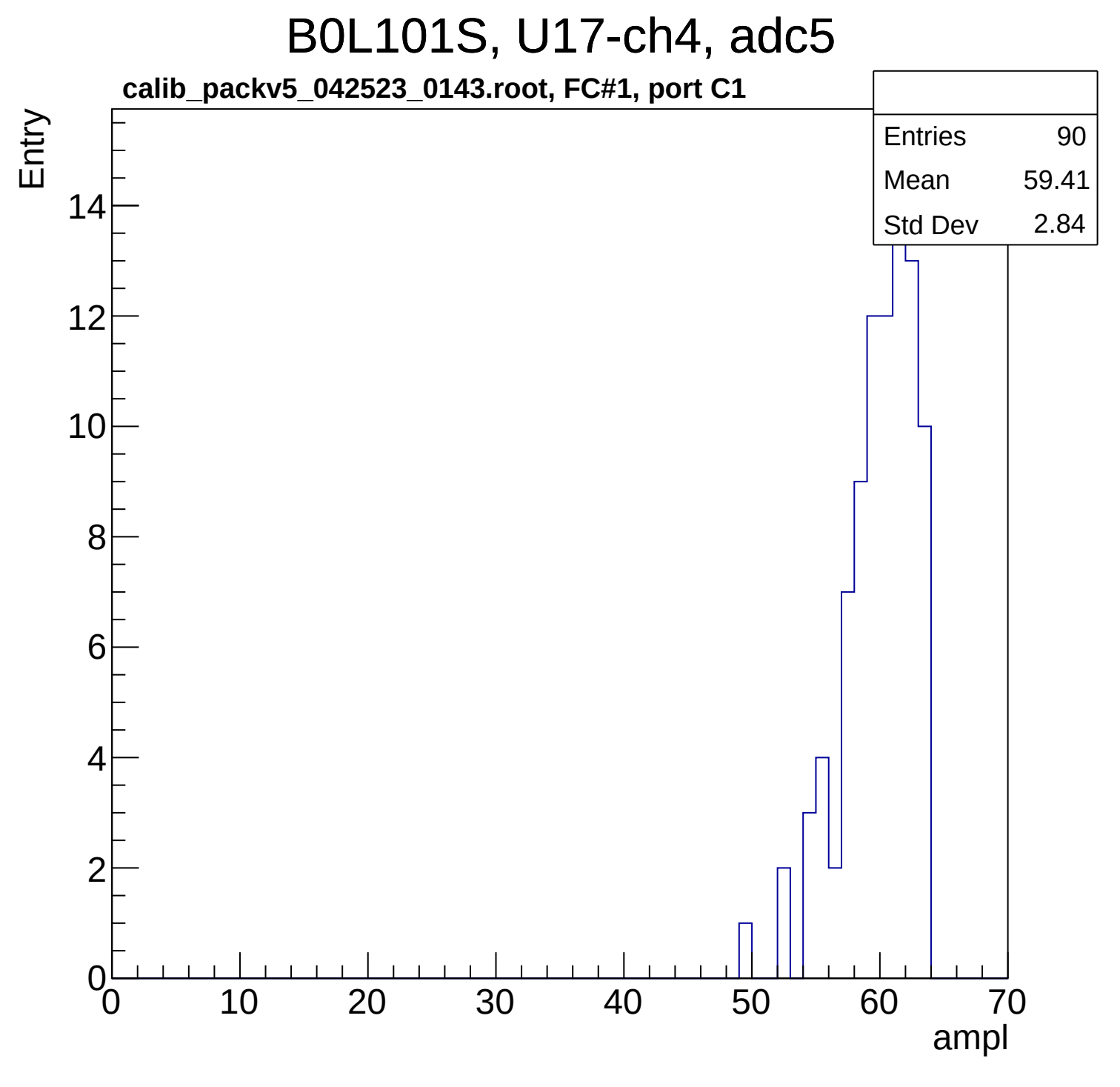

B0L101S, U17-ch4, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 169
Mean 29.3
Std Dev 4.406
Gaus mean : 30.1743
Gaus Width: 3.9485
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

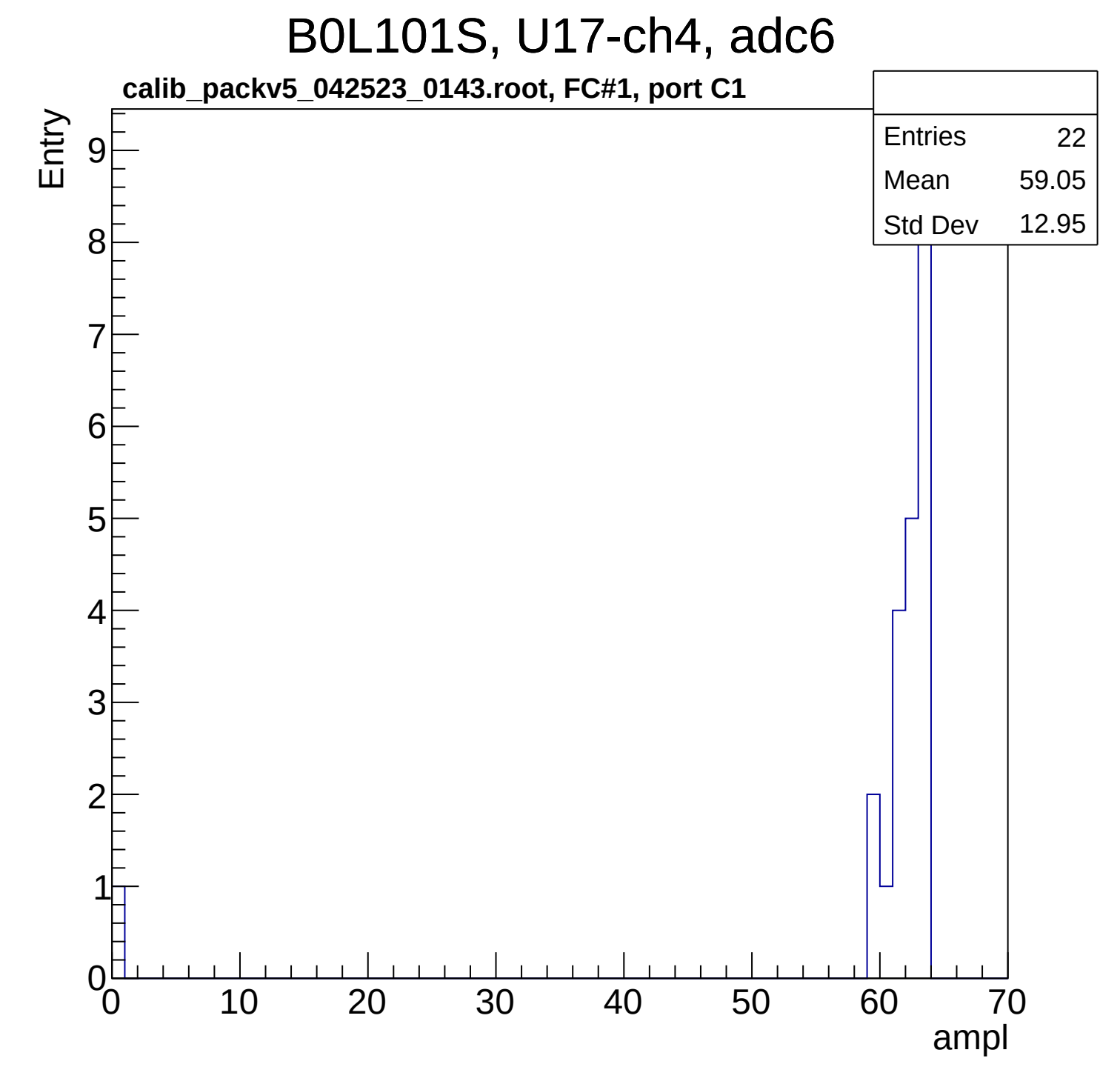

B0L101S, U17-ch4, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 36.5
Std Dev 4.903
Gaus mean : 37.0590
Gaus Width: 3.6569
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

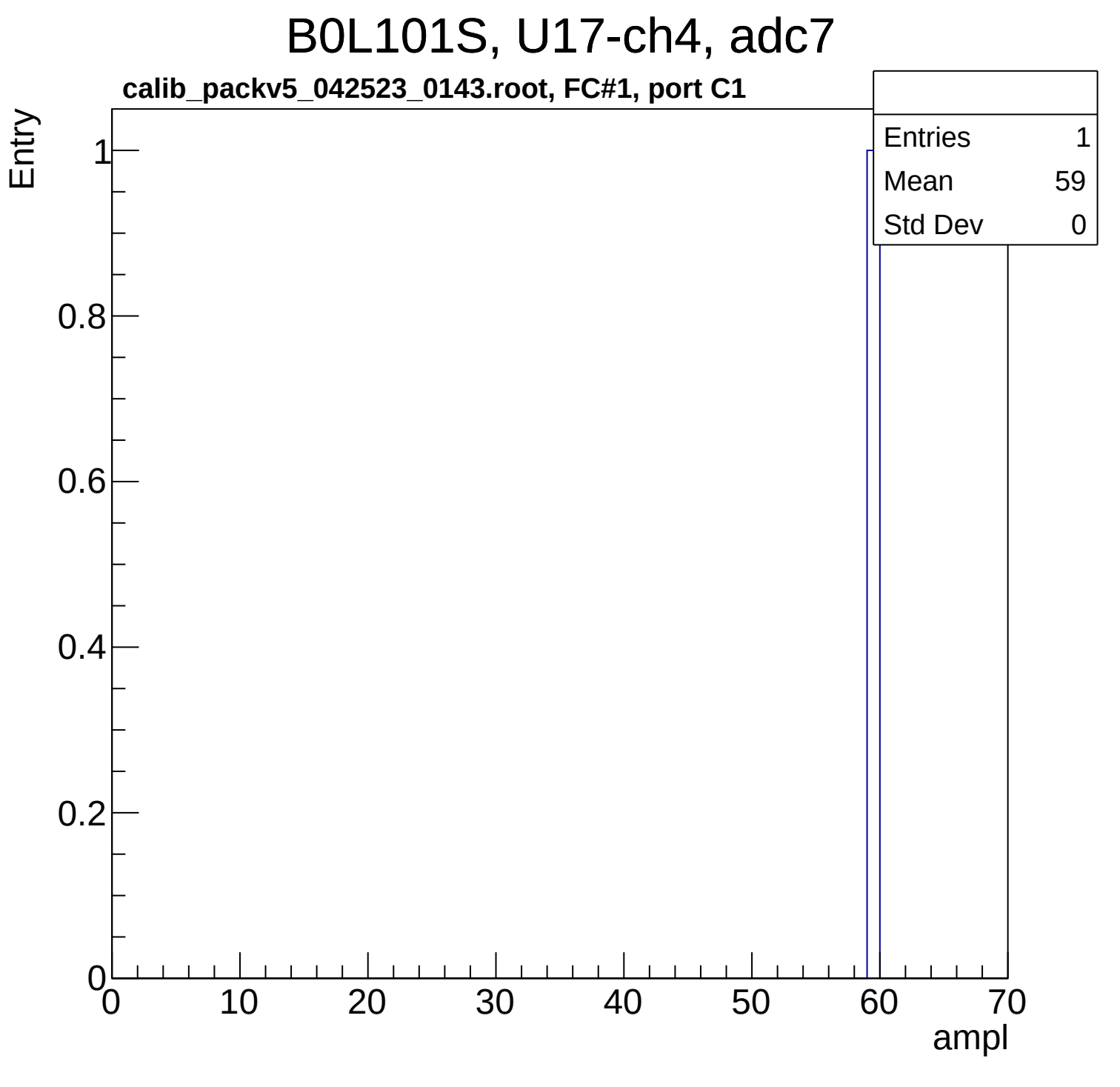

B0L101S, U17-ch4, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 93
Mean 42.32
Std Dev 3.529
Gaus mean : 42.7338
Gaus Width: 3.6880
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

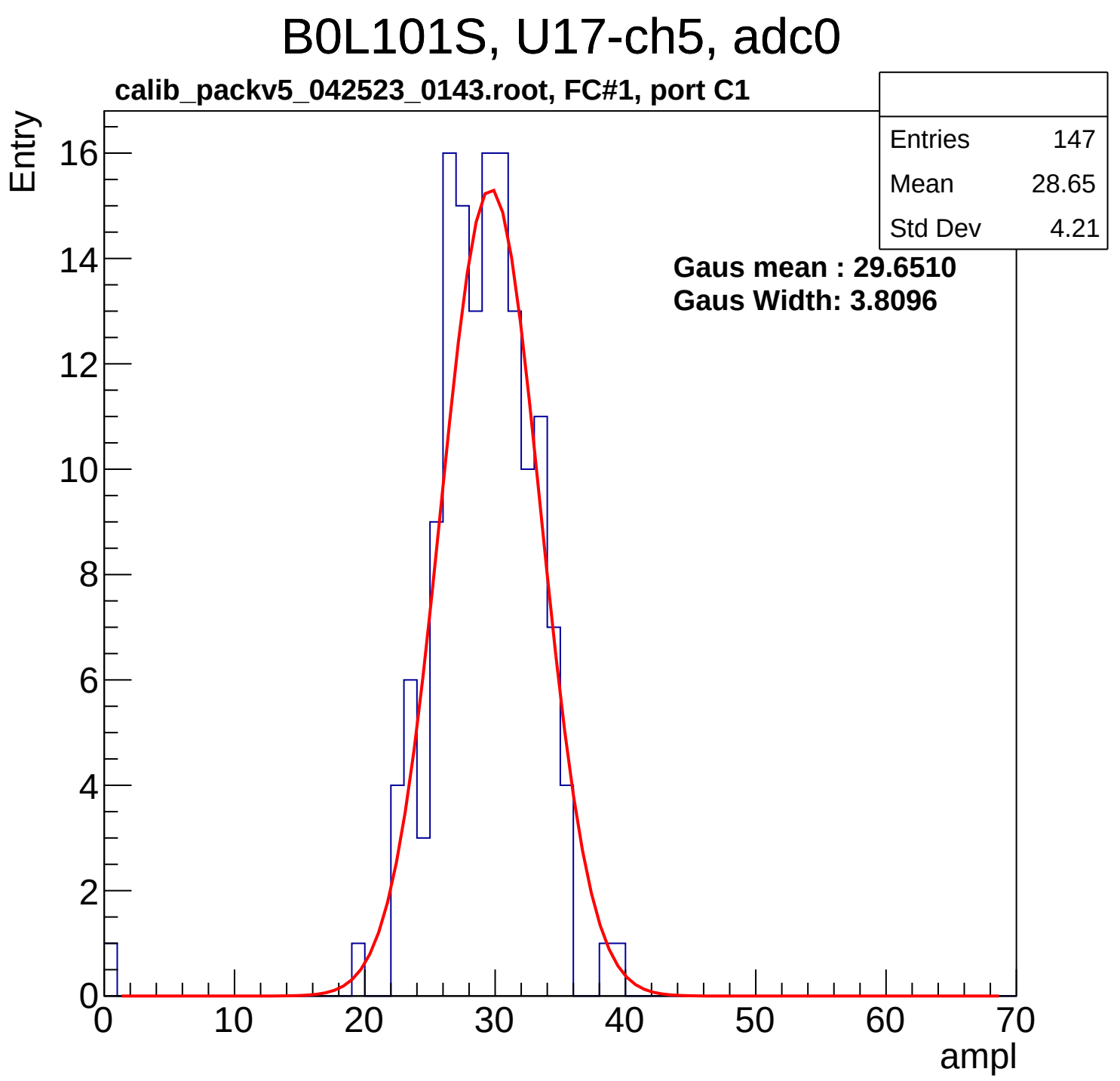

B0L101S, U17-ch4, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 48.25
Std Dev 3.732
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

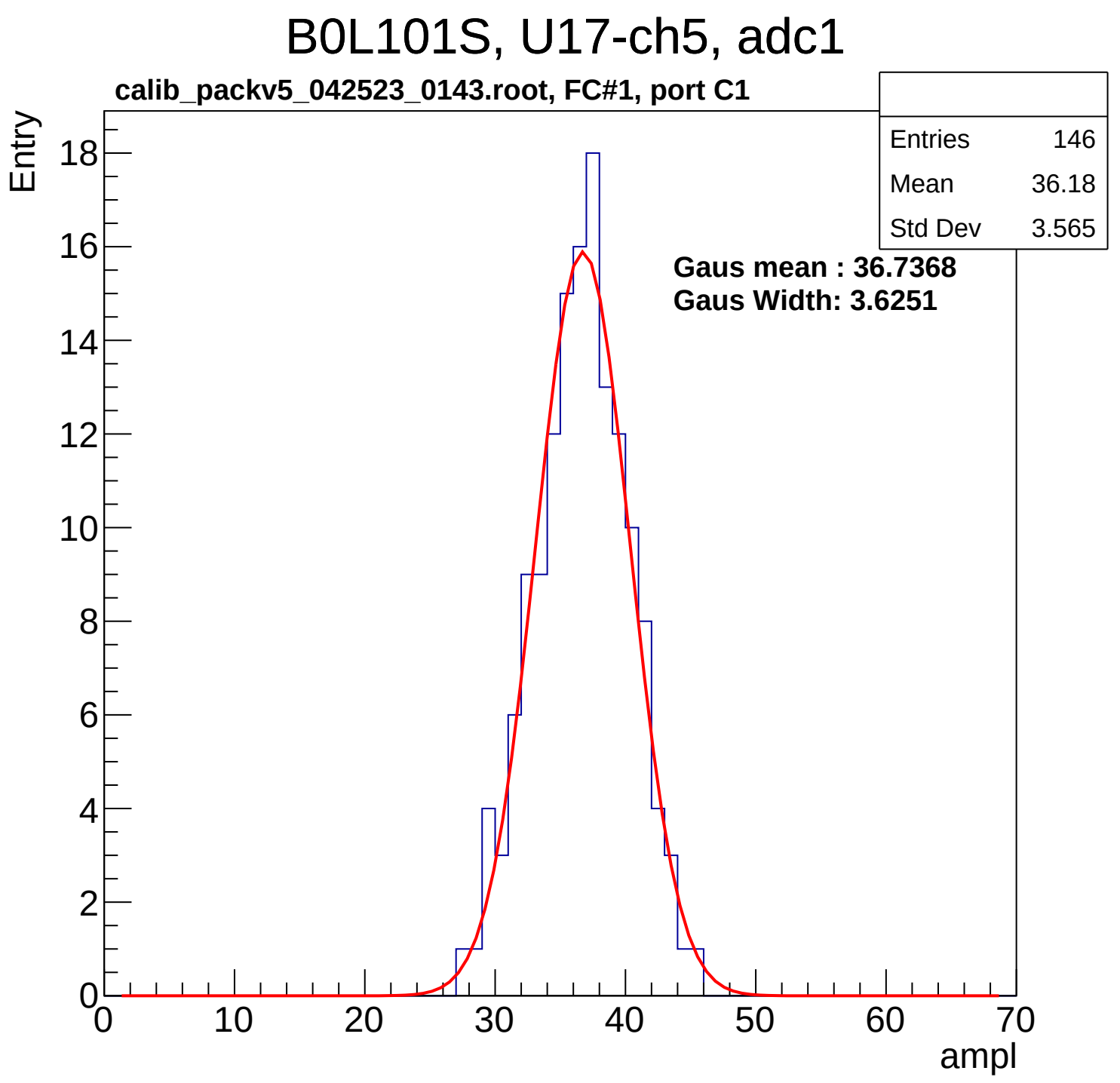

B0L101S, U17-ch4, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 54.47
Std Dev 5.978
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

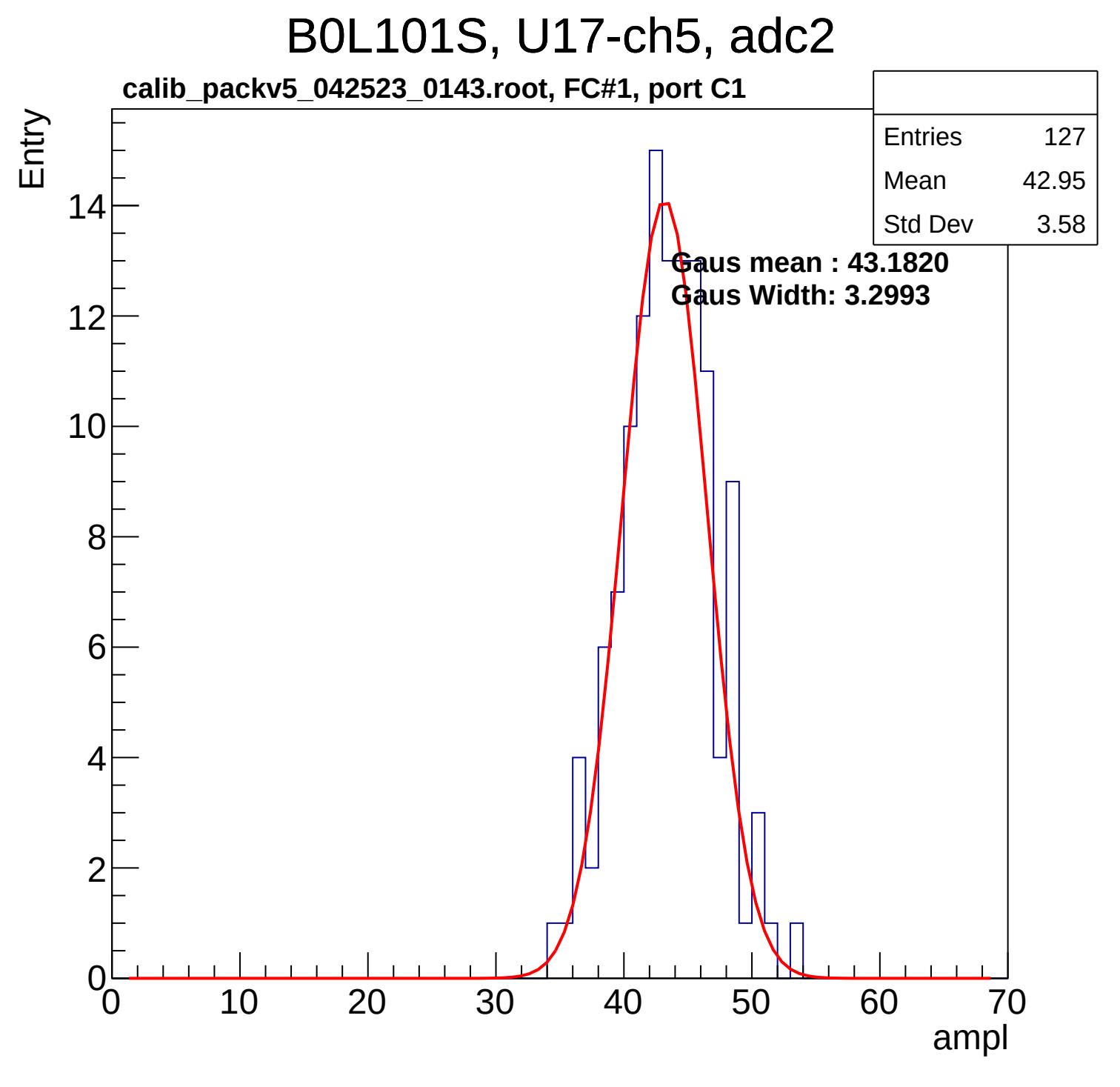

B0L101S, U17-ch4, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 90
Mean 59.41
Std Dev 2.84
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

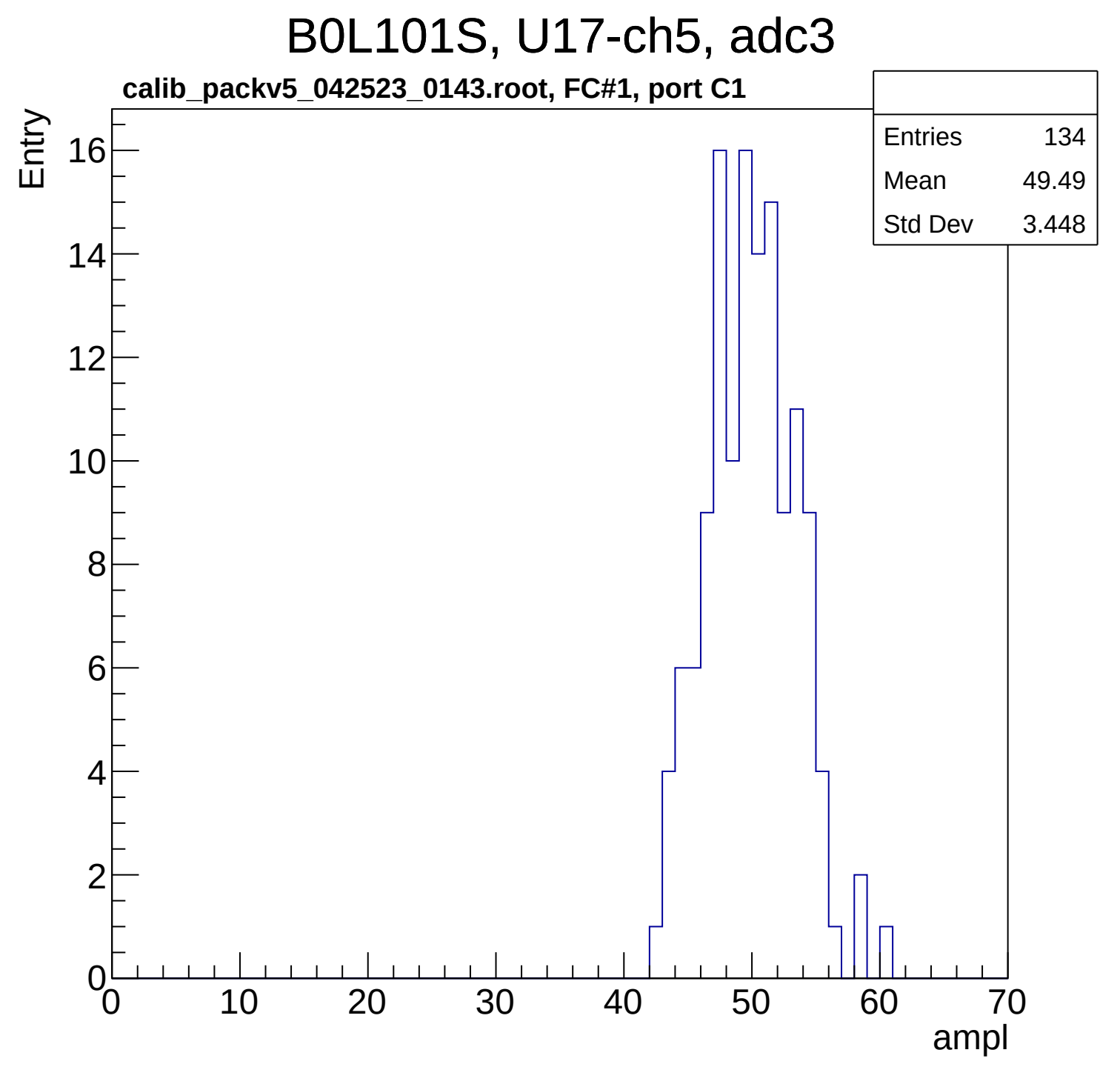

B0L101S, U17-ch4, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 22
Mean 59.05
Std Dev 12.95
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

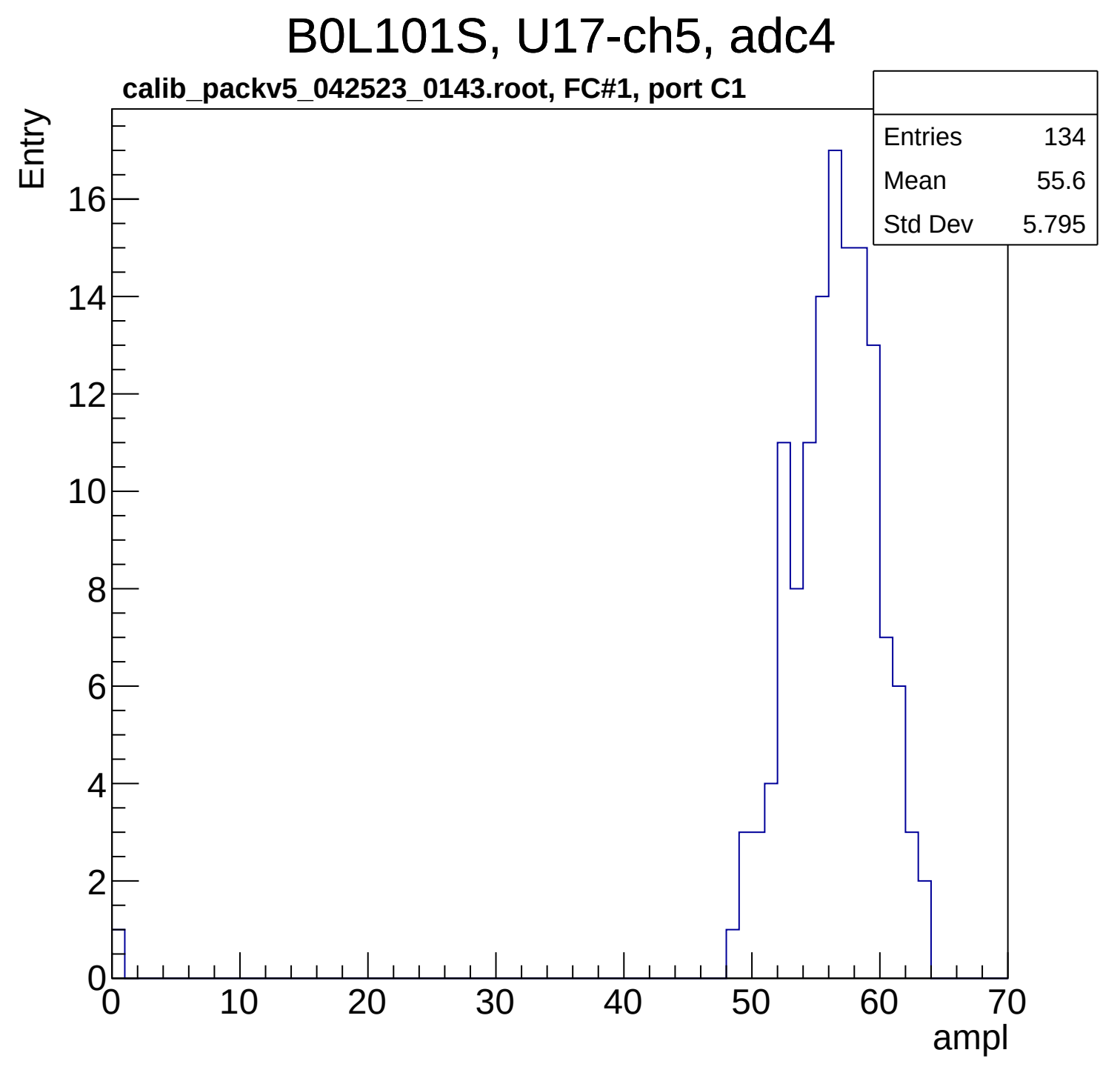

B0L101S, U17-ch4, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

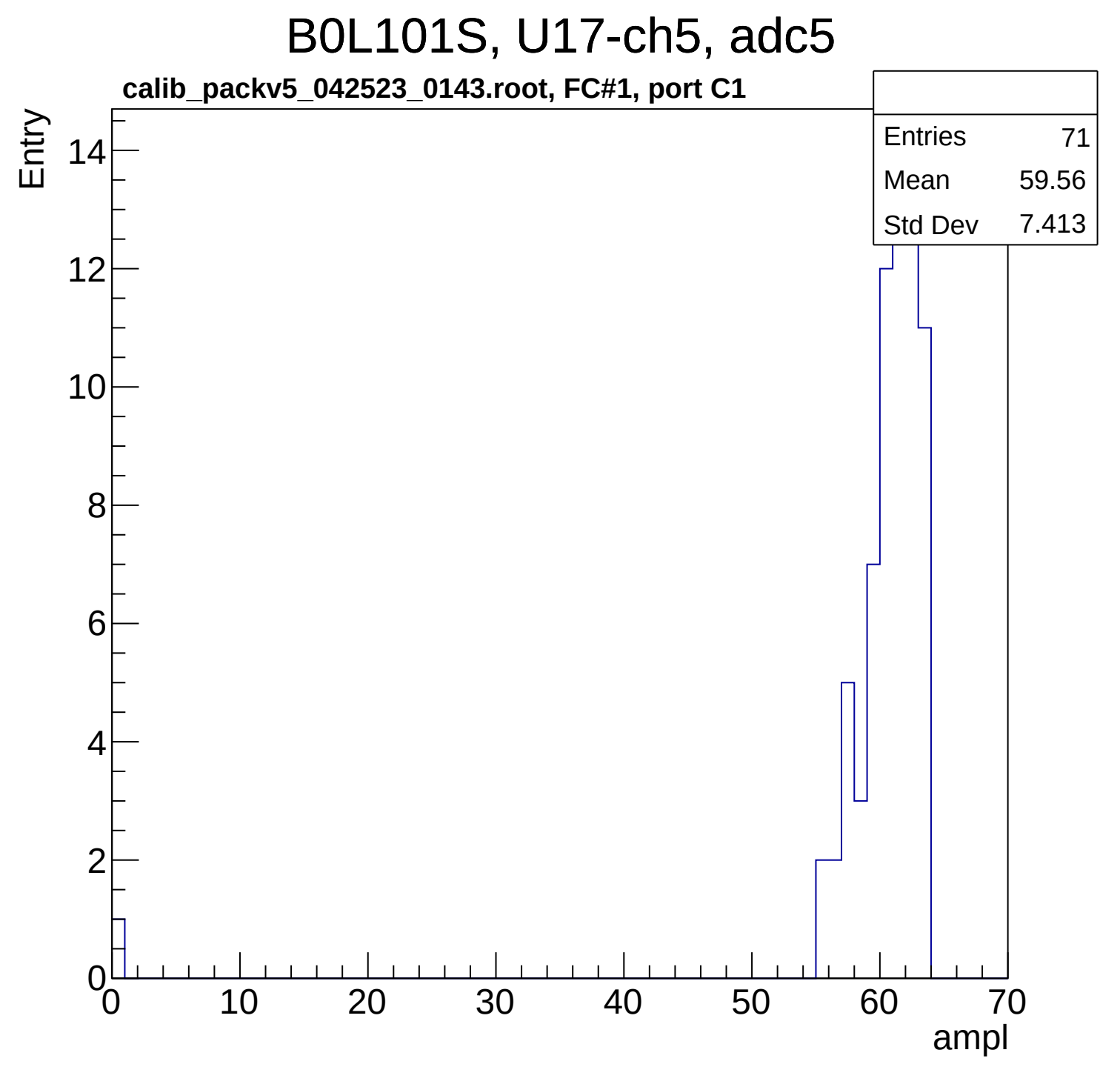

B0L101S, U17-ch5, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 28.65
Std Dev 4.21
Gaus mean : 29.6510
Gaus Width: 3.8096
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

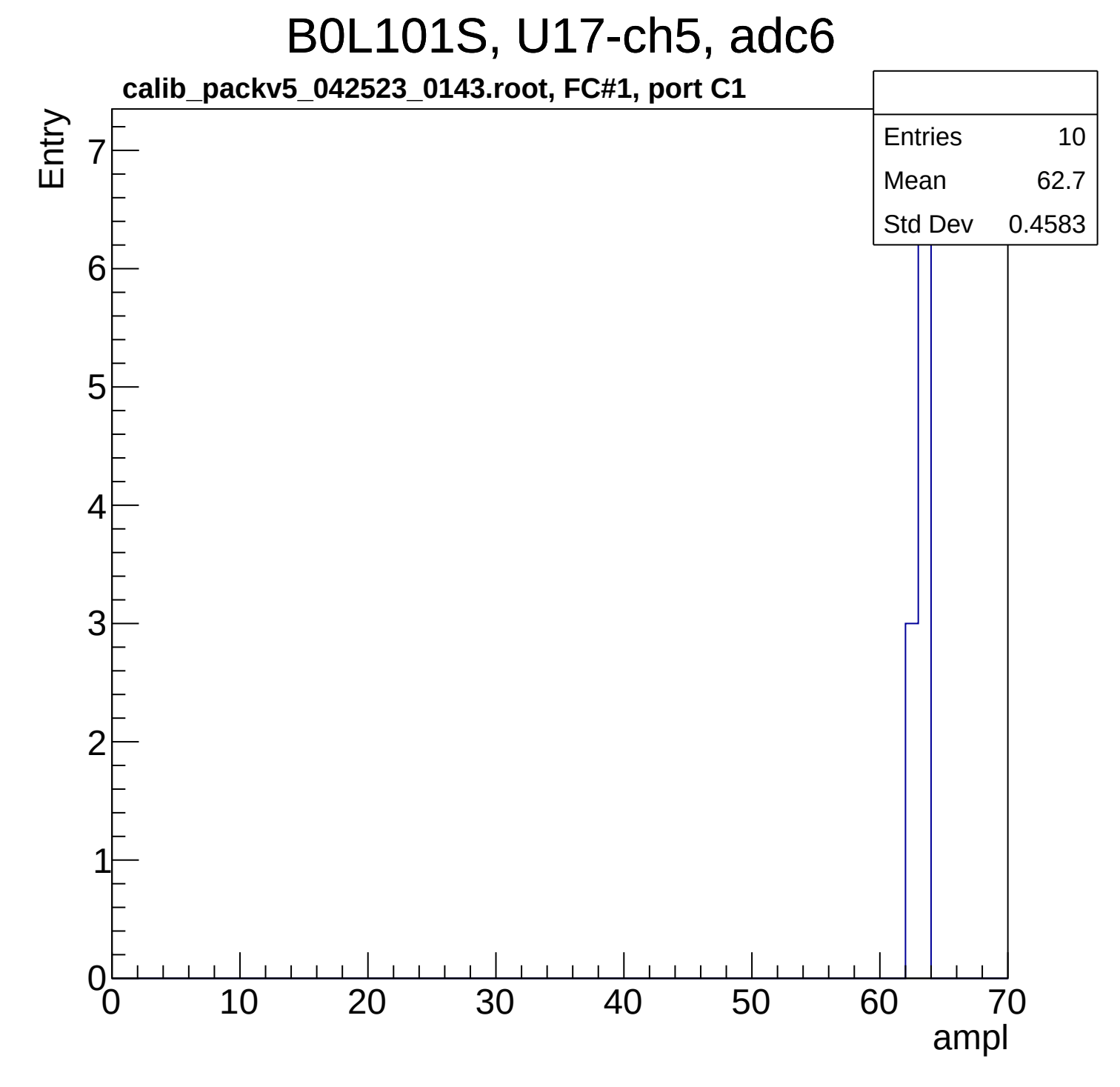

B0L101S, U17-ch5, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 36.18
Std Dev 3.565
Gaus mean : 36.7368
Gaus Width: 3.6251
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

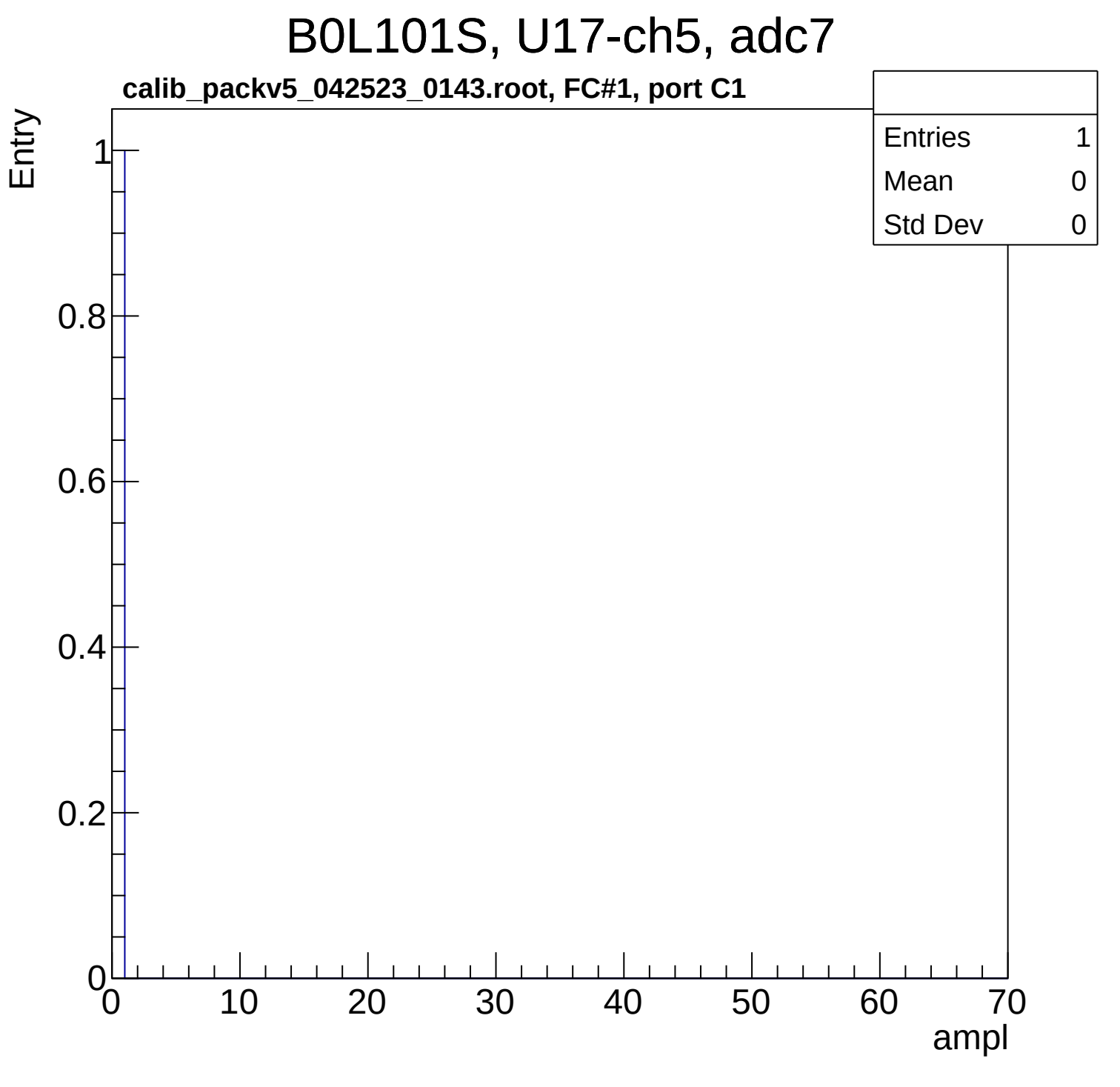

B0L101S, U17-ch5, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 42.95
Std Dev 3.58
Gaus mean : 43.1820
Gaus Width: 3.2993
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

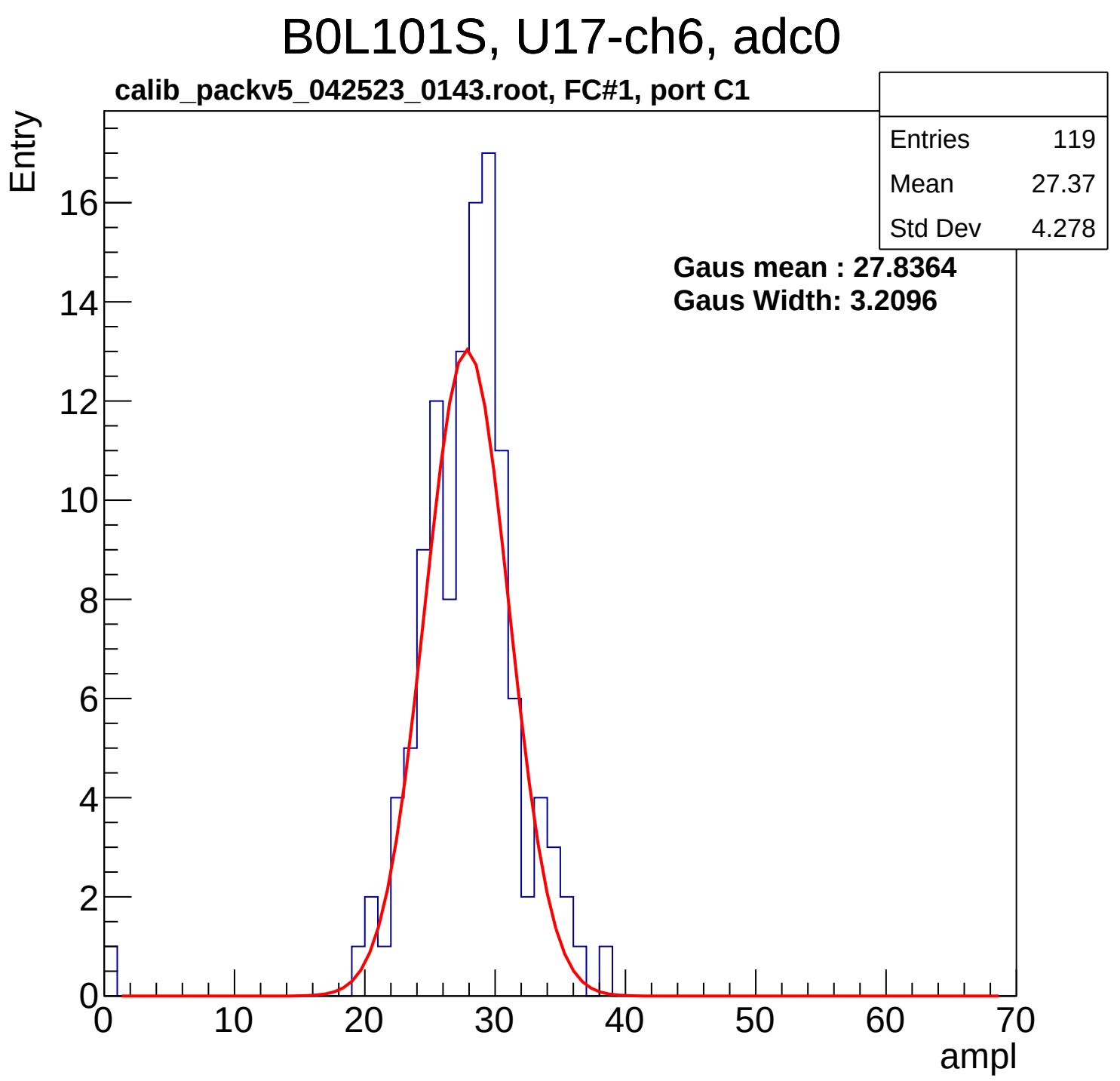

B0L101S, U17-ch5, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 49.49
Std Dev 3.448
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

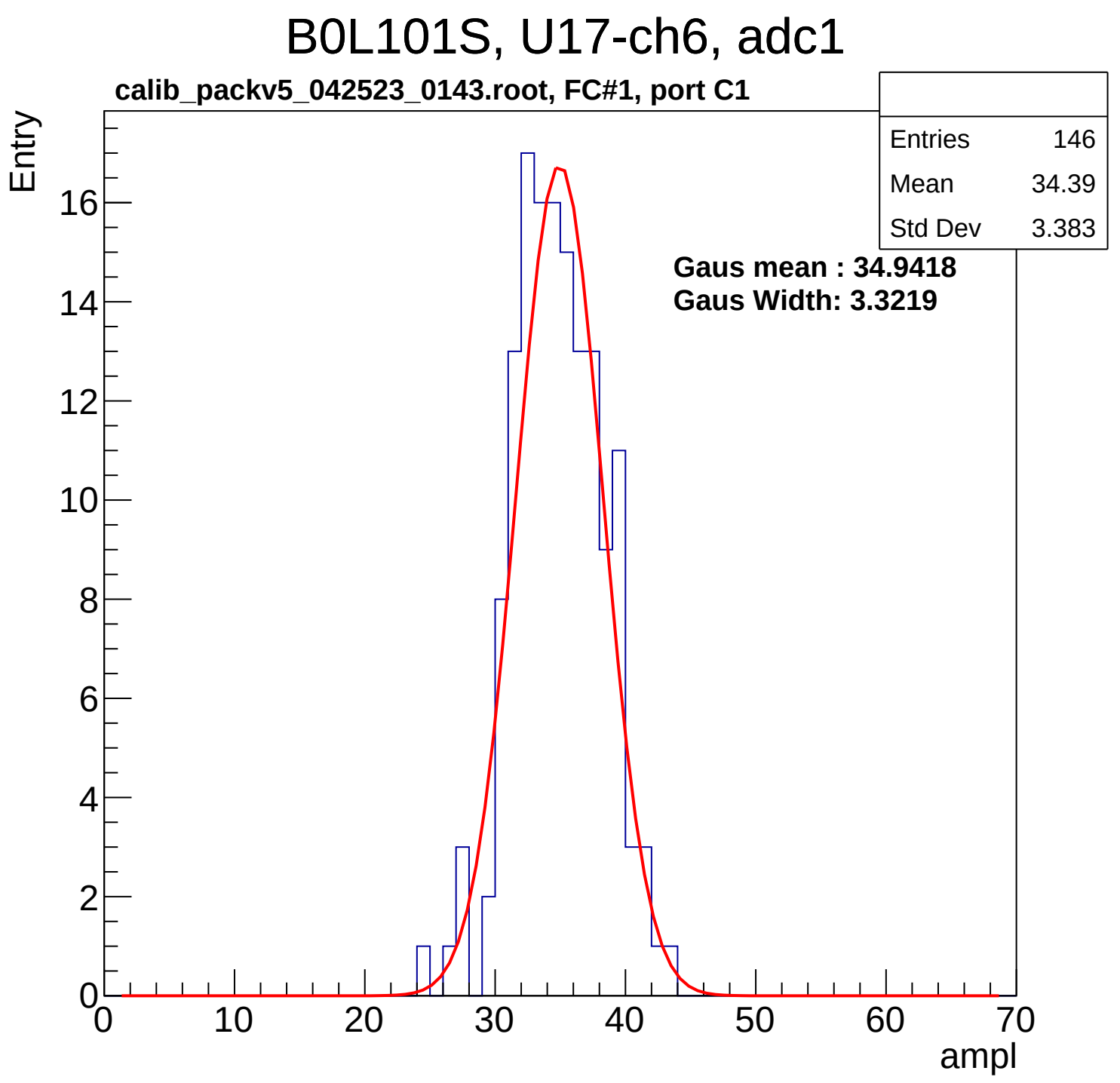

B0L101S, U17-ch5, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 55.6
Std Dev 5.795
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

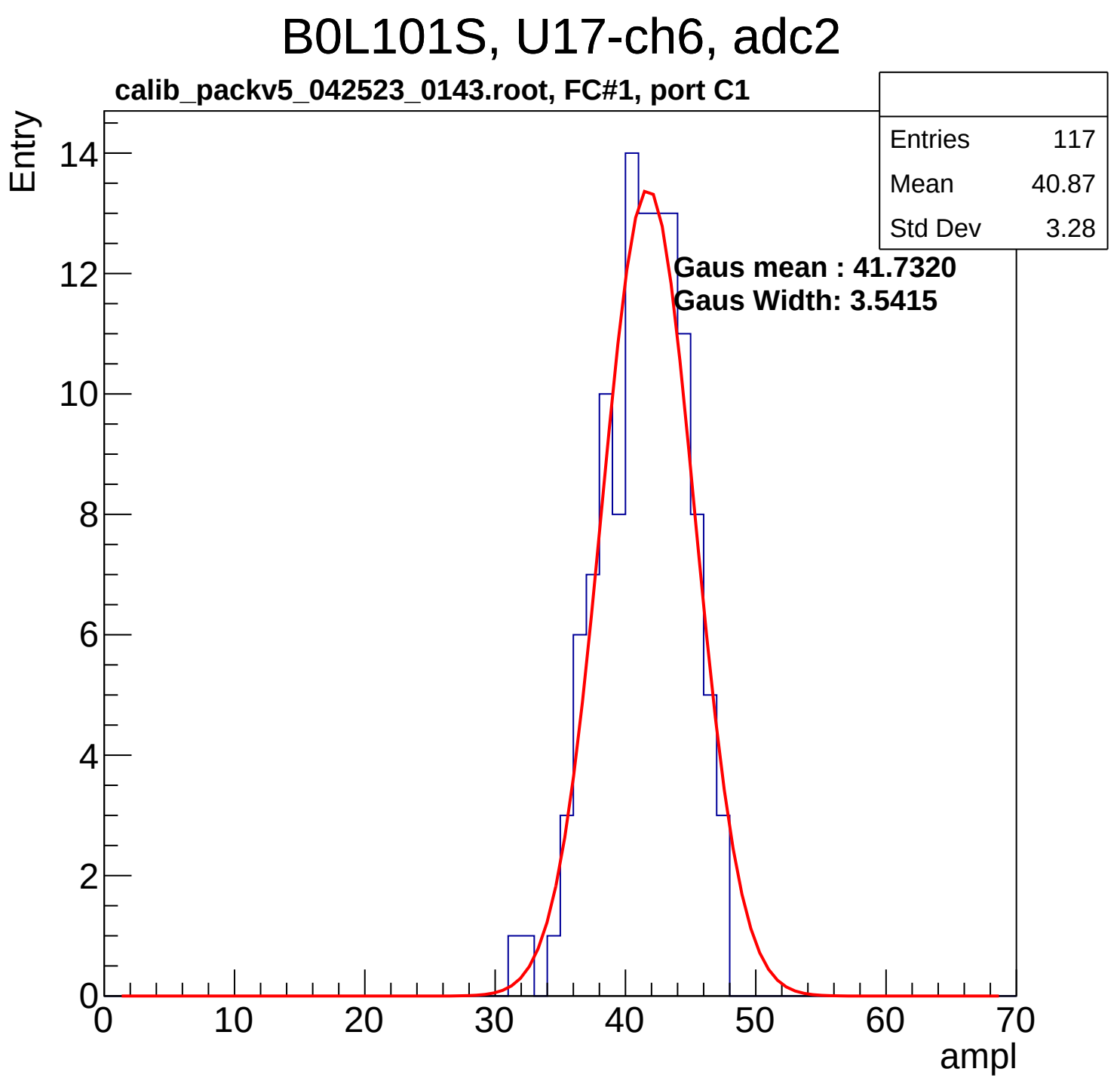

B0L101S, U17-ch5, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 71
Mean 59.56
Std Dev 7.413
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

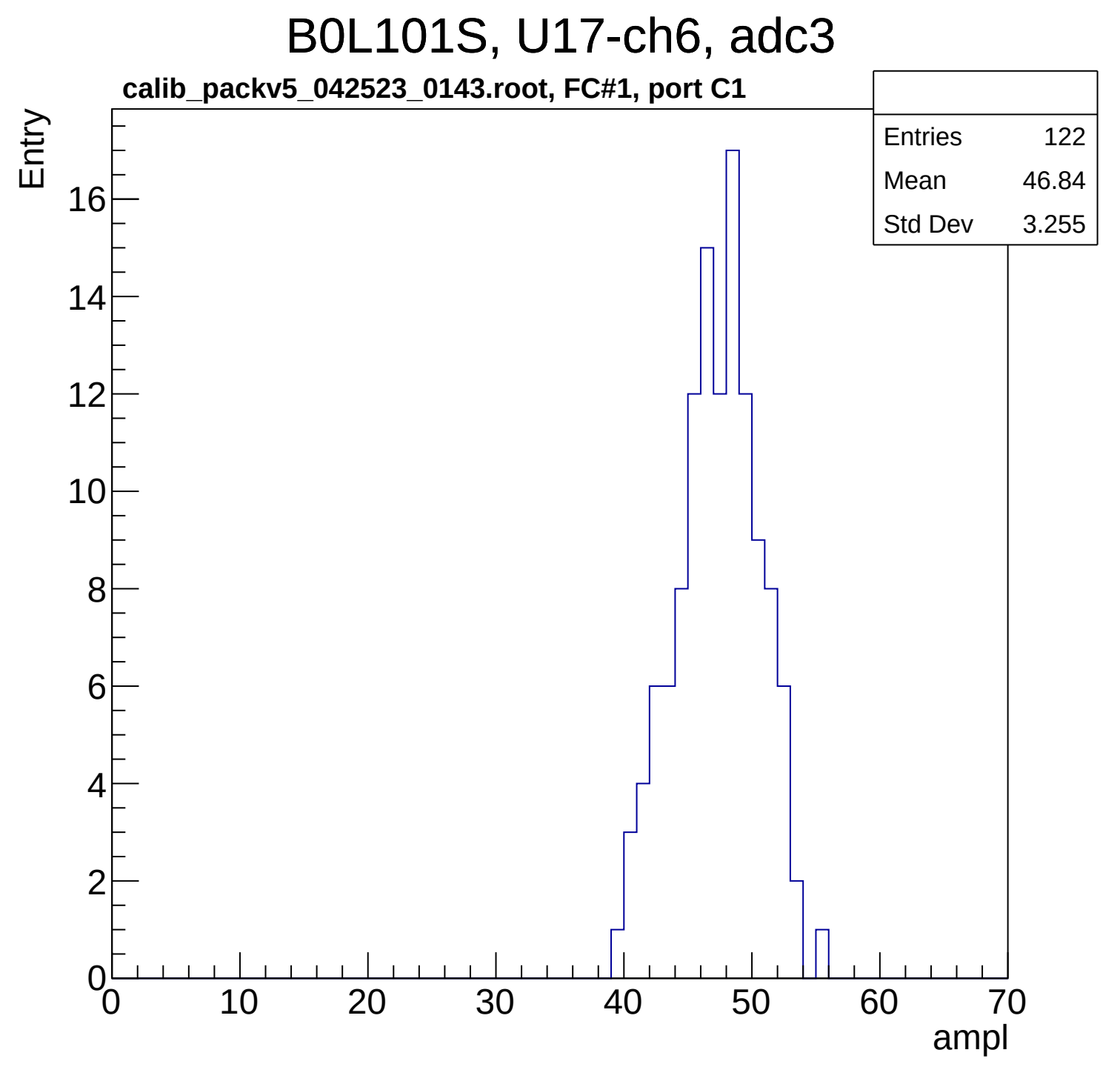

B0L101S, U17-ch5, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 62.7
Std Dev 0.4583
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

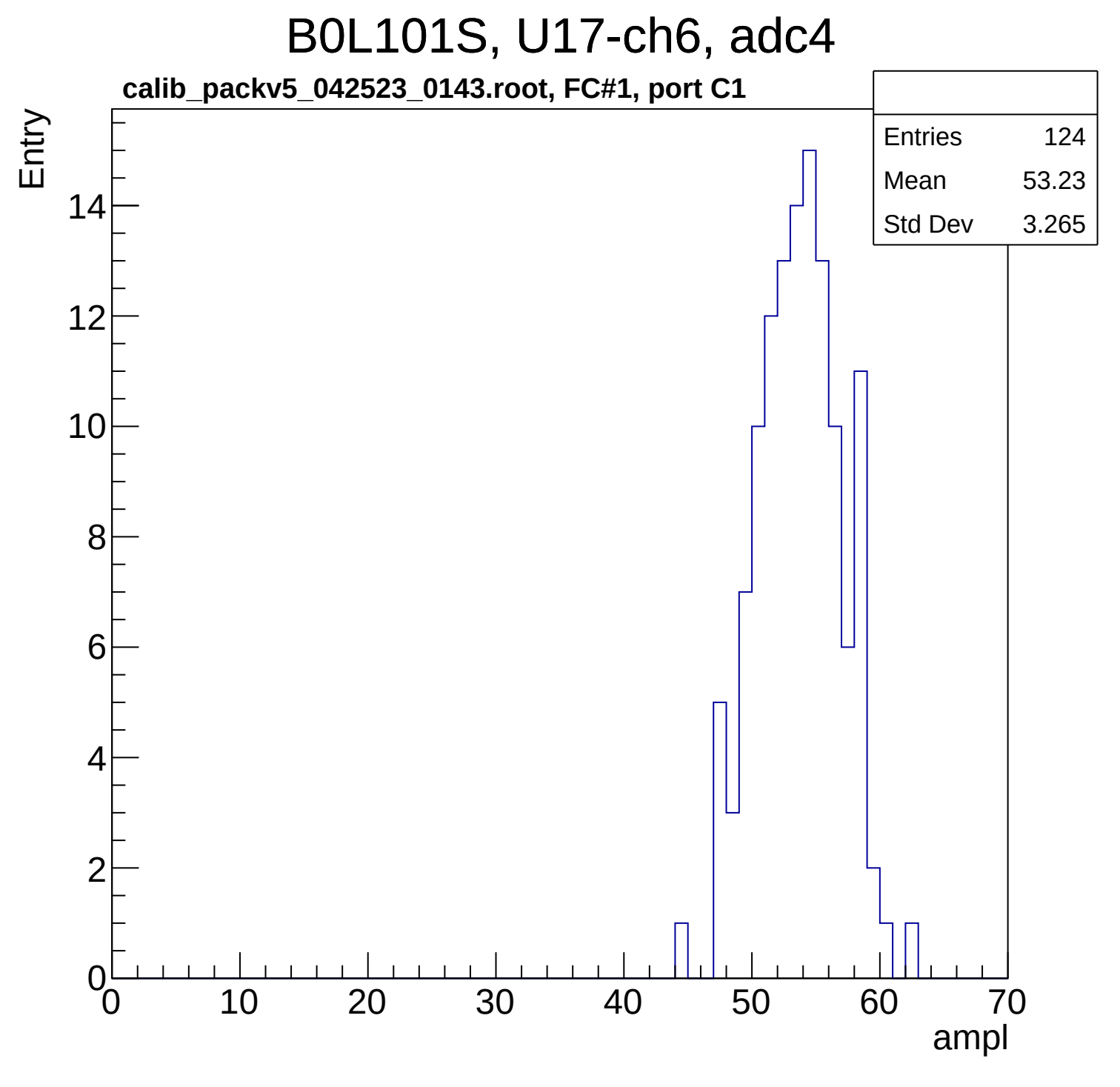

B0L101S, U17-ch5, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

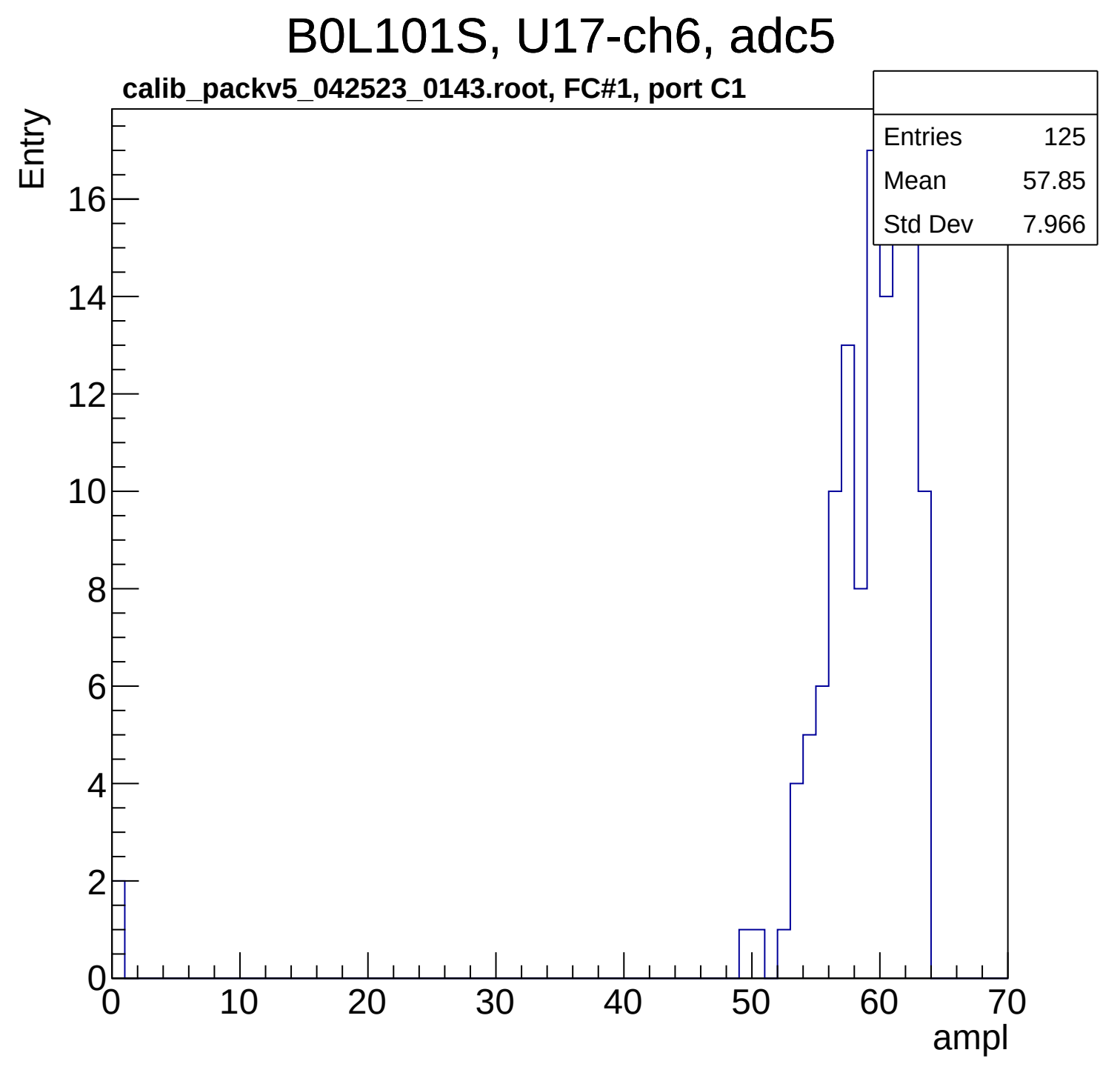

B0L101S, U17-ch6, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 27.37
Std Dev 4.278
Gaus mean : 27.8364
Gaus Width: 3.2096
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

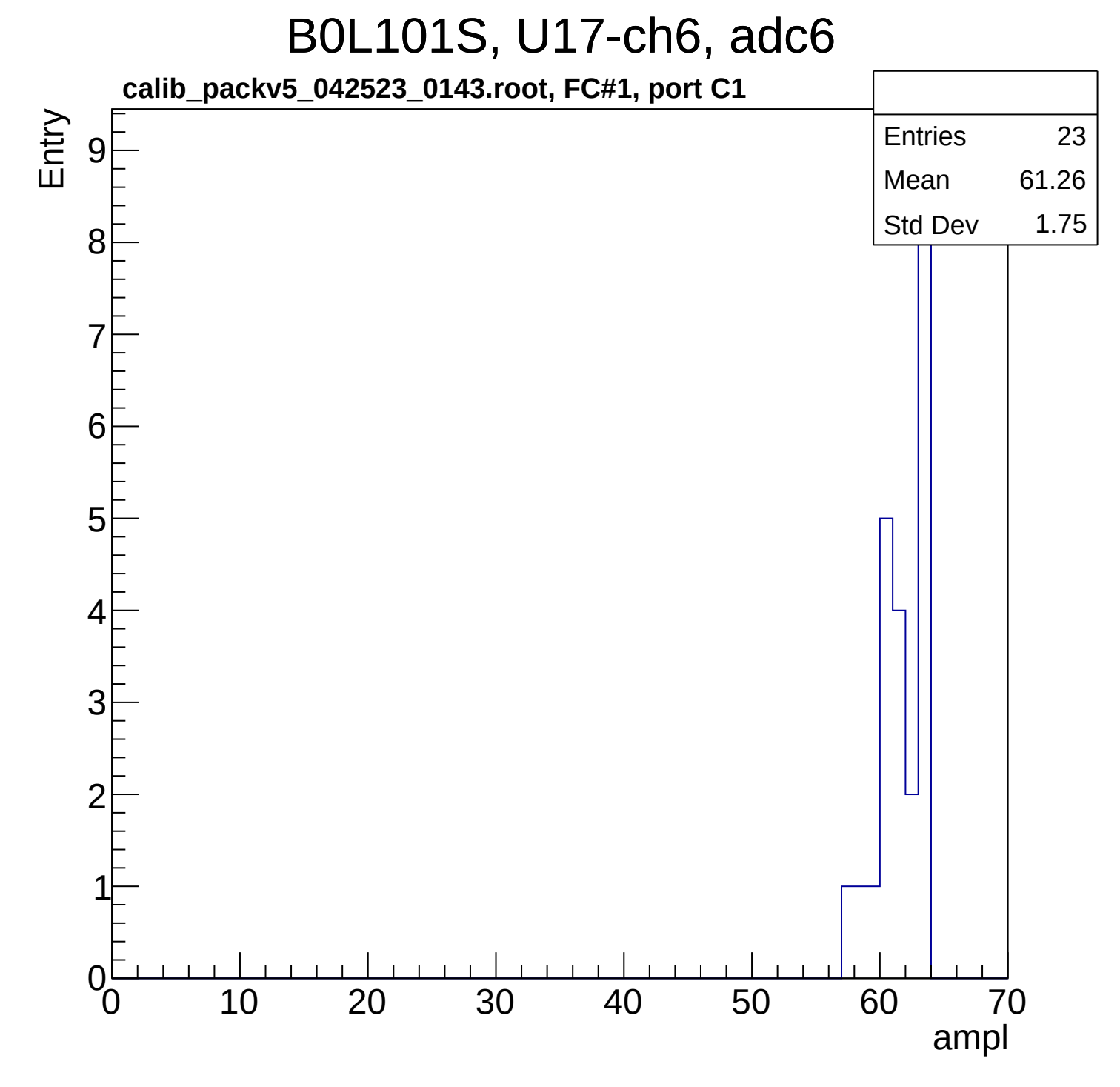

B0L101S, U17-ch6, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 34.39
Std Dev 3.383
Gaus mean : 34.9418
Gaus Width: 3.3219
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

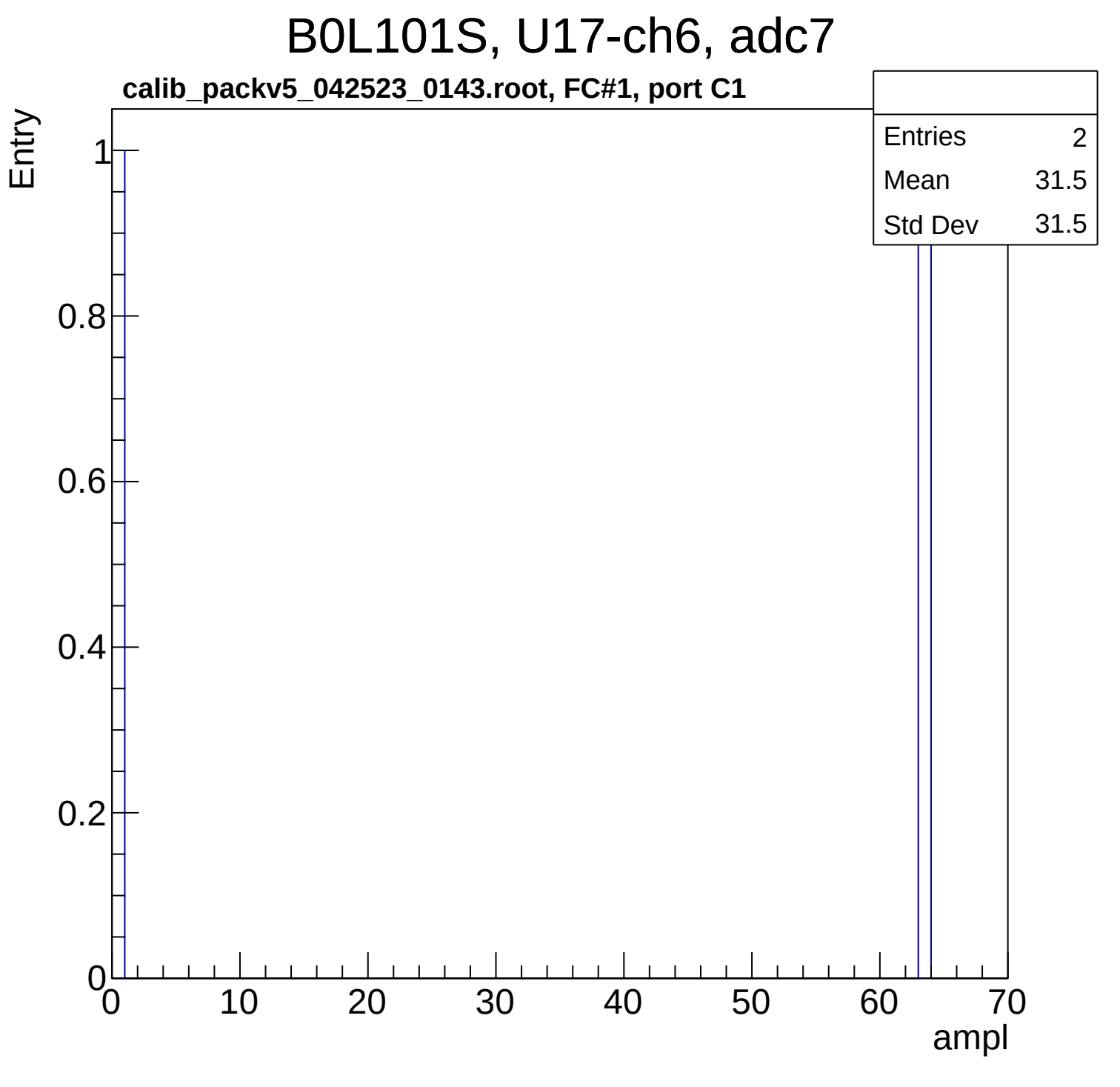

B0L101S, U17-ch6, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 40.87
Std Dev 3.28
Gaus mean : 41.7320
Gaus Width: 3.5415
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

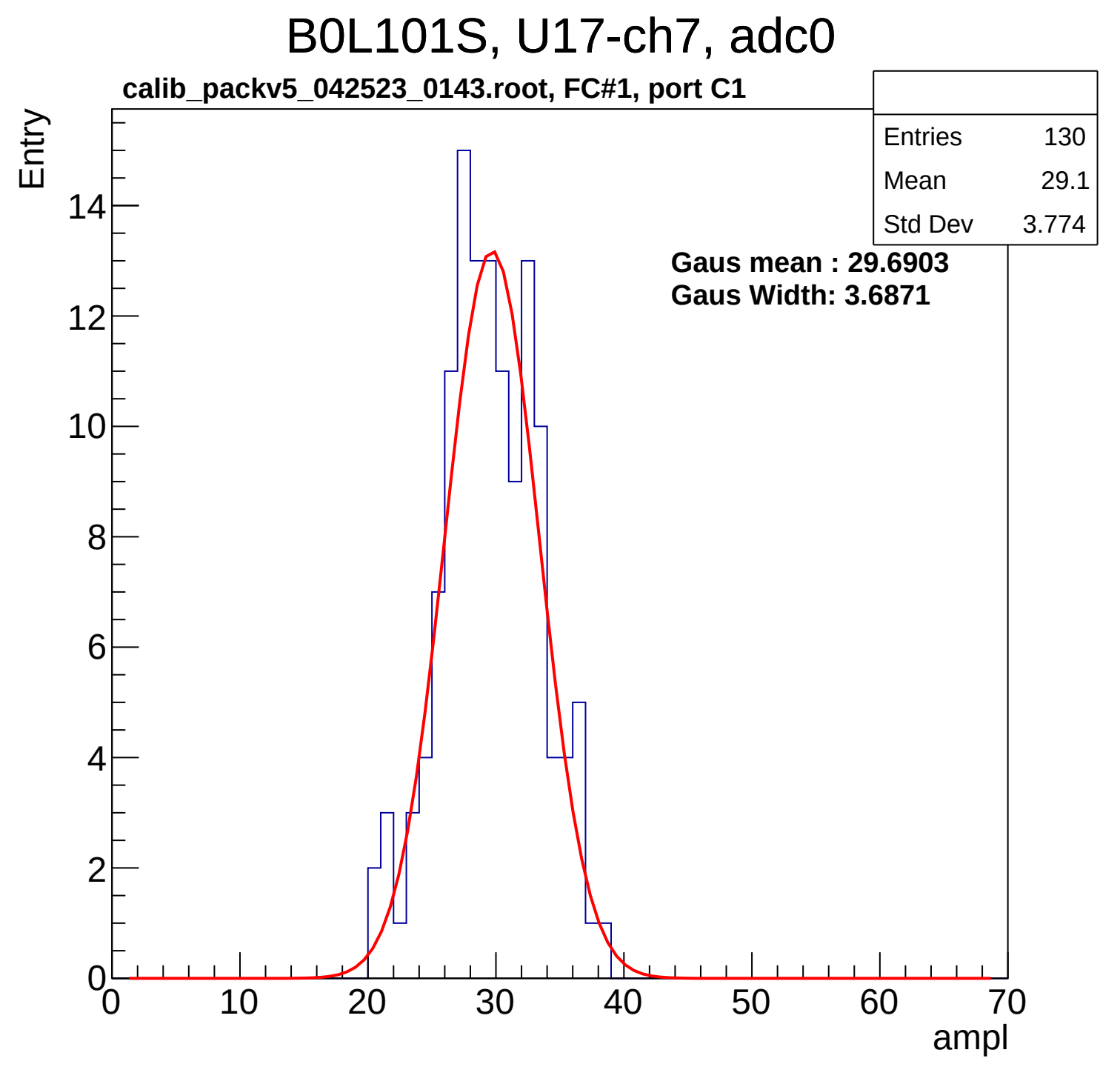

B0L101S, U17-ch6, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 46.84
Std Dev 3.255
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

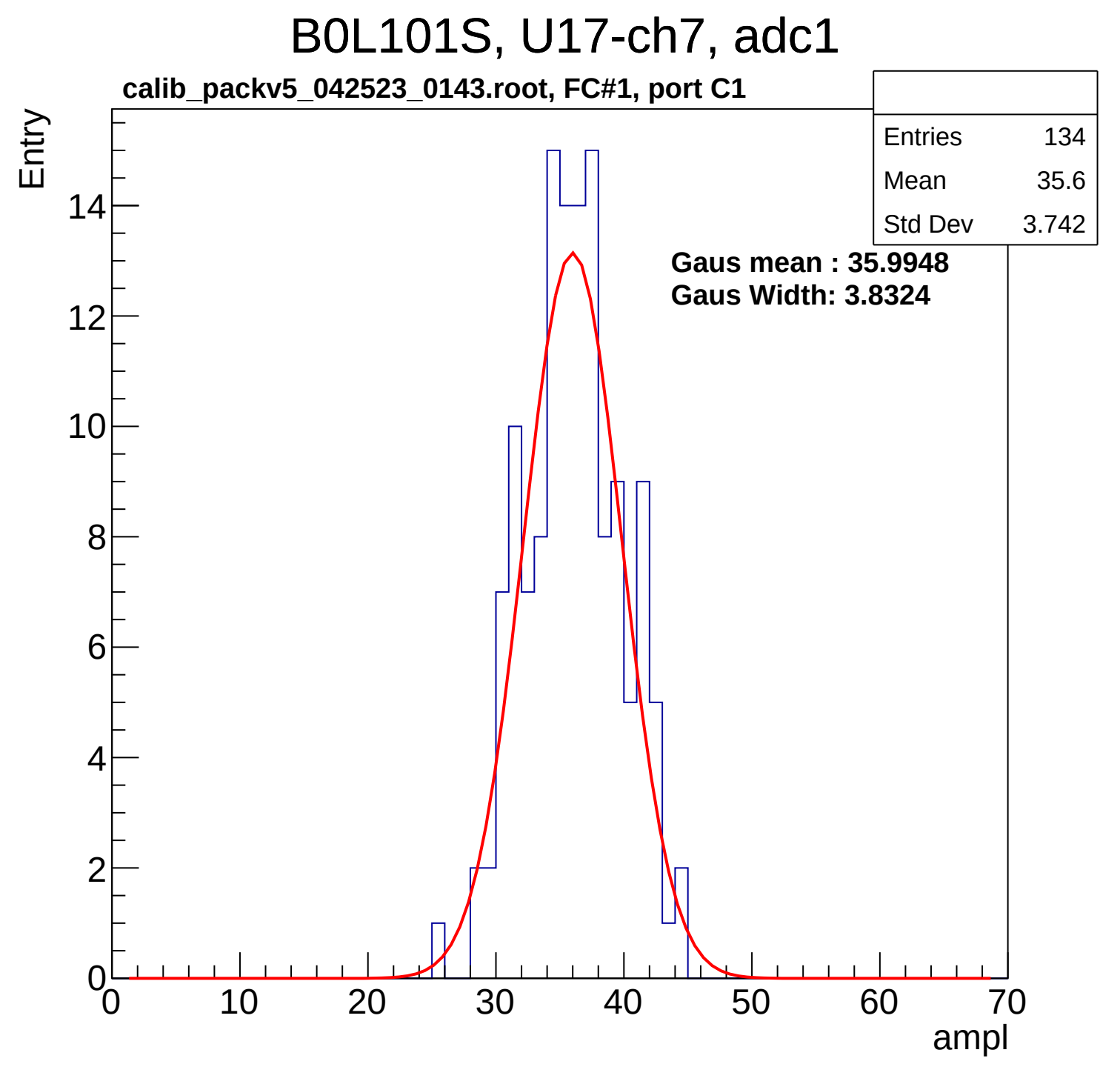

B0L101S, U17-ch6, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 53.23
Std Dev 3.265
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

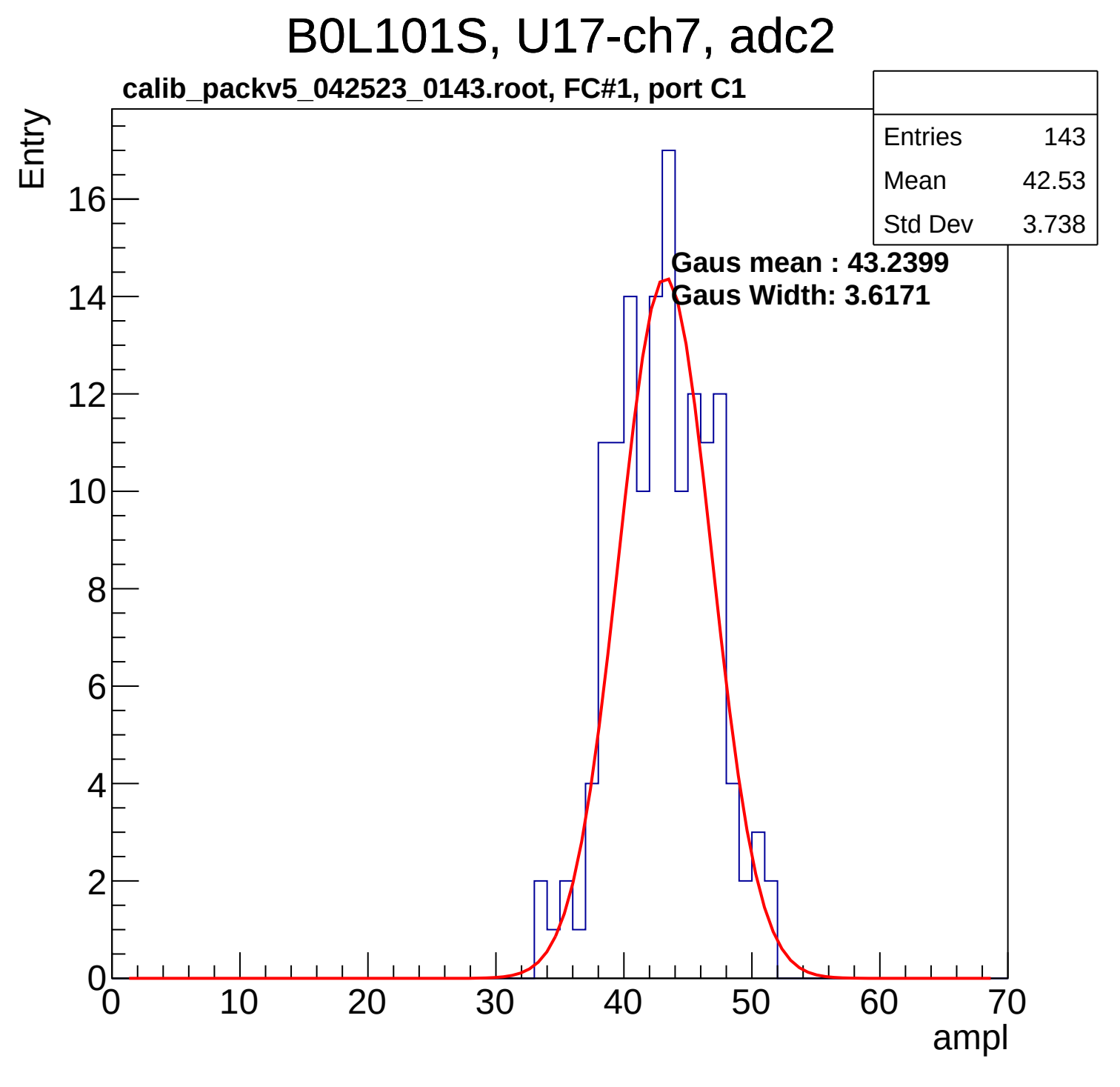

B0L101S, U17-ch6, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 57.85
Std Dev 7.966
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

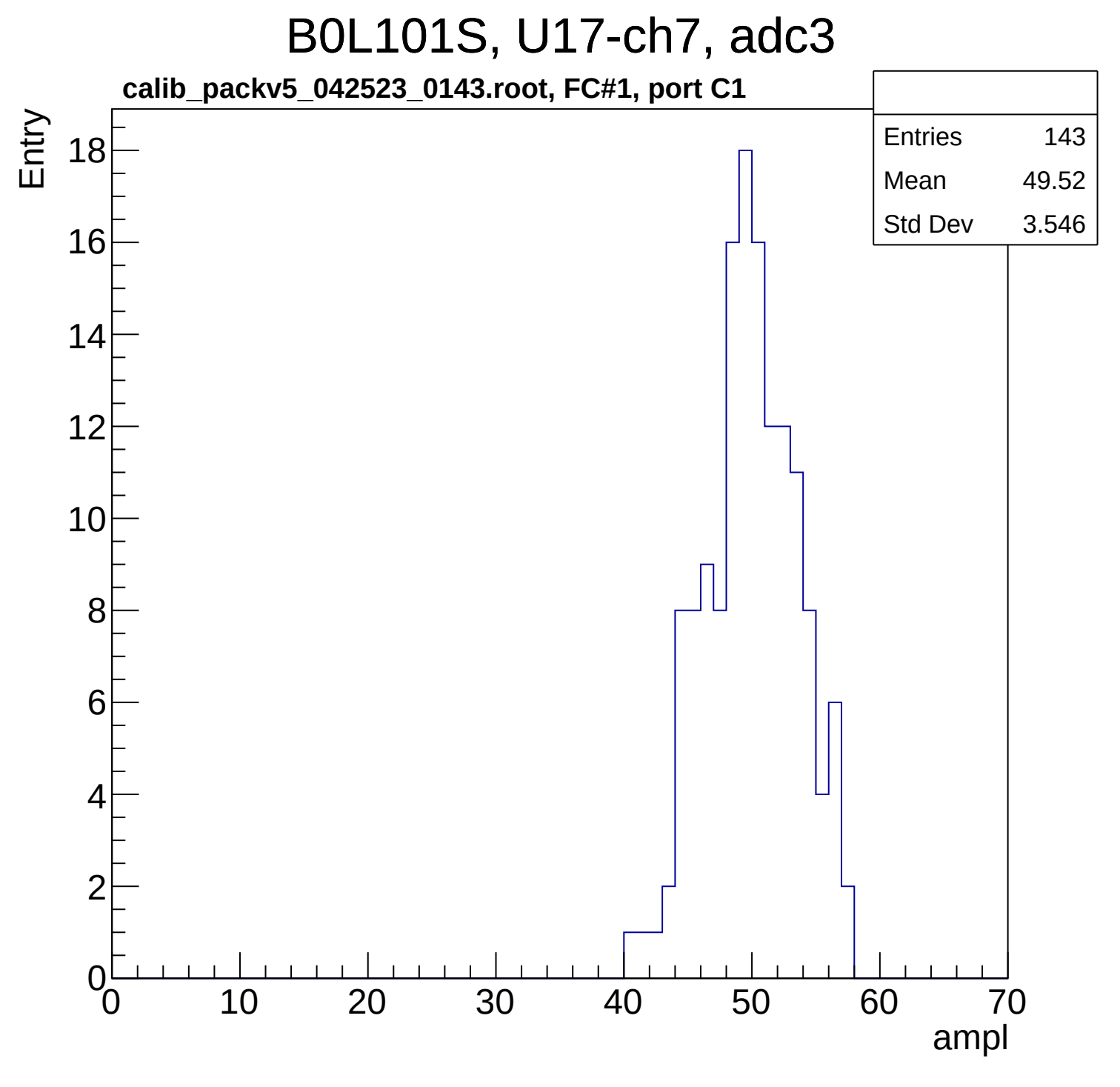

B0L101S, U17-ch6, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 23
Mean 61.26
Std Dev 1.75
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

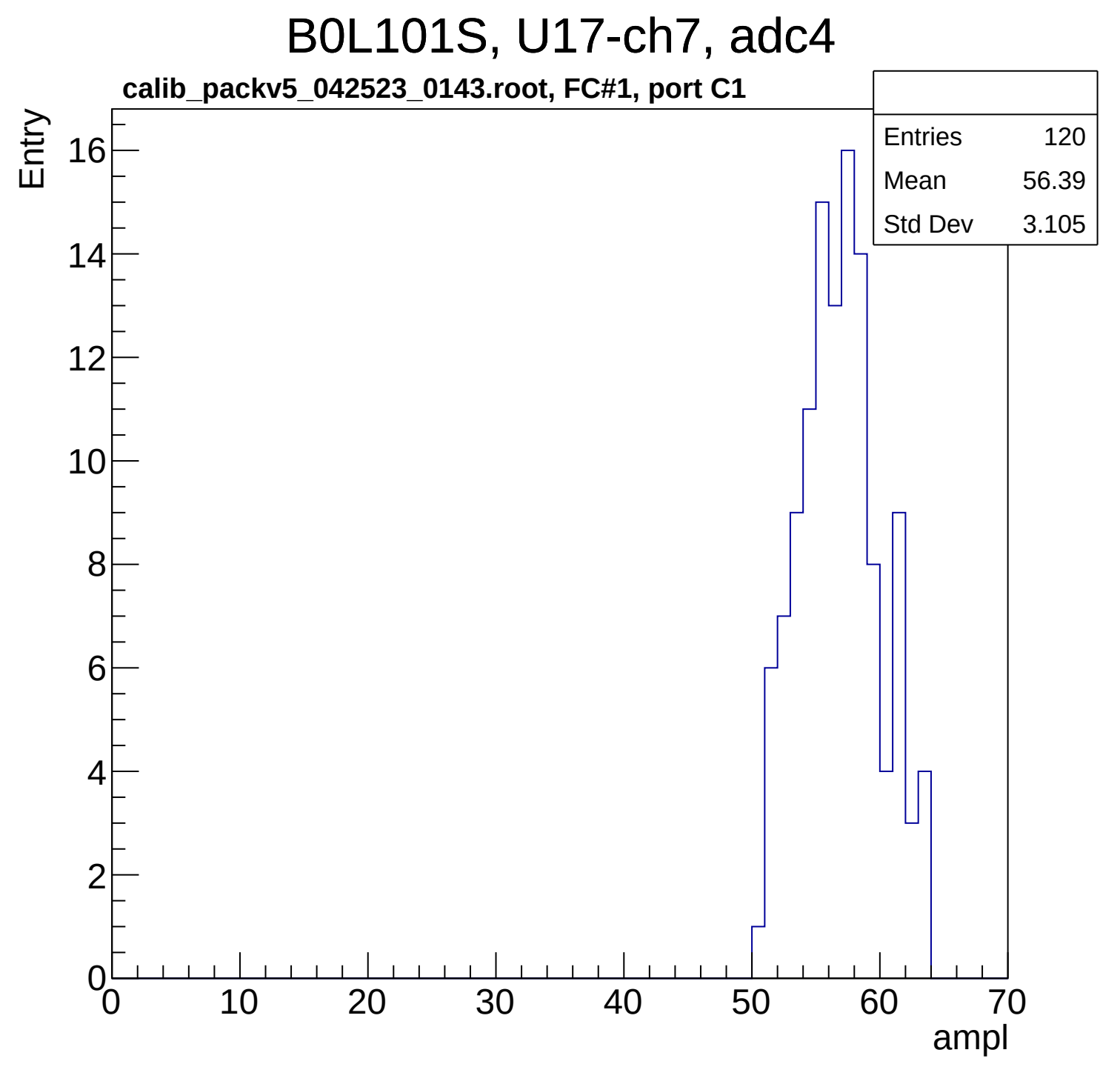

B0L101S, U17-ch6, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

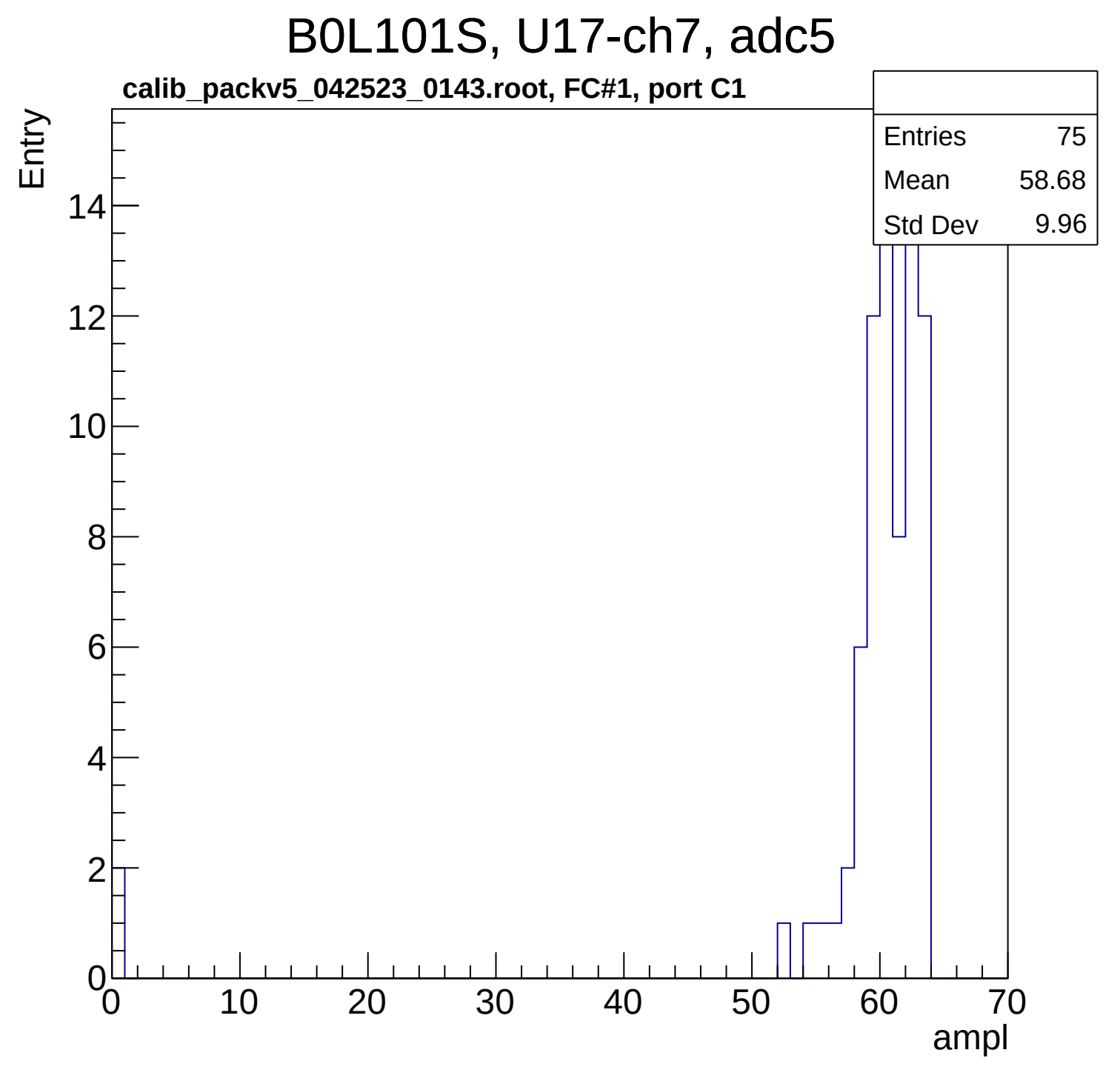

B0L101S, U17-ch7, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 29.1
Std Dev 3.774
Gaus mean : 29.6903
Gaus Width: 3.6871
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

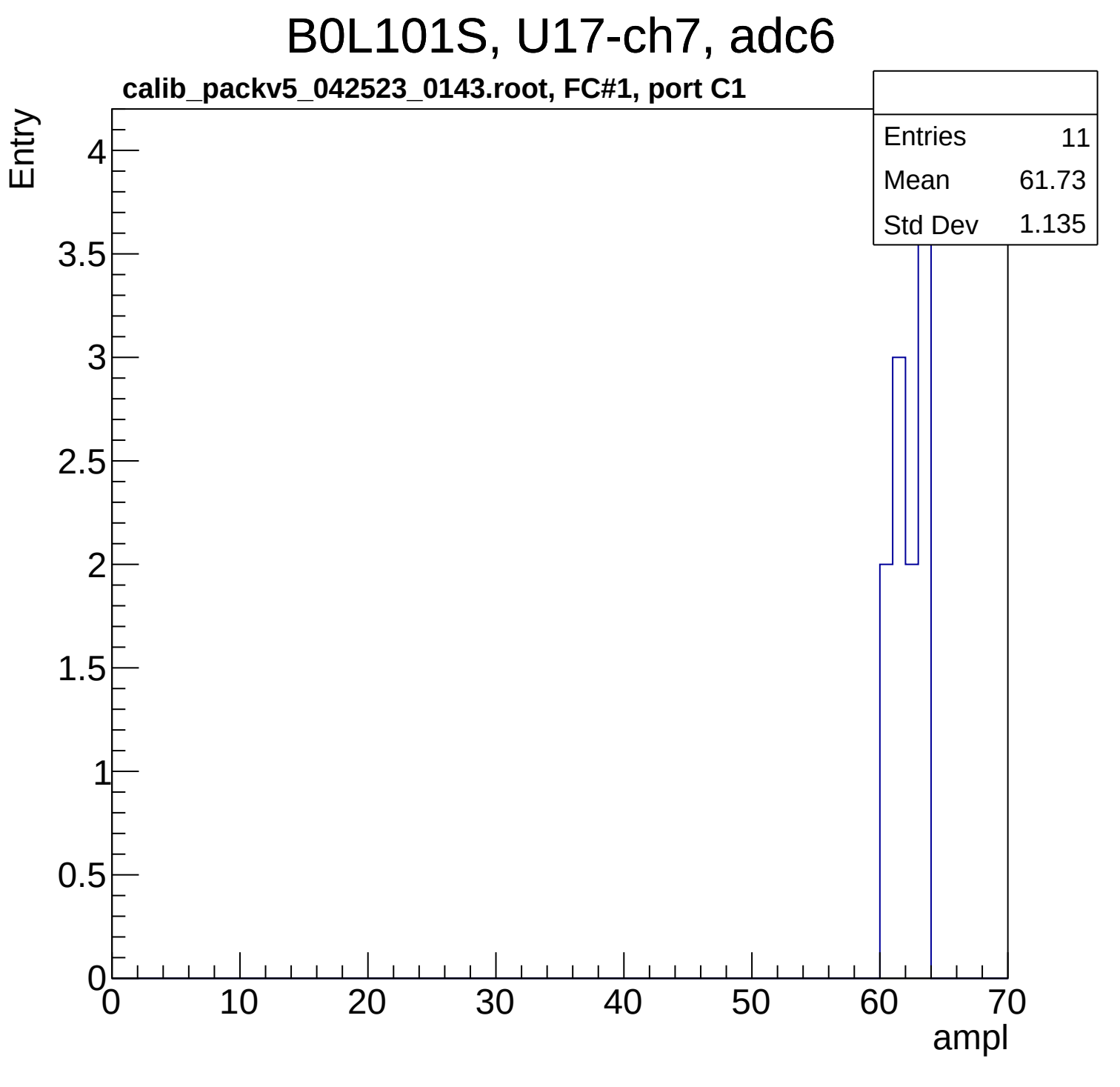

B0L101S, U17-ch7, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 35.6
Std Dev 3.742
Gaus mean : 35.9948
Gaus Width: 3.8324
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

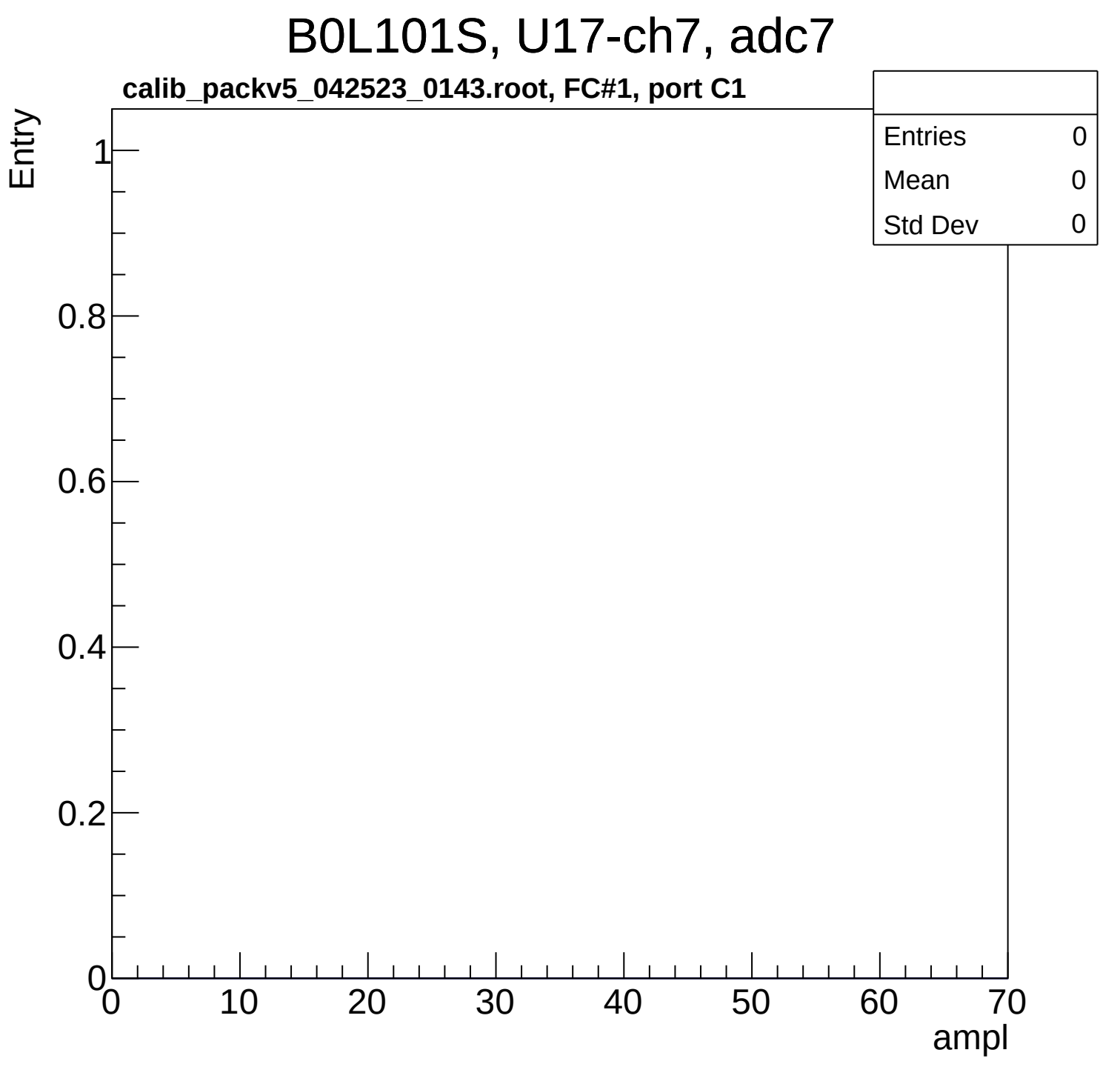

B0L101S, U17-ch7, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 42.53
Std Dev 3.738
Gaus mean : 43.2399
Gaus Width: 3.6171
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

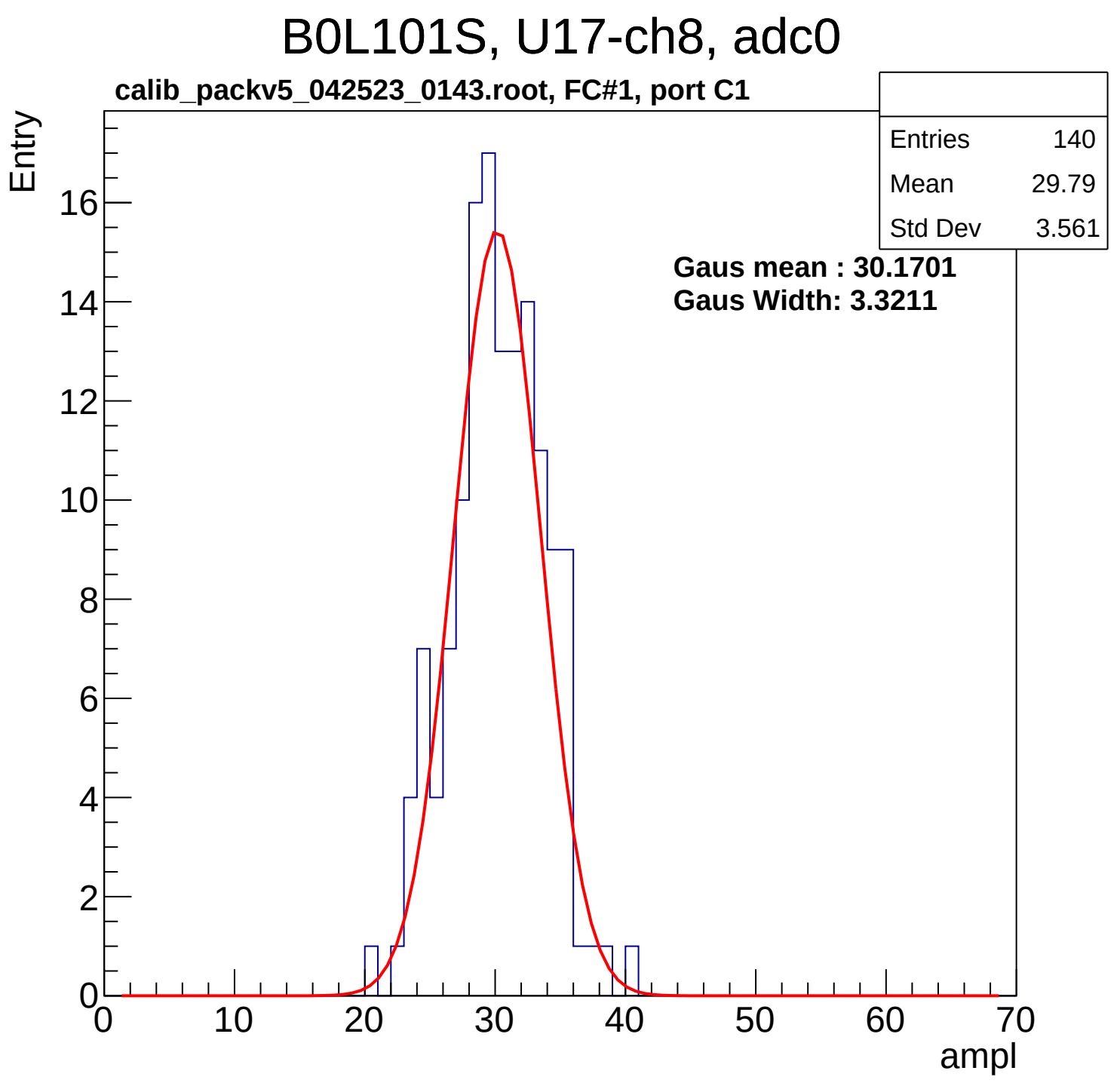

B0L101S, U17-ch7, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 49.52
Std Dev 3.546
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

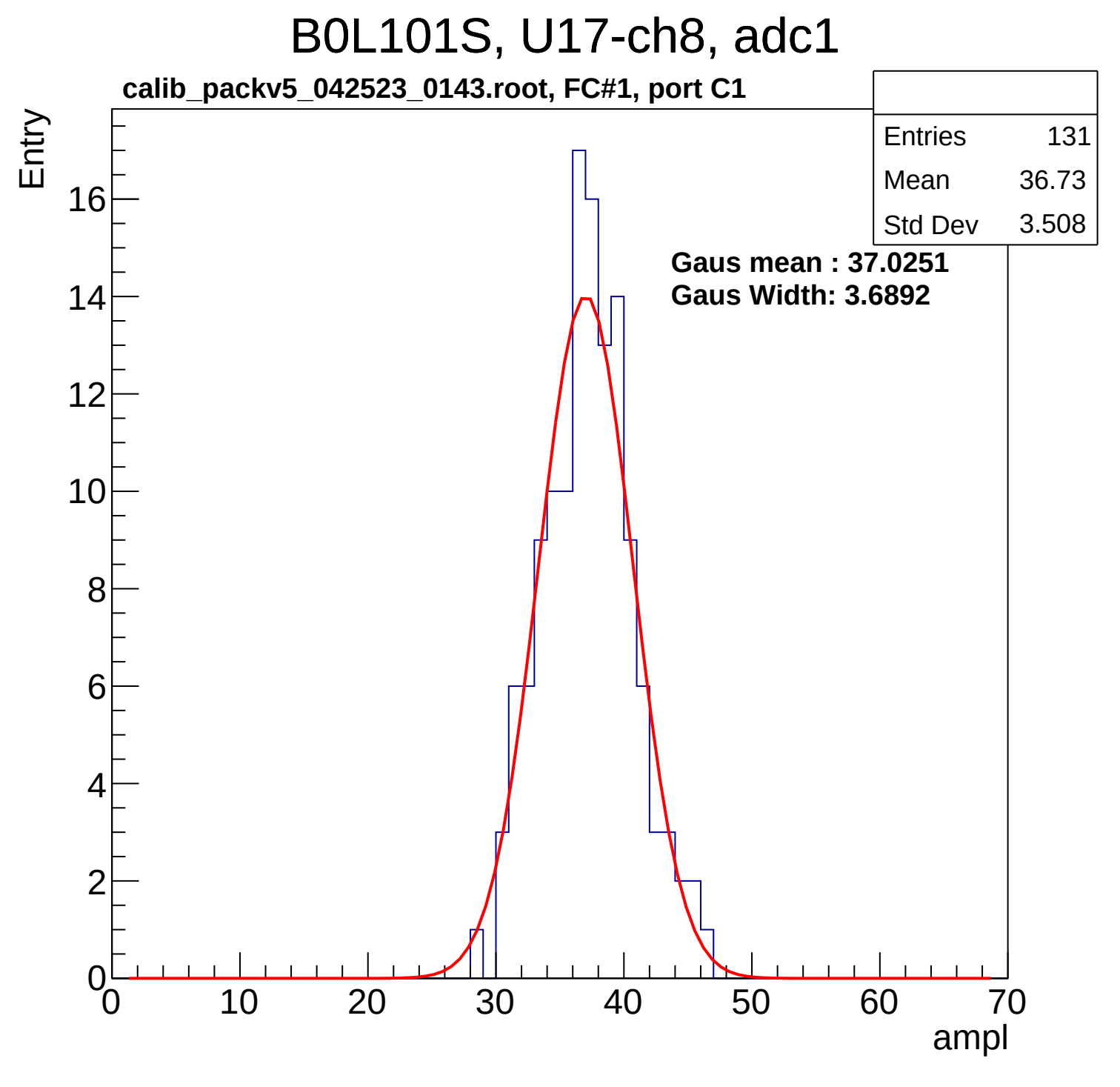

B0L101S, U17-ch7, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 56.39
Std Dev 3.105
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

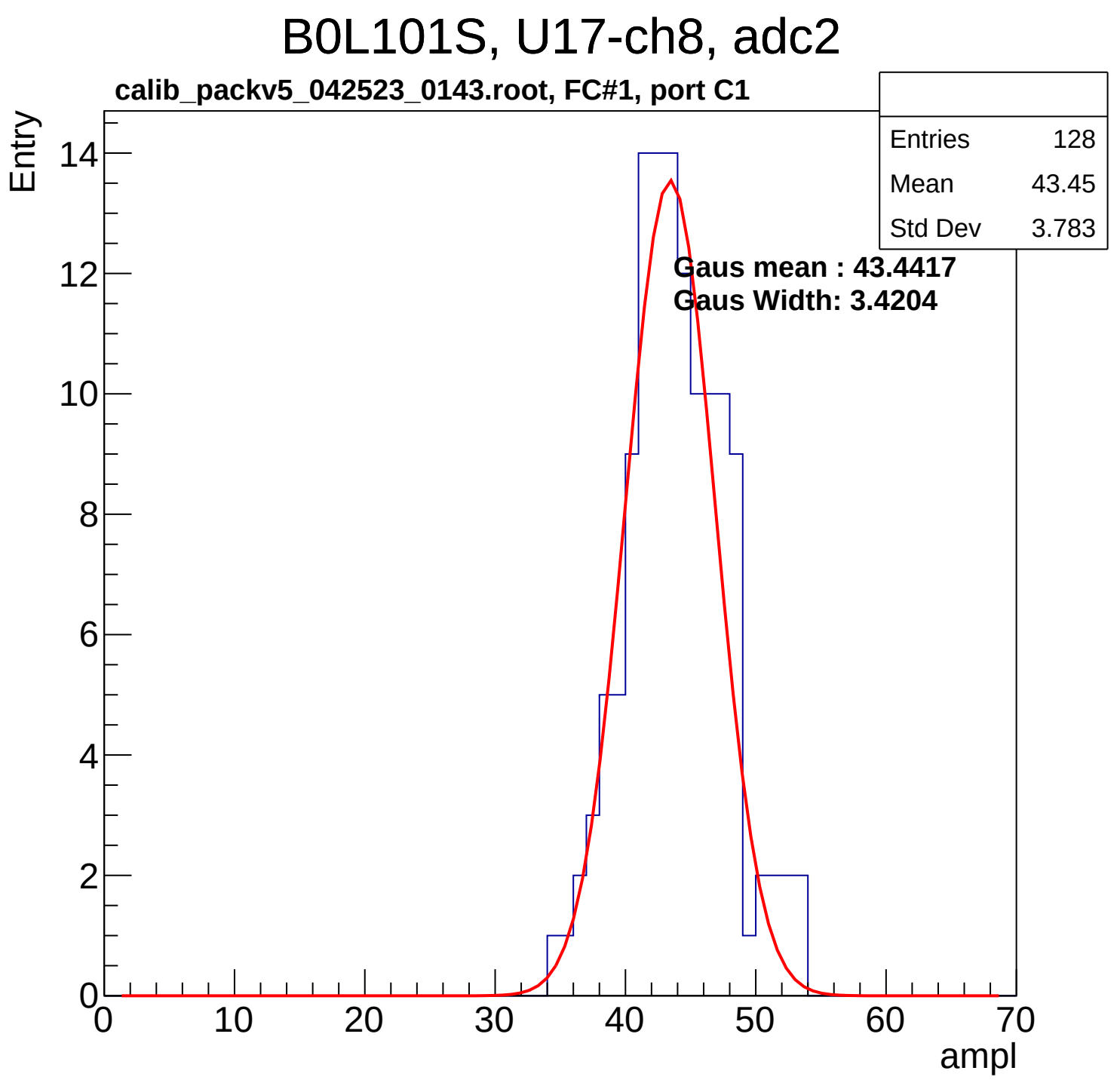

B0L101S, U17-ch7, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 75
Mean 58.68
Std Dev 9.96
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

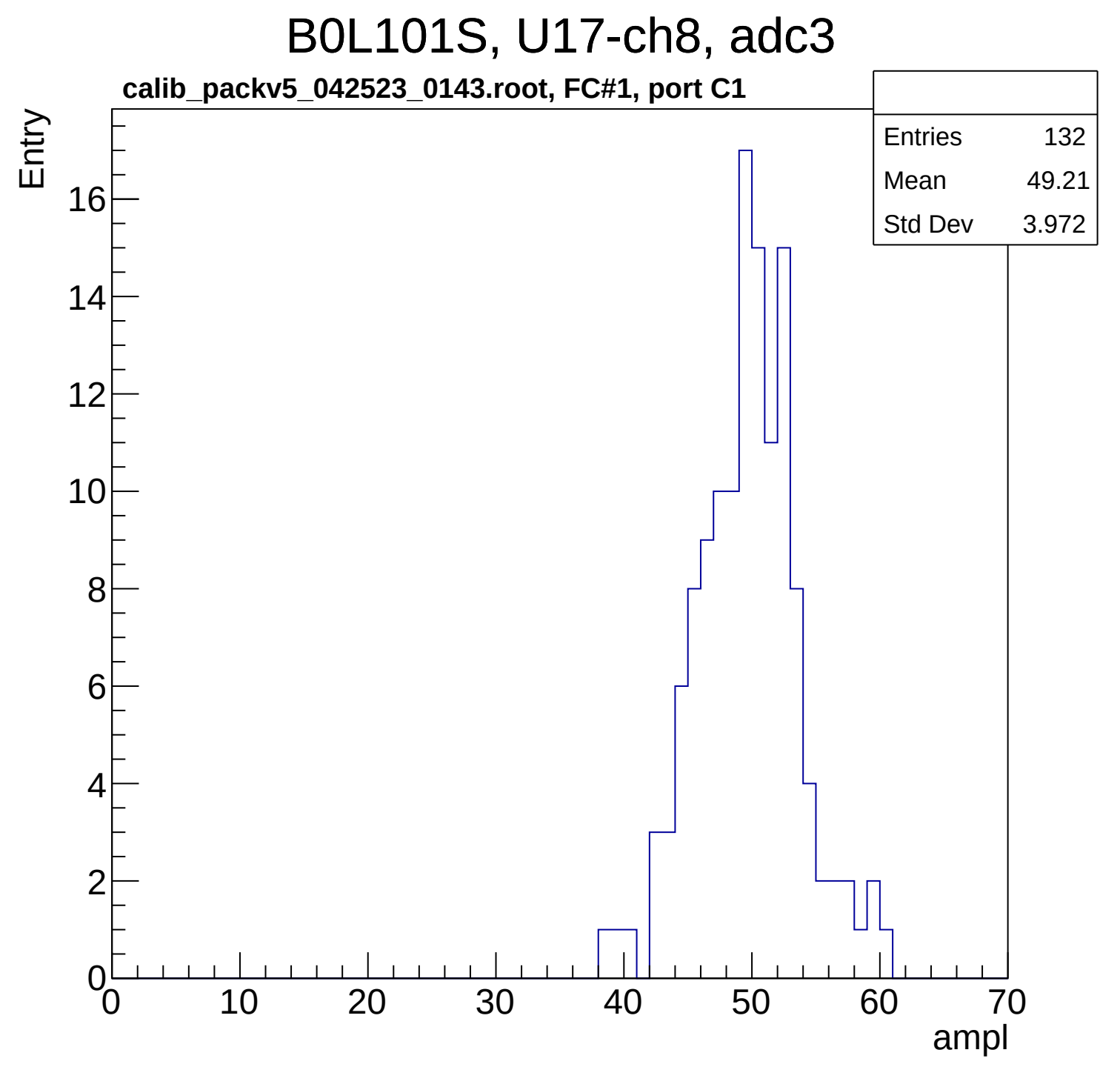

B0L101S, U17-ch7, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 61.73
Std Dev 1.135
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

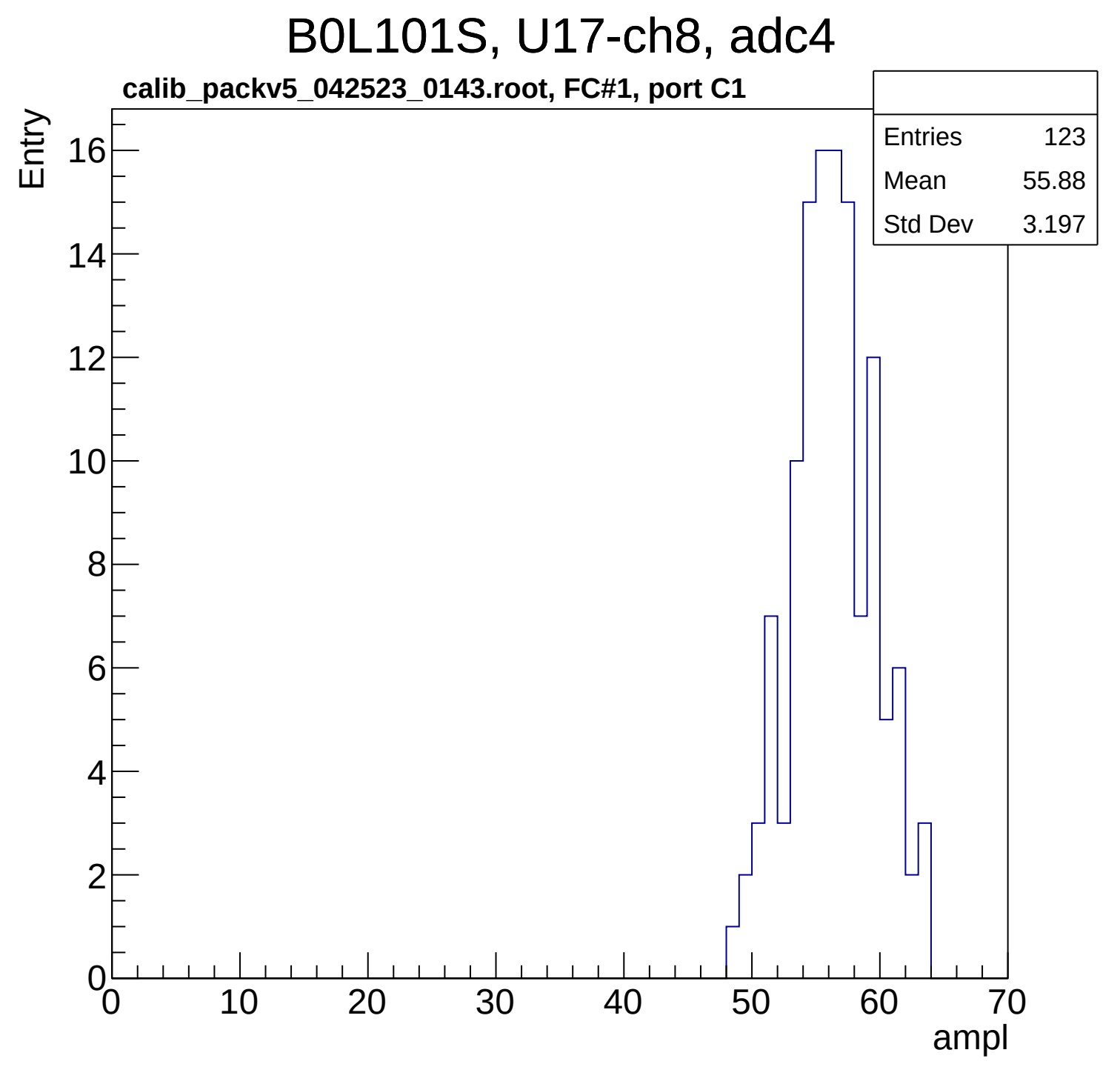

B0L101S, U17-ch7, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

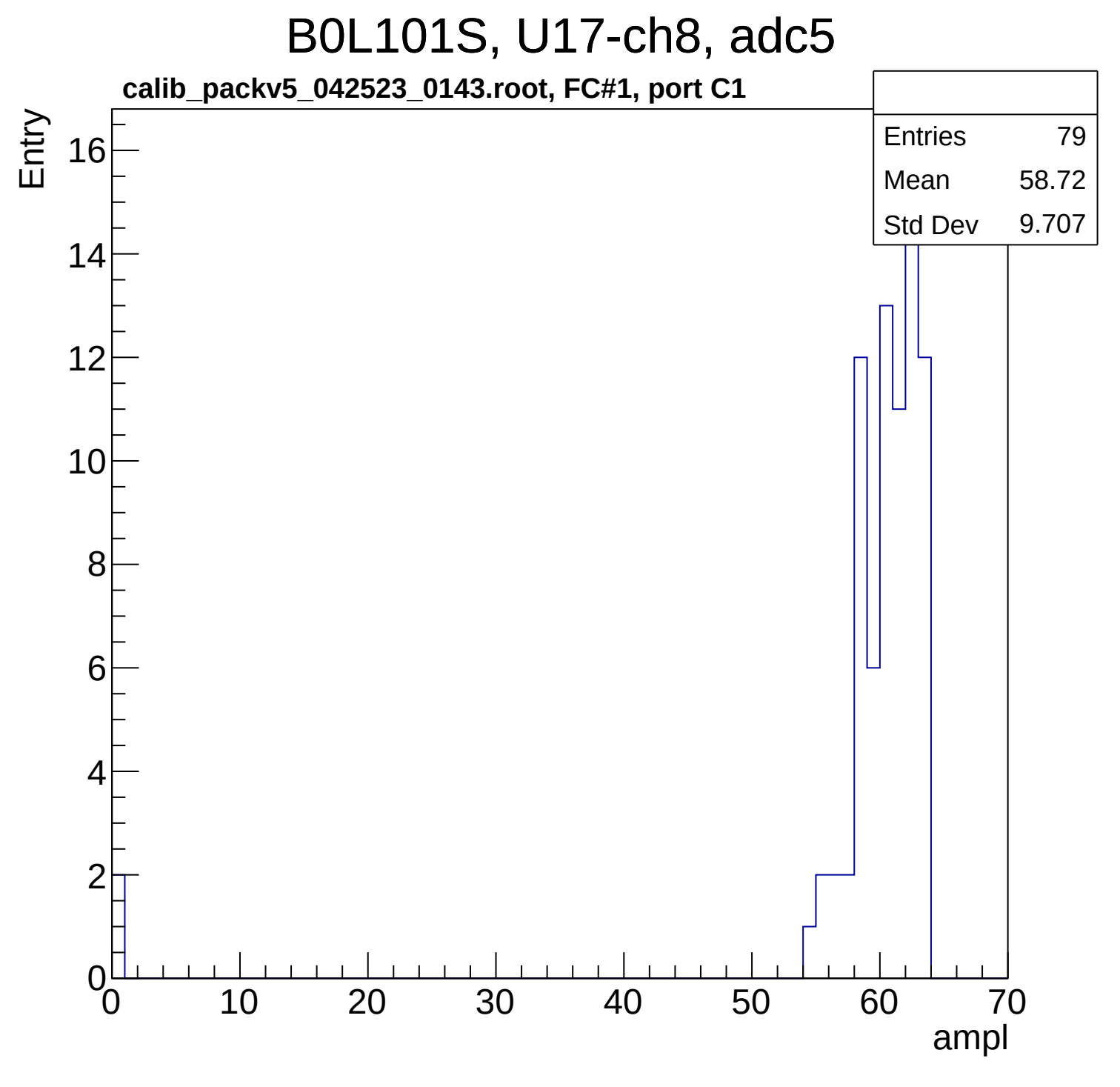

B0L101S, U17-ch8, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 29.79
Std Dev 3.561
Gaus mean : 30.1701
Gaus Width: 3.3211
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

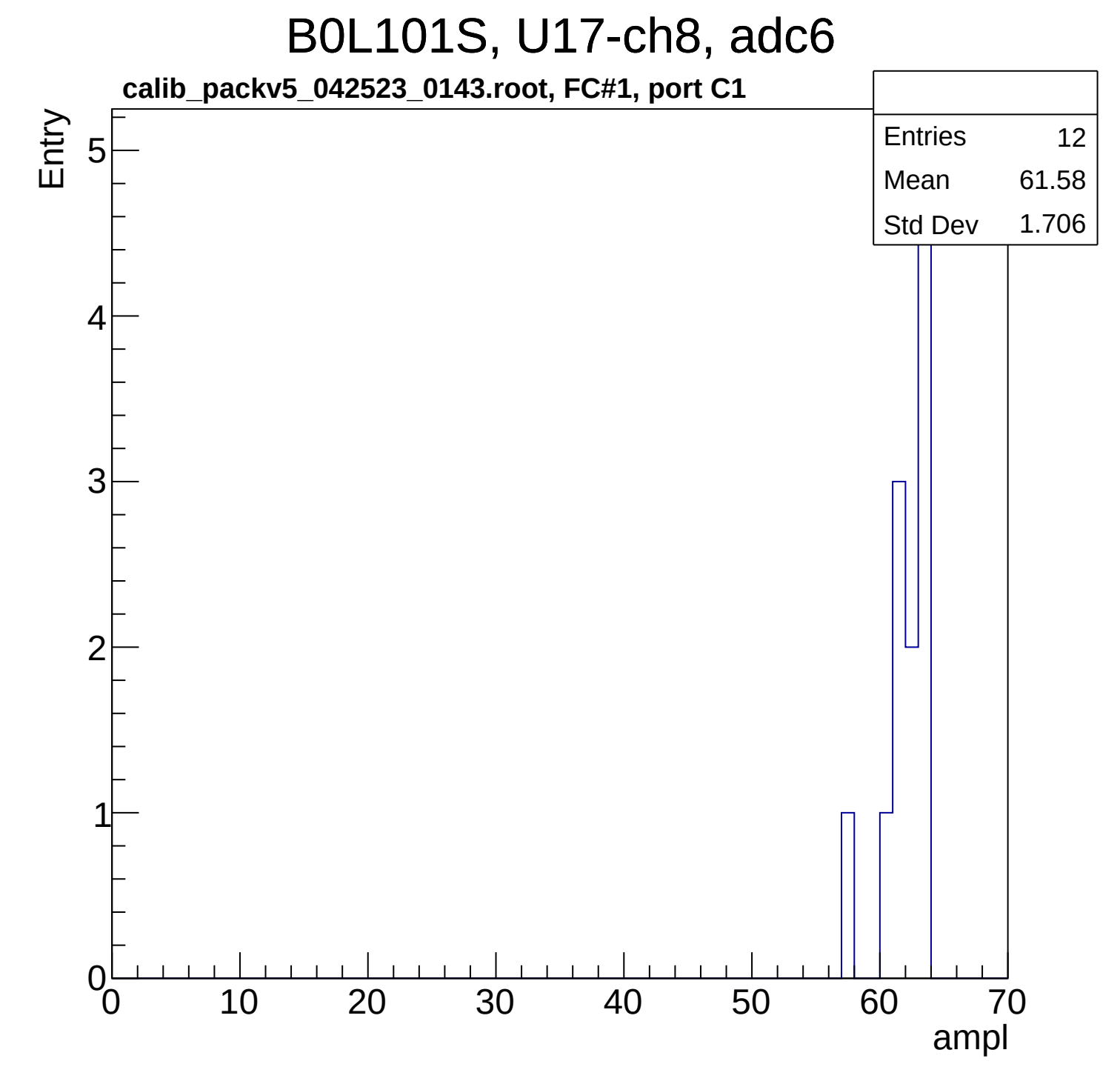

B0L101S, U17-ch8, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 36.73
Std Dev 3.508
Gaus mean : 37.0251
Gaus Width: 3.6892
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

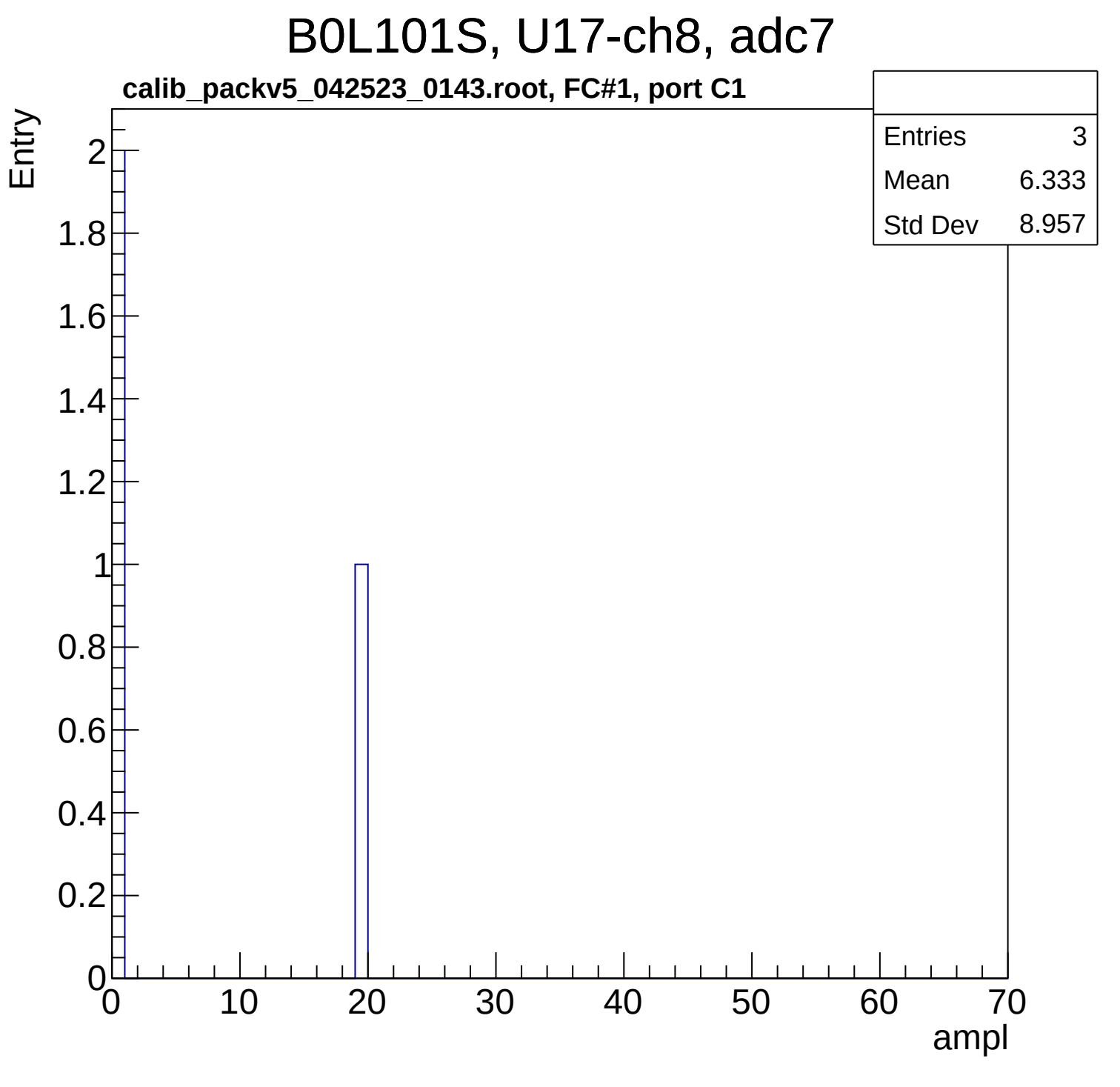

B0L101S, U17-ch8, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 43.45
Std Dev 3.783
Gaus mean : 43.4417
Gaus Width: 3.4204
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

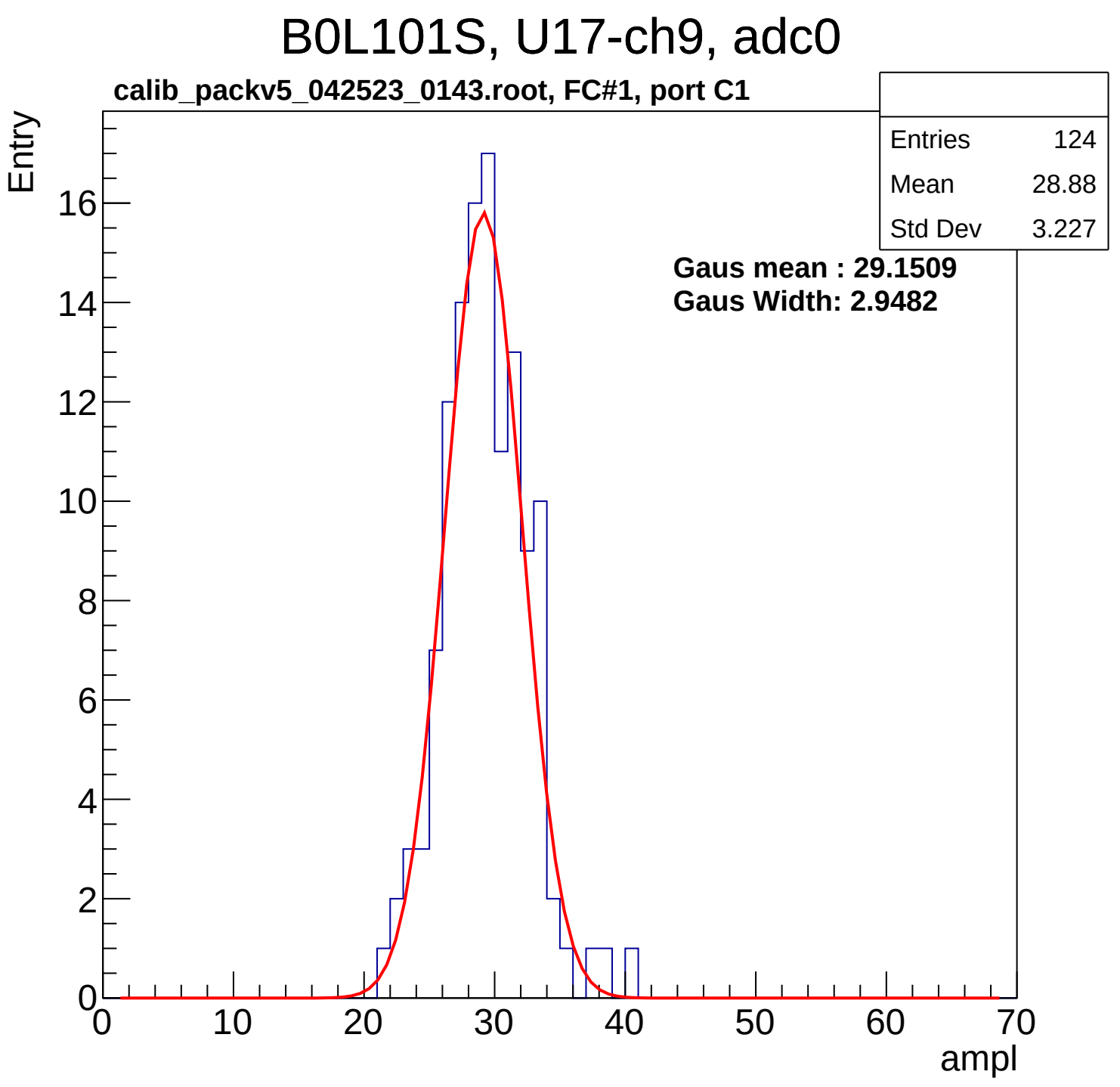

B0L101S, U17-ch8, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 49.21
Std Dev 3.972
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

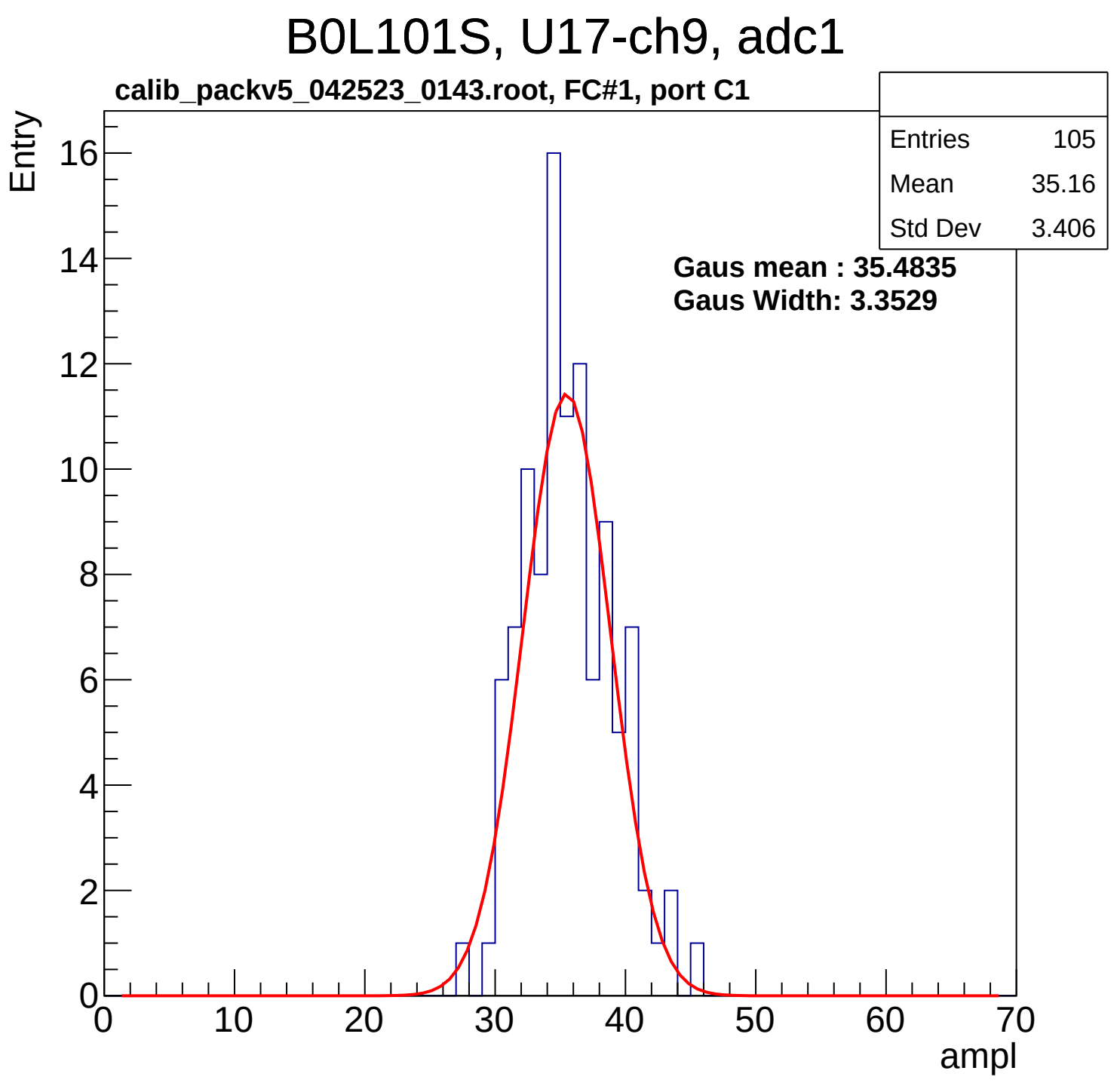

B0L101S, U17-ch8, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 55.88
Std Dev 3.197
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

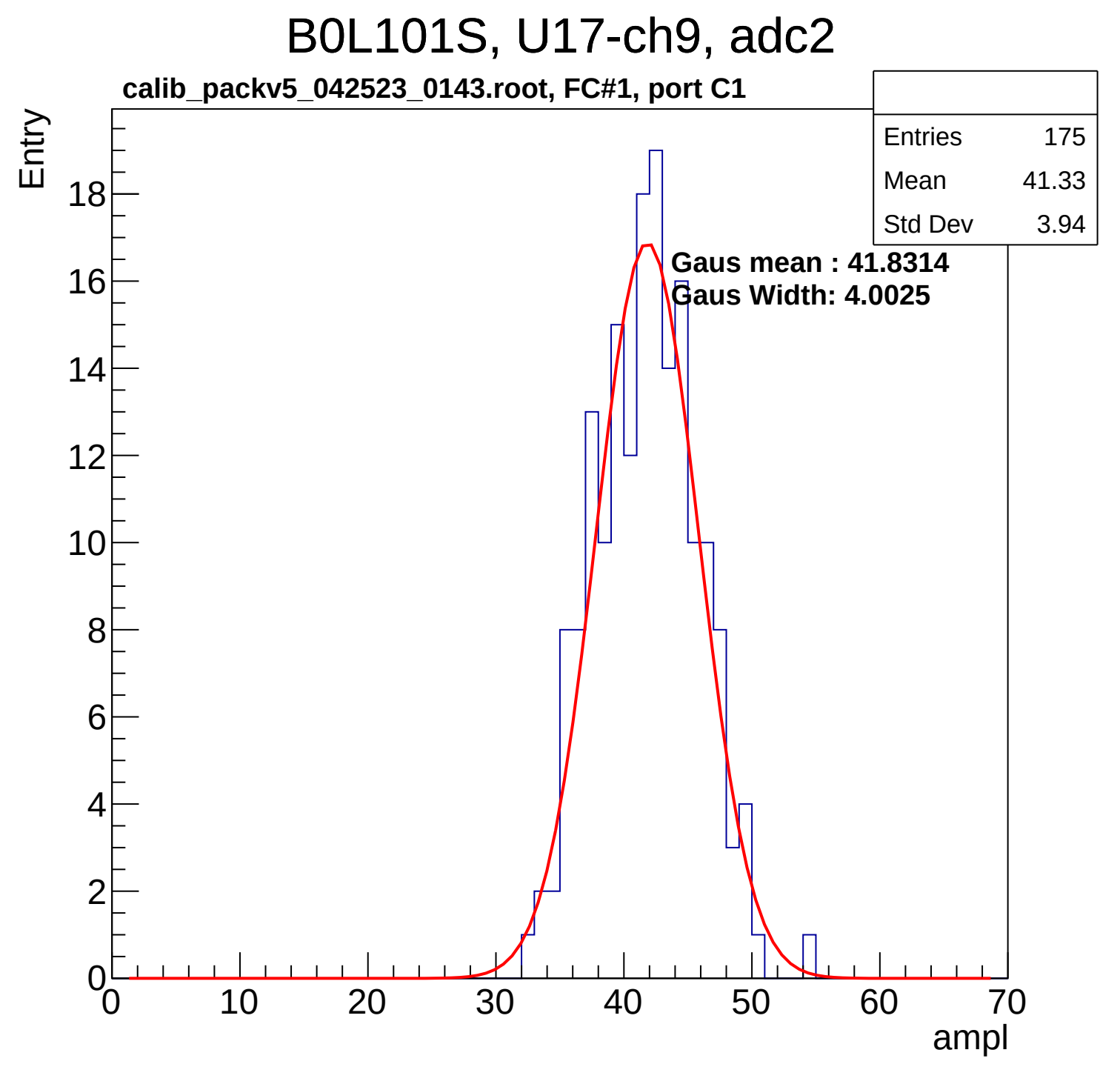

B0L101S, U17-ch8, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 79
Mean 58.72
Std Dev 9.707
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

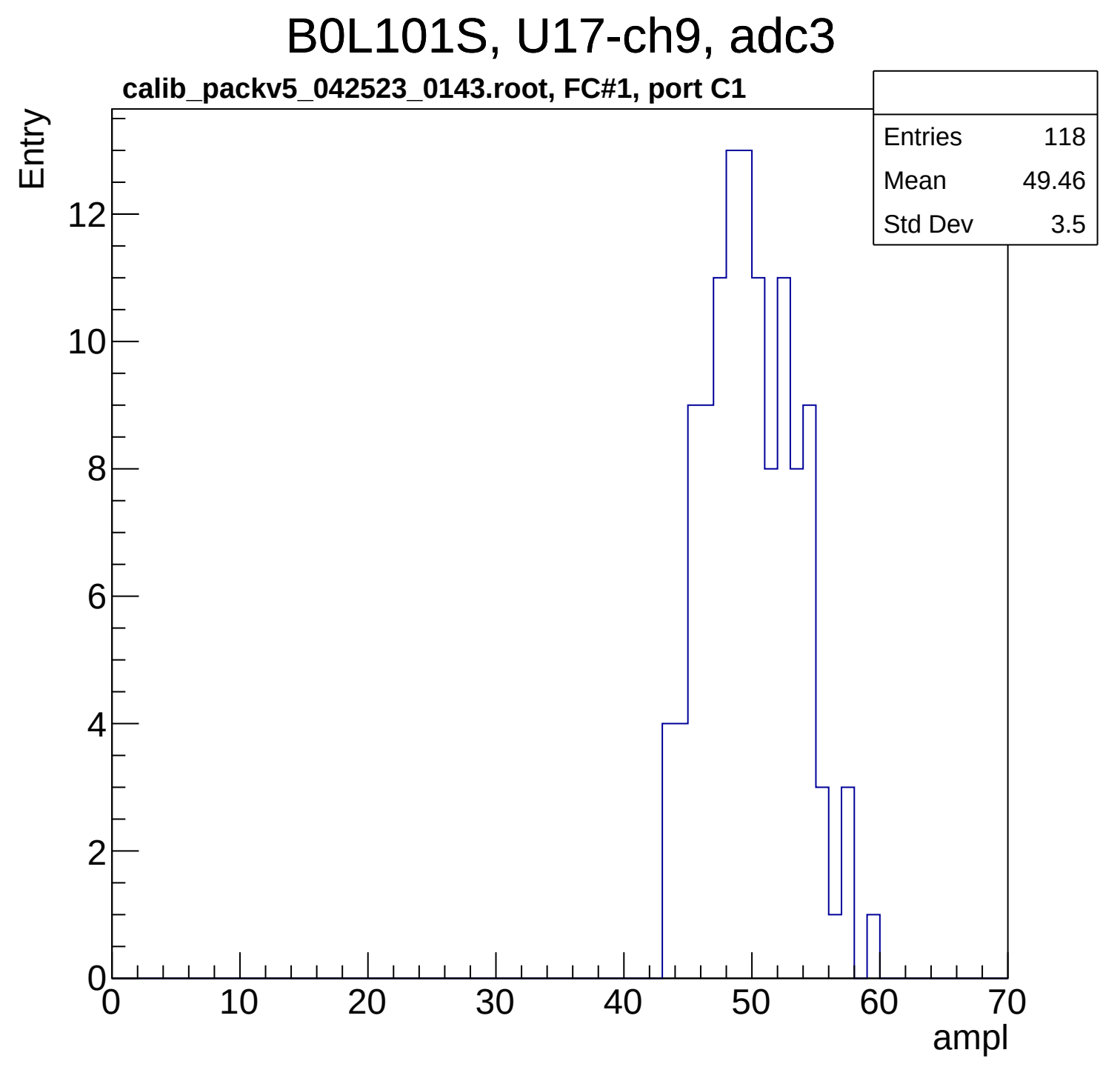

B0L101S, U17-ch8, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.58
Std Dev 1.706
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

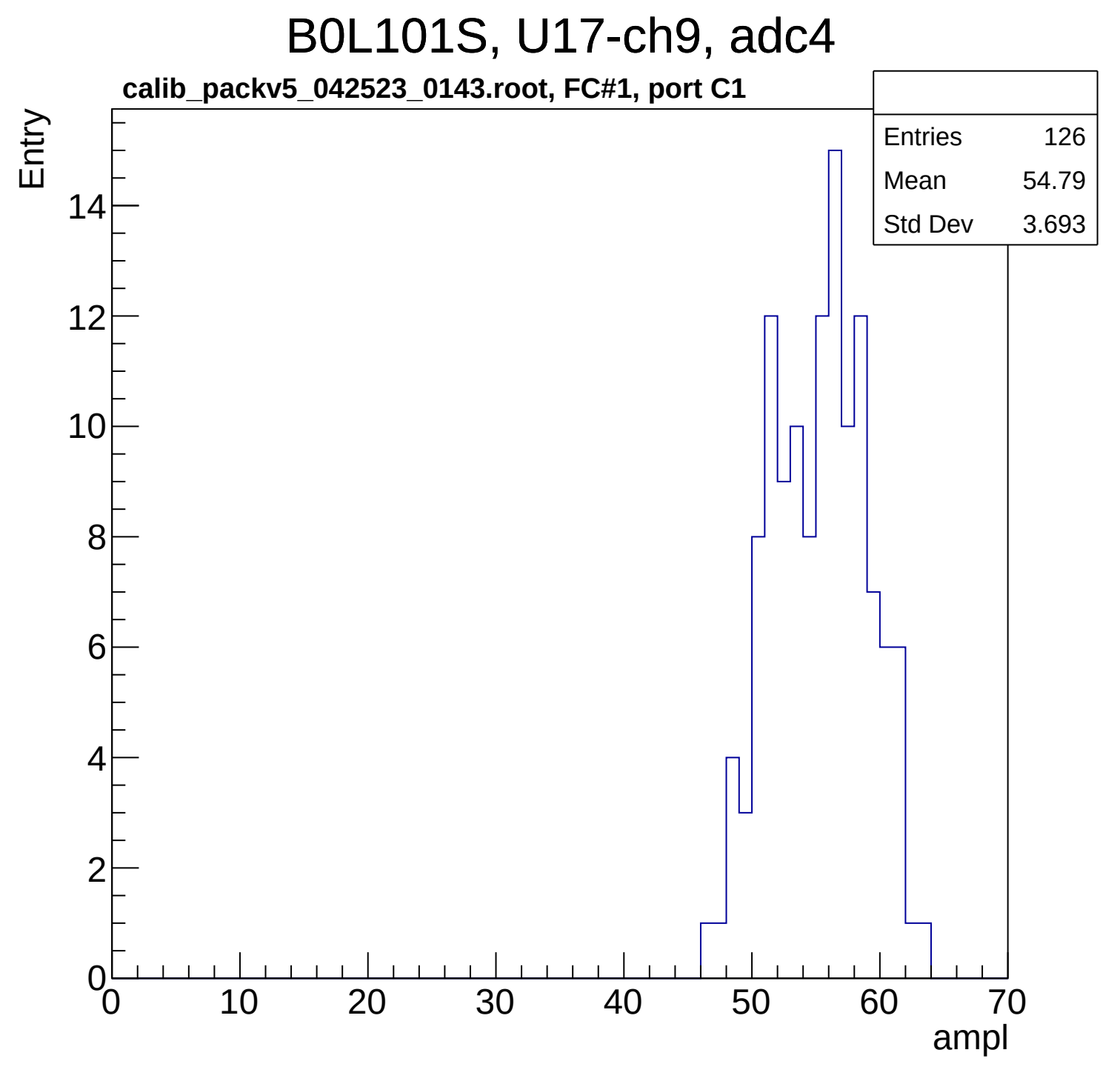

B0L101S, U17-ch8, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

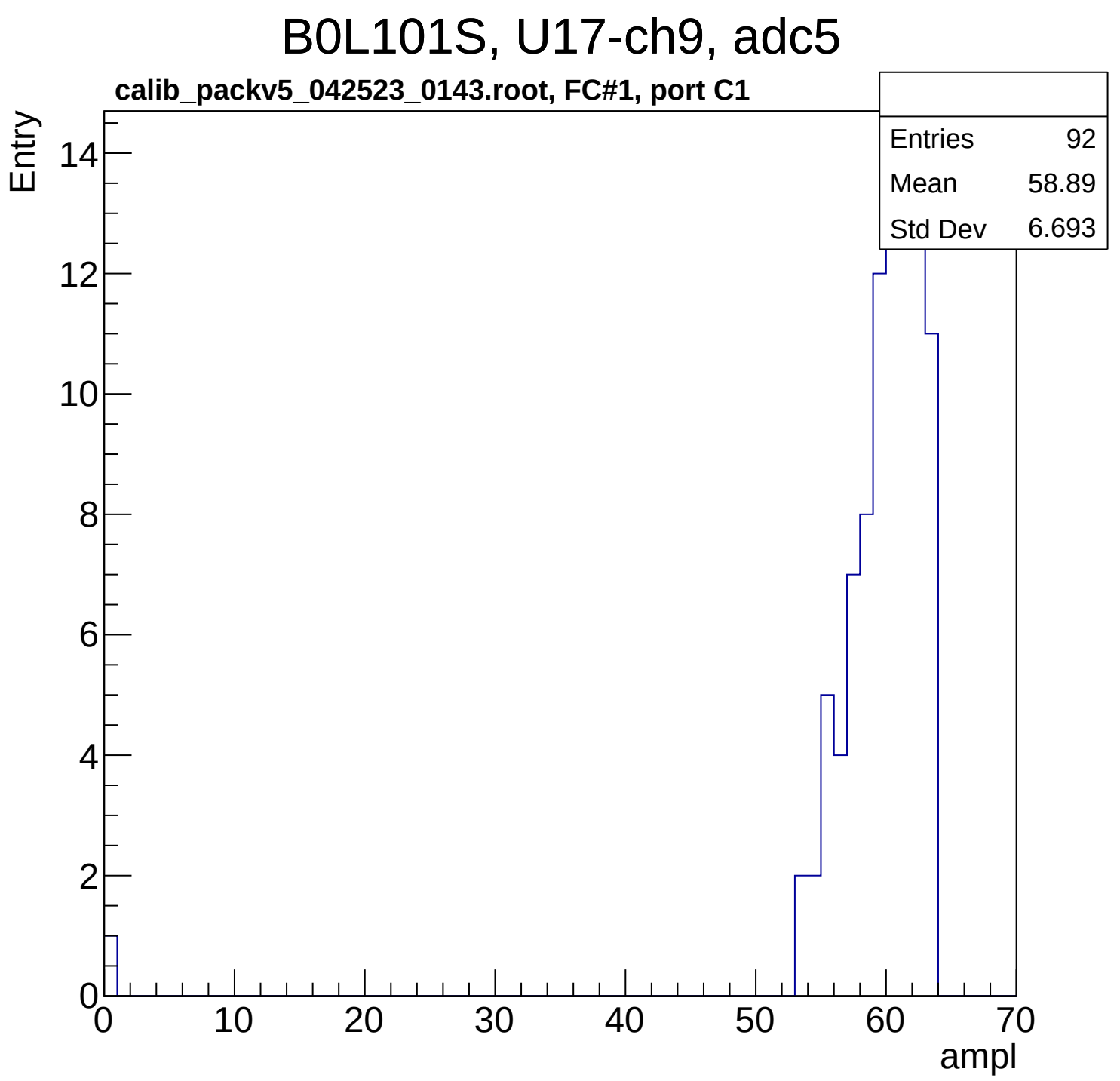

B0L101S, U17-ch9, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 28.88
Std Dev 3.227
Gaus mean : 29.1509
Gaus Width: 2.9482
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

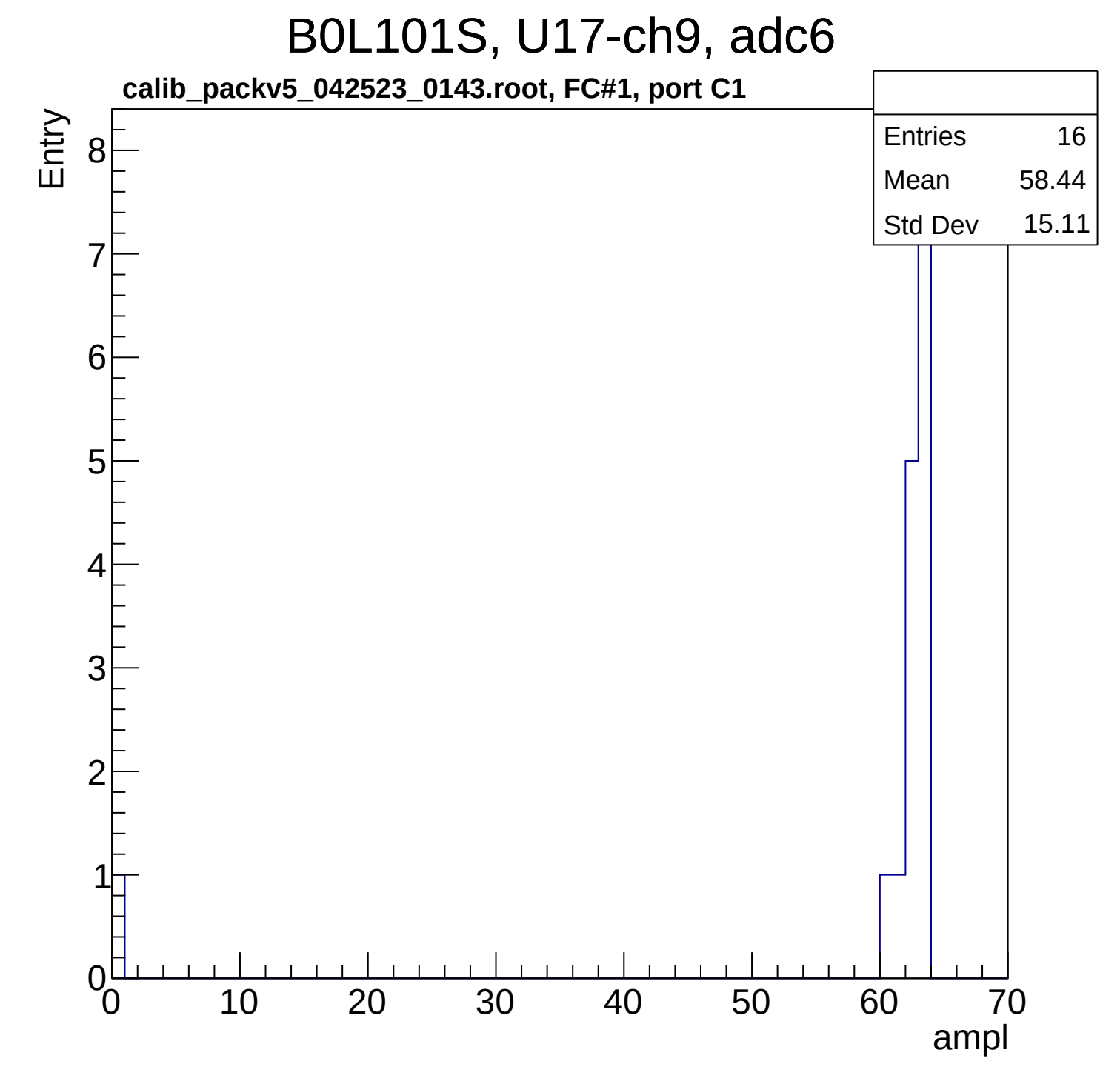

B0L101S, U17-ch9, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 35.16
Std Dev 3.406
Gaus mean : 35.4835
Gaus Width: 3.3529
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

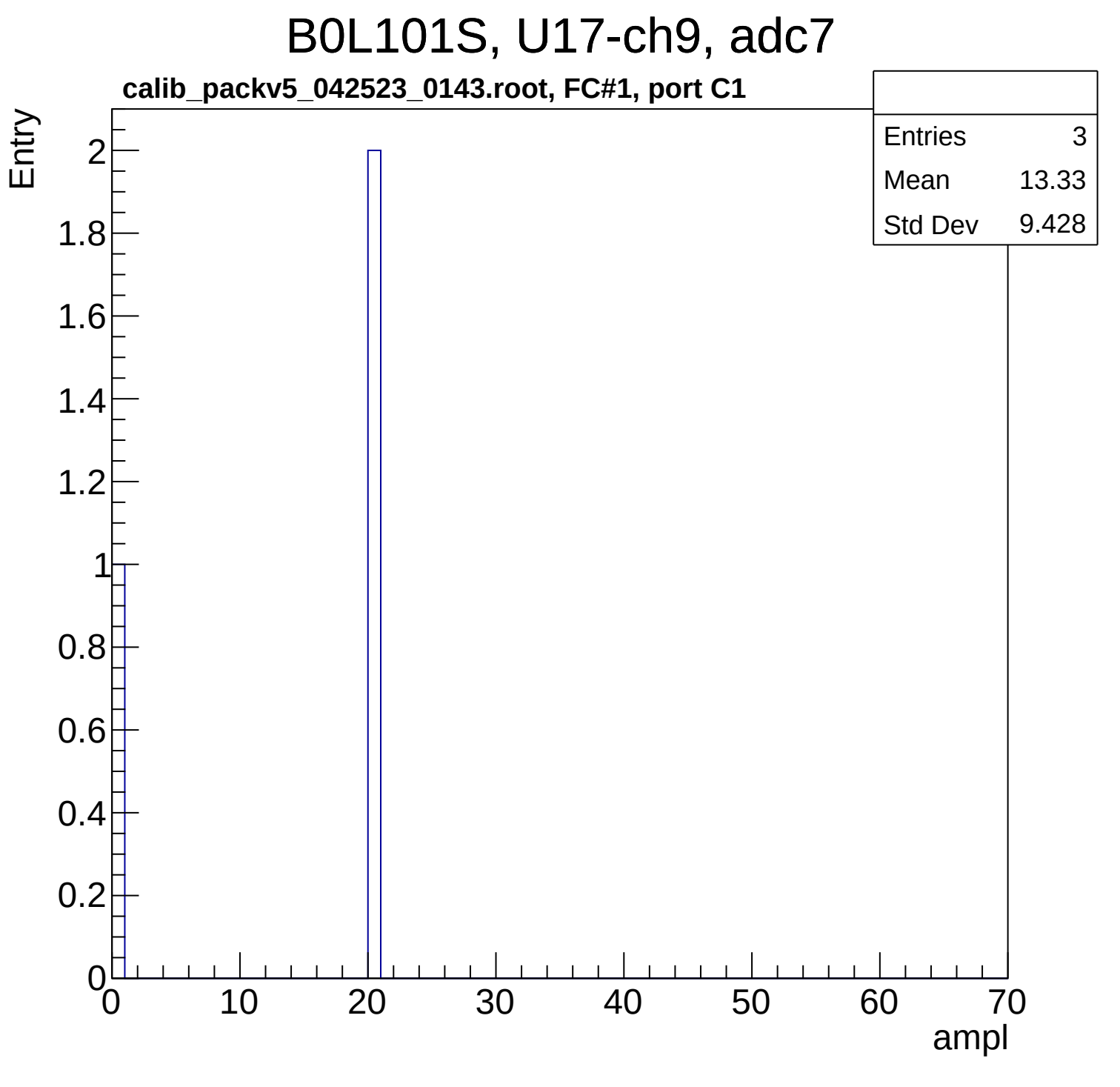

B0L101S, U17-ch9, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 175
Mean 41.33
Std Dev 3.94
Gaus mean : 41.8314
Gaus Width: 4.0025
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

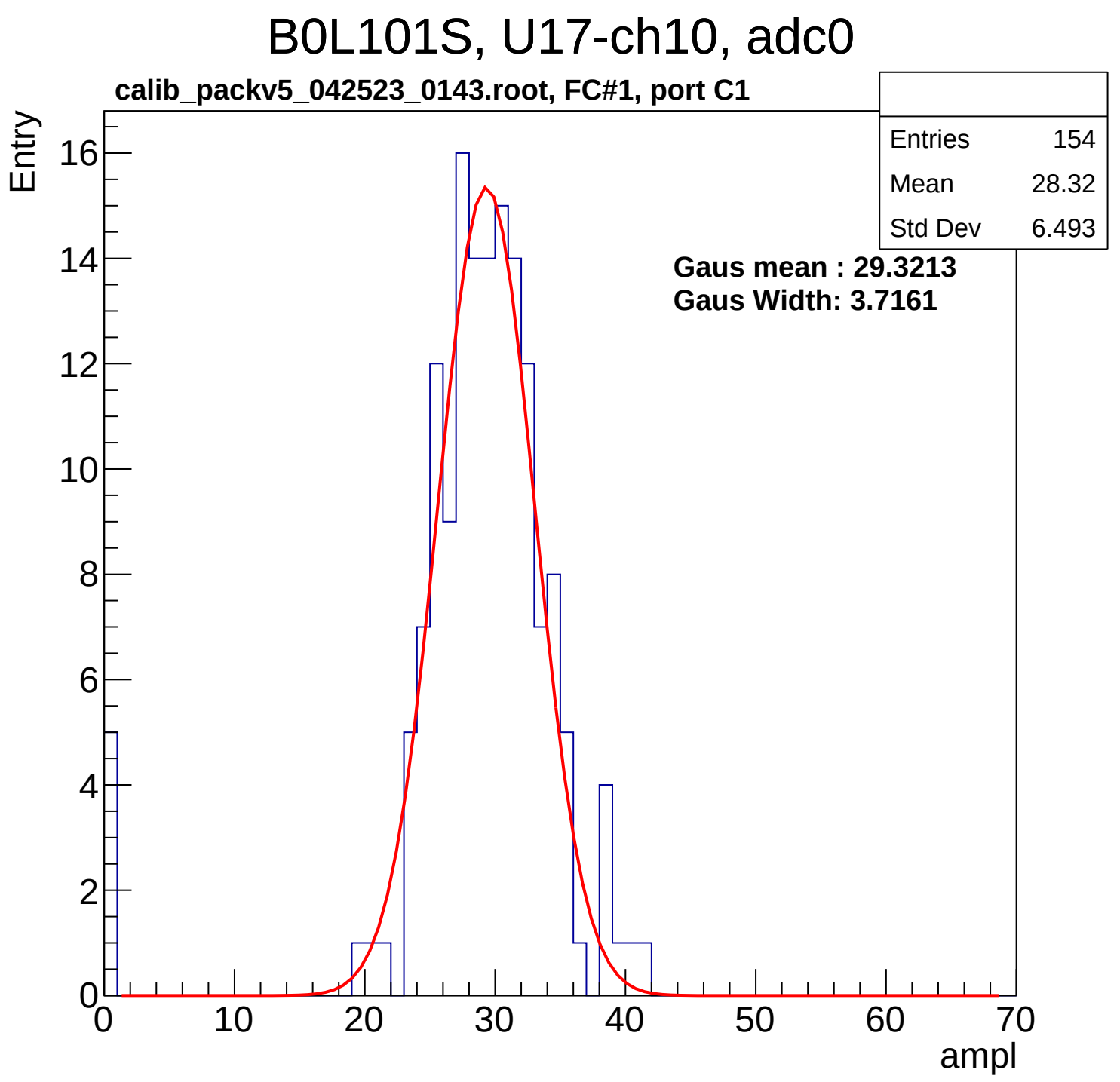

B0L101S, U17-ch9, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 49.46
Std Dev 3.5
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

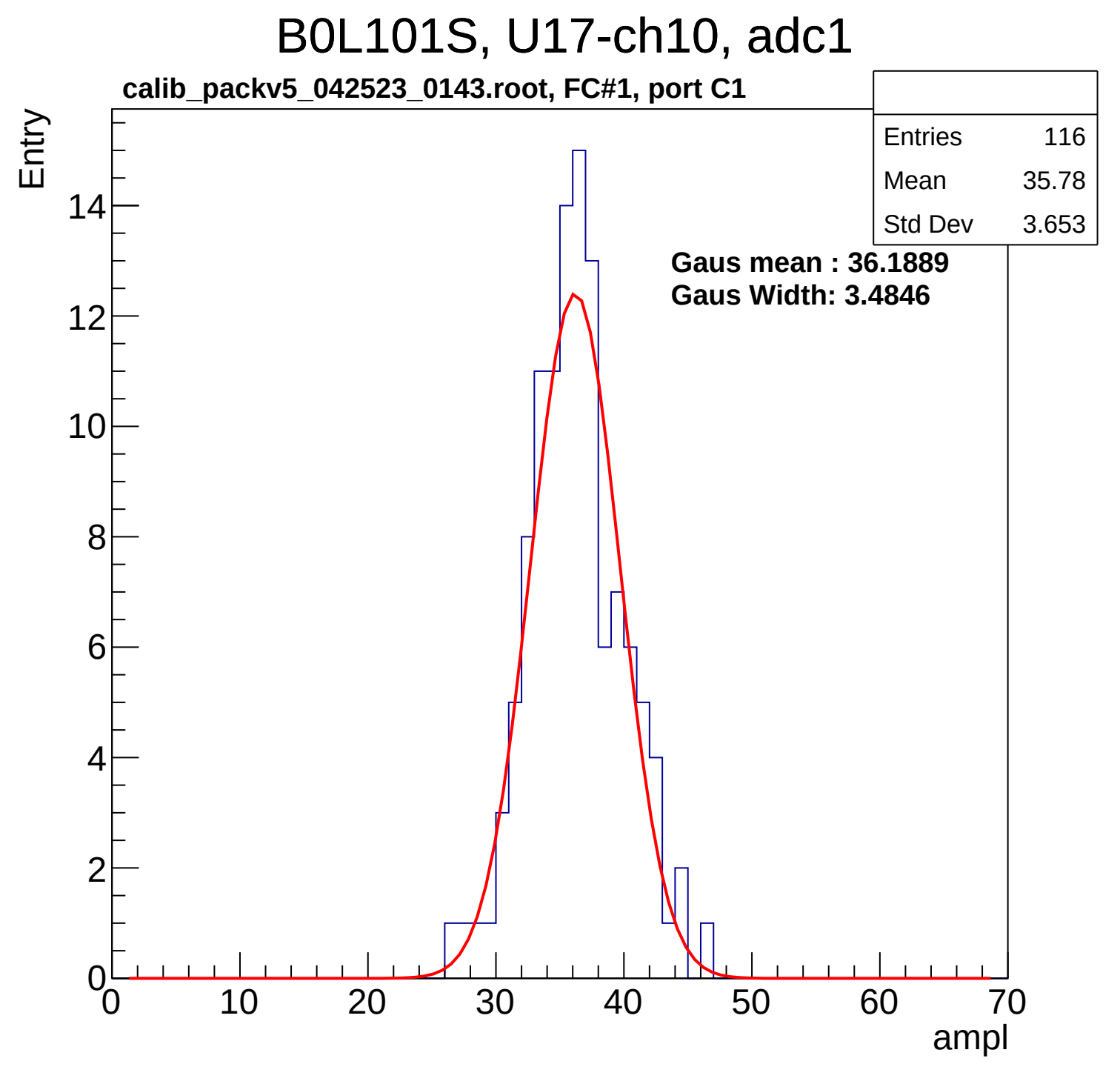

B0L101S, U17-ch9, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 54.79
Std Dev 3.693
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

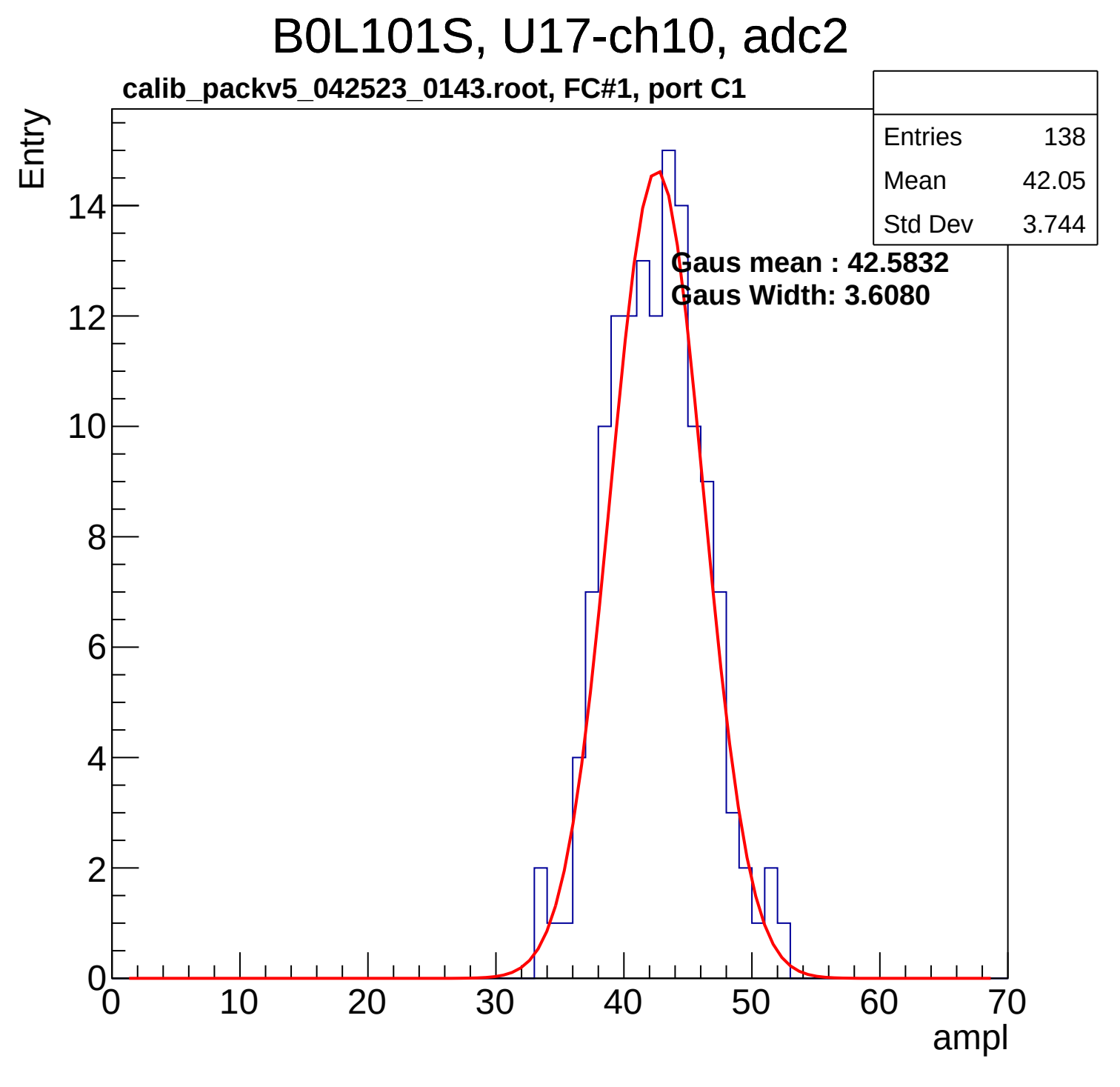

B0L101S, U17-ch9, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 92
Mean 58.89
Std Dev 6.693
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

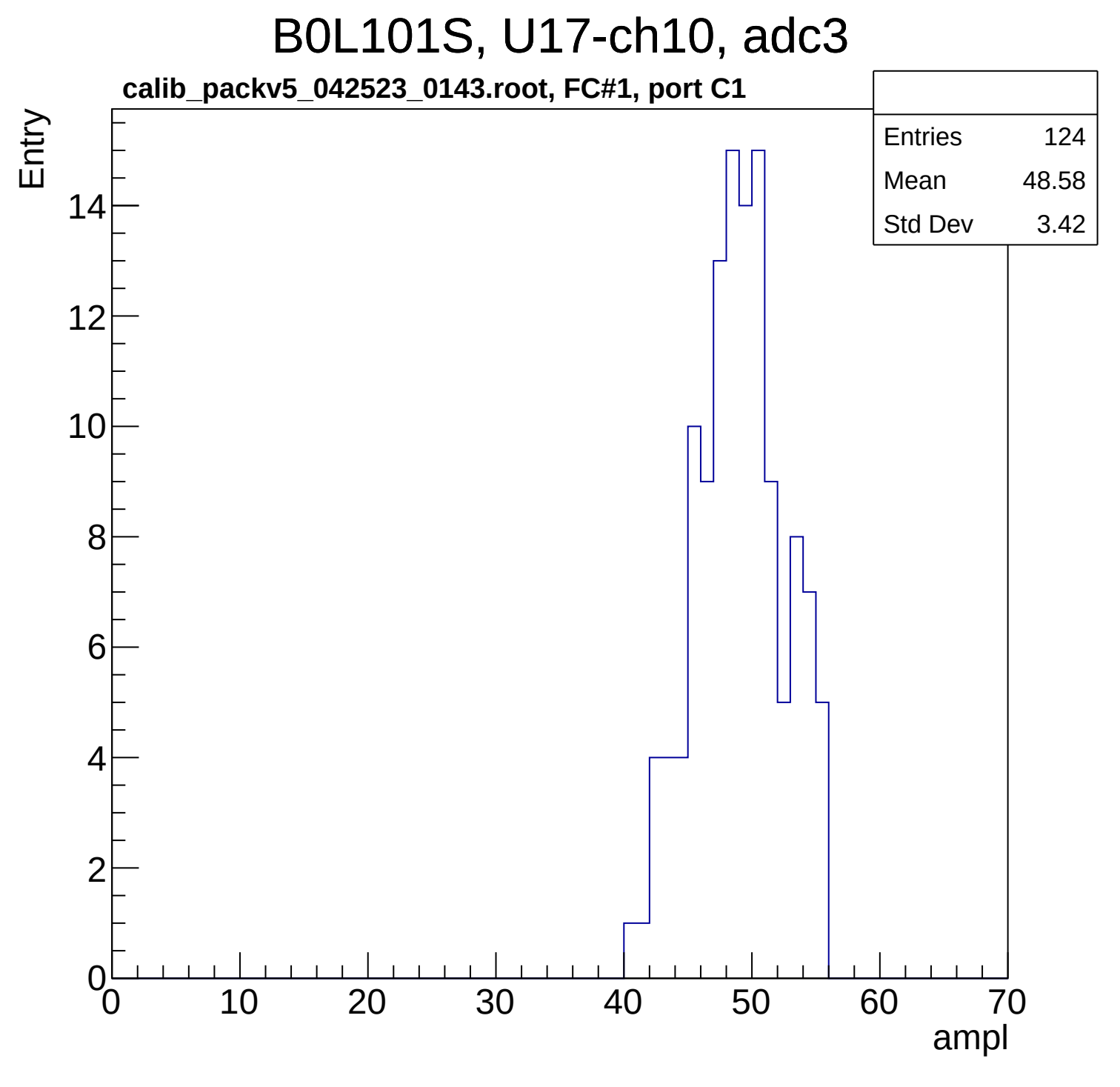

B0L101S, U17-ch9, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 58.44
Std Dev 15.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

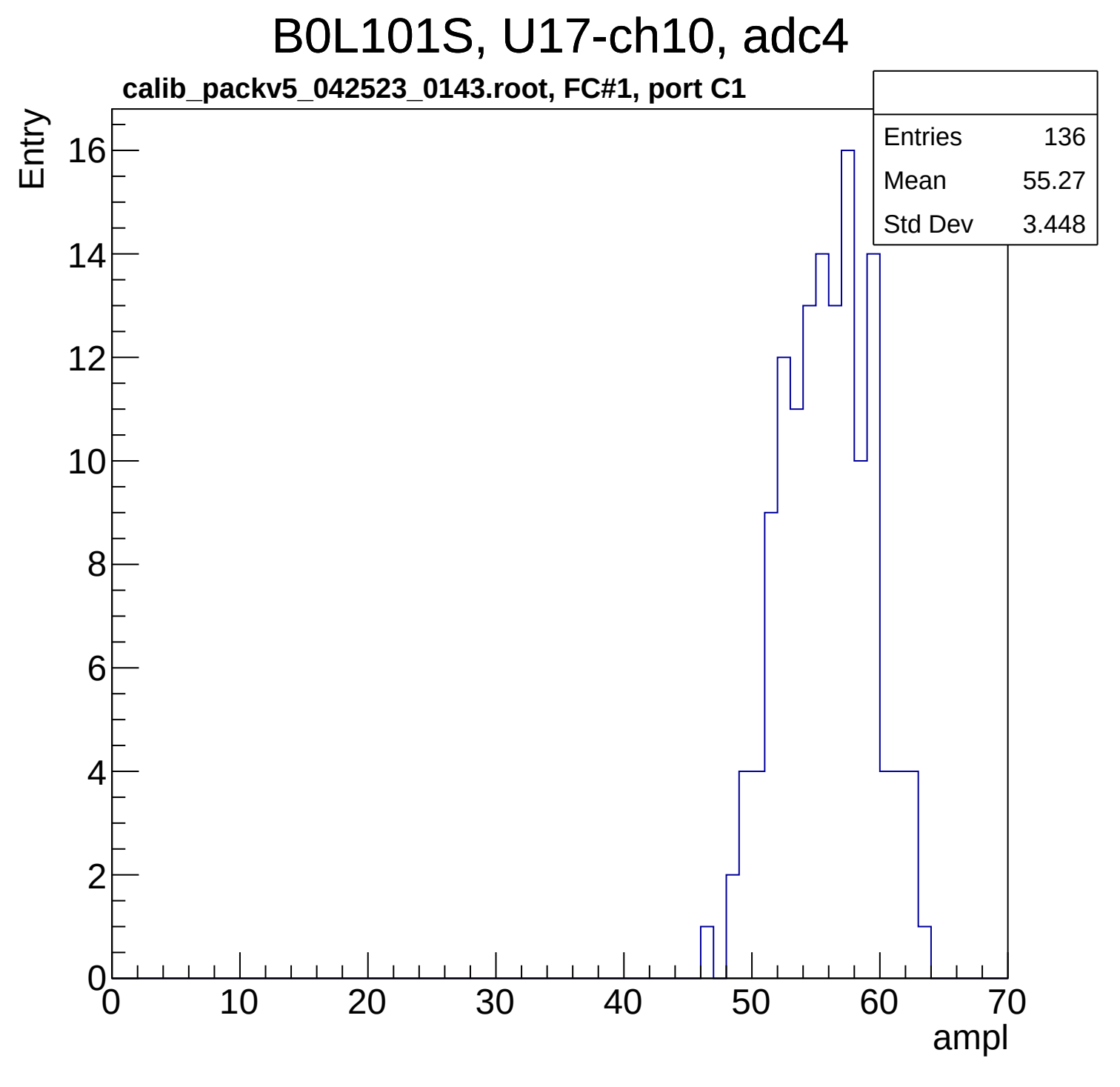

B0L101S, U17-ch9, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 13.33
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

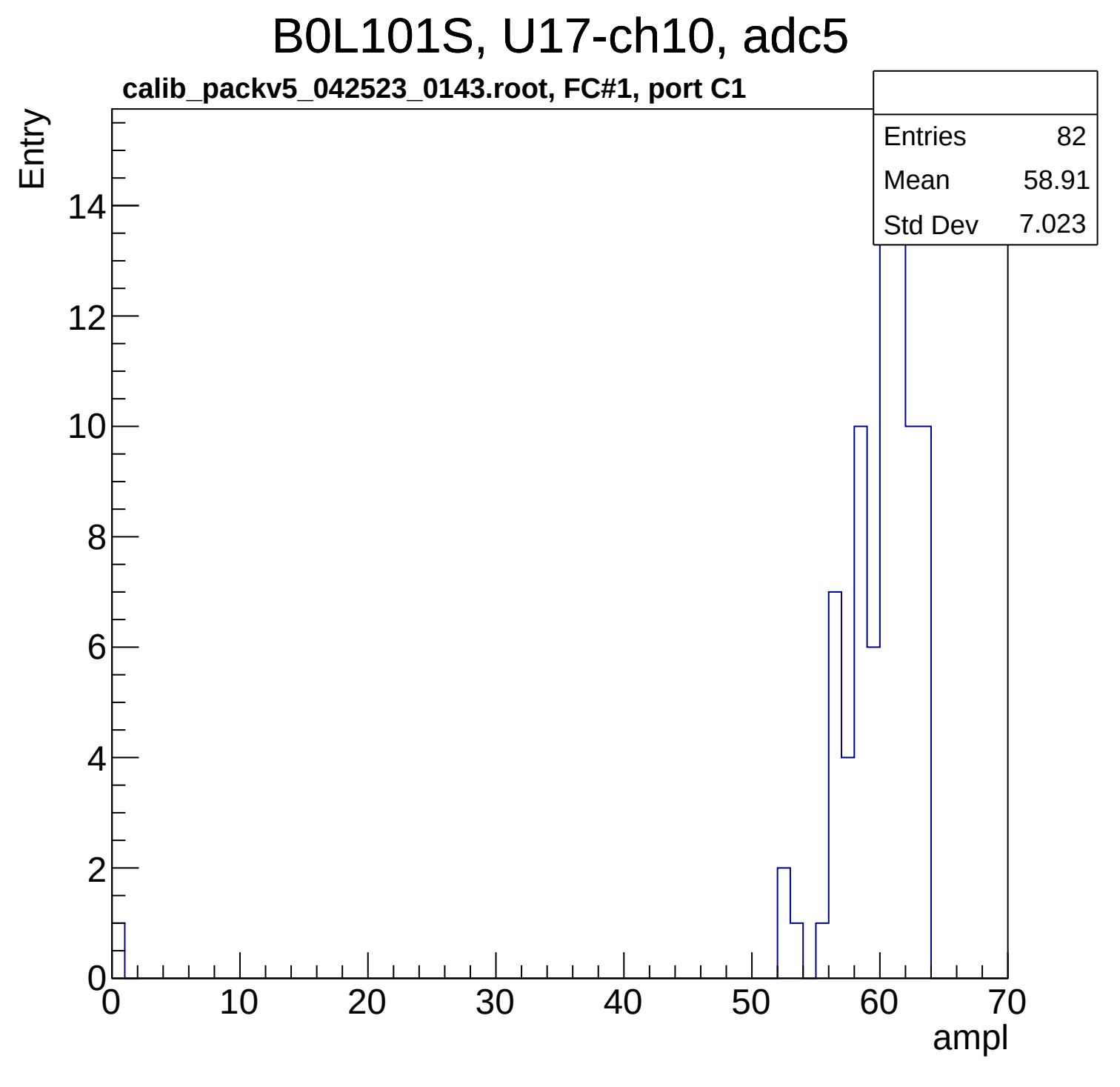

B0L101S, U17-ch10, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 28.32
Std Dev 6.493
Gaus mean : 29.3213
Gaus Width: 3.7161
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

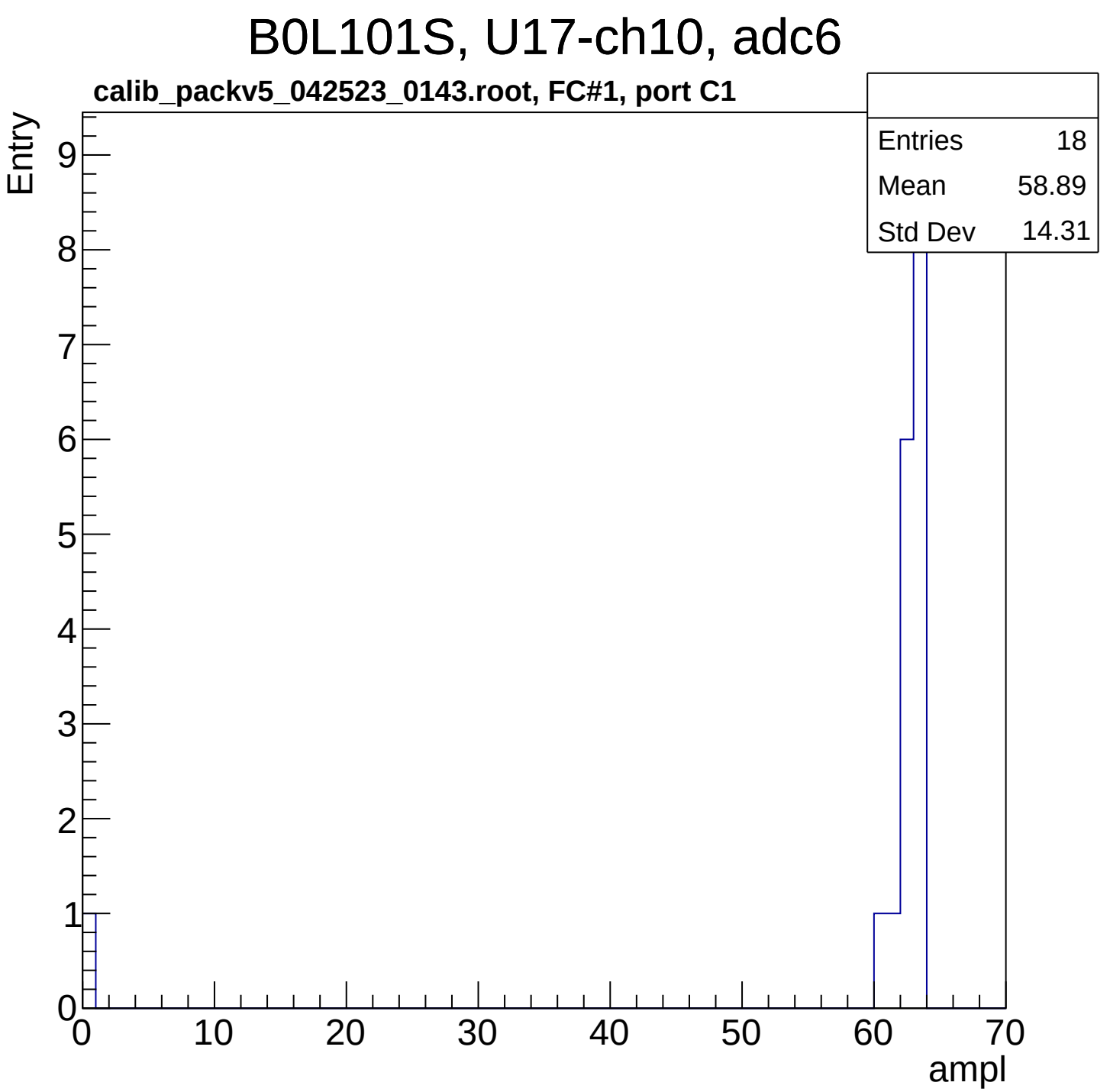

B0L101S, U17-ch10, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 35.78
Std Dev 3.653
Gaus mean : 36.1889
Gaus Width: 3.4846
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

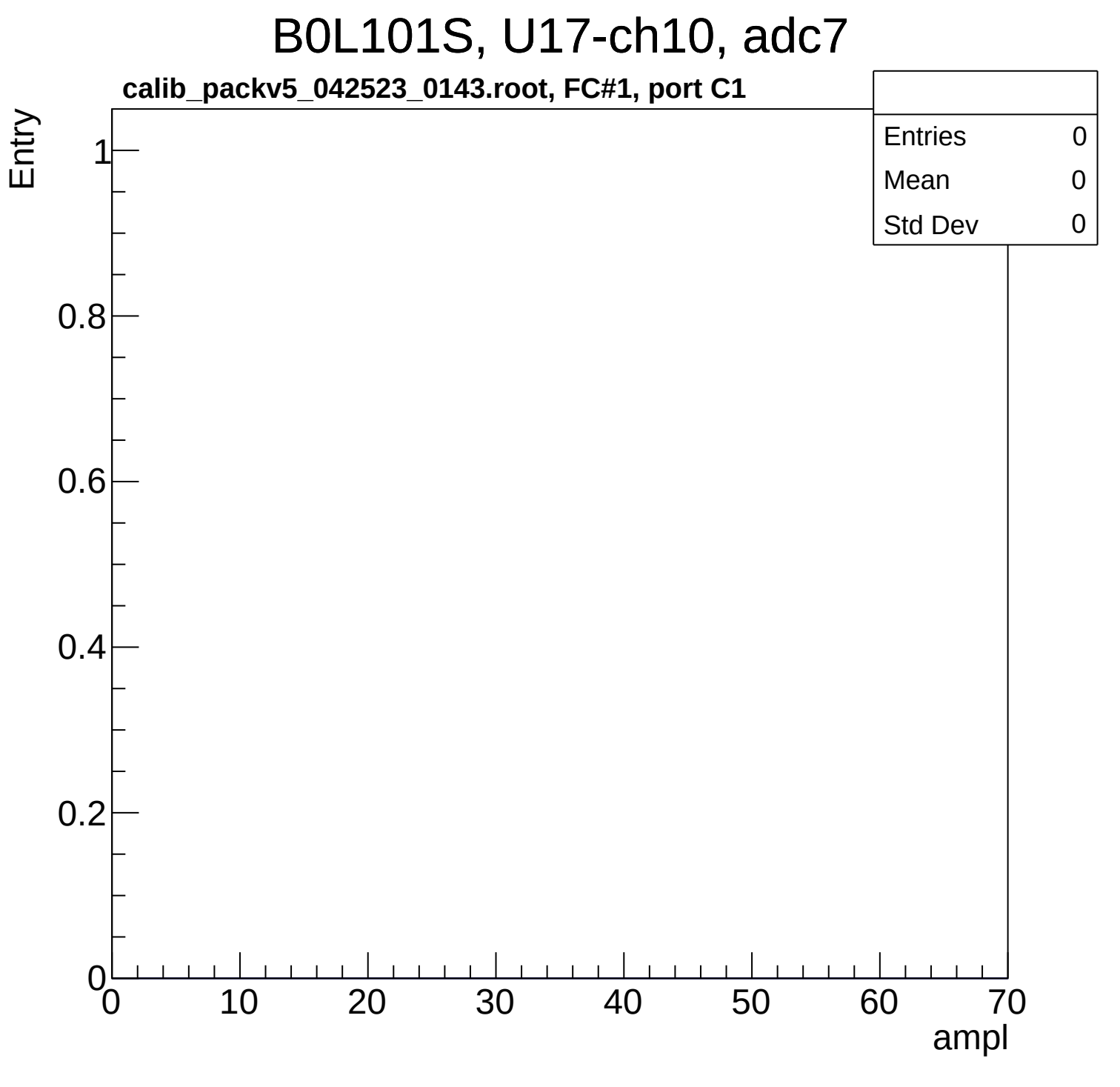

B0L101S, U17-ch10, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 42.05
Std Dev 3.744
Gaus mean : 42.5832
Gaus Width: 3.6080
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

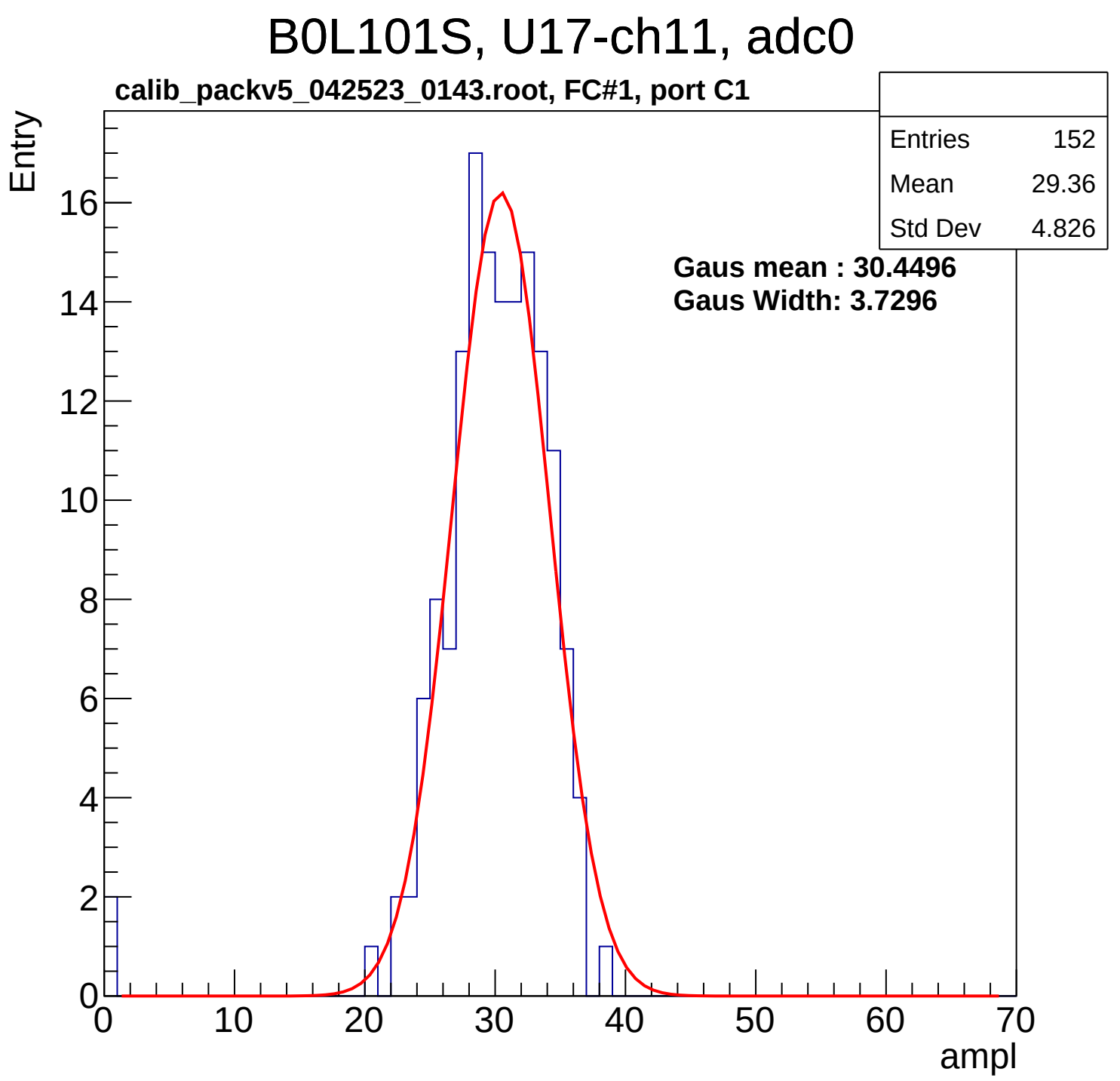

B0L101S, U17-ch10, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 48.58
Std Dev 3.42
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

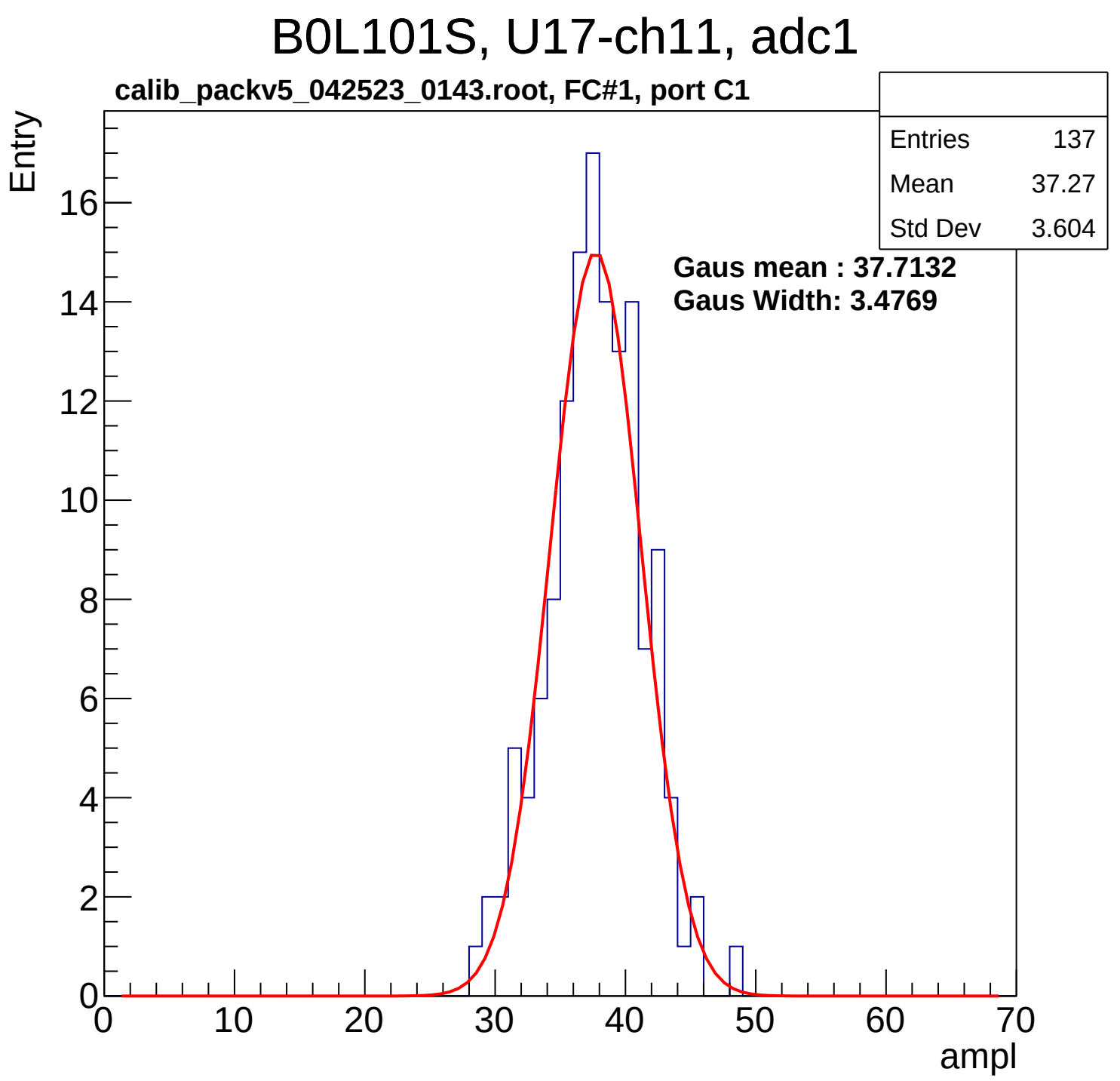

B0L101S, U17-ch10, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 55.27
Std Dev 3.448
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

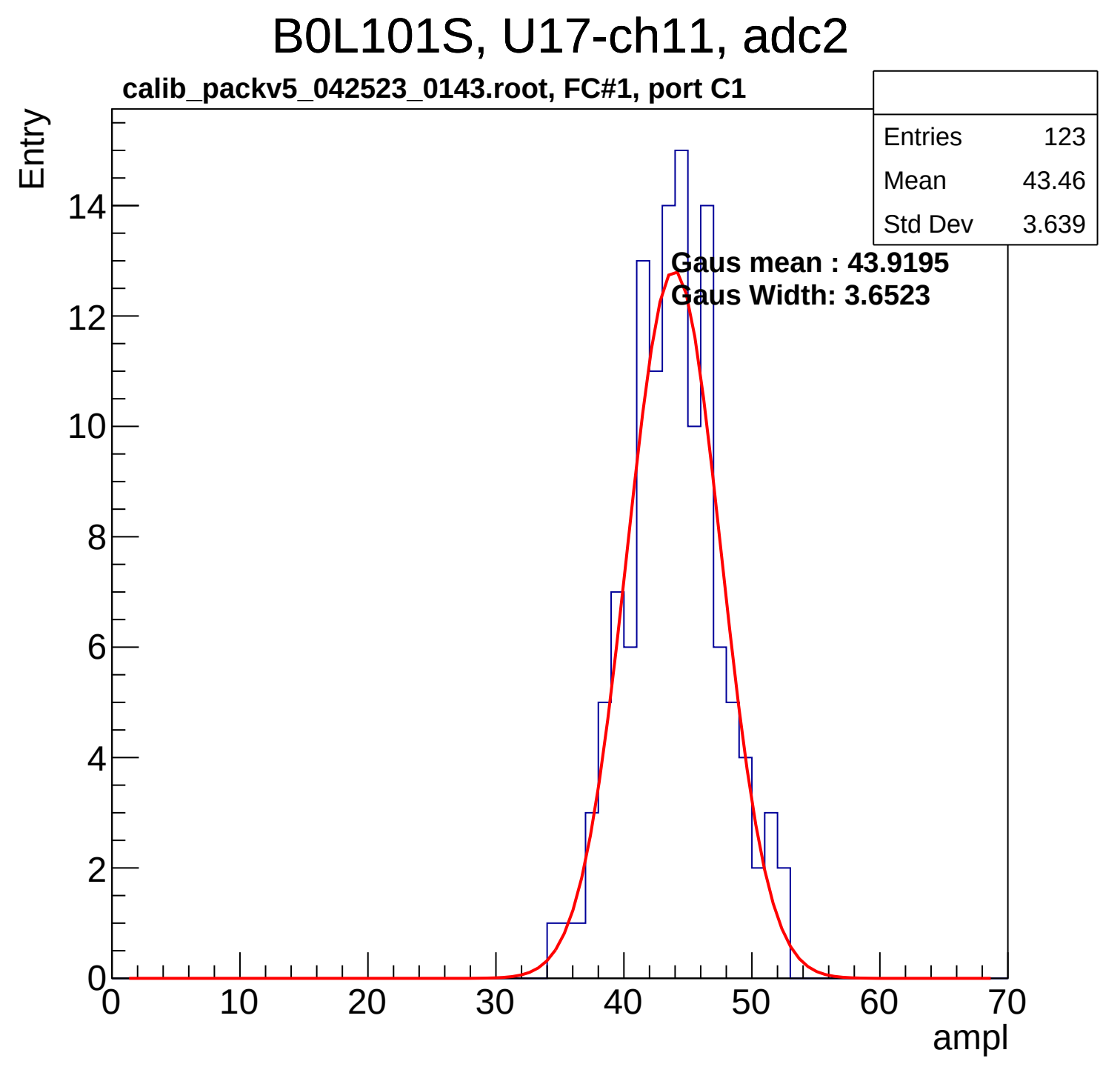

B0L101S, U17-ch10, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 82
Mean 58.91
Std Dev 7.023
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

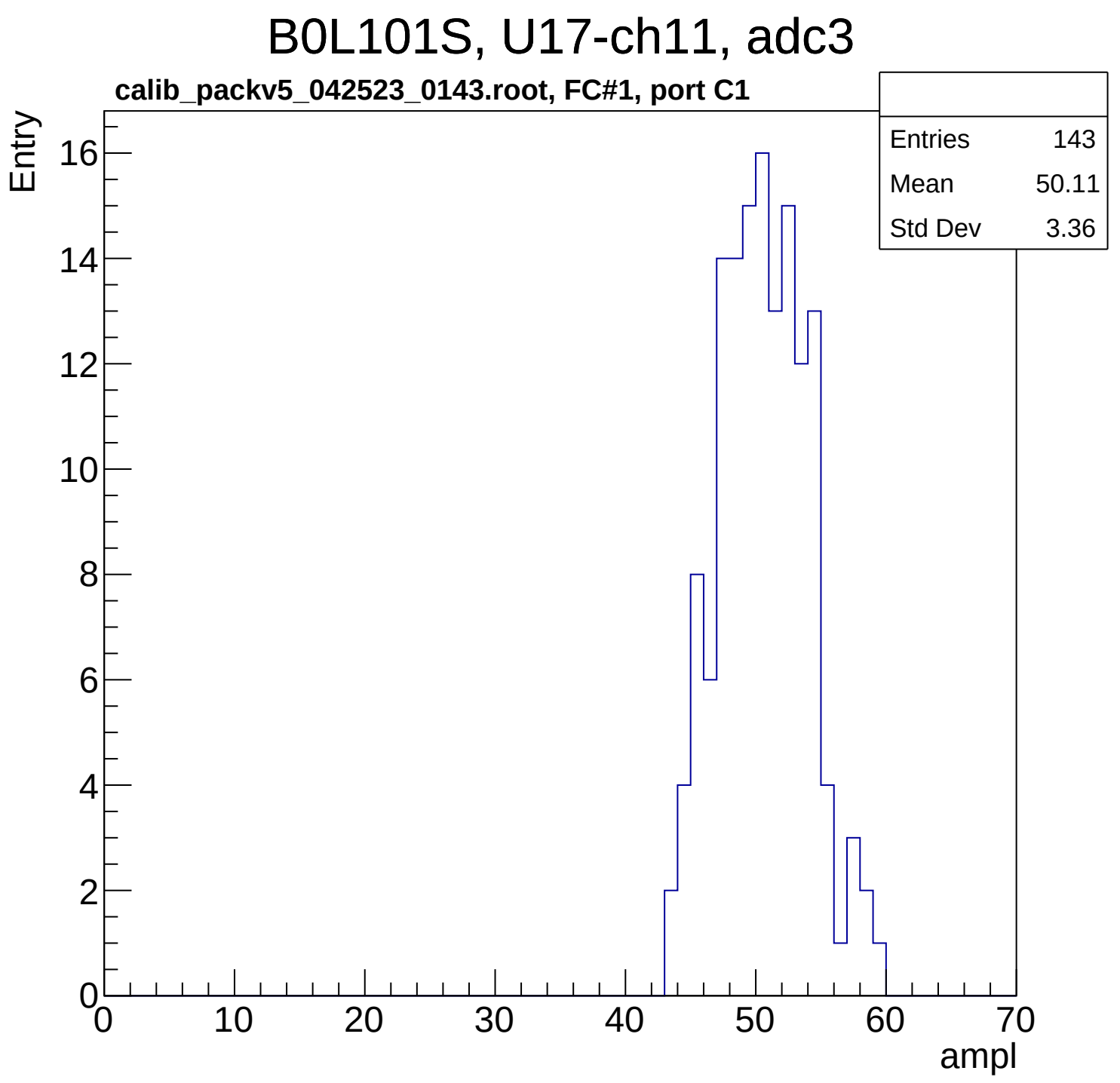

B0L101S, U17-ch10, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 18
Mean 58.89
Std Dev 14.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

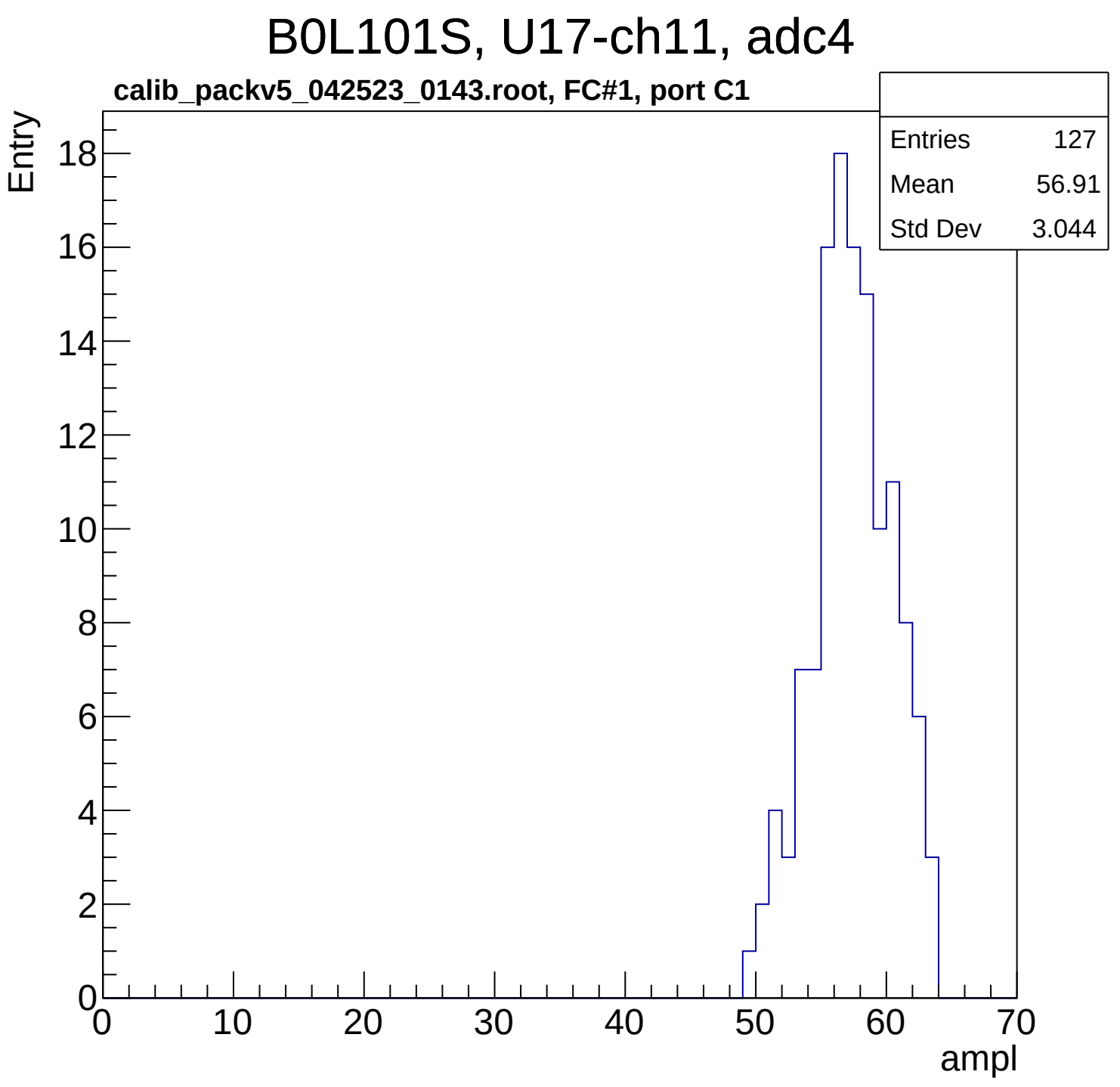

B0L101S, U17-ch10, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

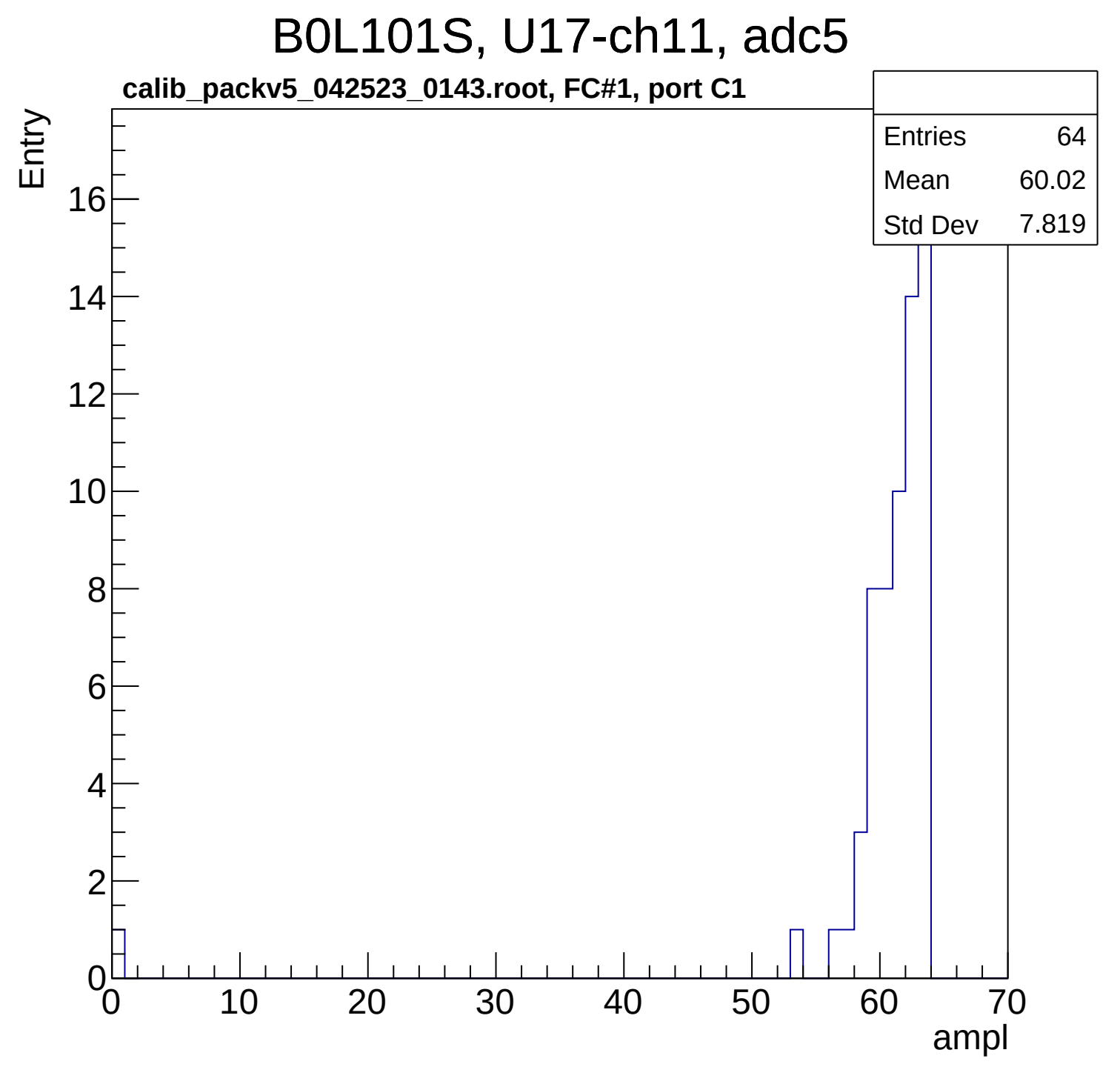

B0L101S, U17-ch11, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 152
Mean 29.36
Std Dev 4.826
Gaus mean : 30.4496
Gaus Width: 3.7296
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

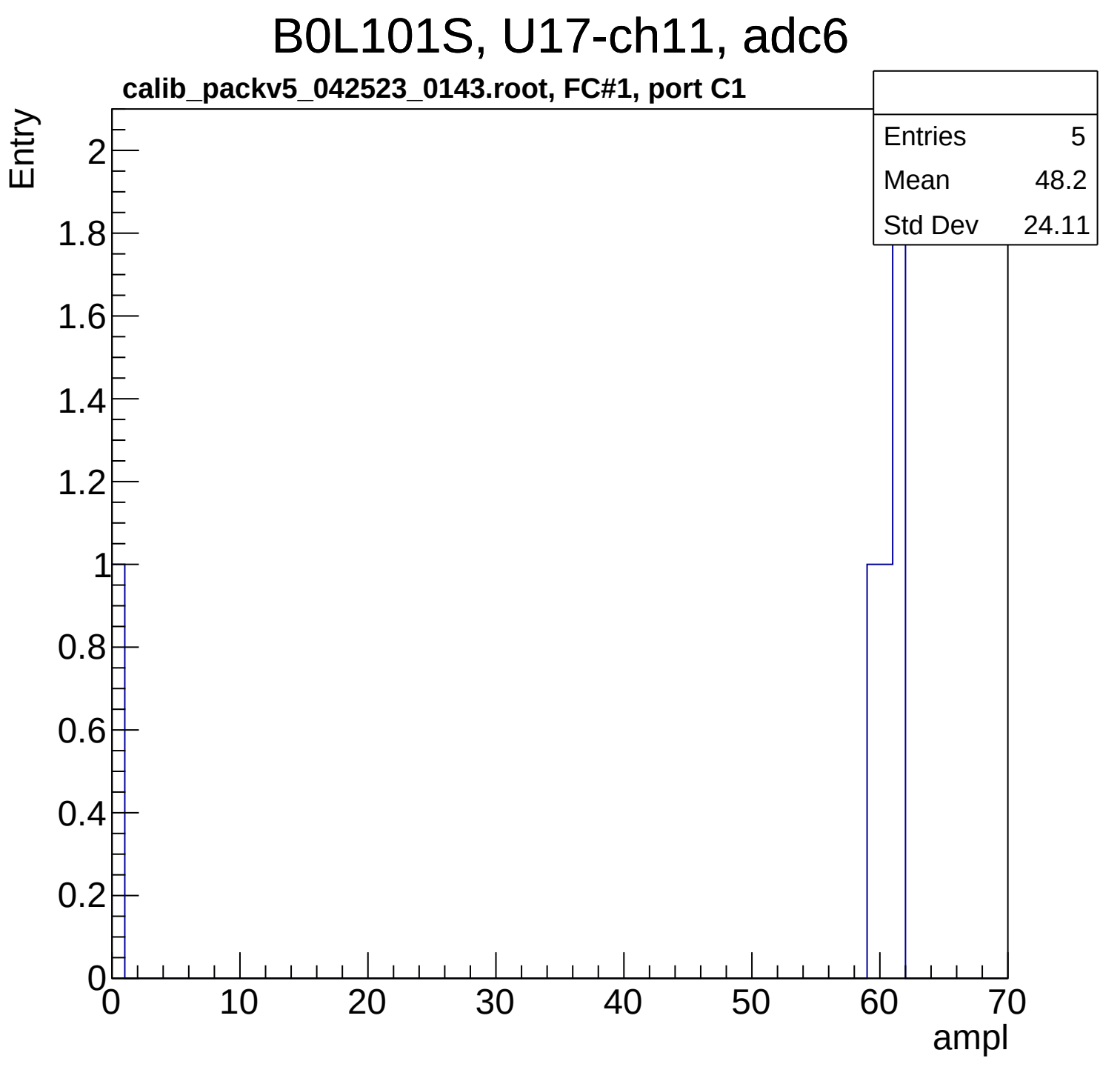

B0L101S, U17-ch11, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 37.27
Std Dev 3.604
Gaus mean : 37.7132
Gaus Width: 3.4769
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

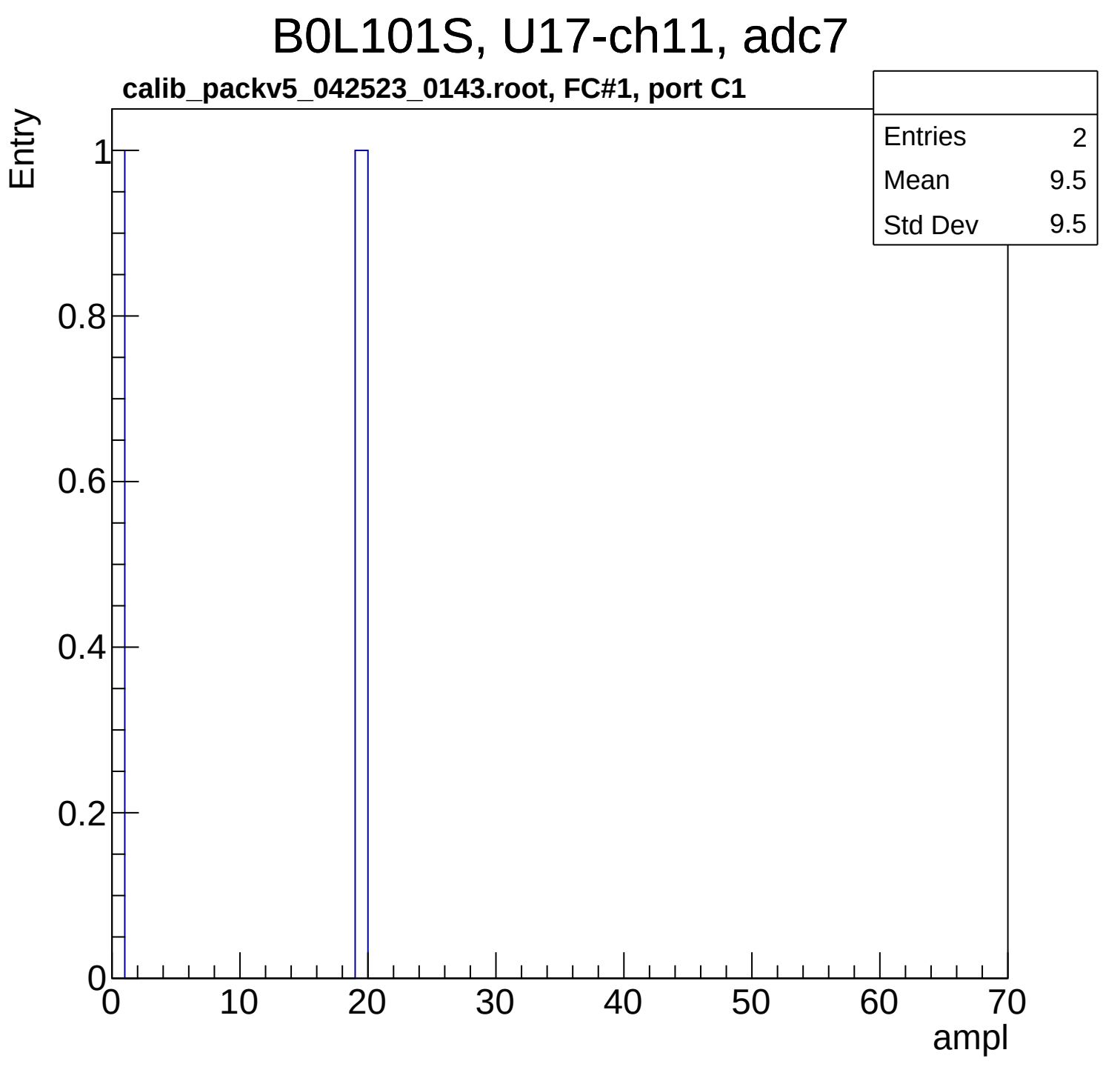

B0L101S, U17-ch11, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 43.46
Std Dev 3.639
Gaus mean : 43.9195
Gaus Width: 3.6523
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

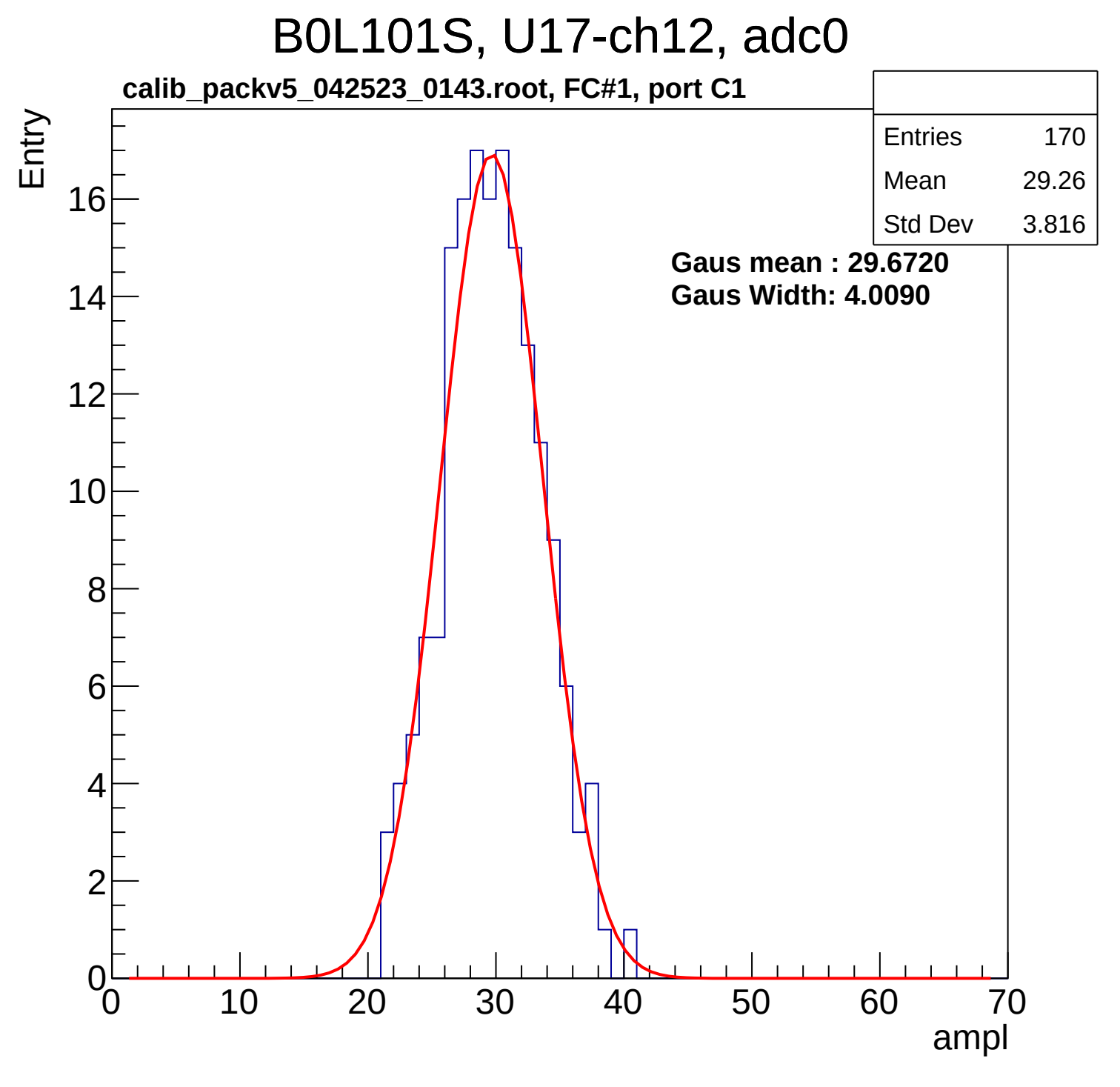

B0L101S, U17-ch11, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 50.11
Std Dev 3.36
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

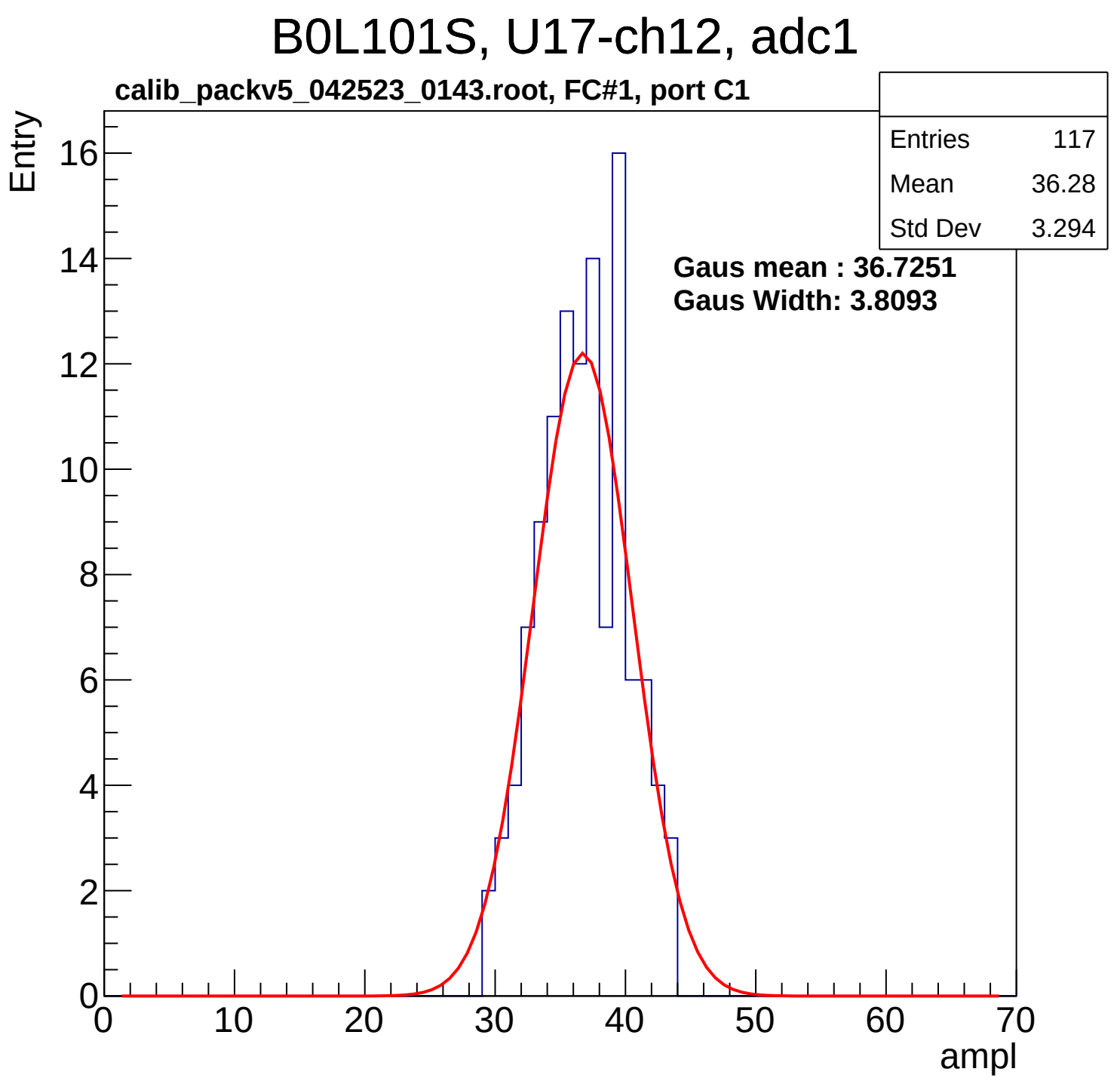

B0L101S, U17-ch11, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 56.91
Std Dev 3.044
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

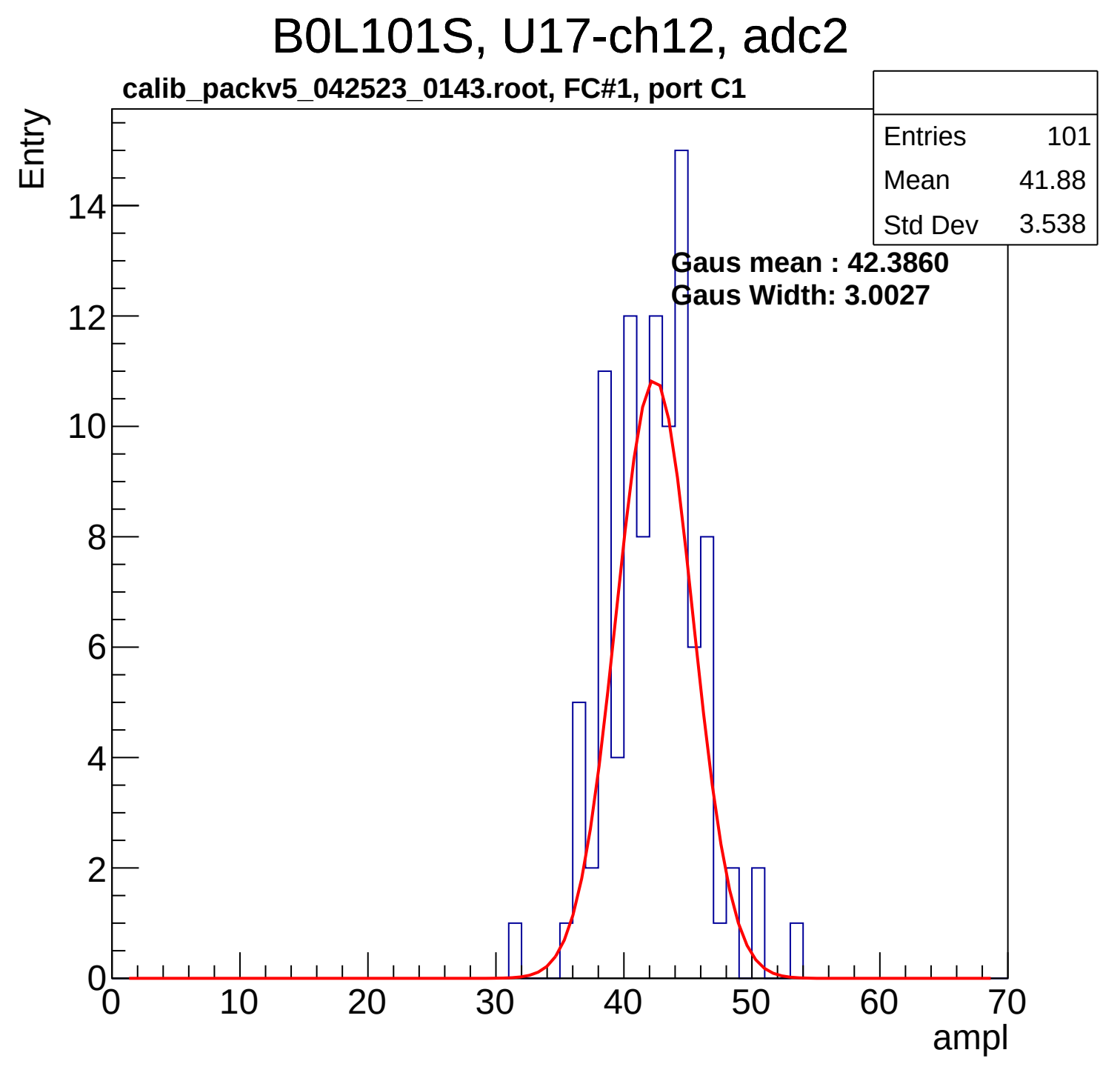

B0L101S, U17-ch11, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 64
Mean 60.02
Std Dev 7.819
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

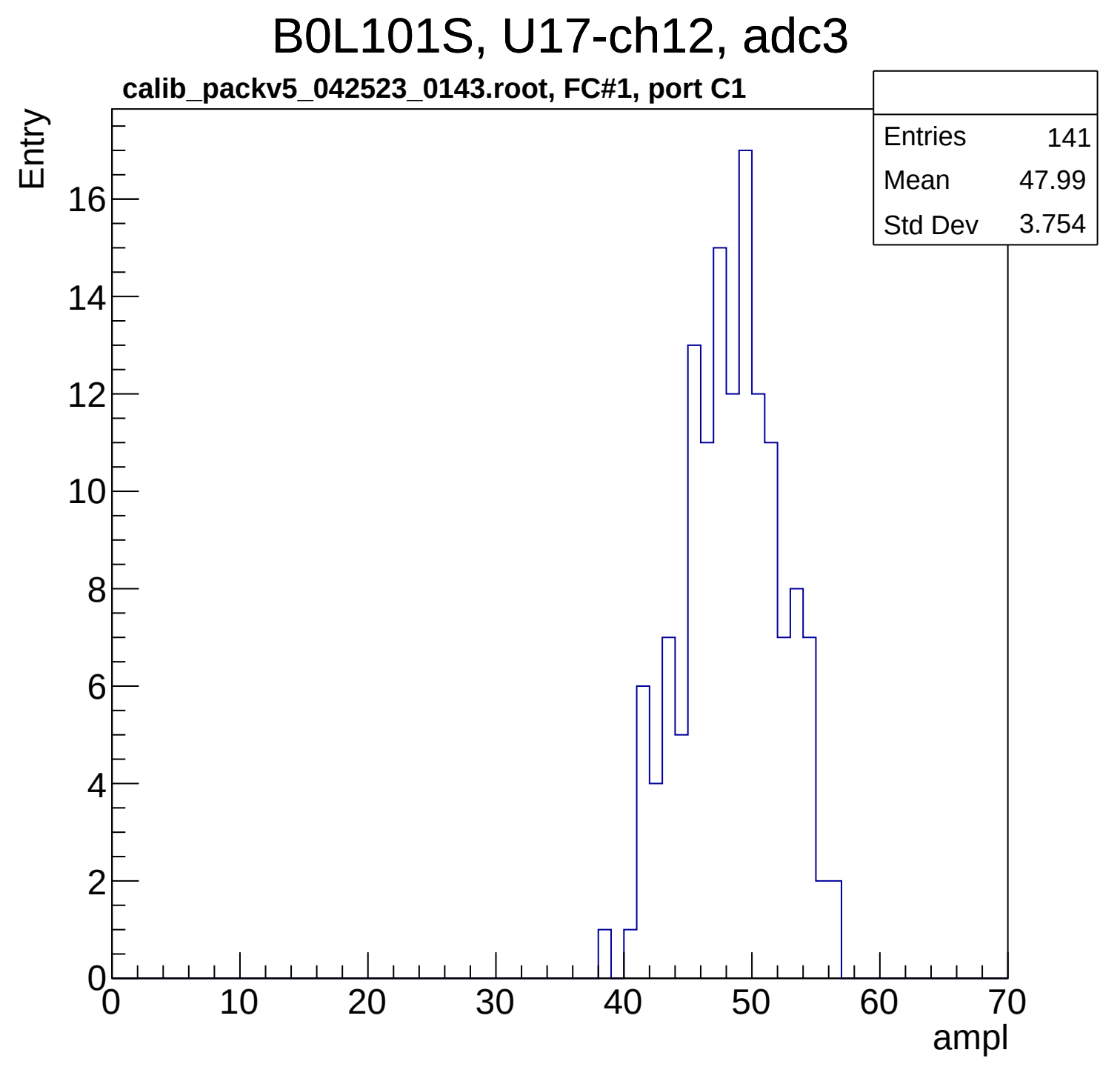

B0L101S, U17-ch11, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 48.2
Std Dev 24.11
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

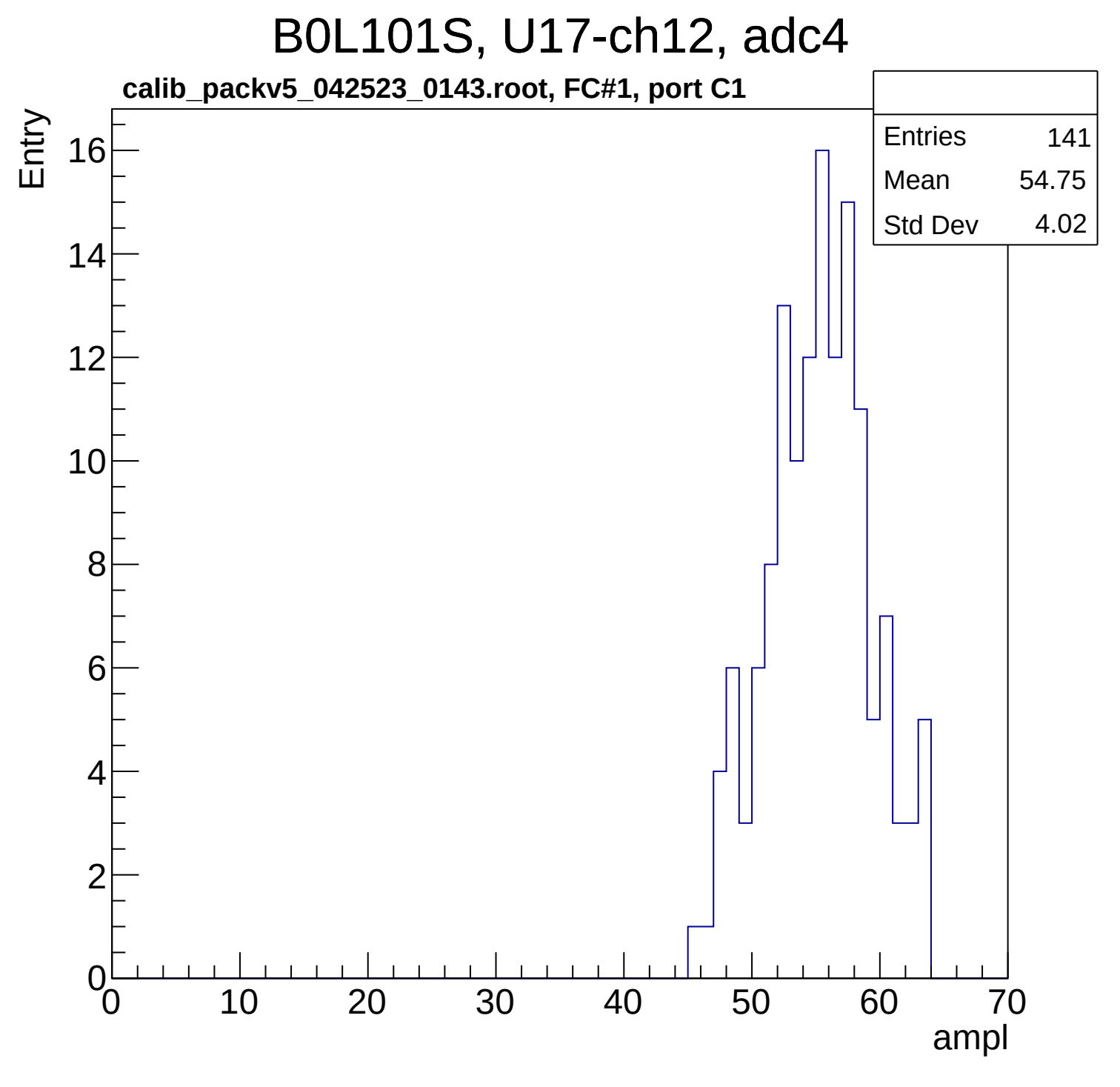

B0L101S, U17-ch11, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

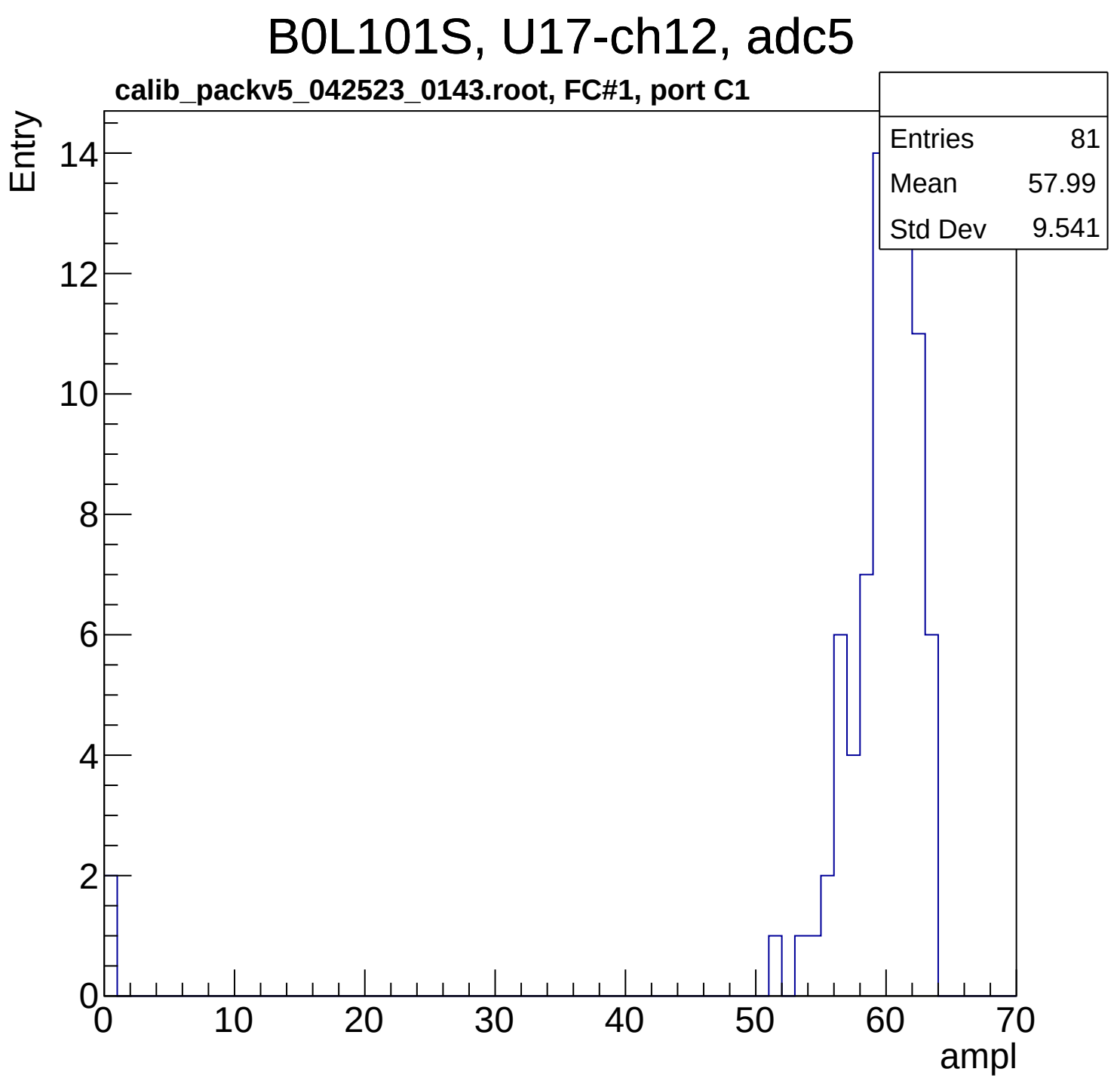

B0L101S, U17-ch12, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 170
Mean 29.26
Std Dev 3.816
Gaus mean : 29.6720
Gaus Width: 4.0090
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

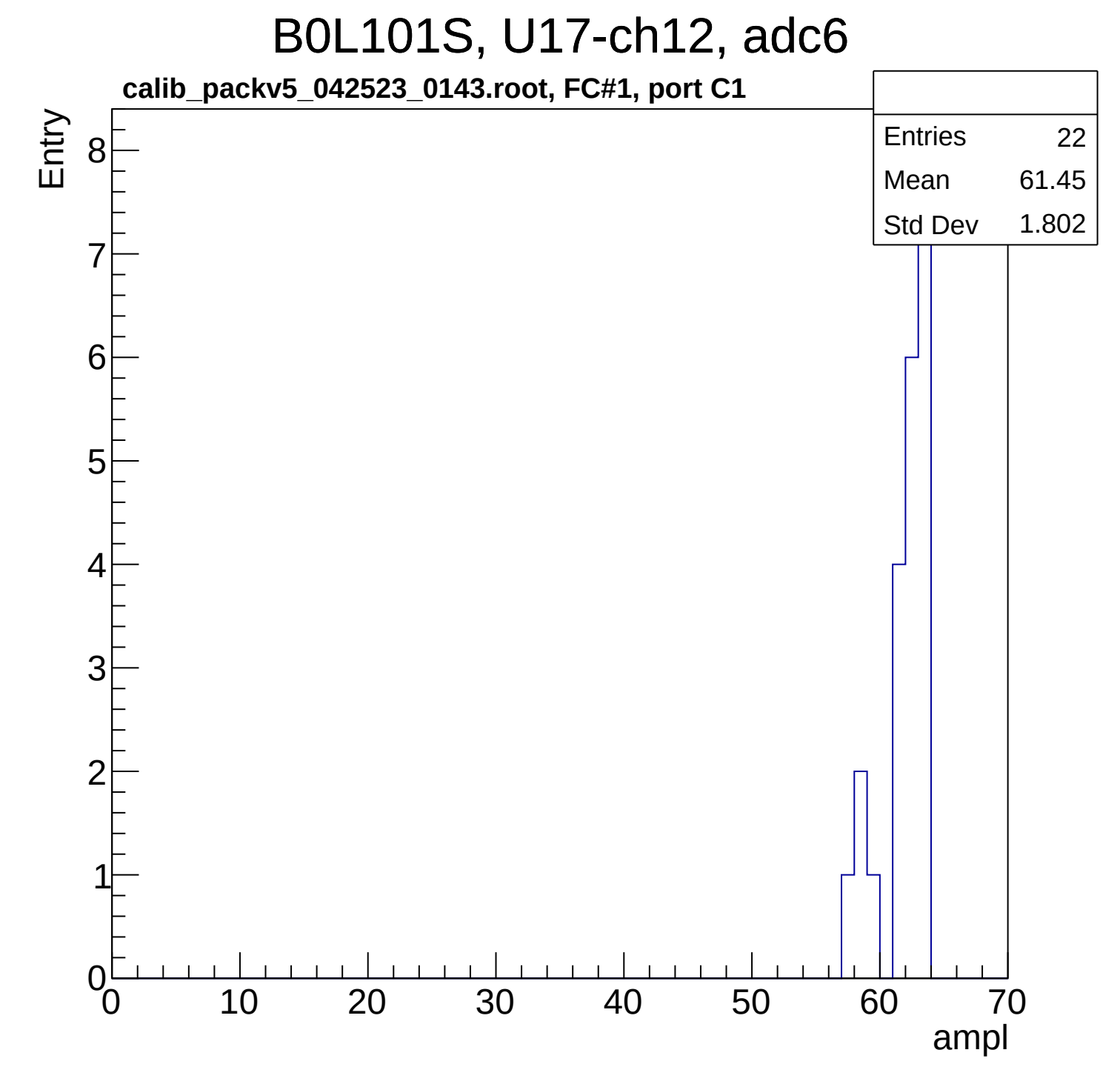

B0L101S, U17-ch12, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 36.28
Std Dev 3.294
Gaus mean : 36.7251
Gaus Width: 3.8093
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

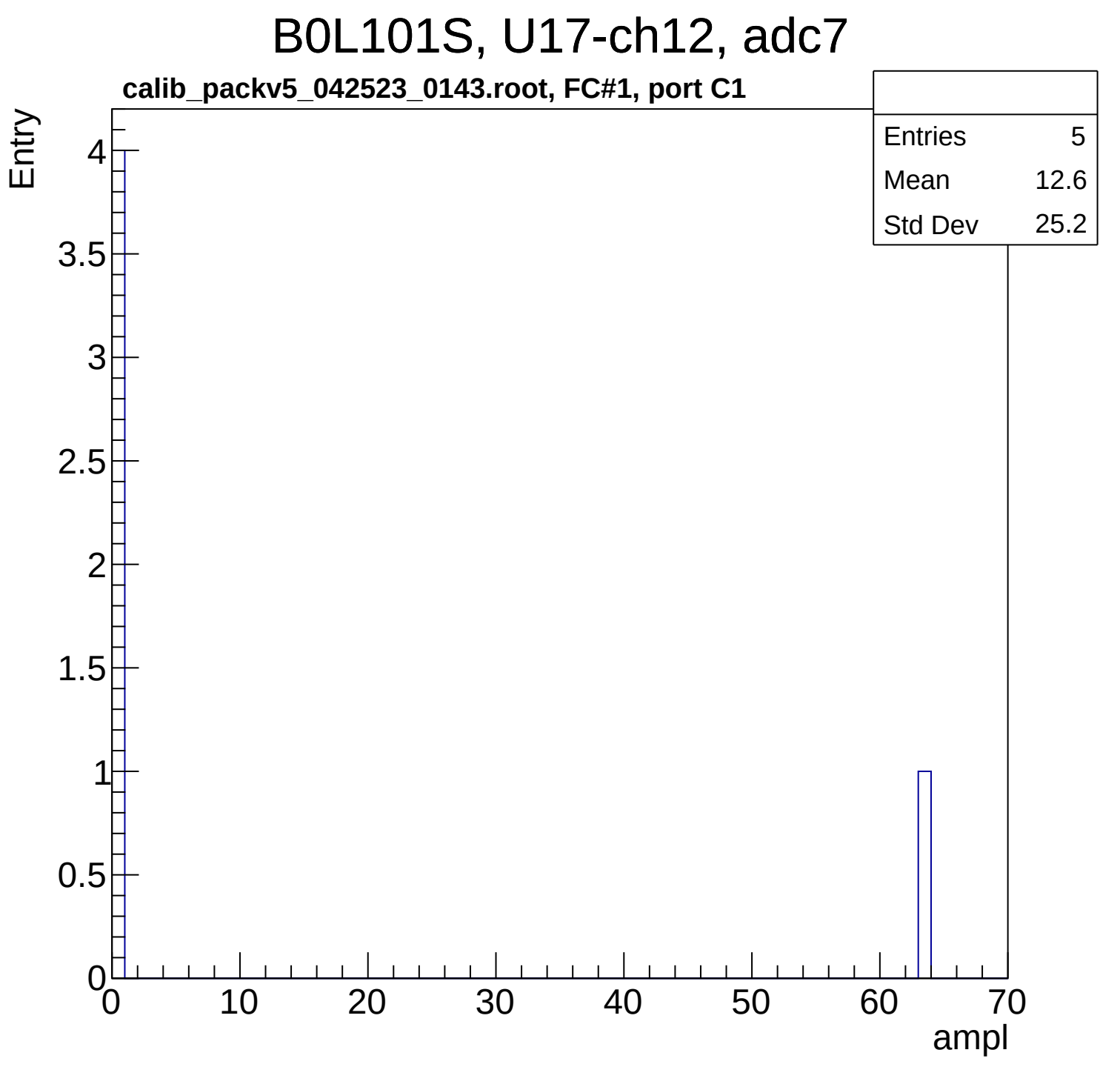

B0L101S, U17-ch12, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 41.88
Std Dev 3.538
Gaus mean : 42.3860
Gaus Width: 3.0027
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

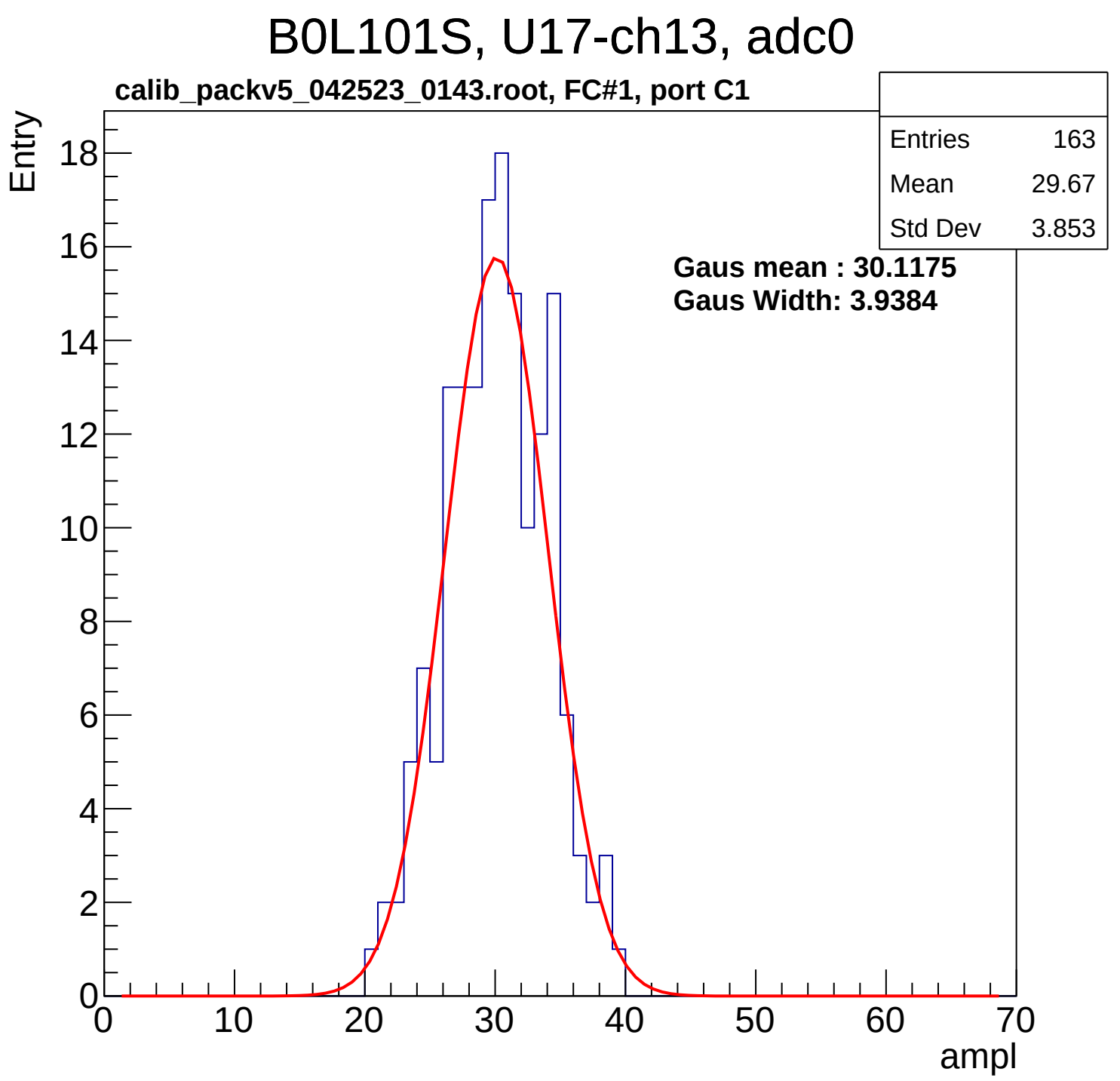

B0L101S, U17-ch12, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 47.99
Std Dev 3.754
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

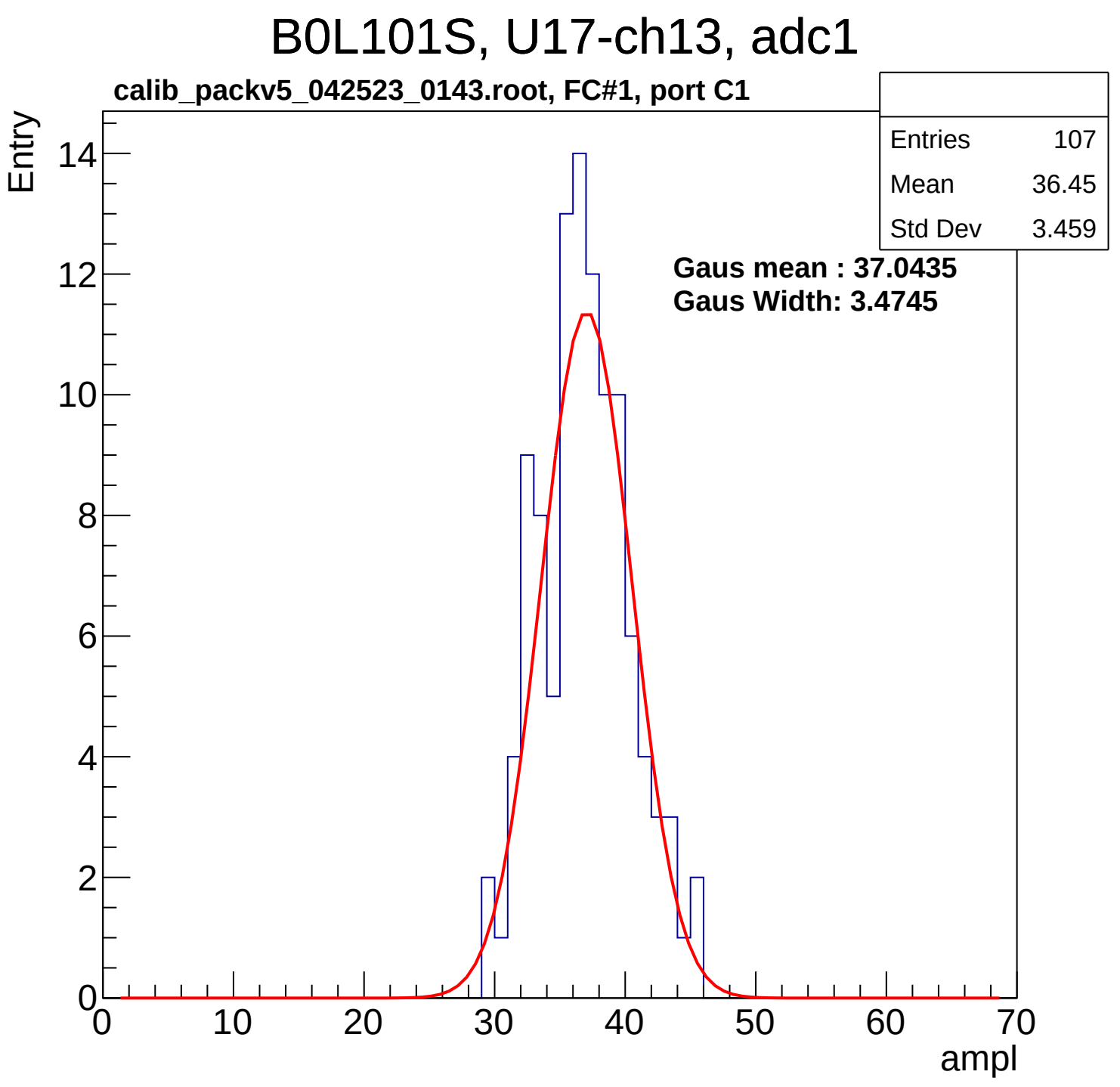

B0L101S, U17-ch12, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 54.75
Std Dev 4.02
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

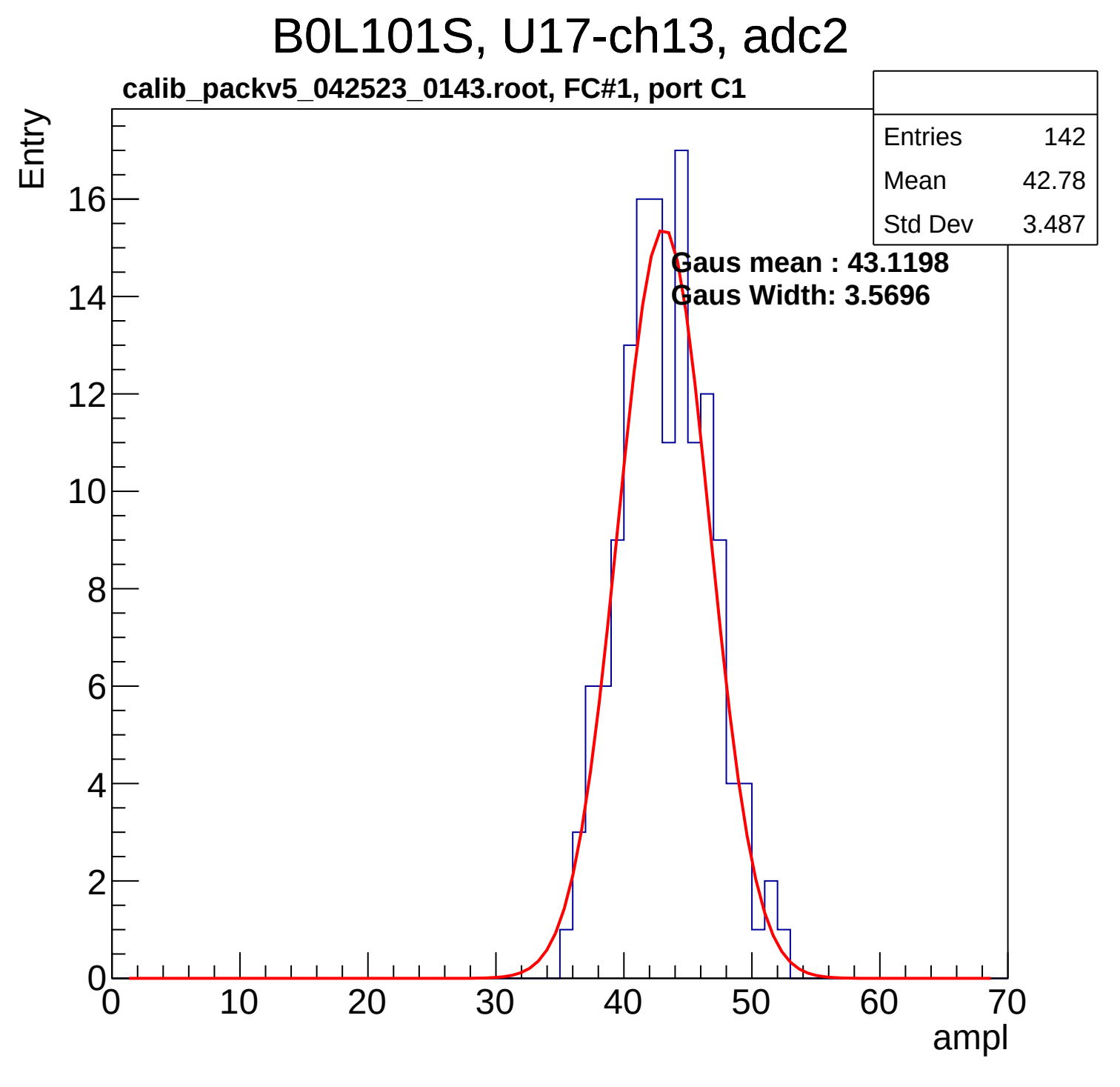

B0L101S, U17-ch12, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 57.99
Std Dev 9.541
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

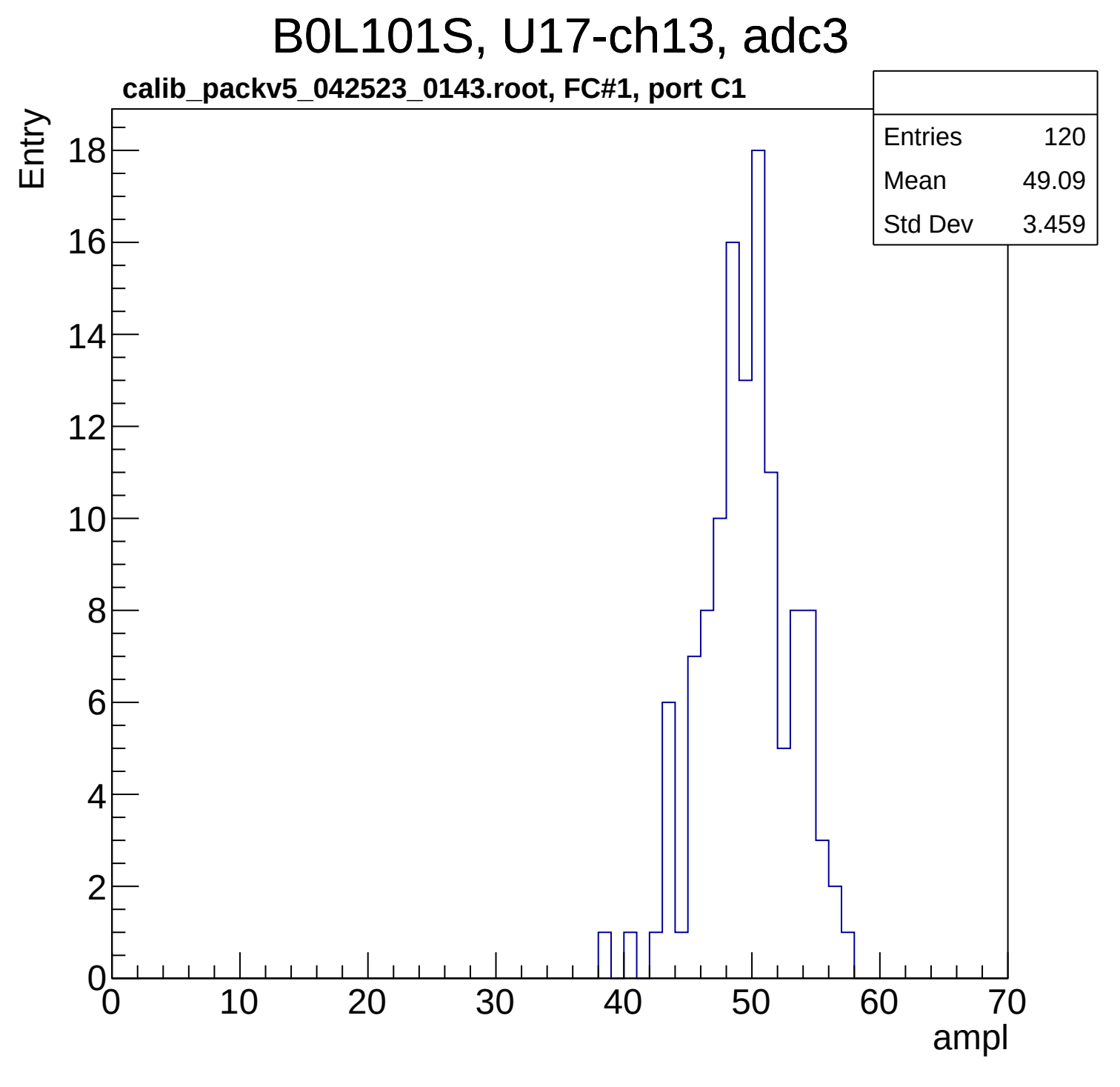

B0L101S, U17-ch12, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 22
Mean 61.45
Std Dev 1.802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

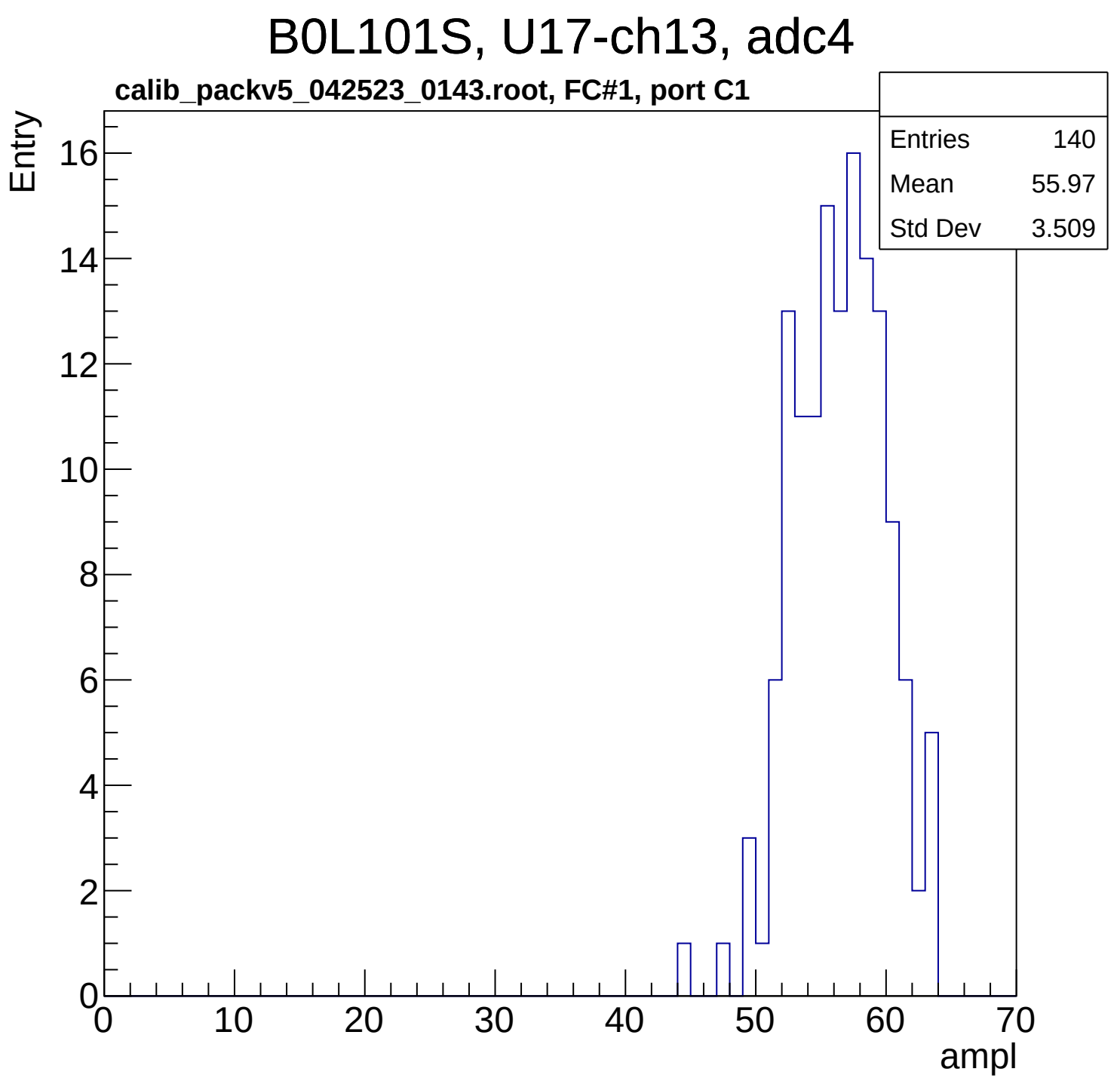

B0L101S, U17-ch12, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 12.6
Std Dev 25.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

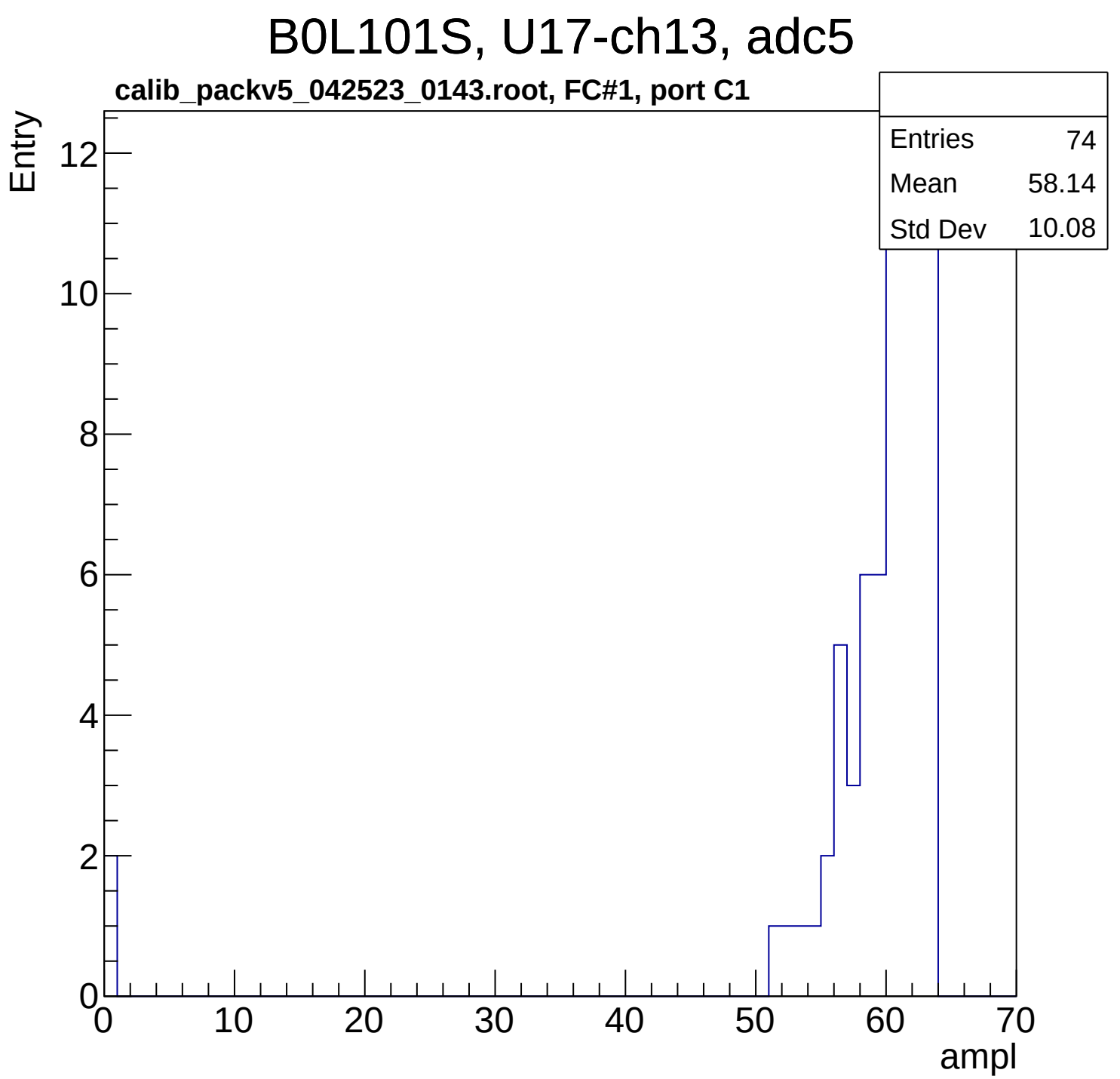

B0L101S, U17-ch13, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 29.67
Std Dev 3.853
Gaus mean : 30.1175
Gaus Width: 3.9384
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

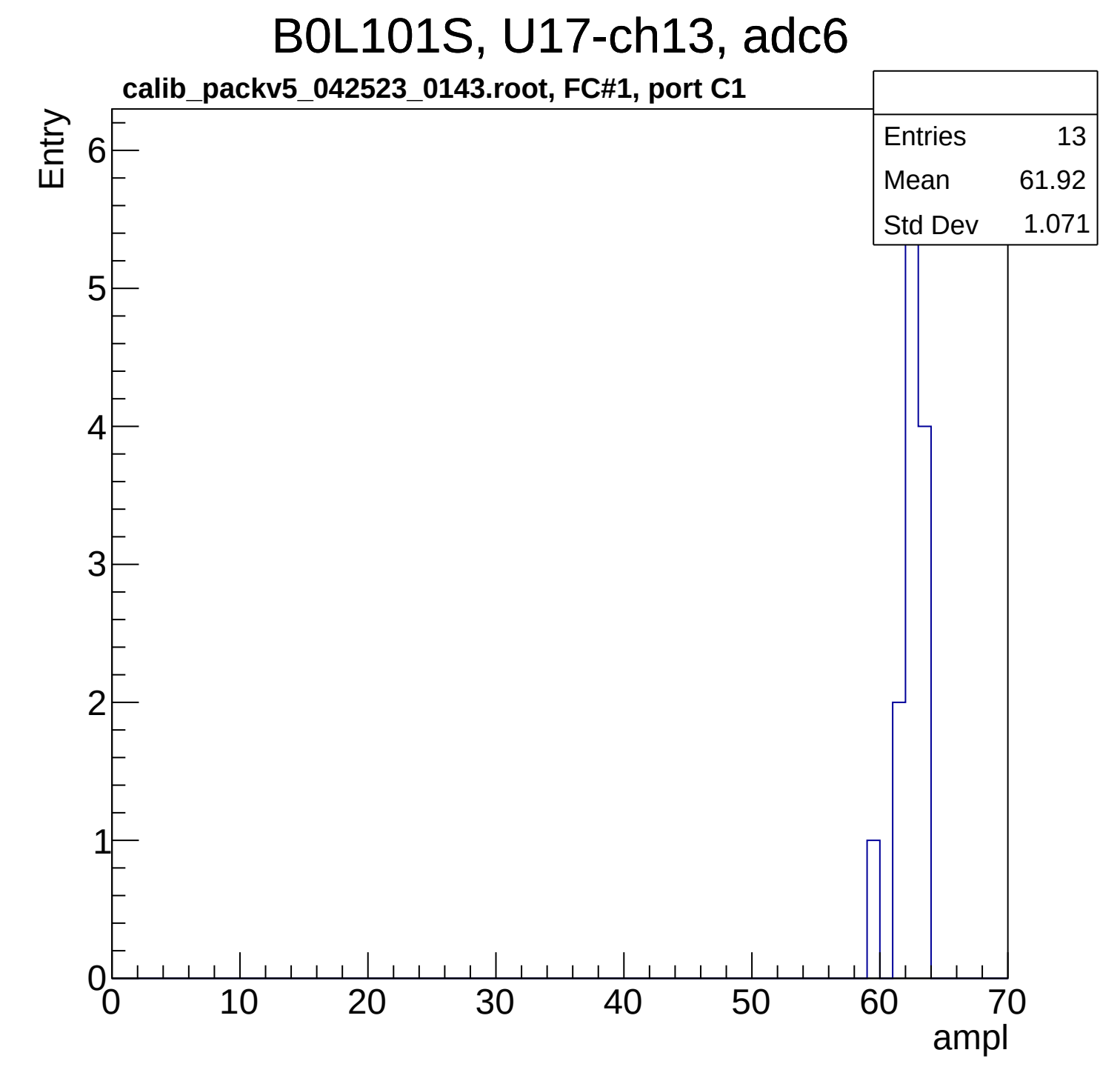

B0L101S, U17-ch13, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 107
Mean 36.45
Std Dev 3.459
Gaus mean : 37.0435
Gaus Width: 3.4745
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

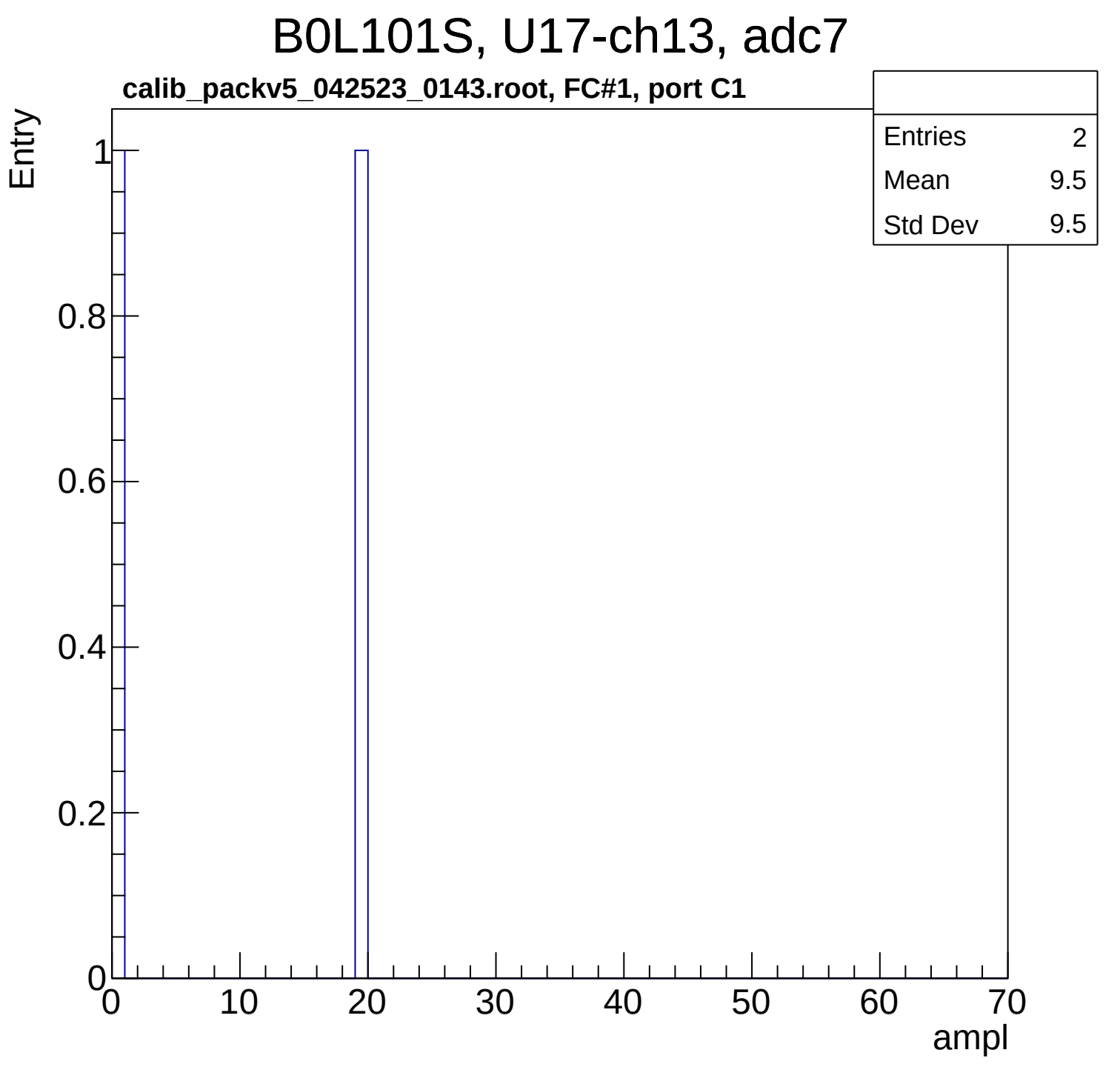

B0L101S, U17-ch13, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 42.78
Std Dev 3.487
Gaus mean : 43.1198
Gaus Width: 3.5696
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

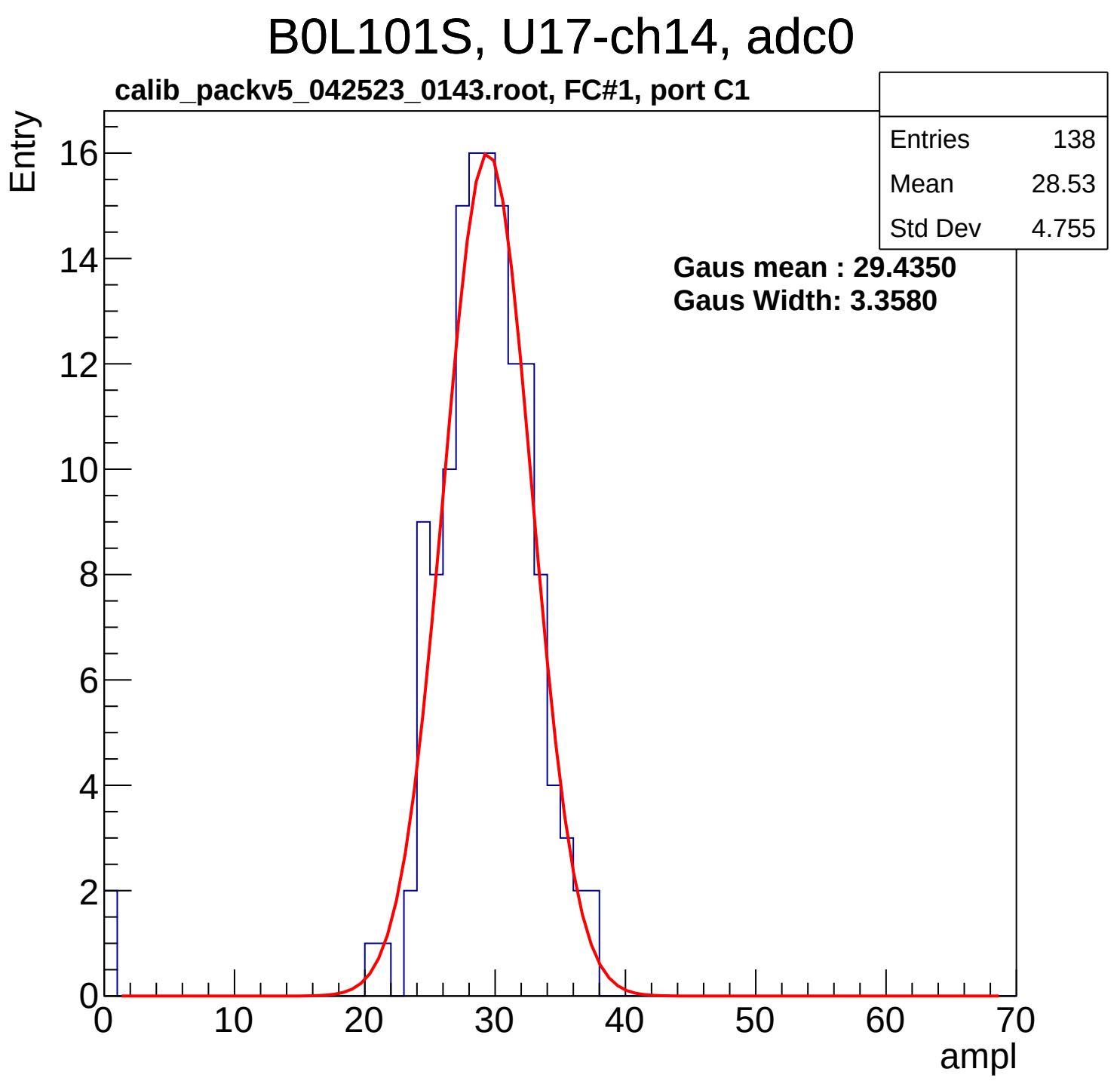

B0L101S, U17-ch13, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 49.09
Std Dev 3.459
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

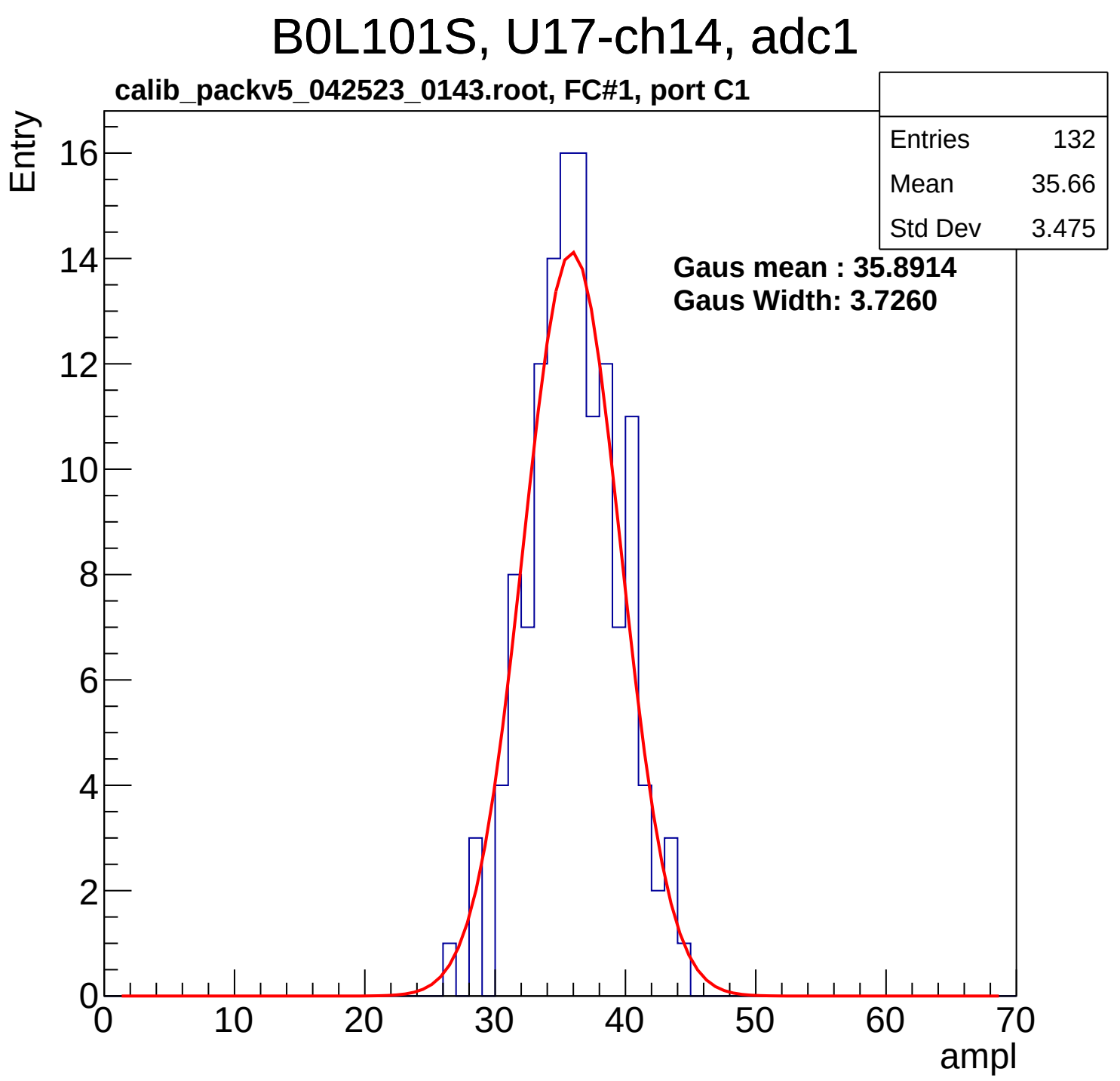

B0L101S, U17-ch13, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 55.97
Std Dev 3.509
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

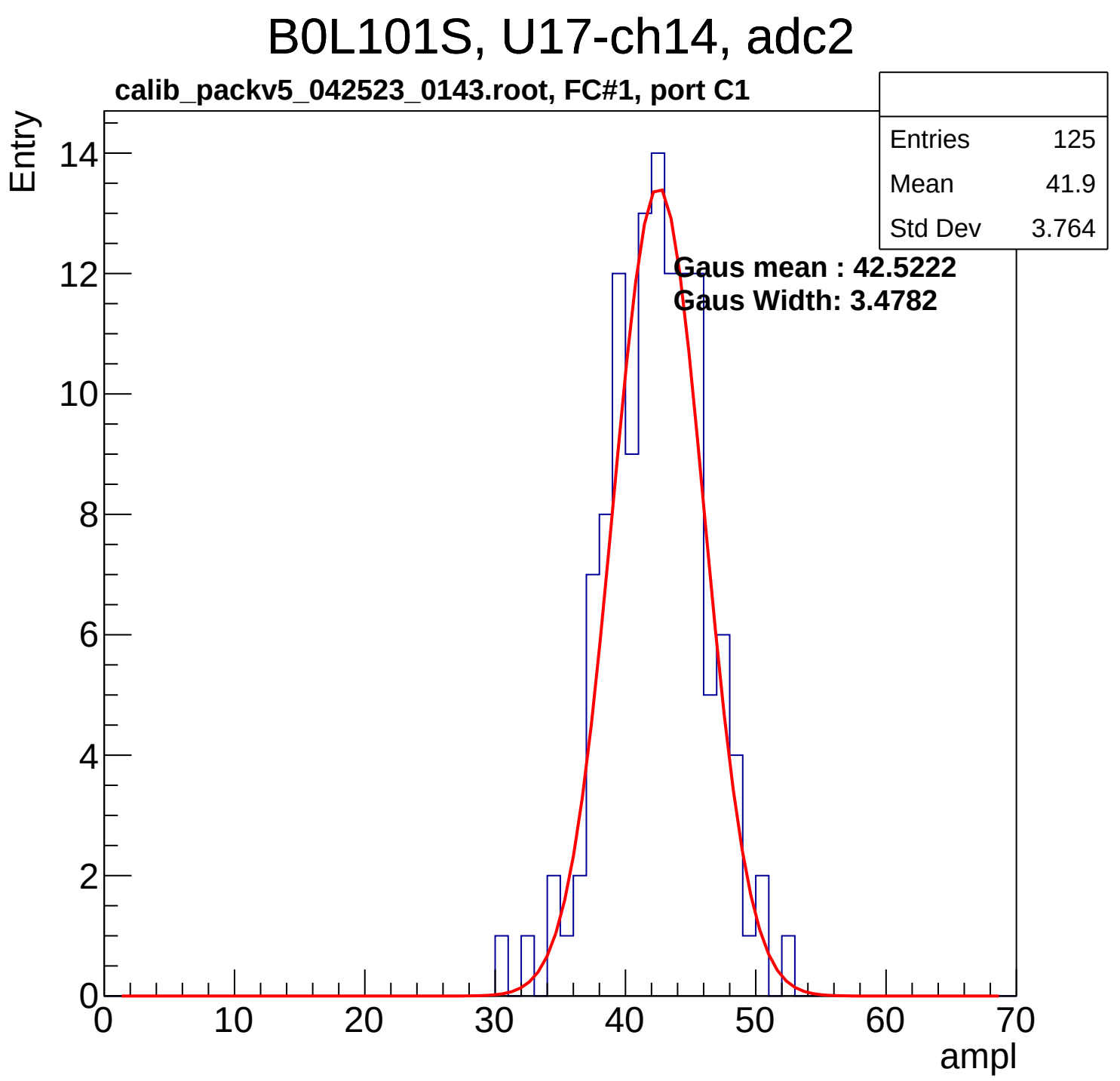

B0L101S, U17-ch13, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 74
Mean 58.14
Std Dev 10.08
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

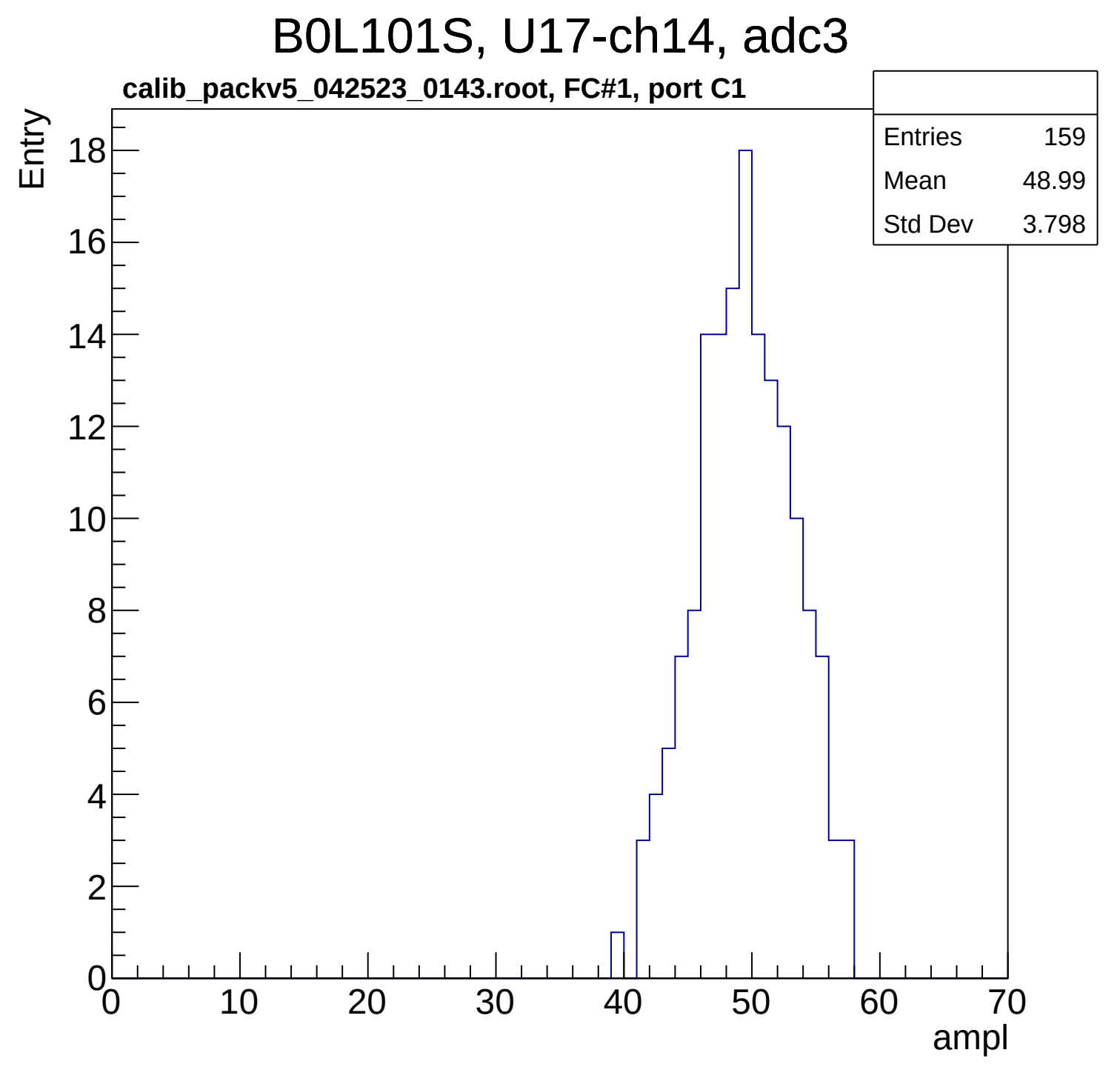

B0L101S, U17-ch13, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 13
Mean 61.92
Std Dev 1.071
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

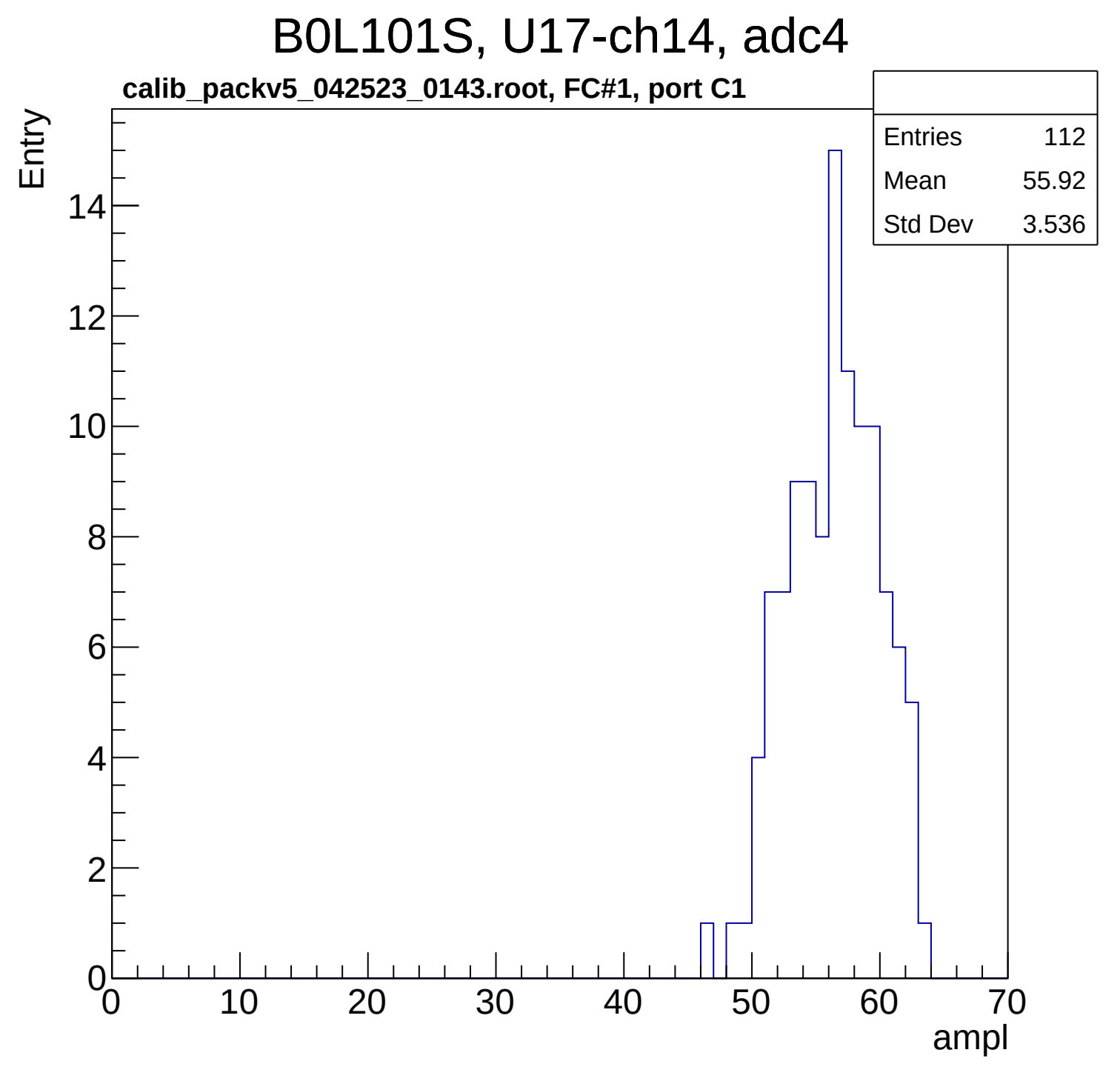

B0L101S, U17-ch13, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

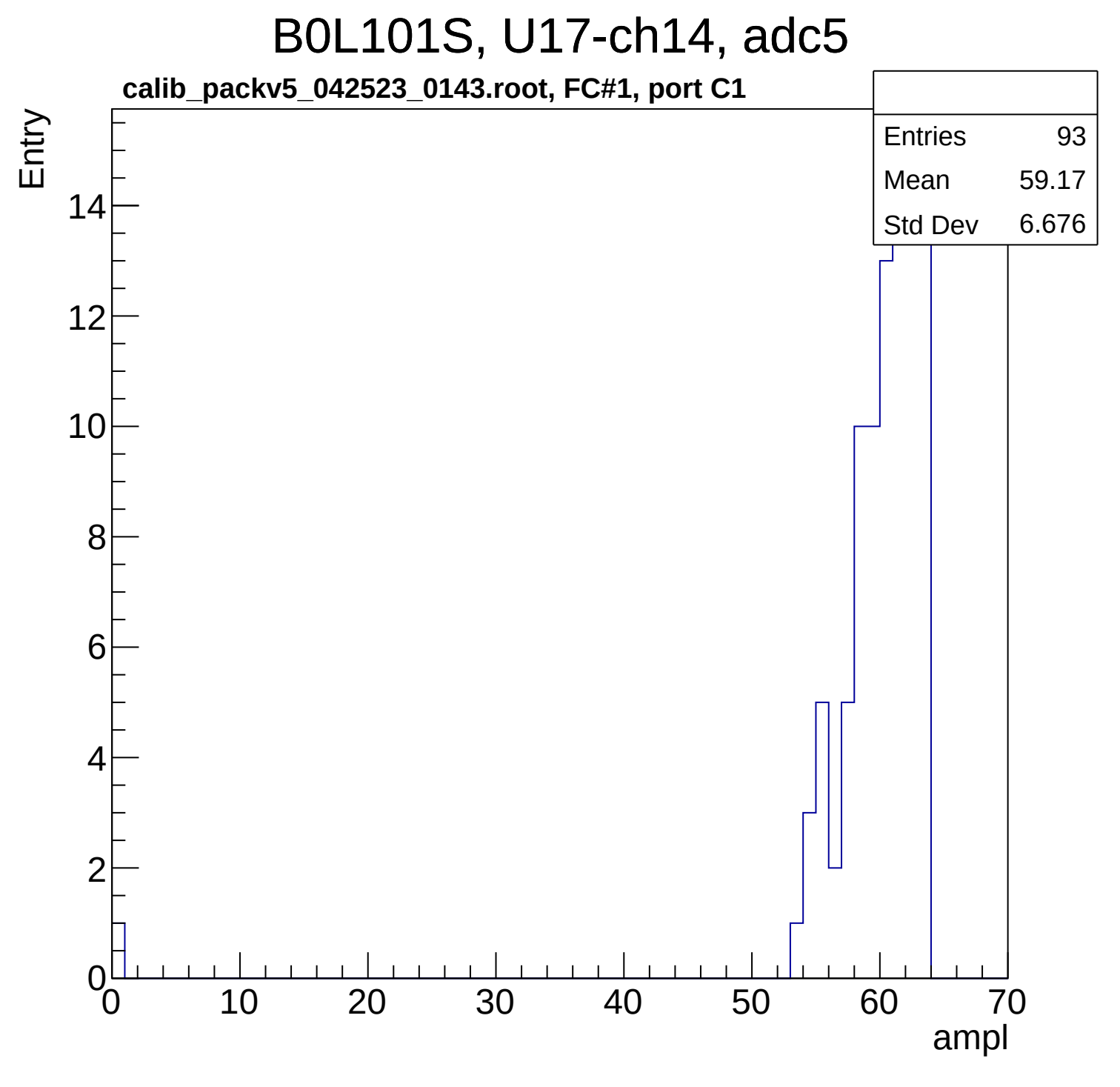

B0L101S, U17-ch14, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 28.53
Std Dev 4.755
Gaus mean : 29.4350
Gaus Width: 3.3580
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

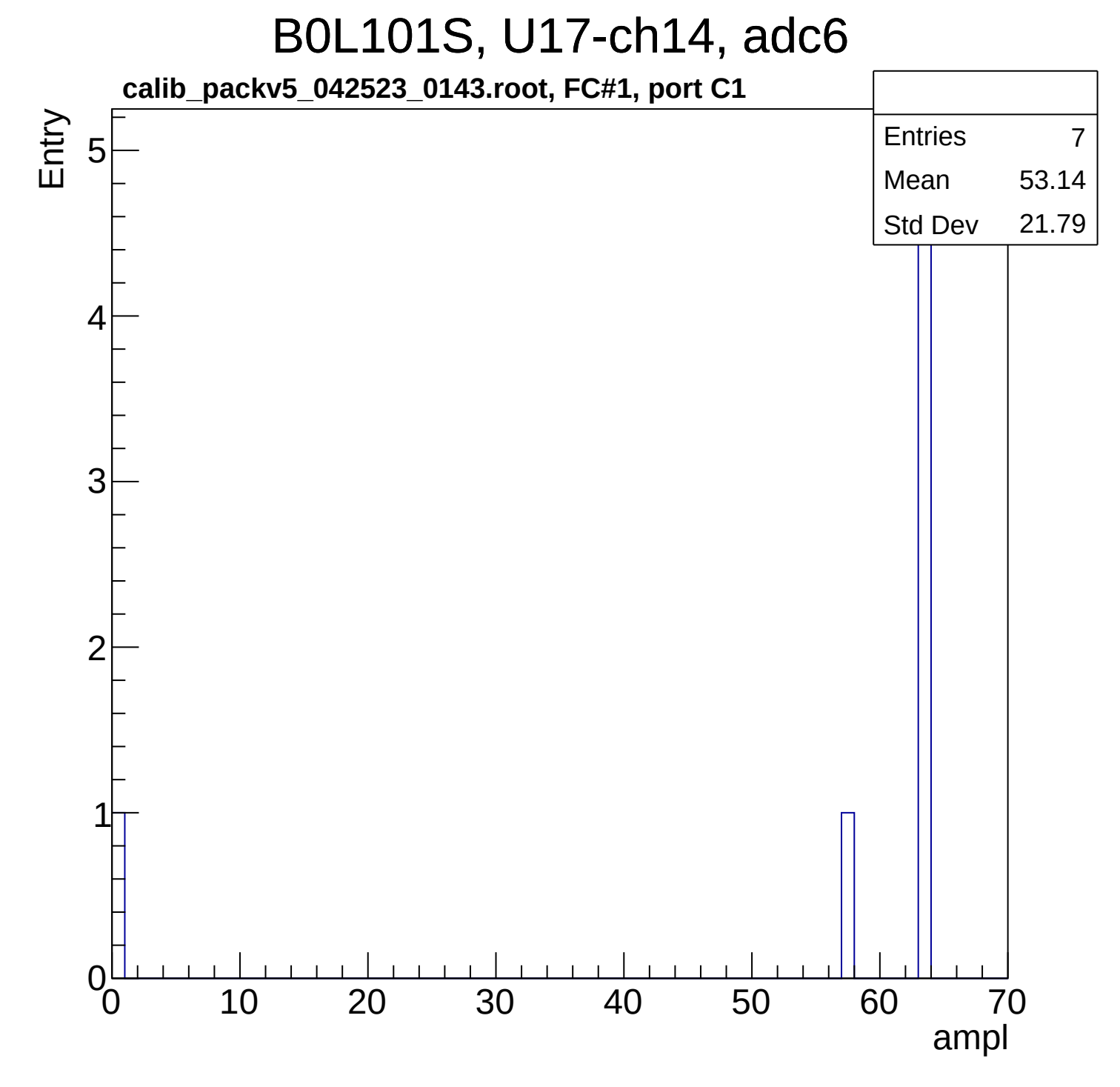

B0L101S, U17-ch14, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 35.66
Std Dev 3.475
Gaus mean : 35.8914
Gaus Width: 3.7260
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

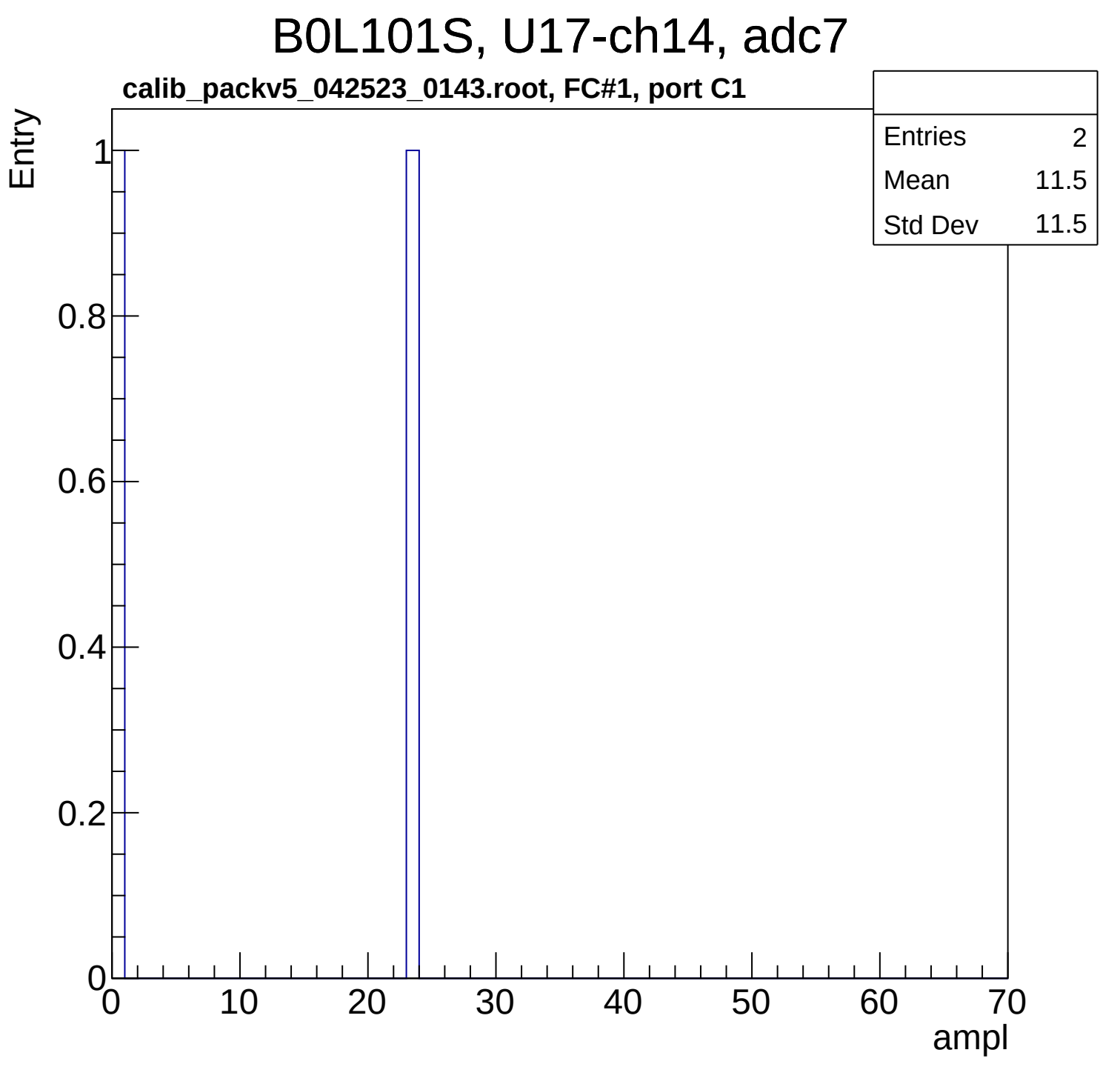

B0L101S, U17-ch14, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 41.9
Std Dev 3.764
Gaus mean : 42.5222
Gaus Width: 3.4782
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

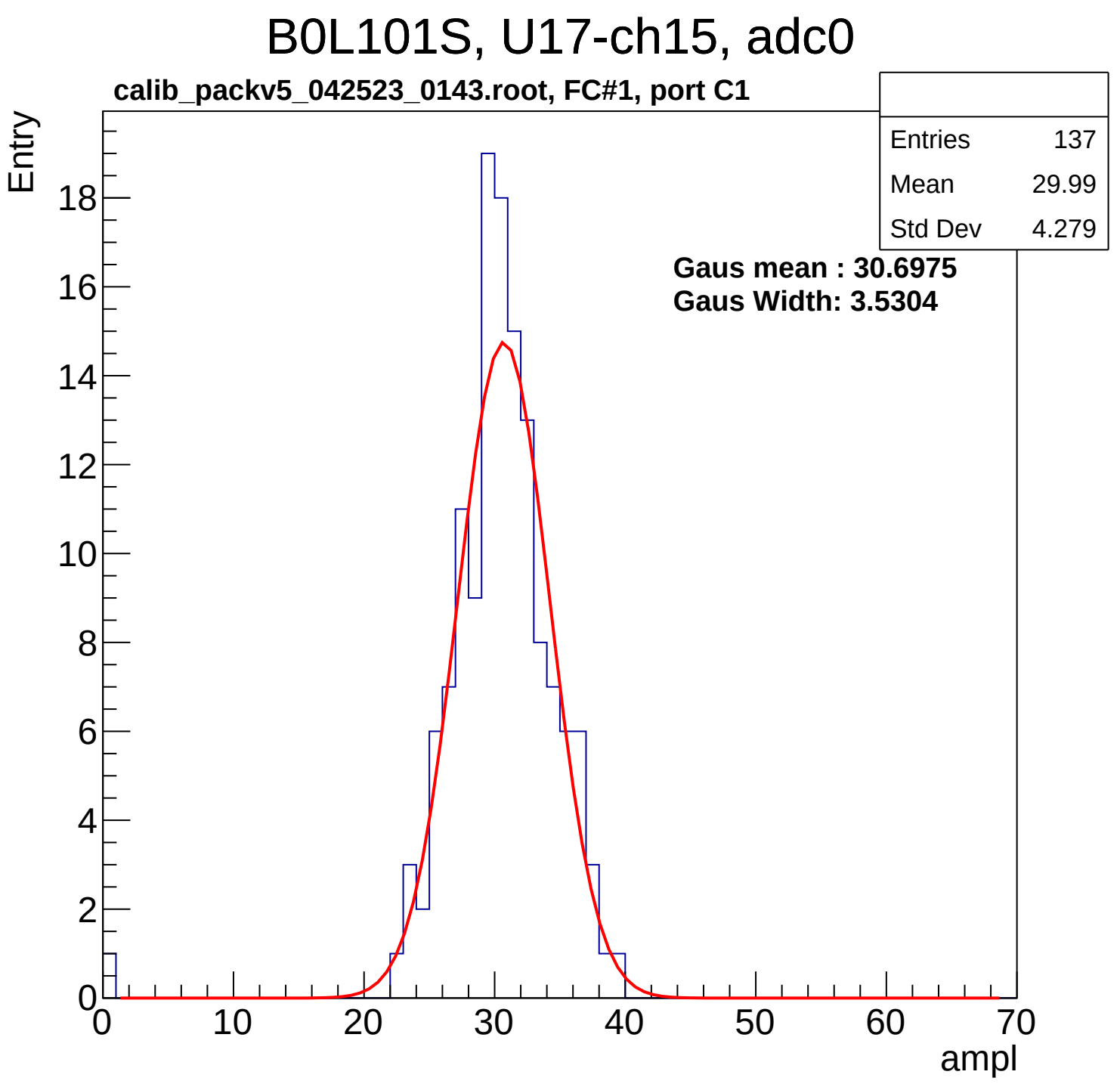

B0L101S, U17-ch14, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 48.99
Std Dev 3.798
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

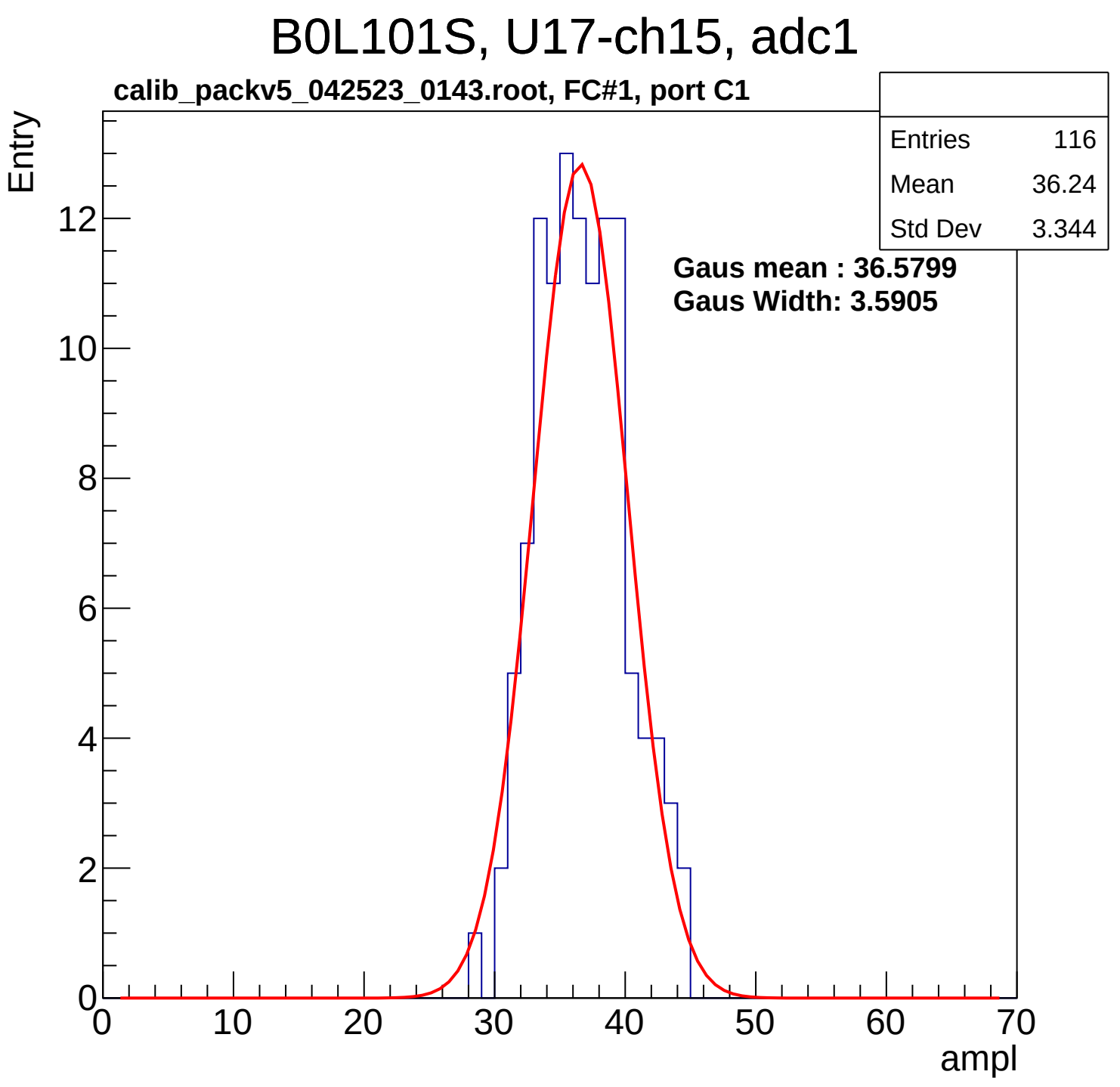

B0L101S, U17-ch14, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 55.92
Std Dev 3.536
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

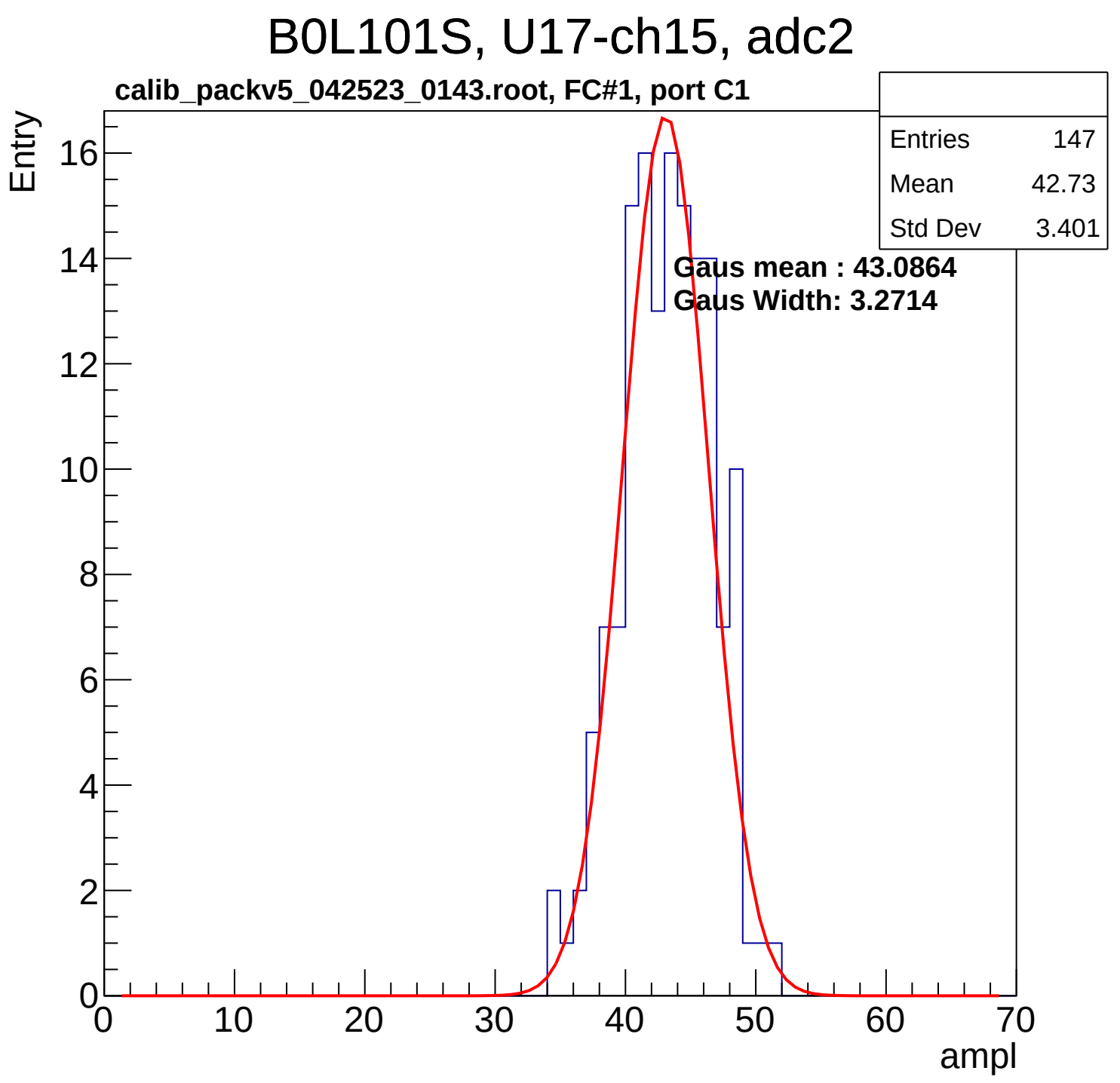

B0L101S, U17-ch14, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 93
Mean 59.17
Std Dev 6.676
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

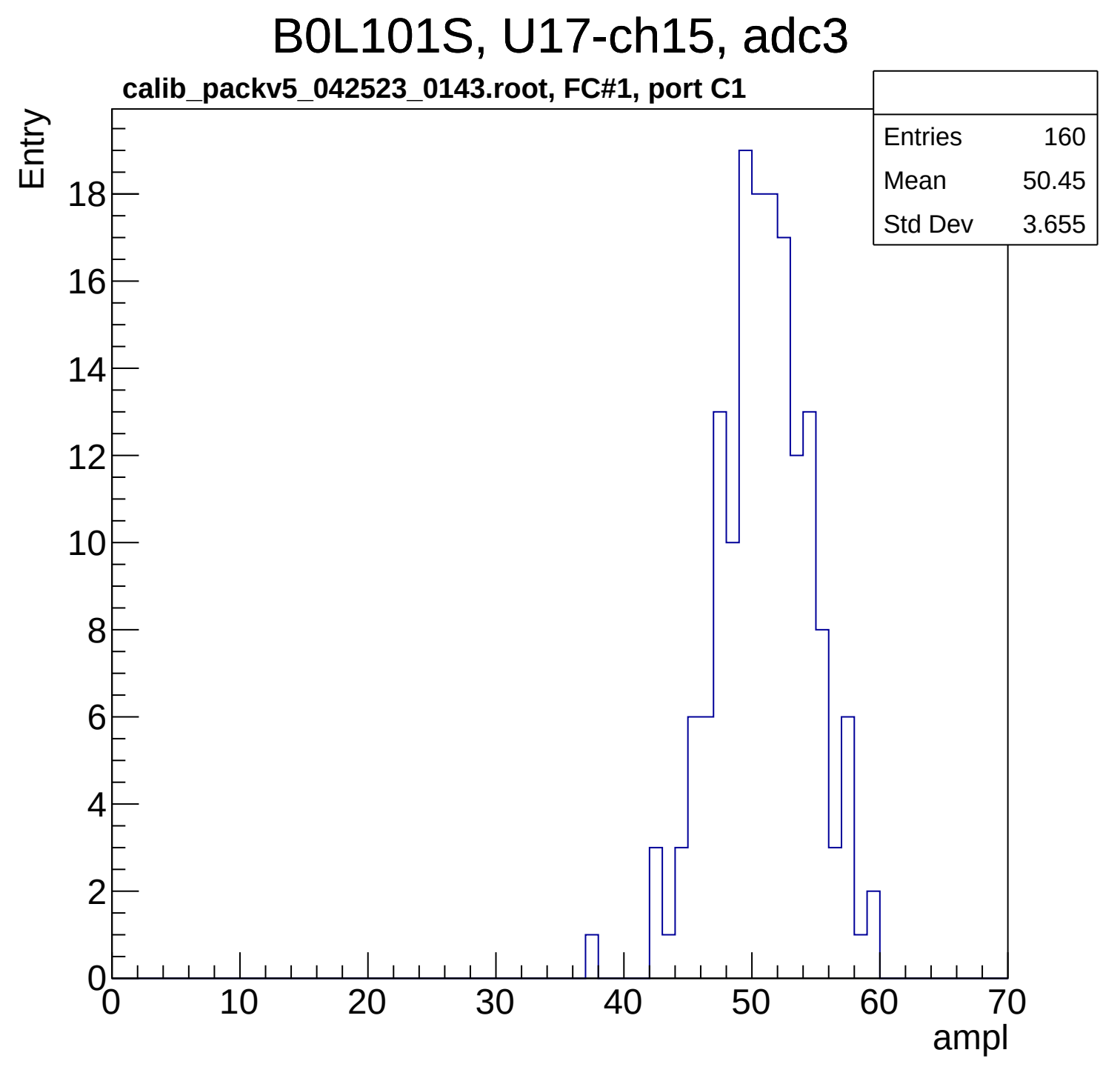

B0L101S, U17-ch14, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 7
Mean 53.14
Std Dev 21.79
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

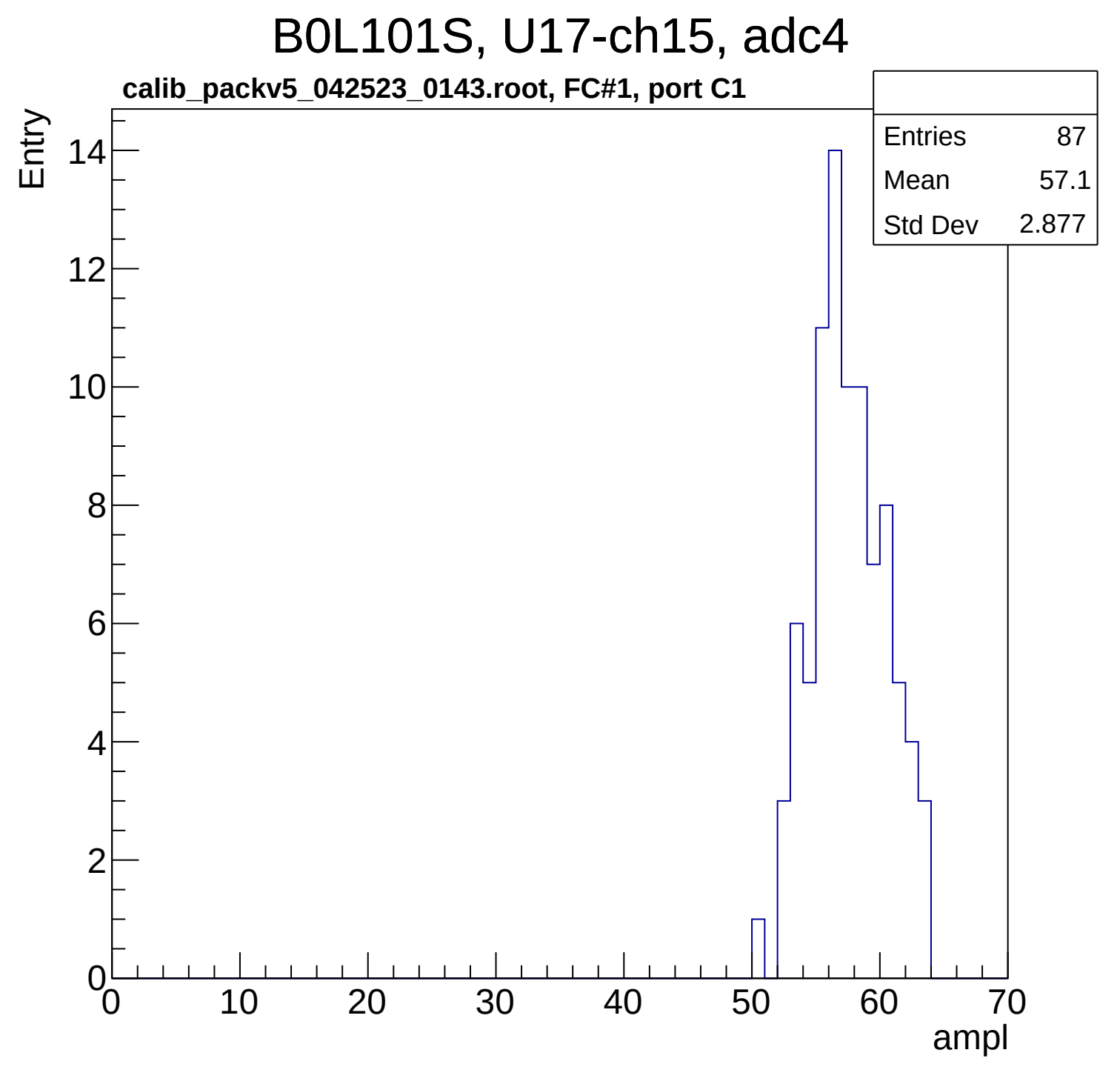

B0L101S, U17-ch14, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

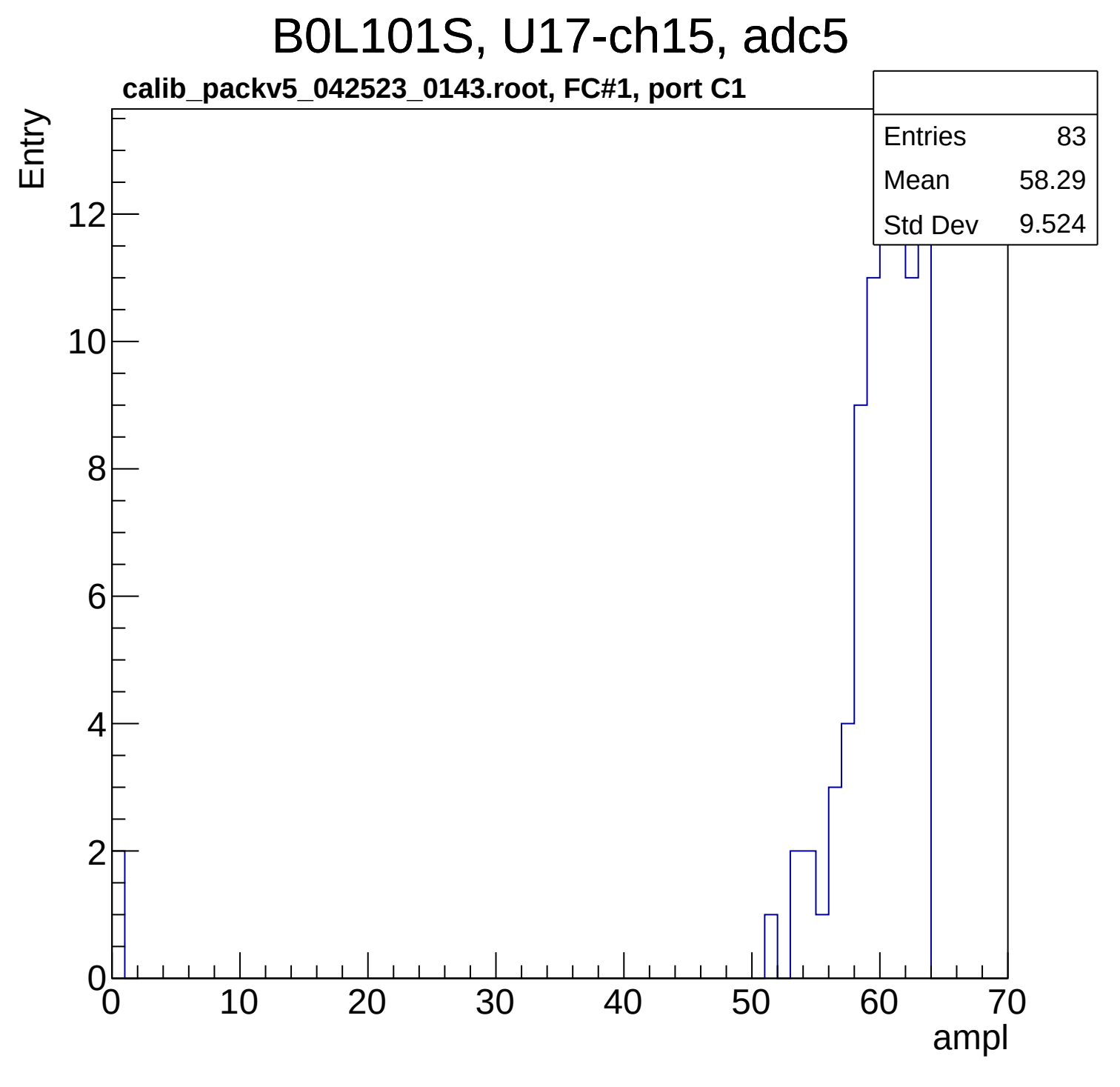

B0L101S, U17-ch15, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 29.99
Std Dev 4.279
Gaus mean : 30.6975
Gaus Width: 3.5304
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

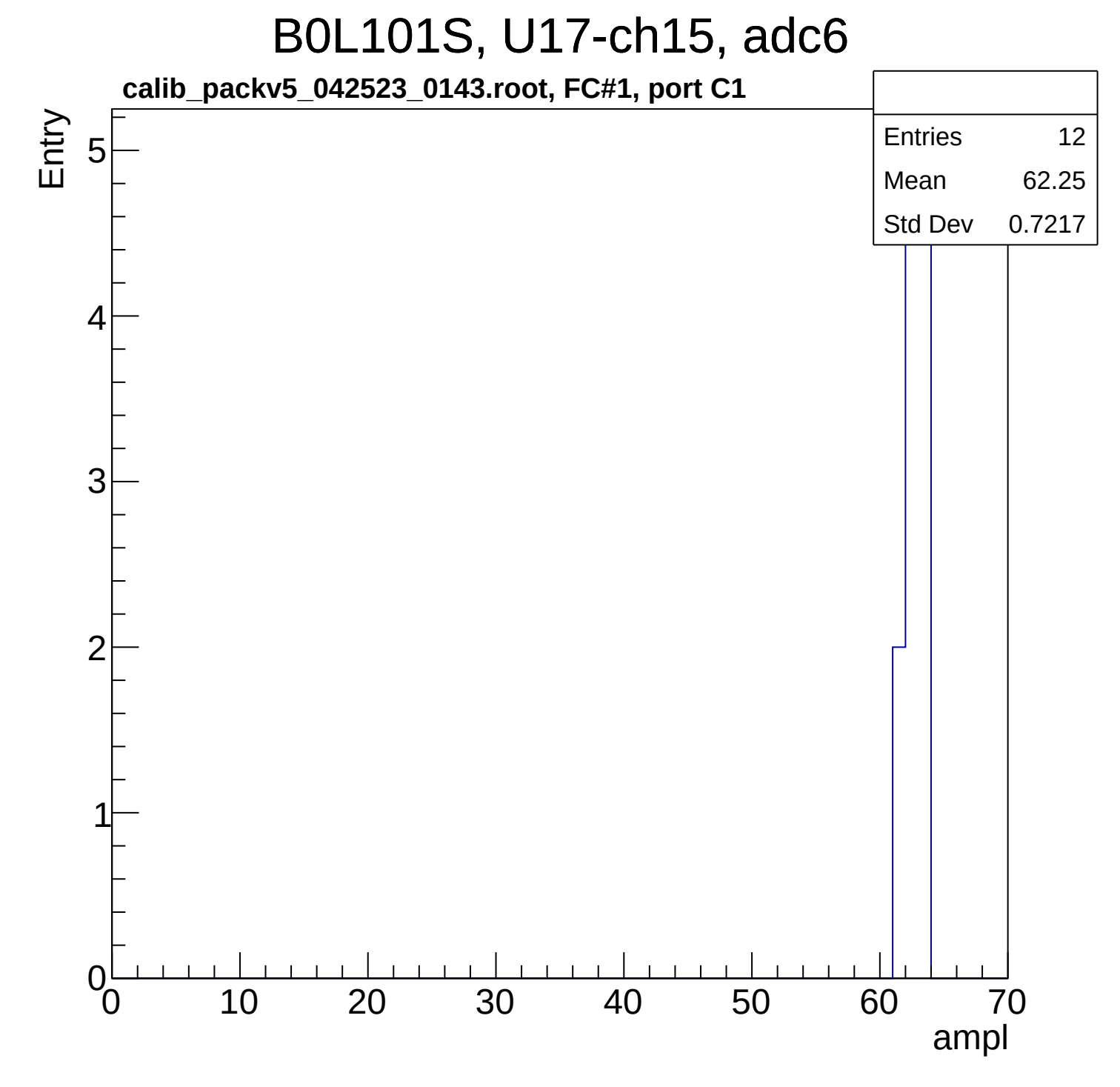

B0L101S, U17-ch15, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 36.24
Std Dev 3.344
Gaus mean : 36.5799
Gaus Width: 3.5905
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

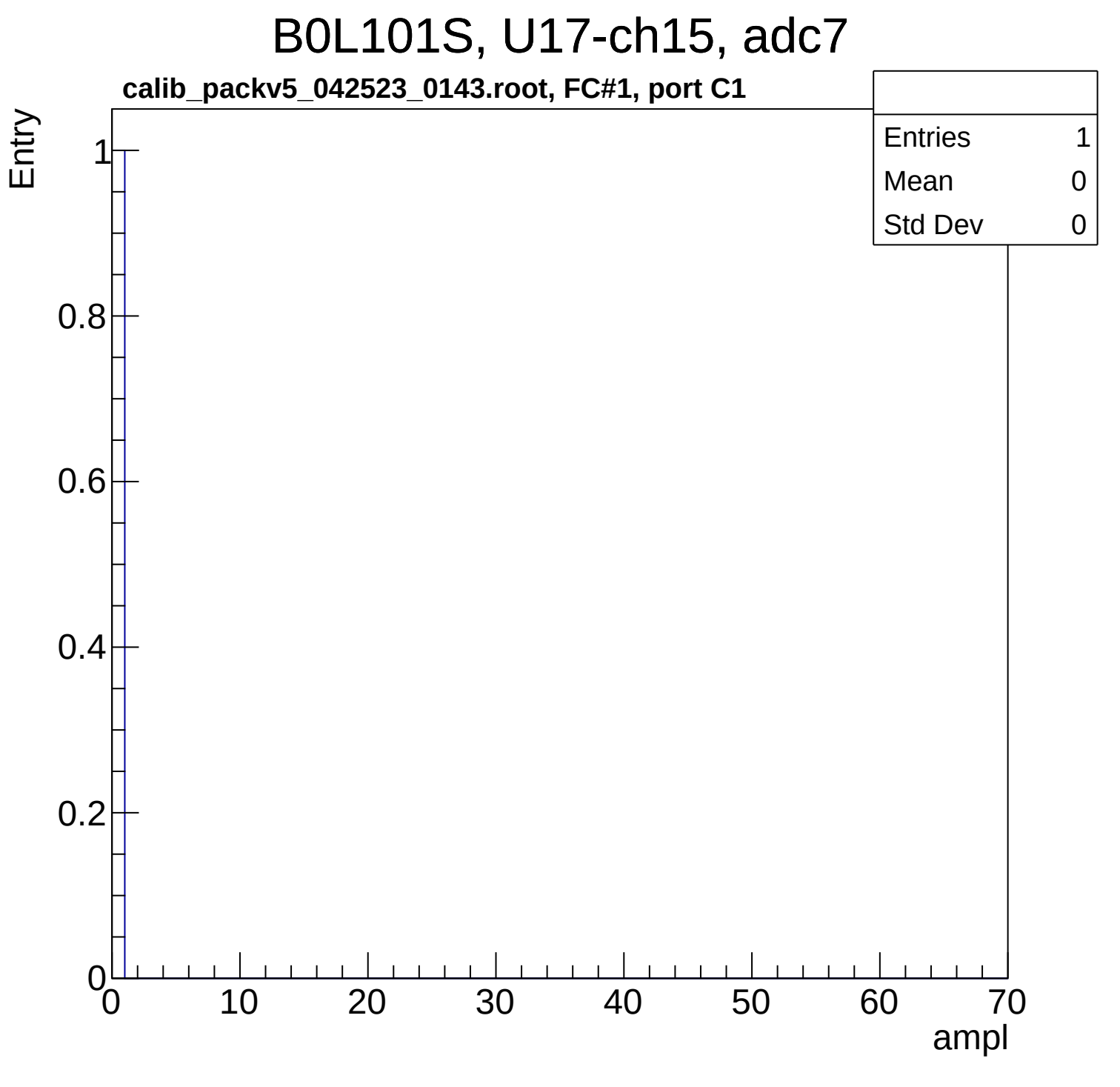

B0L101S, U17-ch15, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 42.73
Std Dev 3.401
Gaus mean : 43.0864
Gaus Width: 3.2714
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

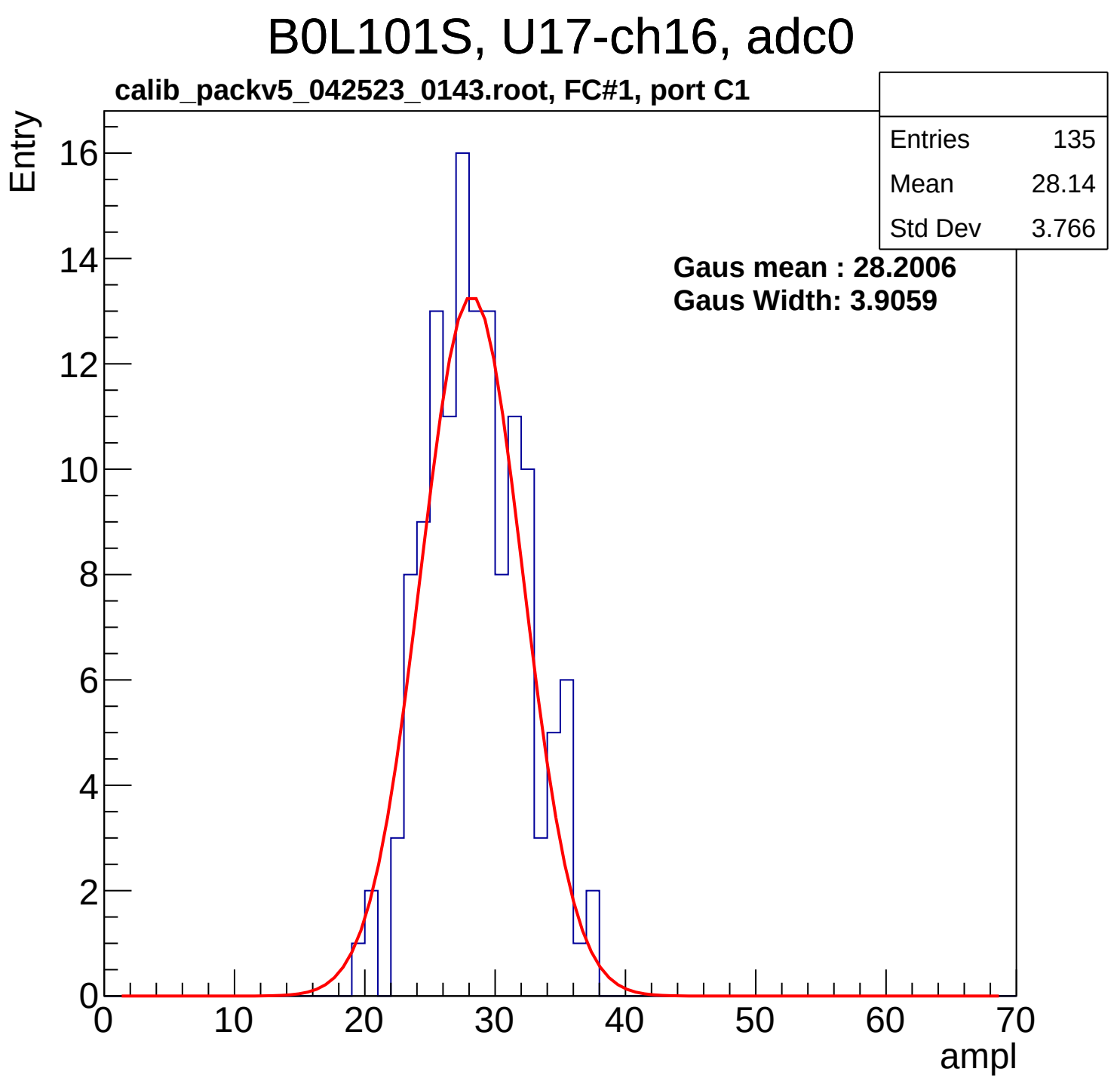

B0L101S, U17-ch15, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 160
Mean 50.45
Std Dev 3.655
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

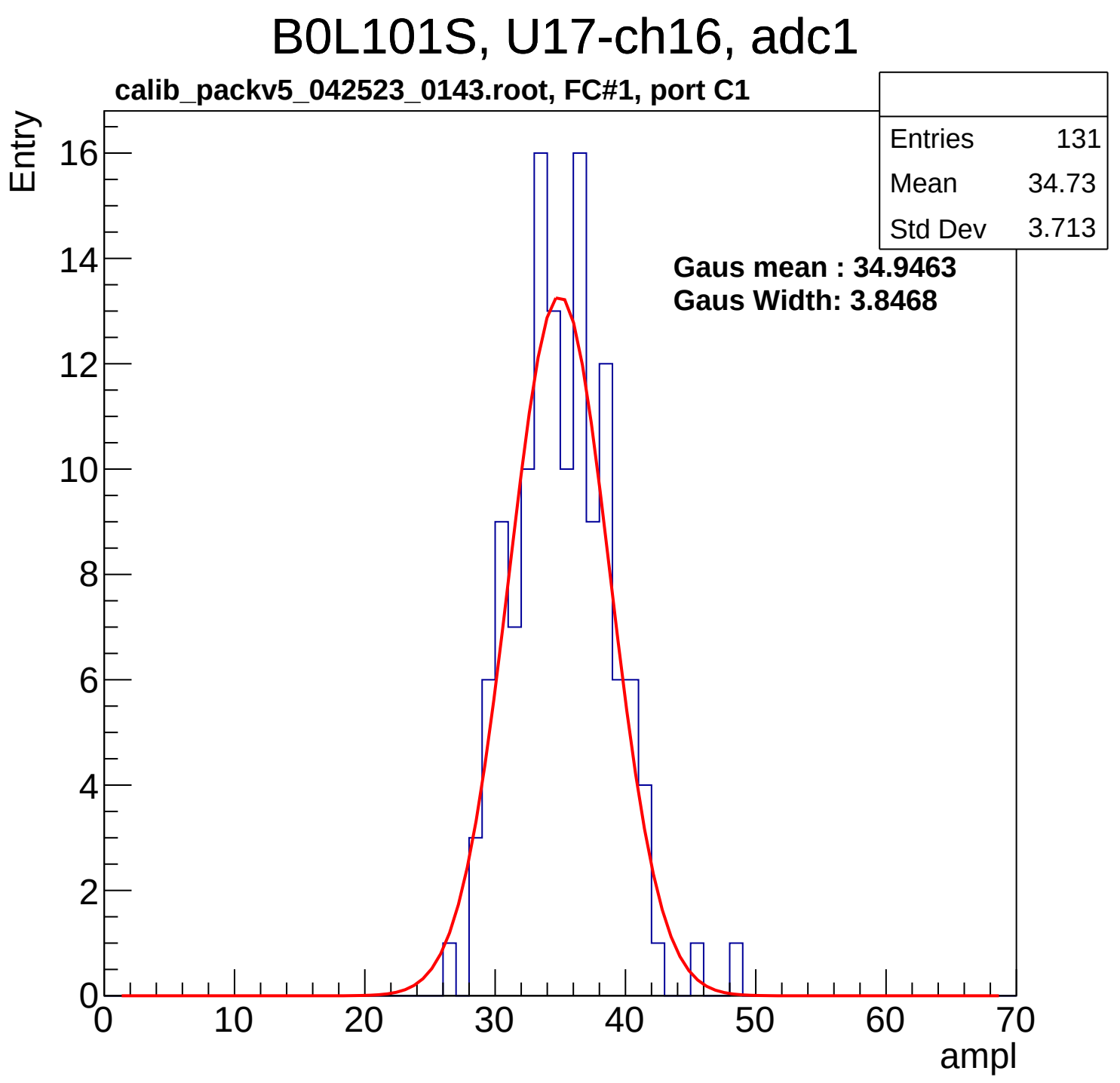

B0L101S, U17-ch15, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 57.1
Std Dev 2.877
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

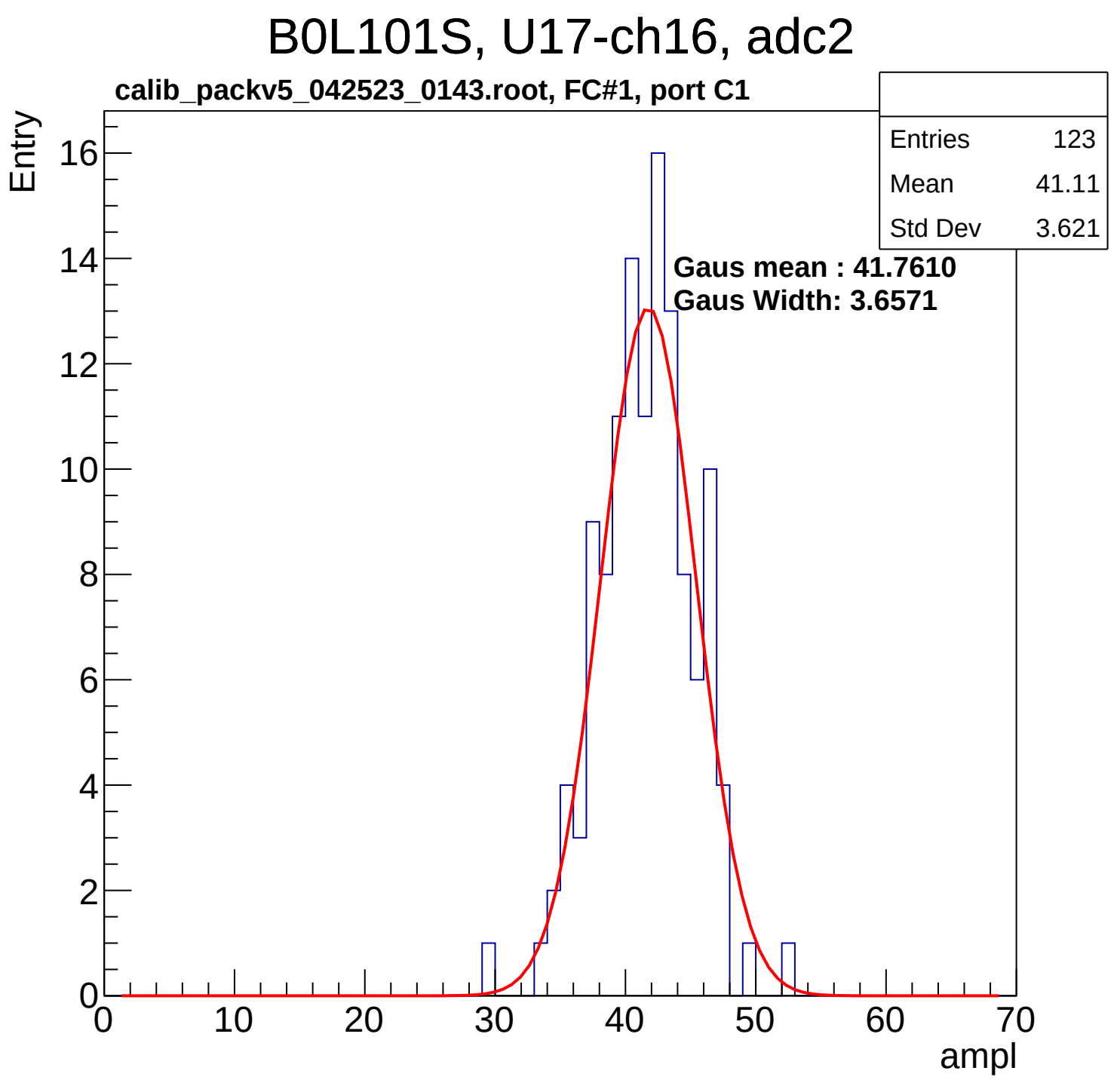

B0L101S, U17-ch15, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 83
Mean 58.29
Std Dev 9.524
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

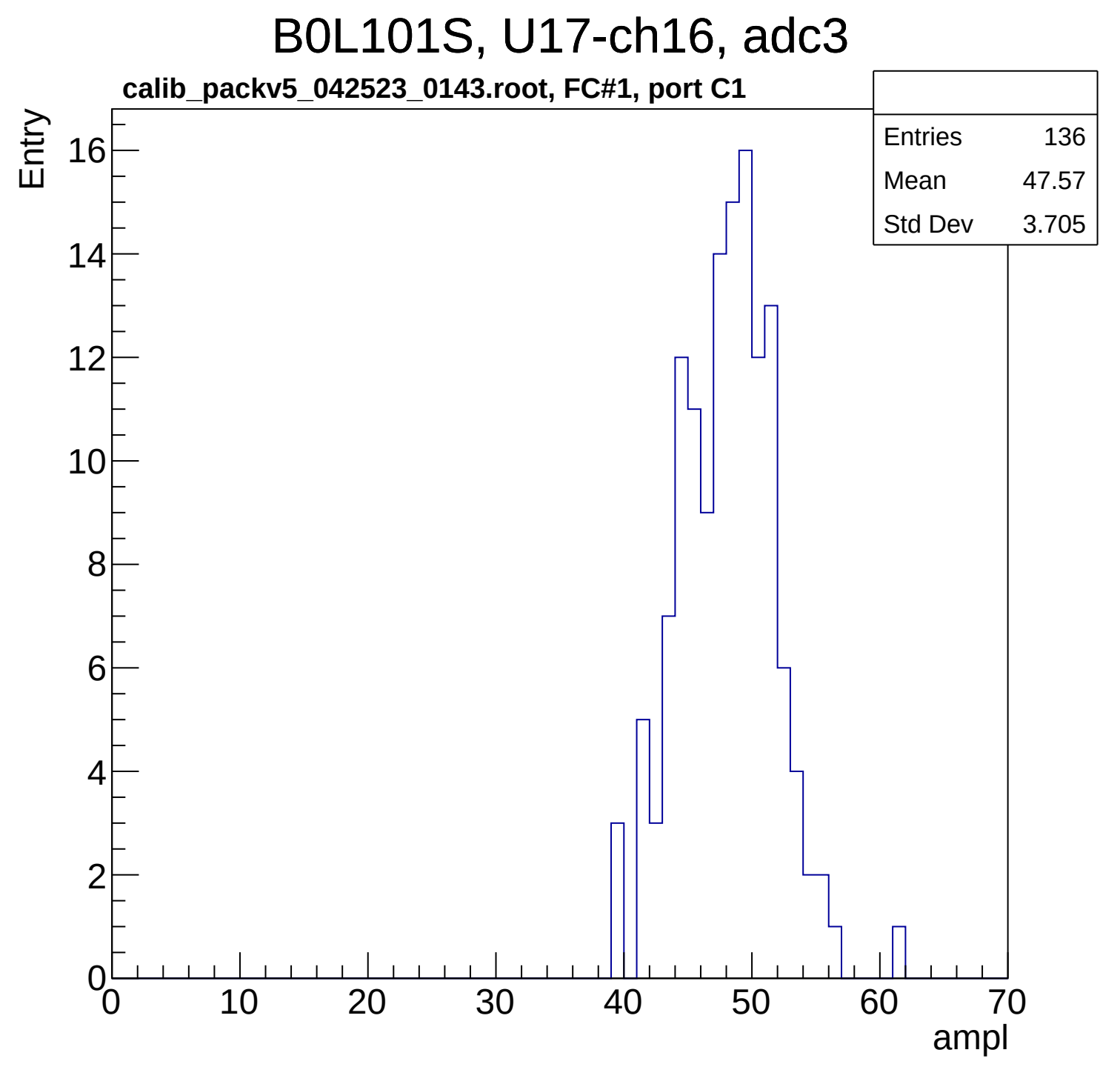

B0L101S, U17-ch15, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 62.25
Std Dev 0.7217
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

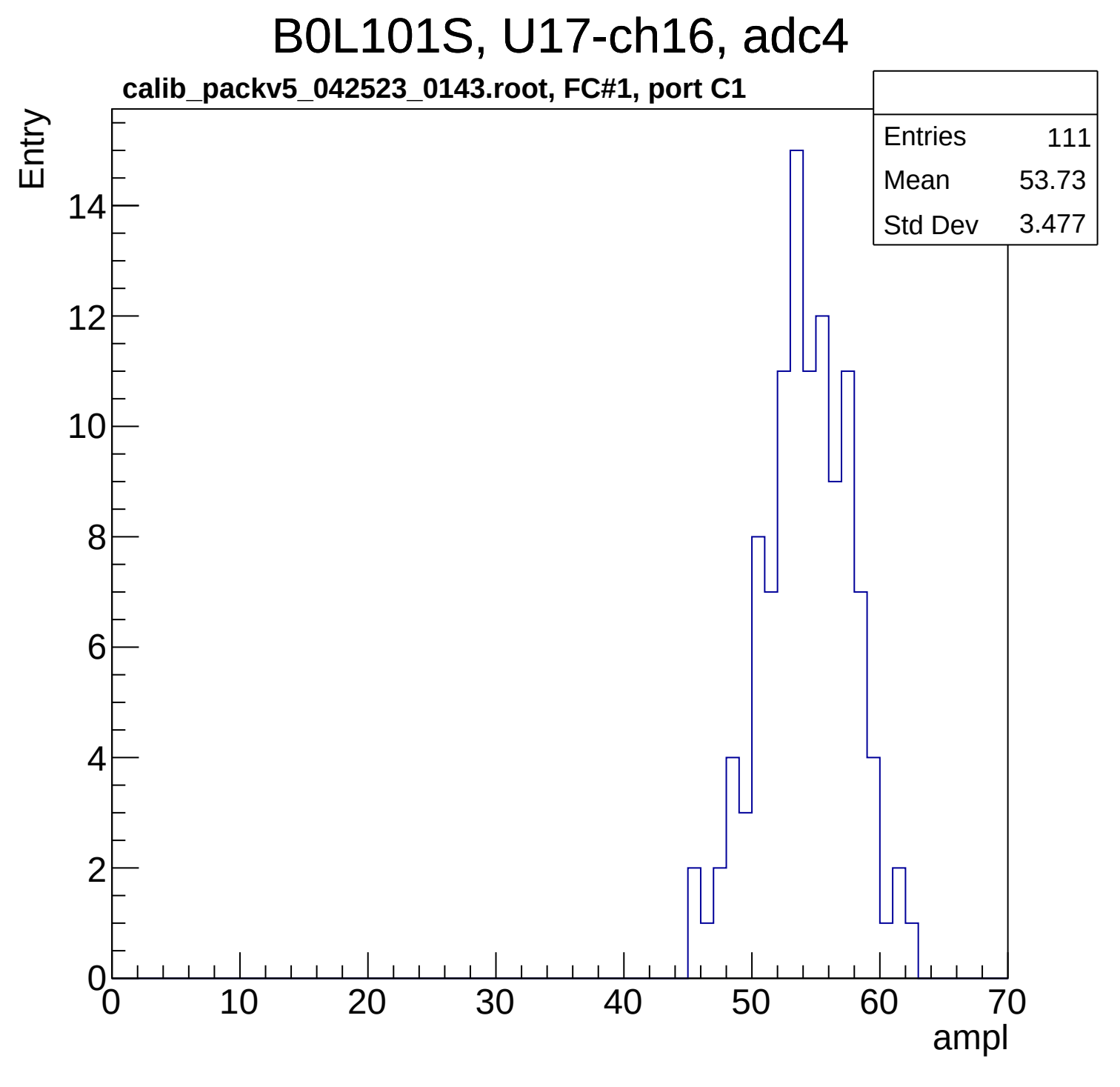

B0L101S, U17-ch15, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

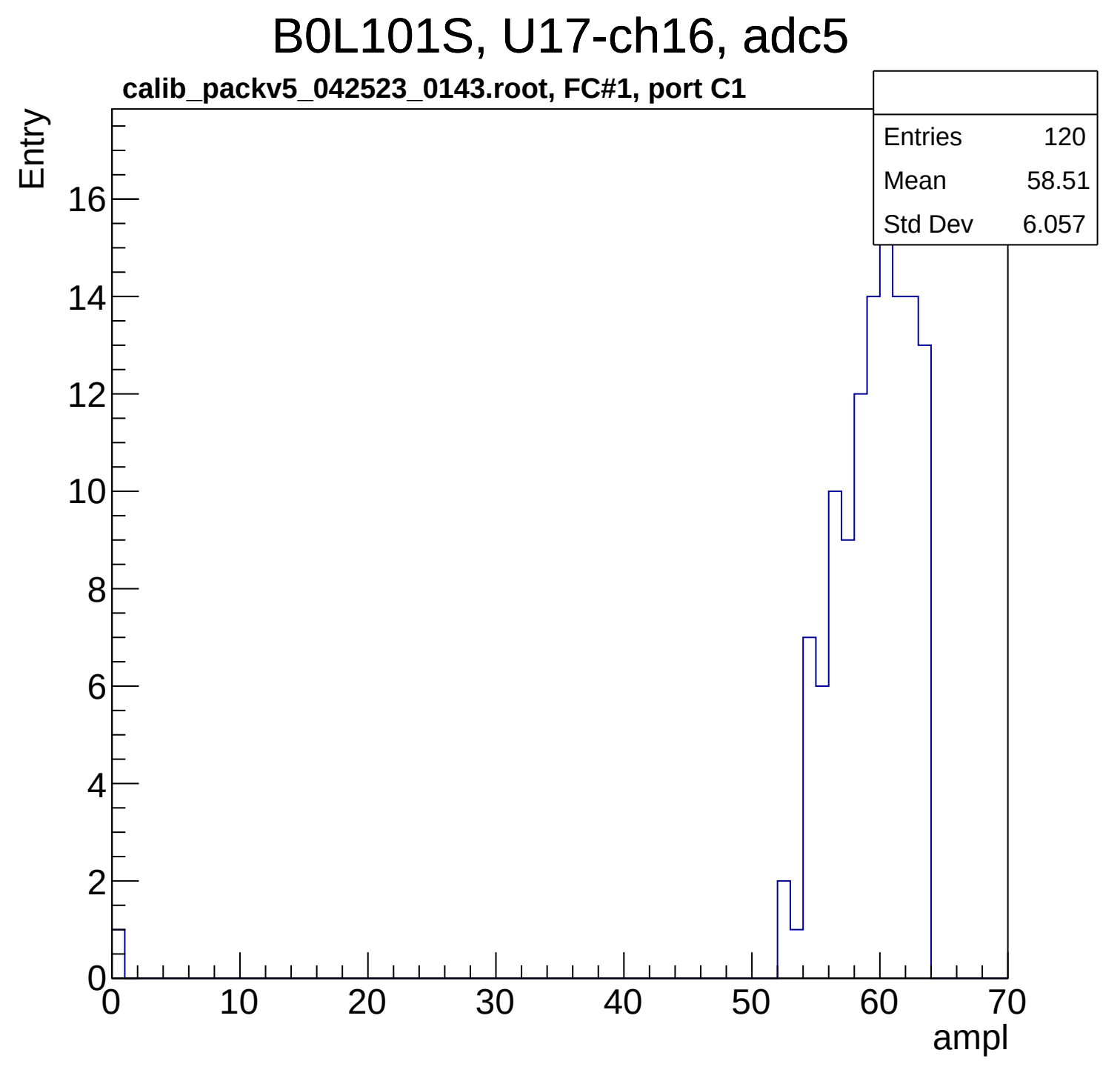

B0L101S, U17-ch16, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 28.14
Std Dev 3.766
Gaus mean : 28.2006
Gaus Width: 3.9059
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

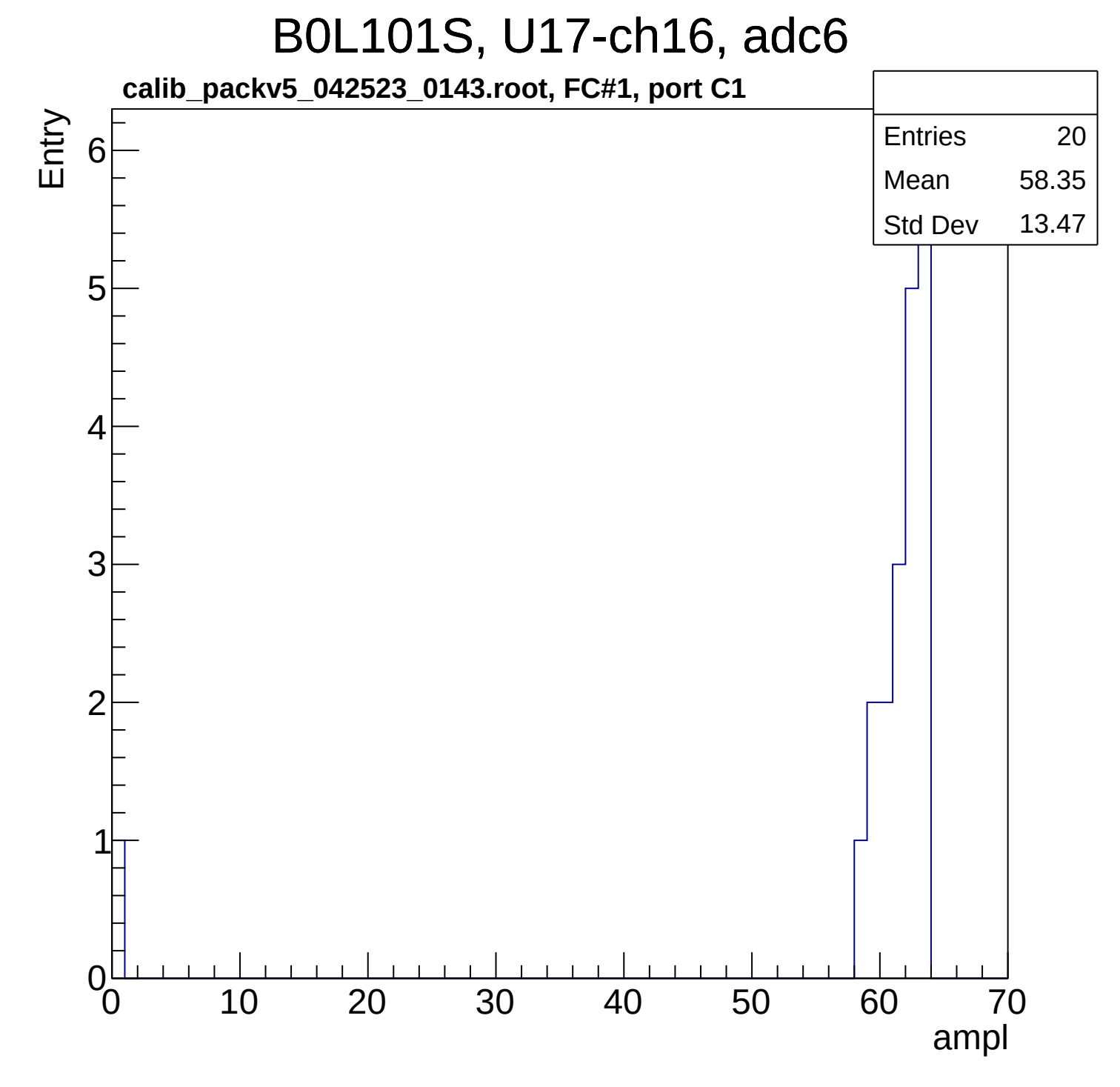

B0L101S, U17-ch16, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 34.73
Std Dev 3.713
Gaus mean : 34.9463
Gaus Width: 3.8468
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

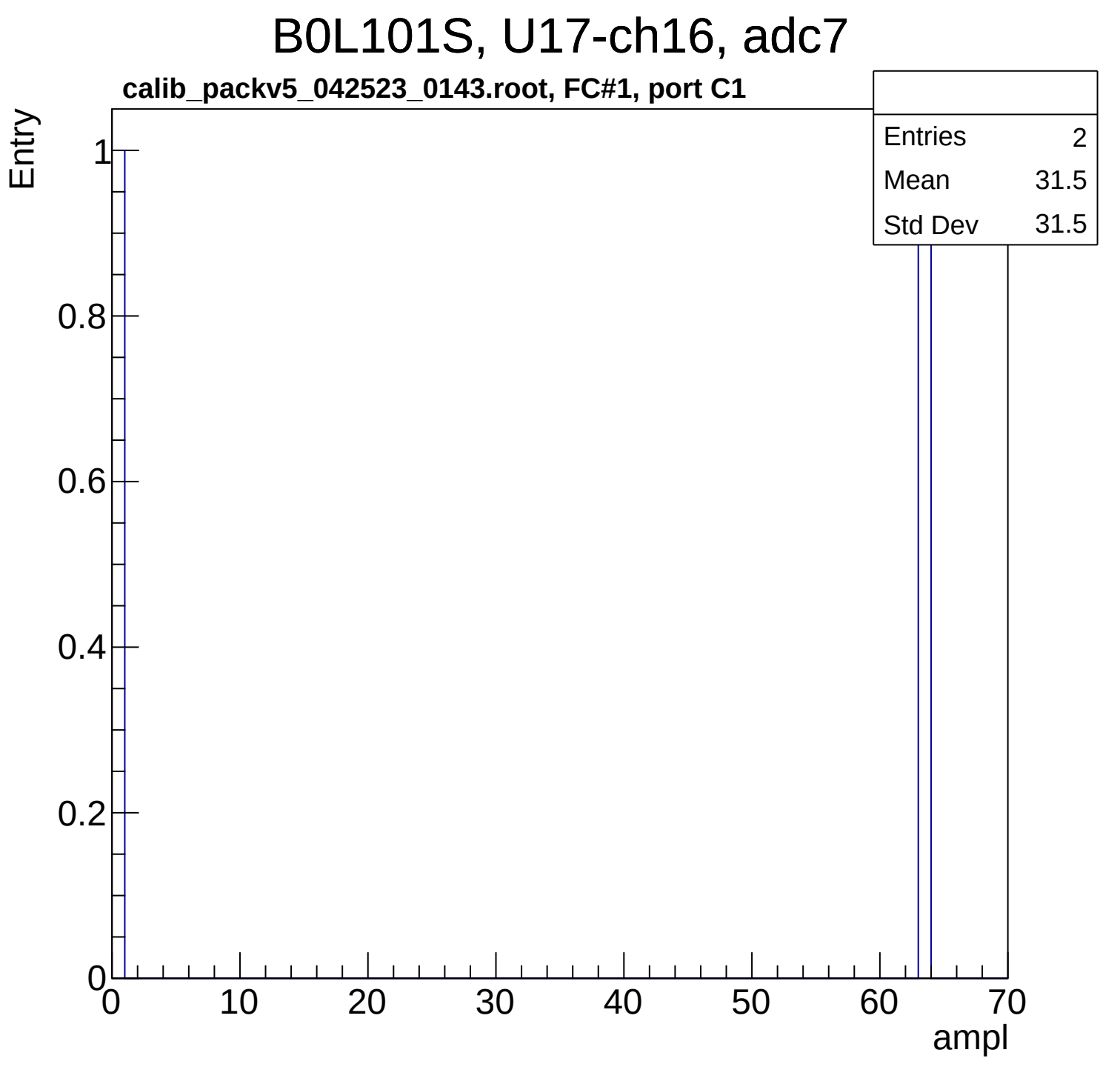

B0L101S, U17-ch16, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 41.11
Std Dev 3.621
Gaus mean : 41.7610
Gaus Width: 3.6571
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

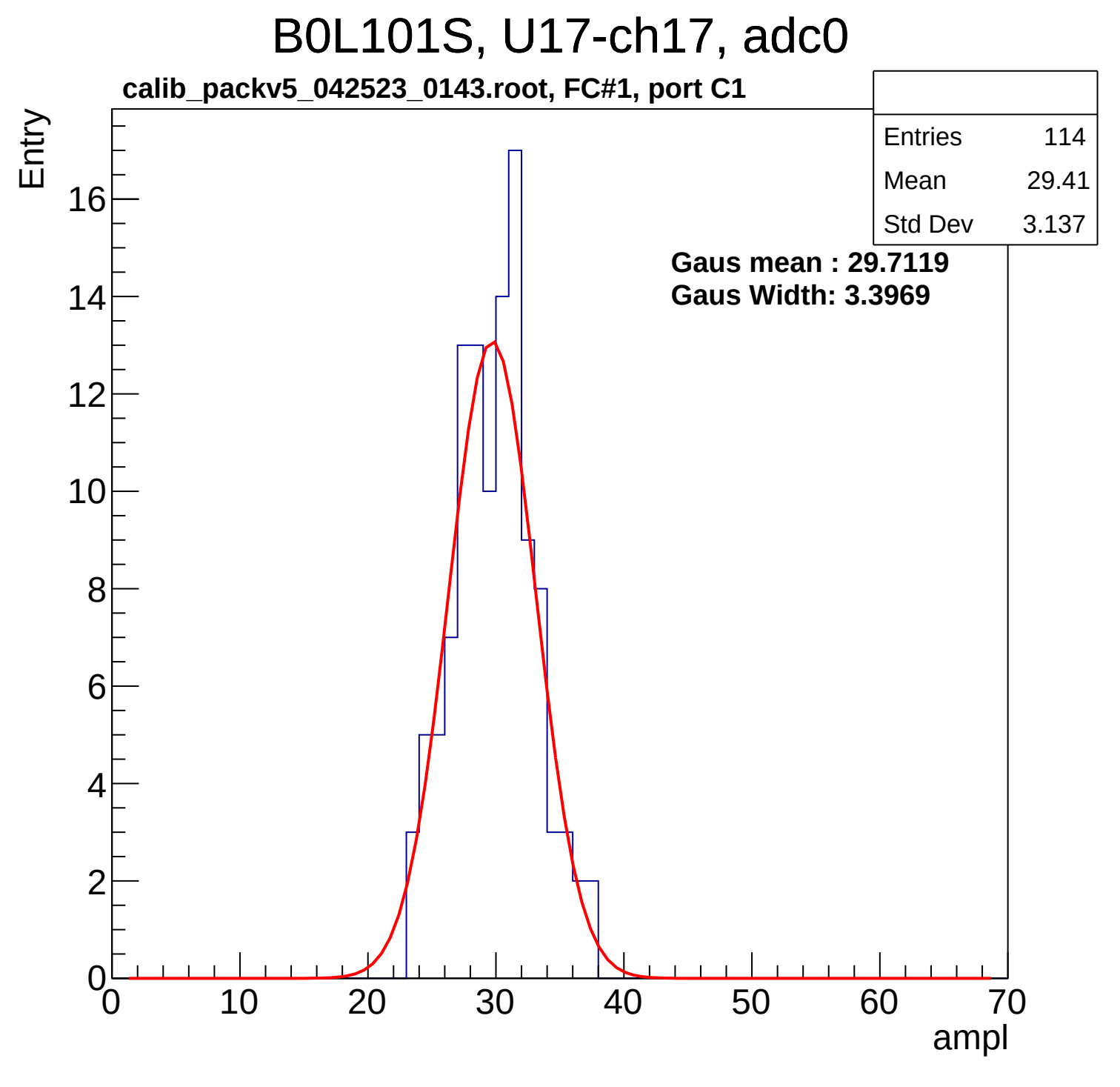

B0L101S, U17-ch16, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 47.57
Std Dev 3.705
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

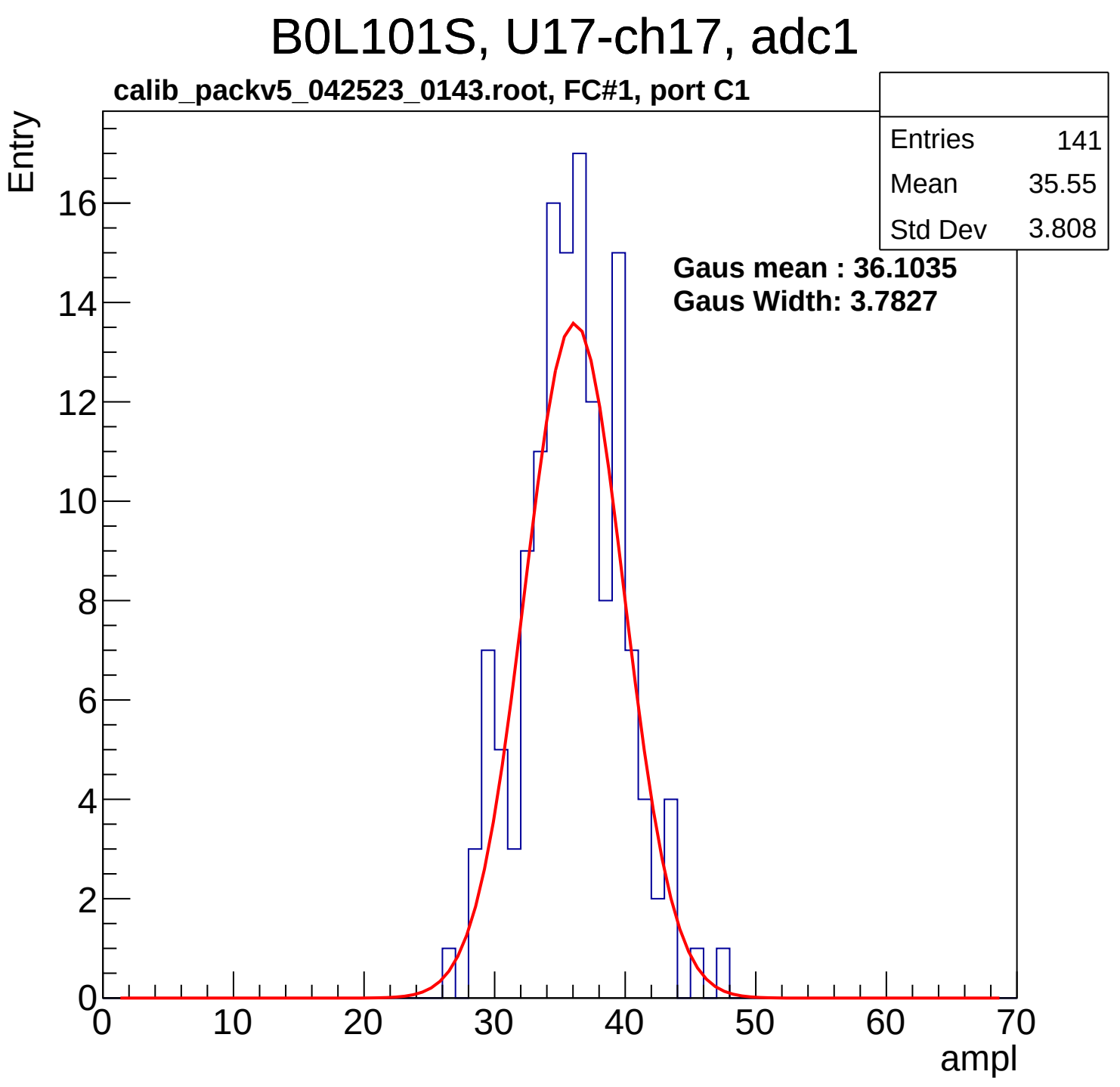

B0L101S, U17-ch16, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 53.73
Std Dev 3.477
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

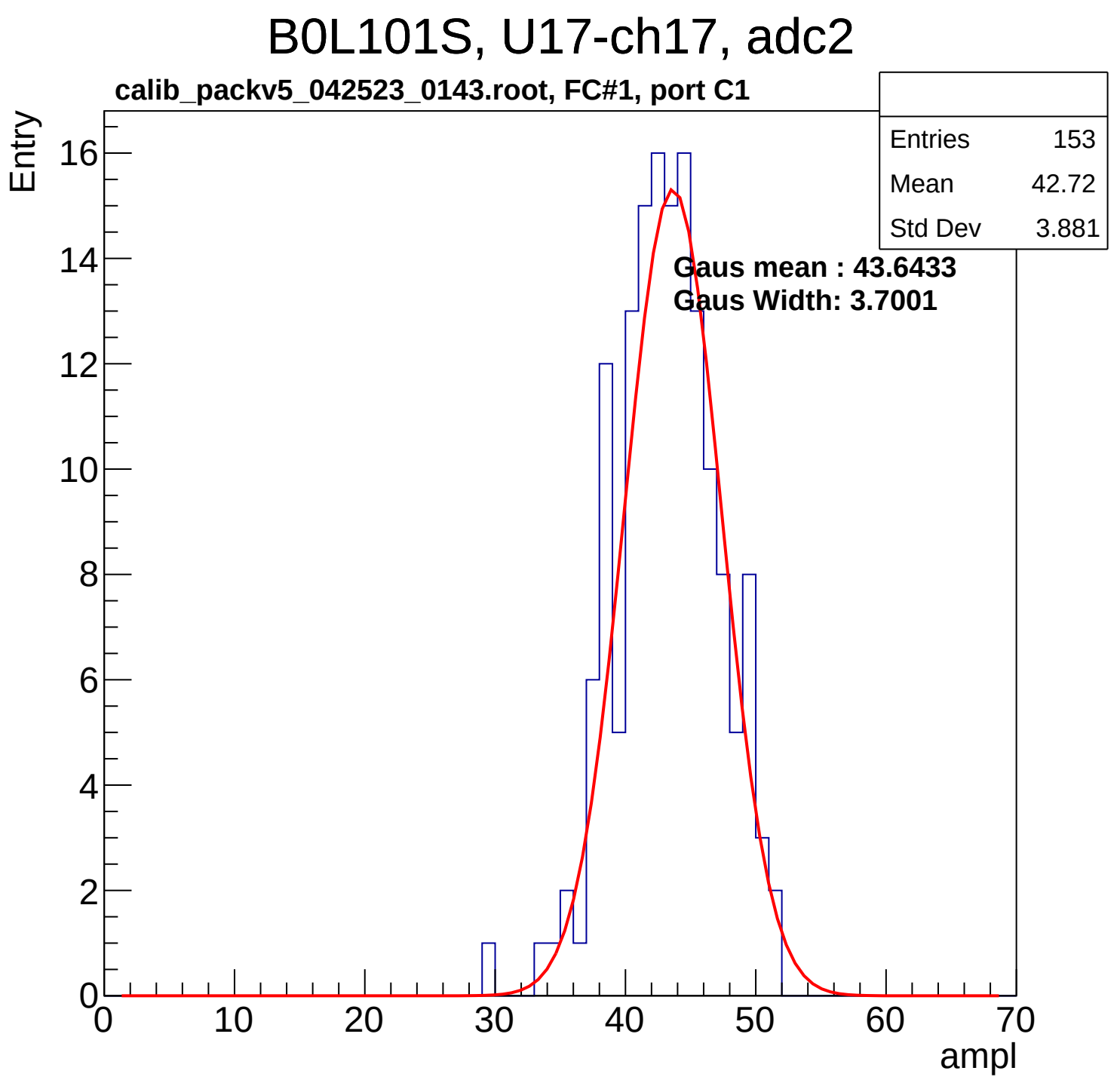

B0L101S, U17-ch16, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 58.51
Std Dev 6.057
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

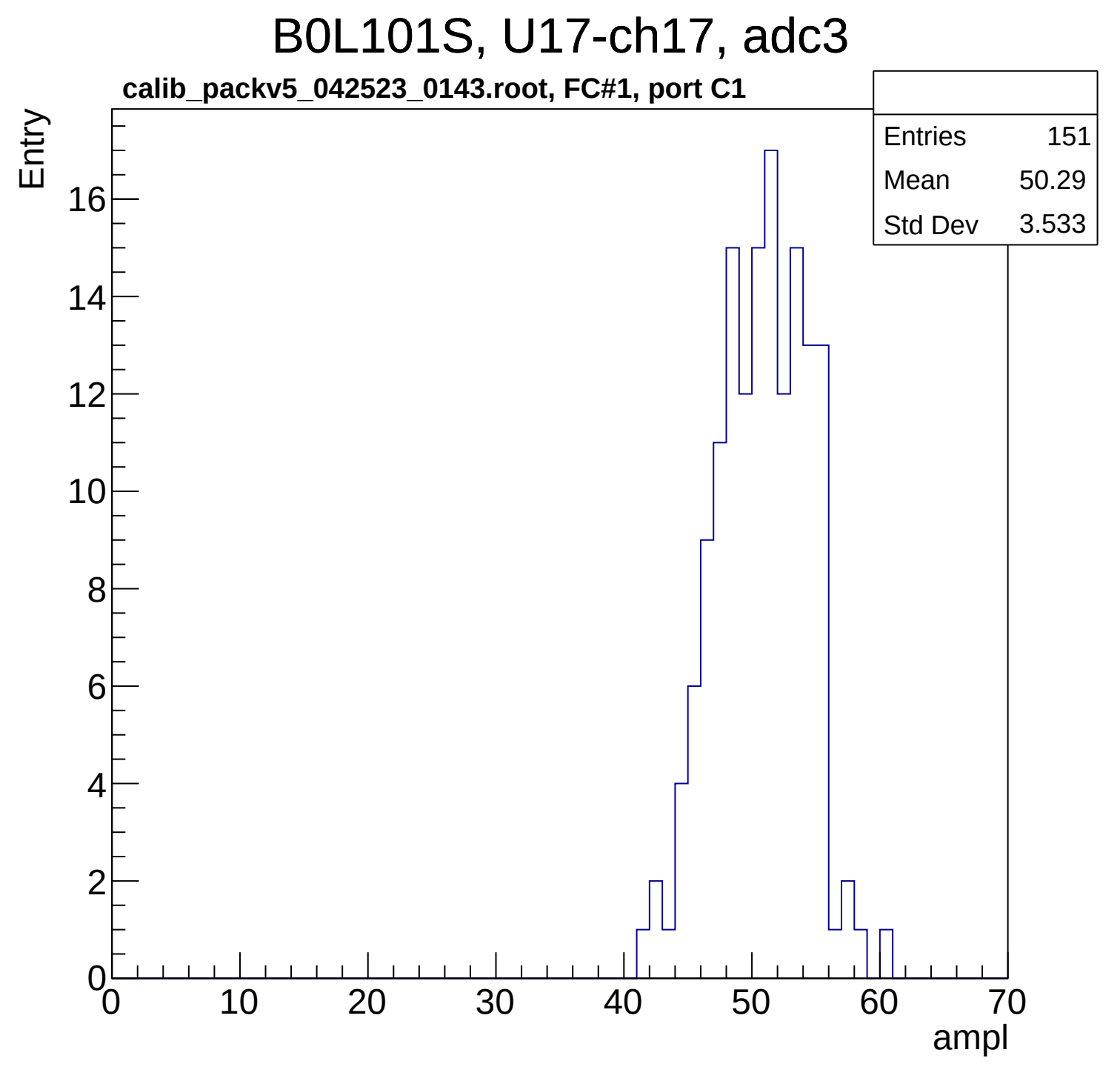

B0L101S, U17-ch16, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 20
Mean 58.35
Std Dev 13.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

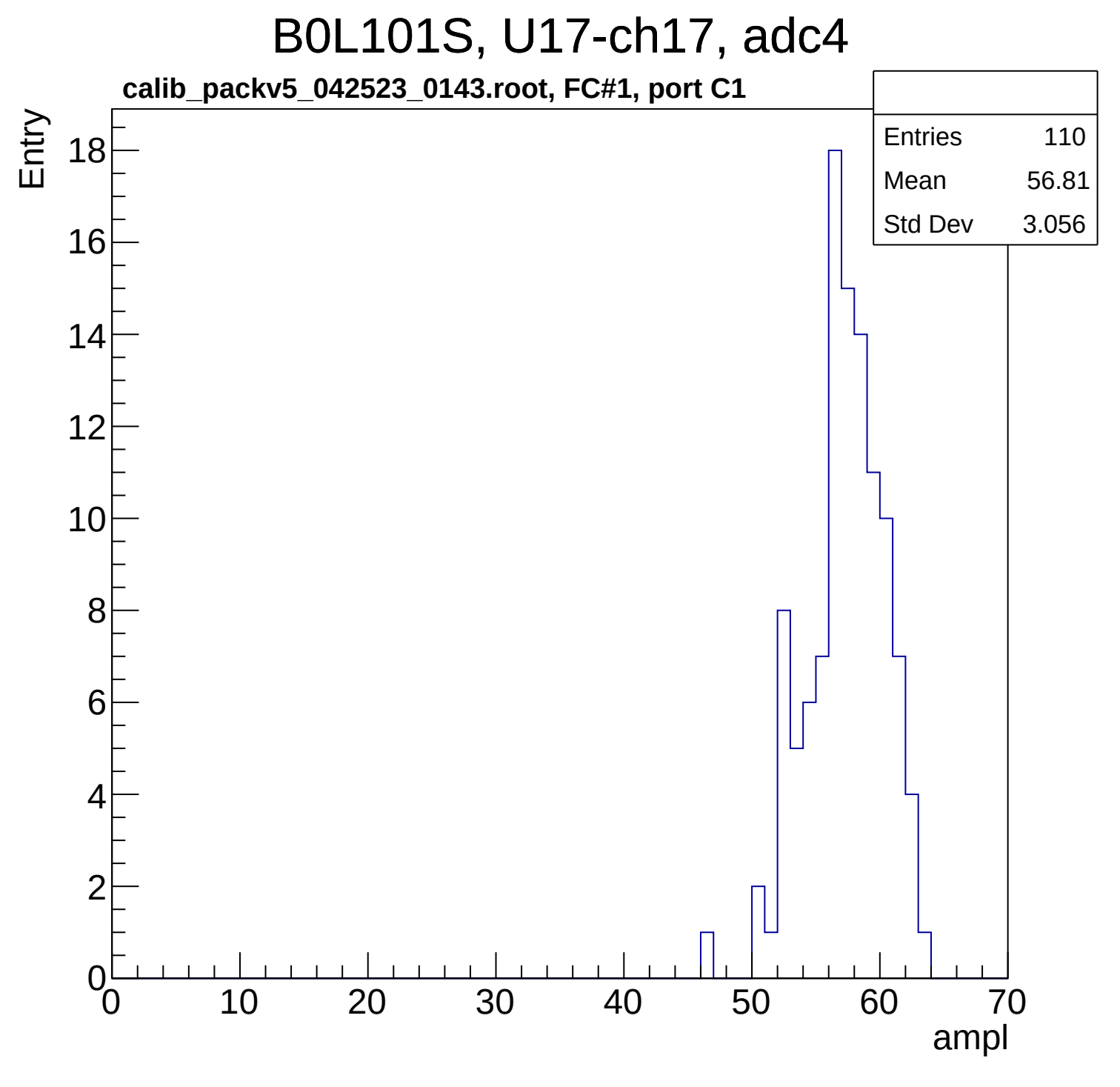

B0L101S, U17-ch16, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

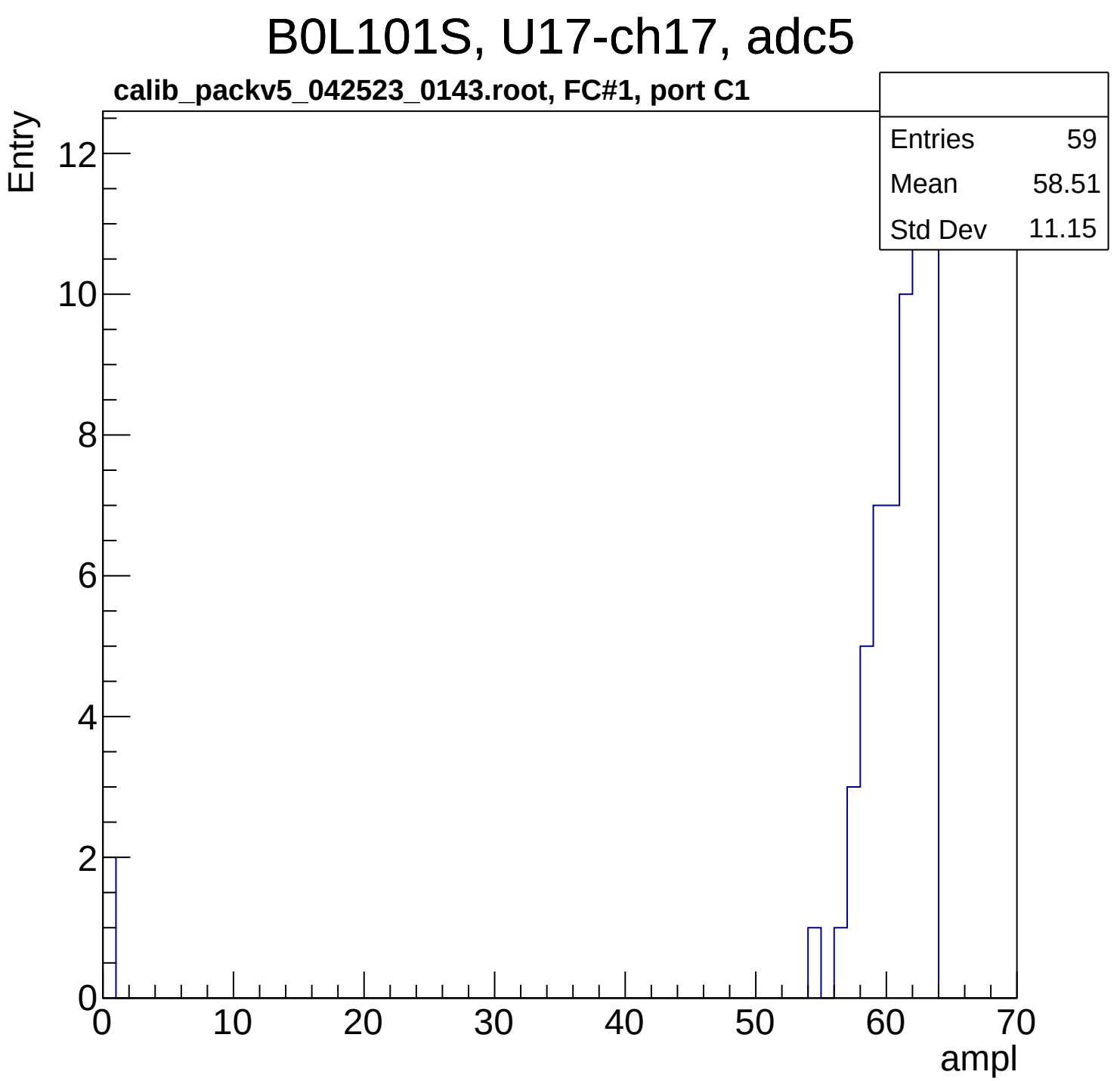

B0L101S, U17-ch17, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 29.41
Std Dev 3.137
Gaus mean : 29.7119
Gaus Width: 3.3969
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

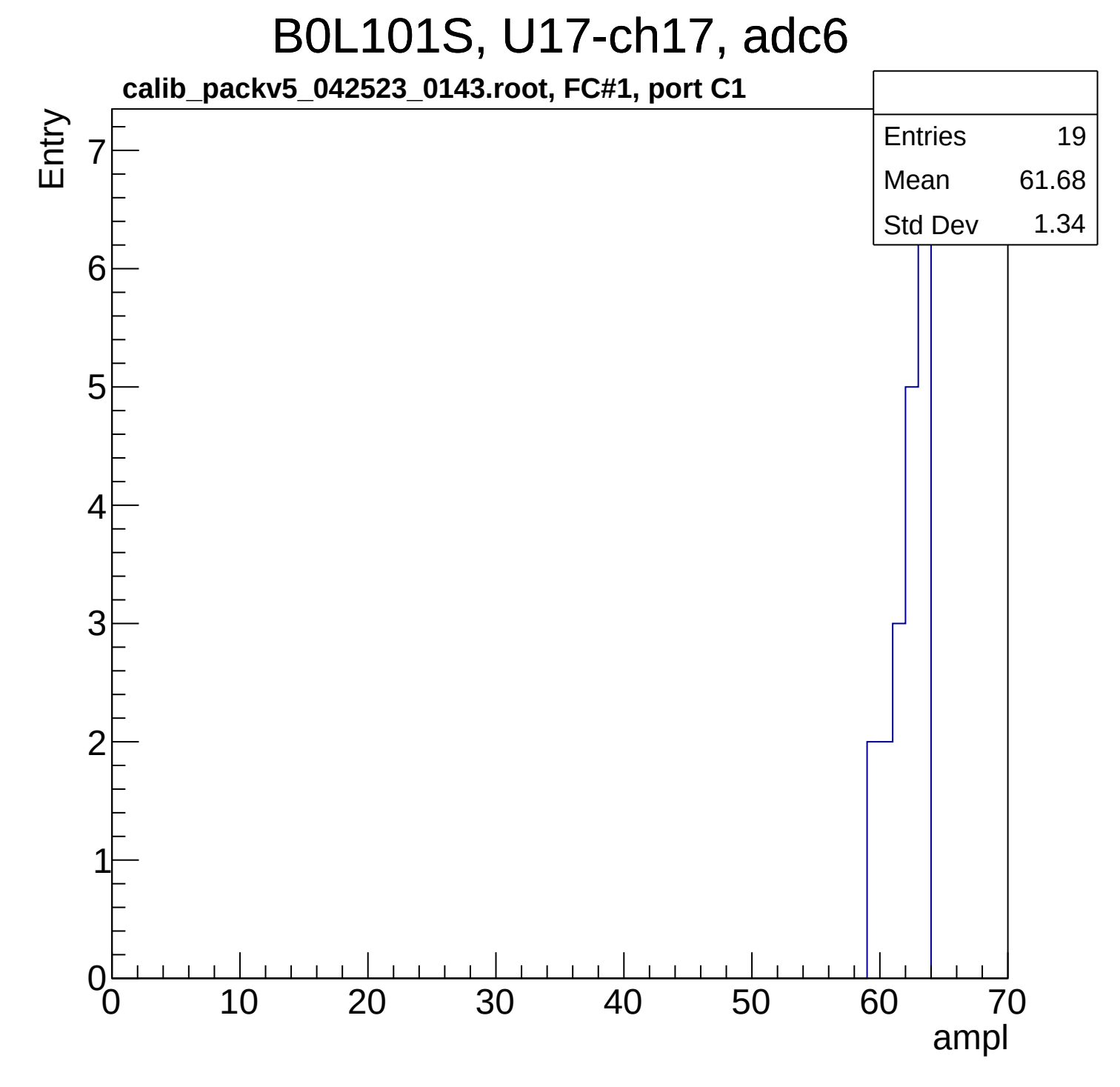

B0L101S, U17-ch17, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 35.55
Std Dev 3.808
Gaus mean : 36.1035
Gaus Width: 3.7827
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

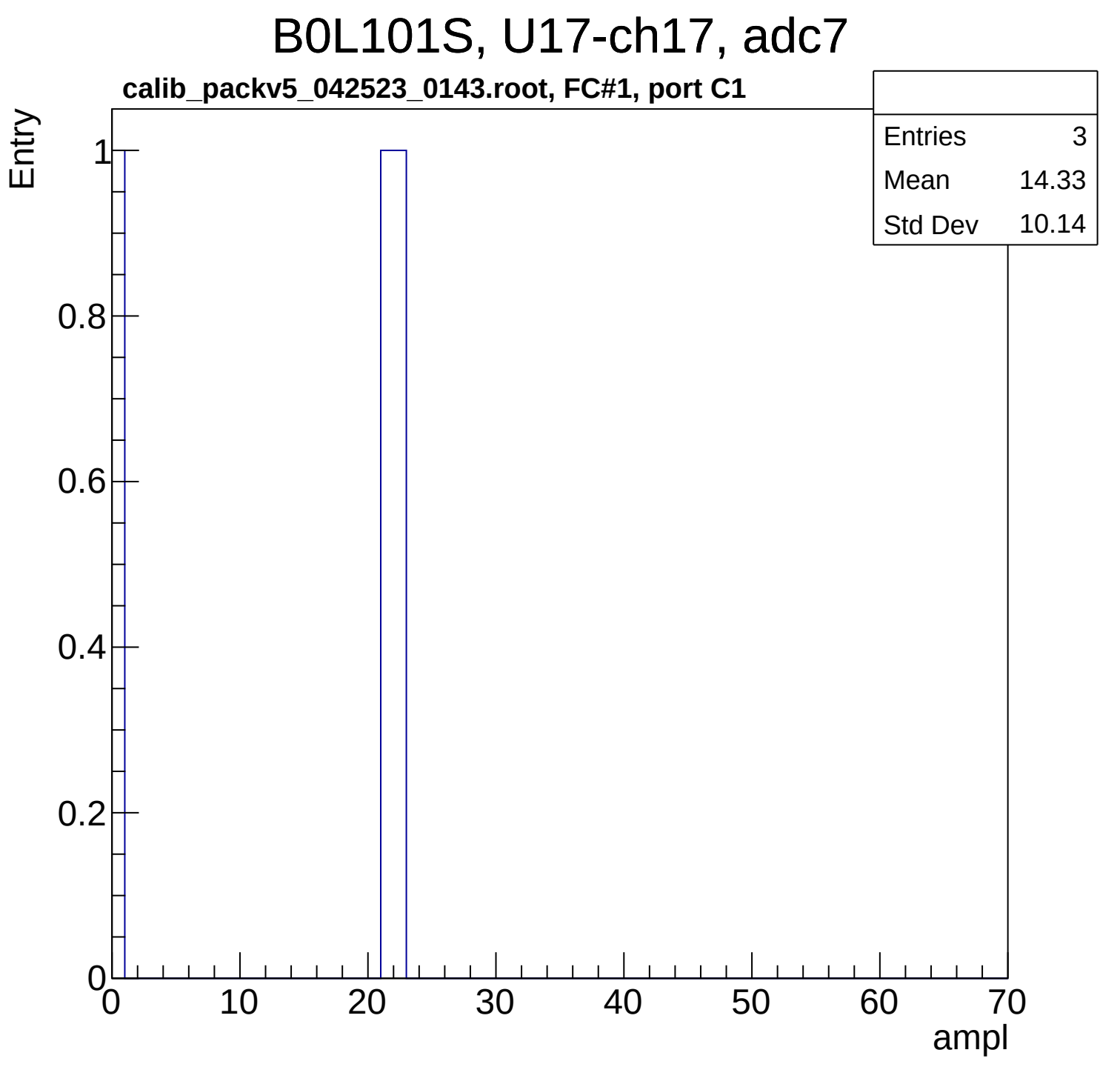

B0L101S, U17-ch17, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 42.72
Std Dev 3.881
Gaus mean : 43.6433
Gaus Width: 3.7001
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

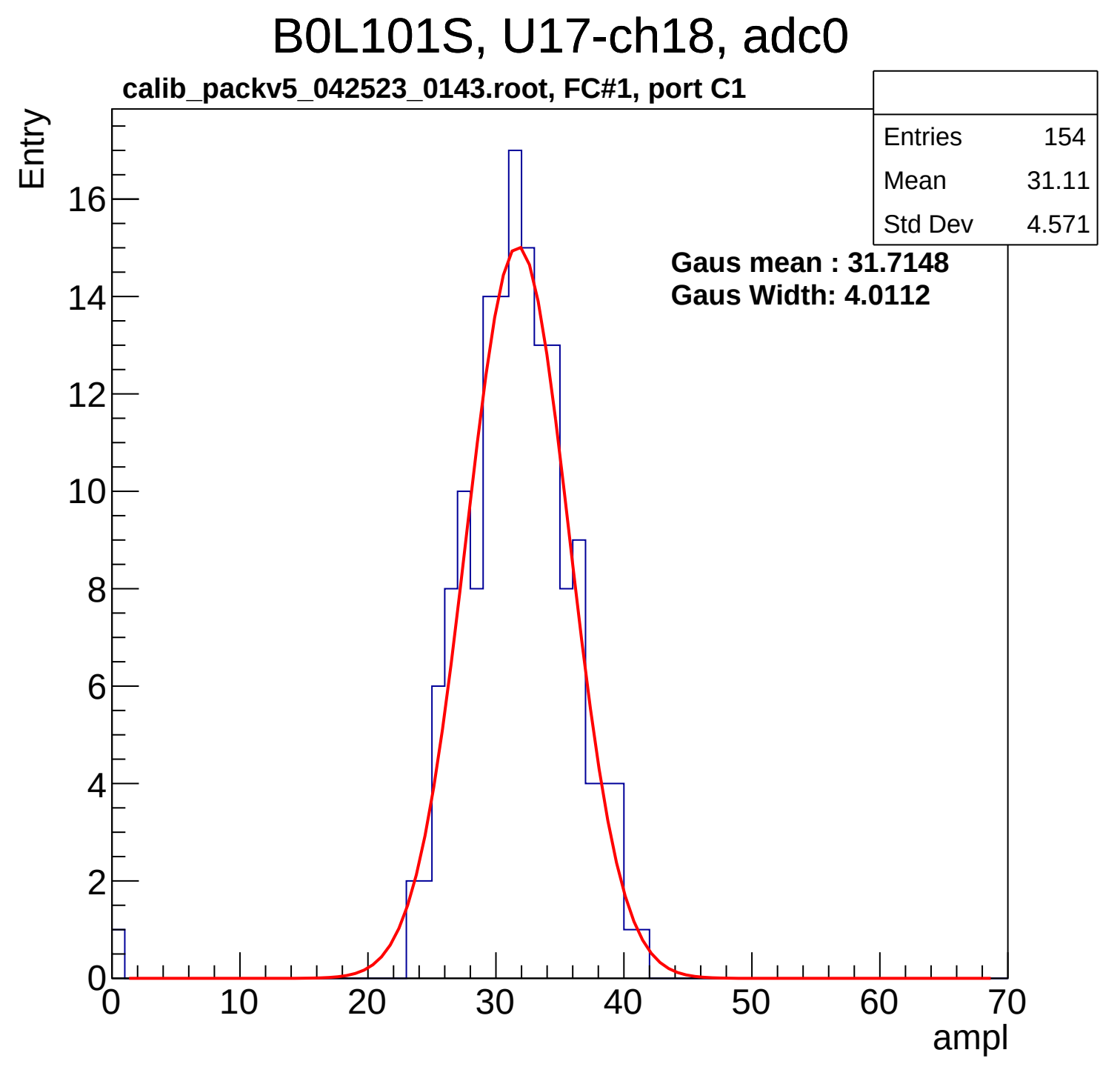

B0L101S, U17-ch17, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 50.29
Std Dev 3.533
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

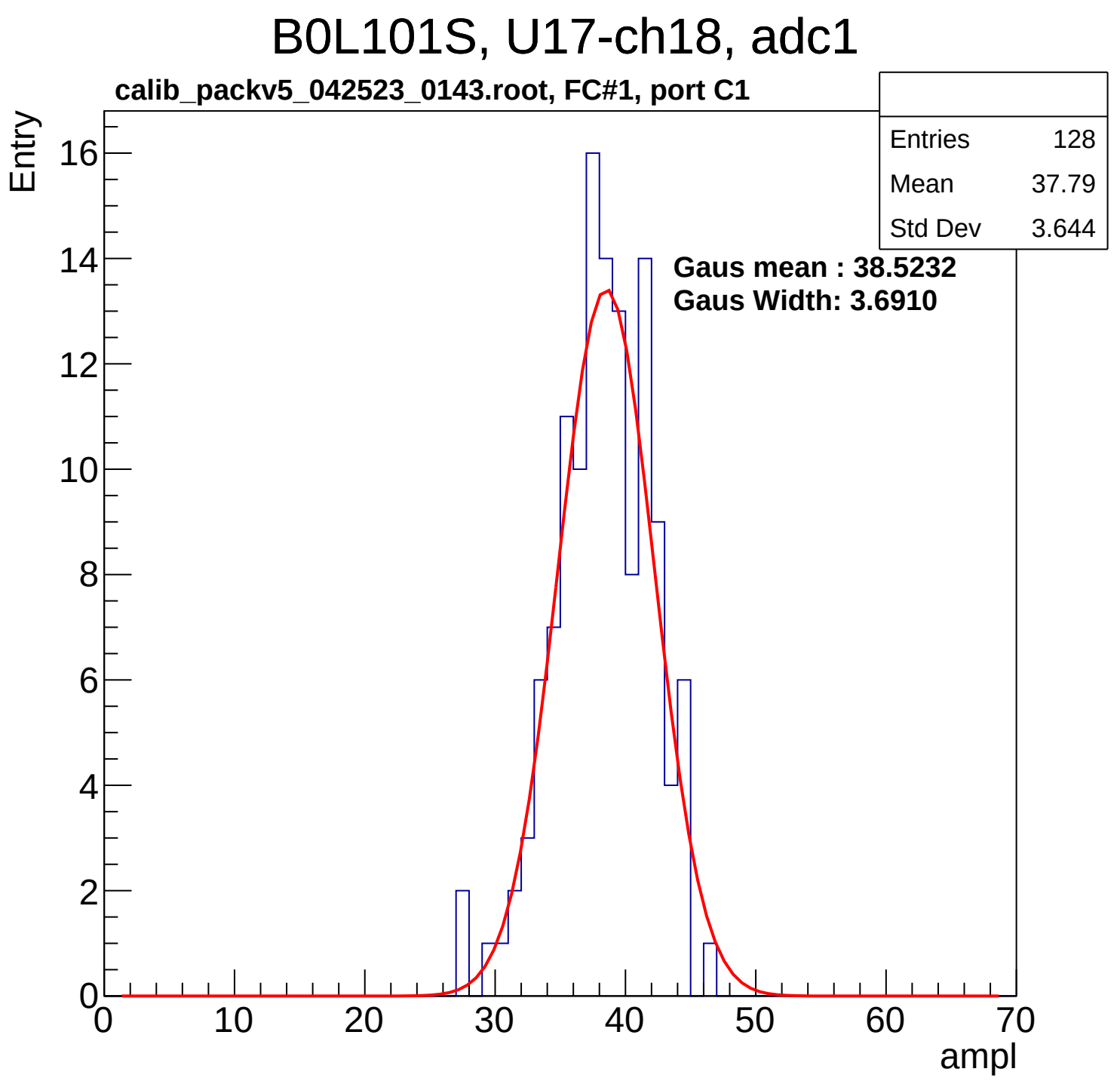

B0L101S, U17-ch17, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 56.81
Std Dev 3.056
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

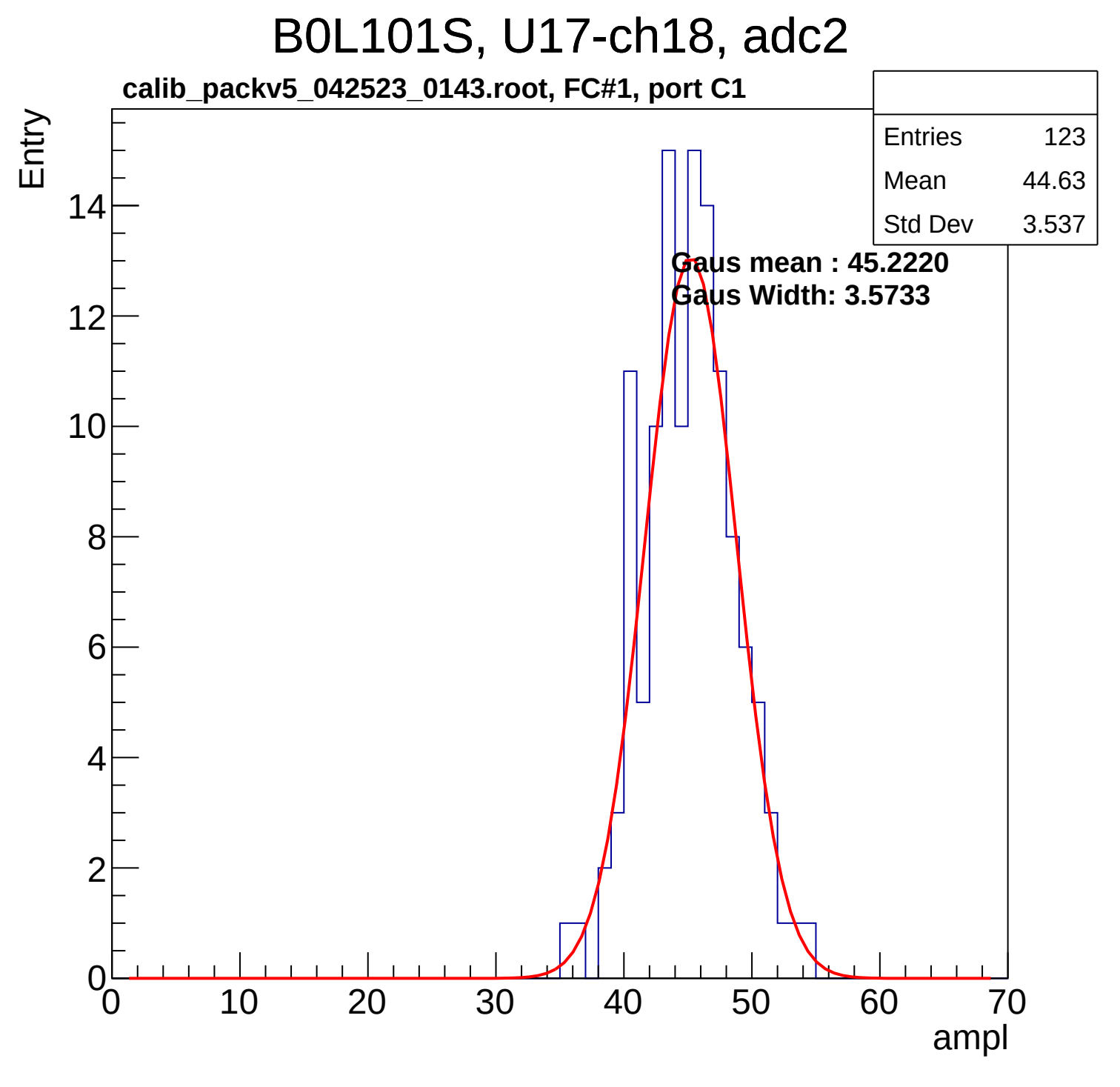

B0L101S, U17-ch17, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 59
Mean 58.51
Std Dev 11.15
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

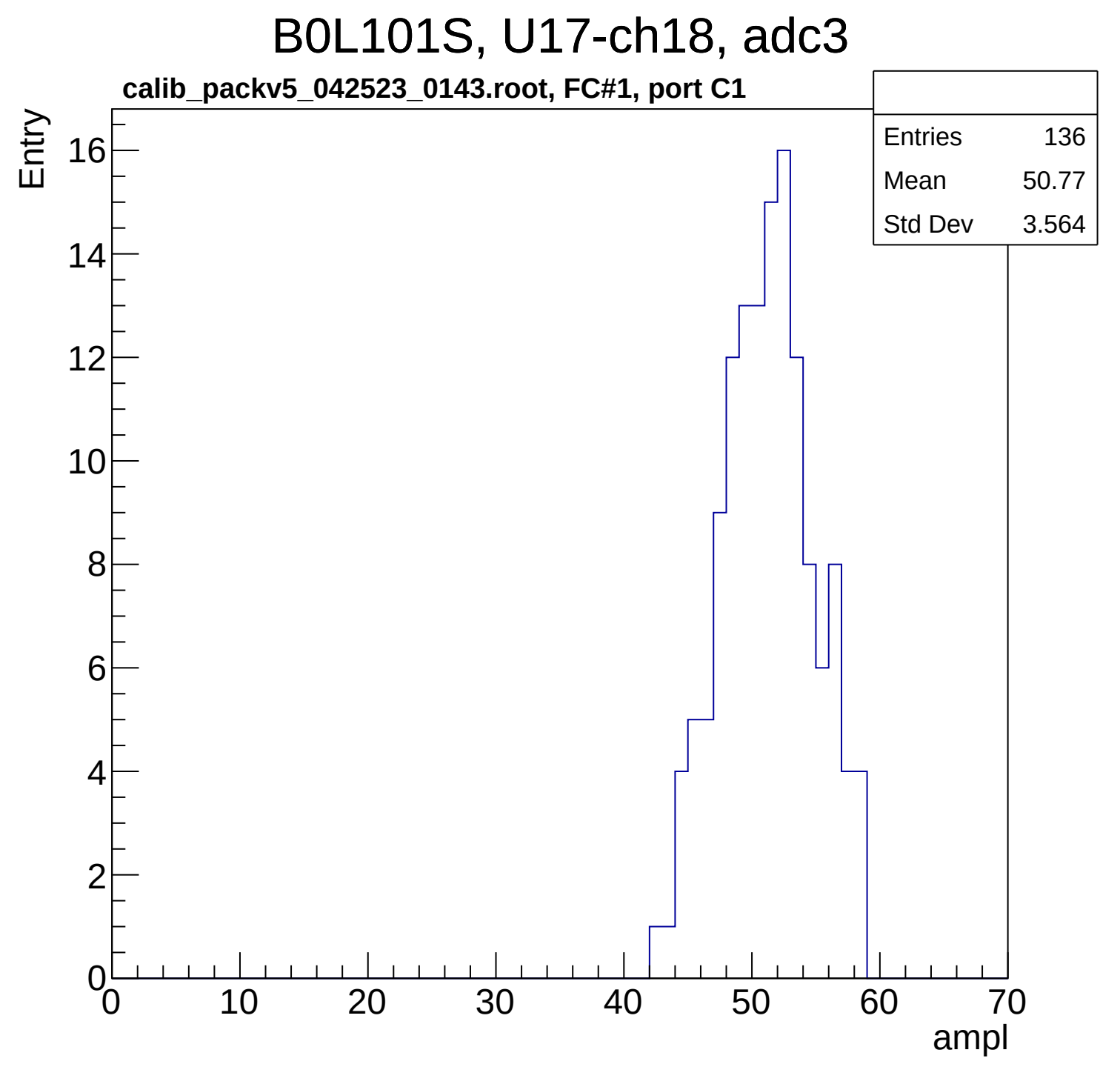

B0L101S, U17-ch17, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 19
Mean 61.68
Std Dev 1.34
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

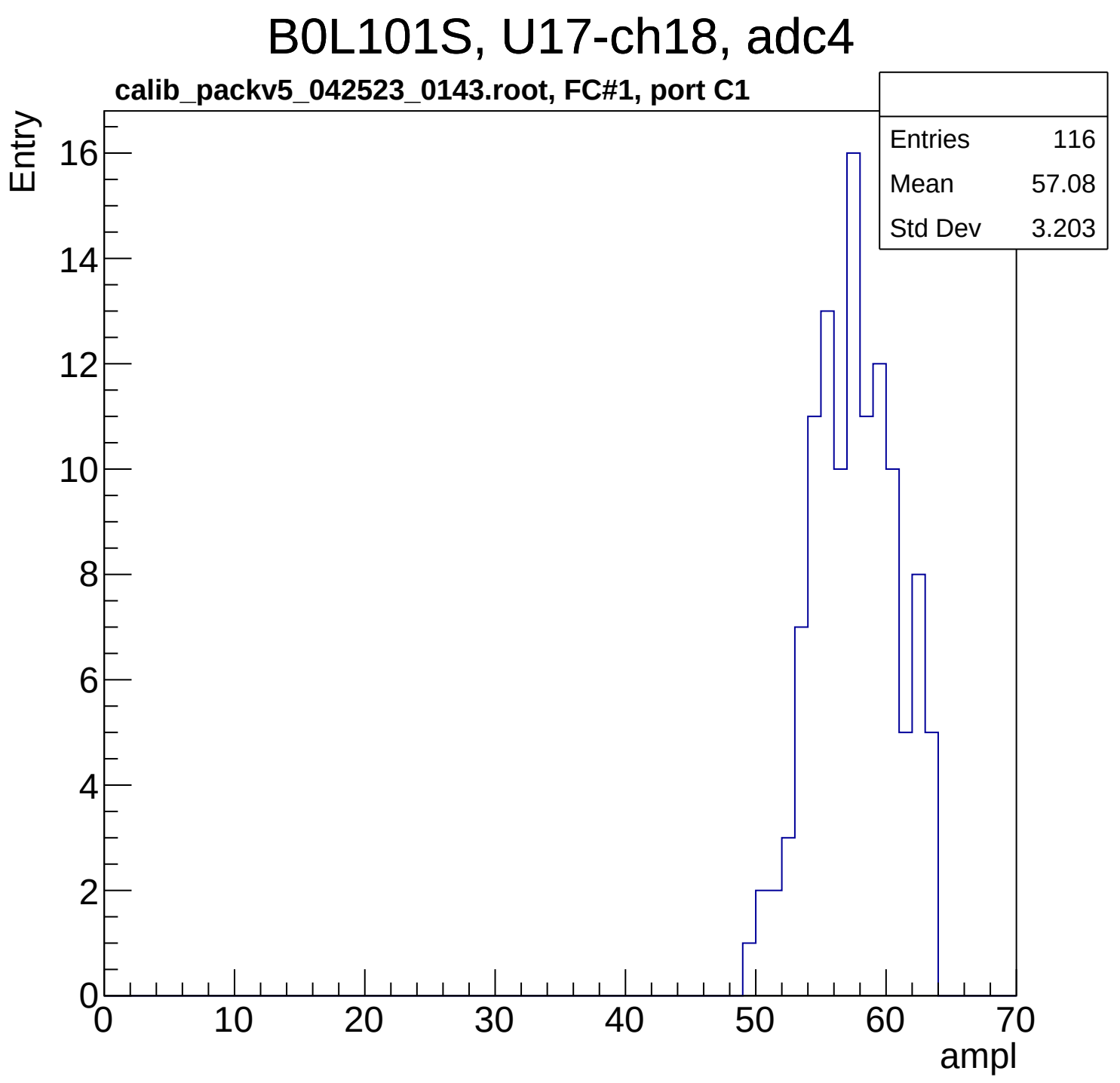

B0L101S, U17-ch17, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 14.33
Std Dev 10.14
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

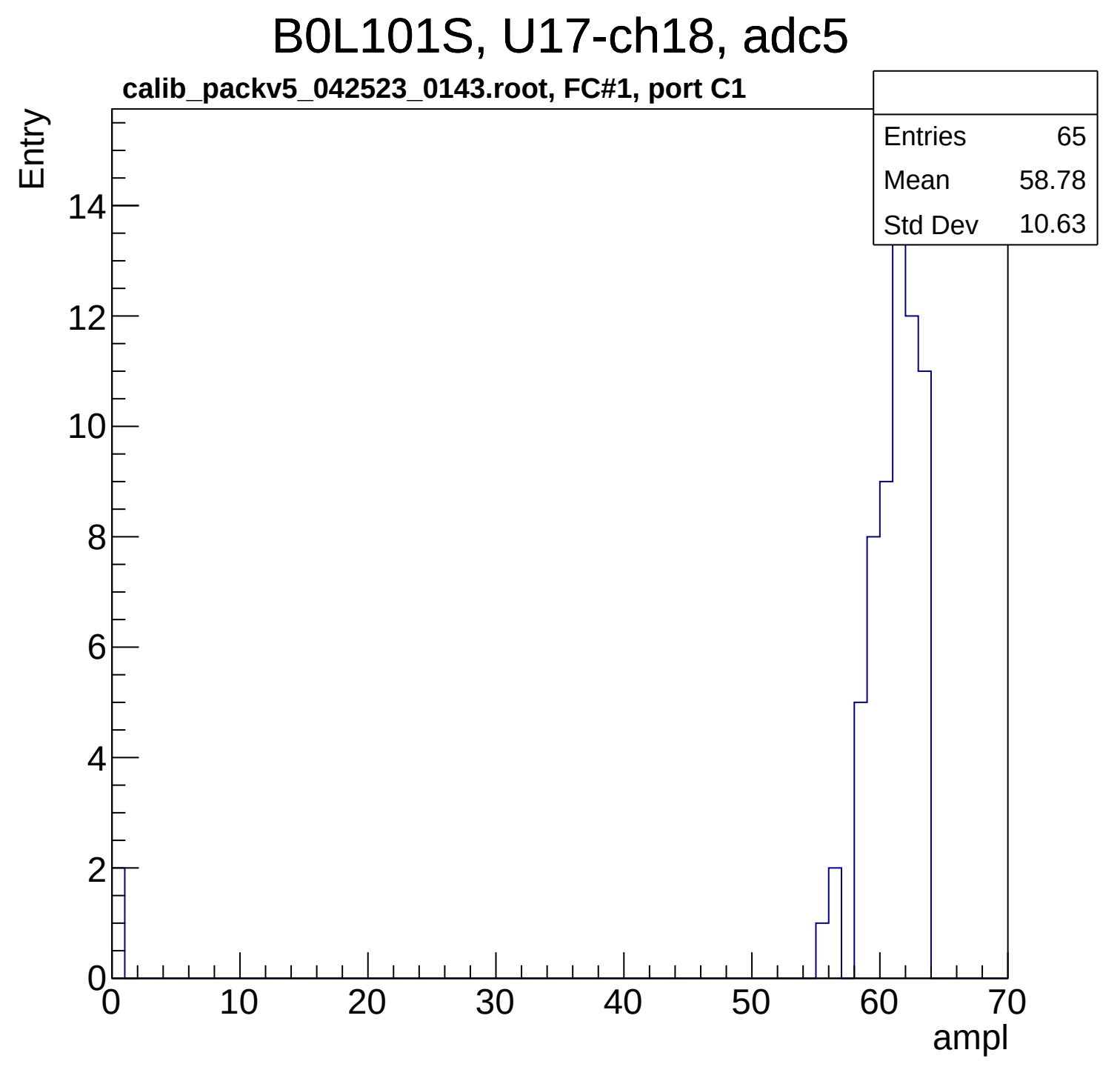

B0L101S, U17-ch18, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 31.11
Std Dev 4.571
Gaus mean : 31.7148
Gaus Width: 4.0112
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

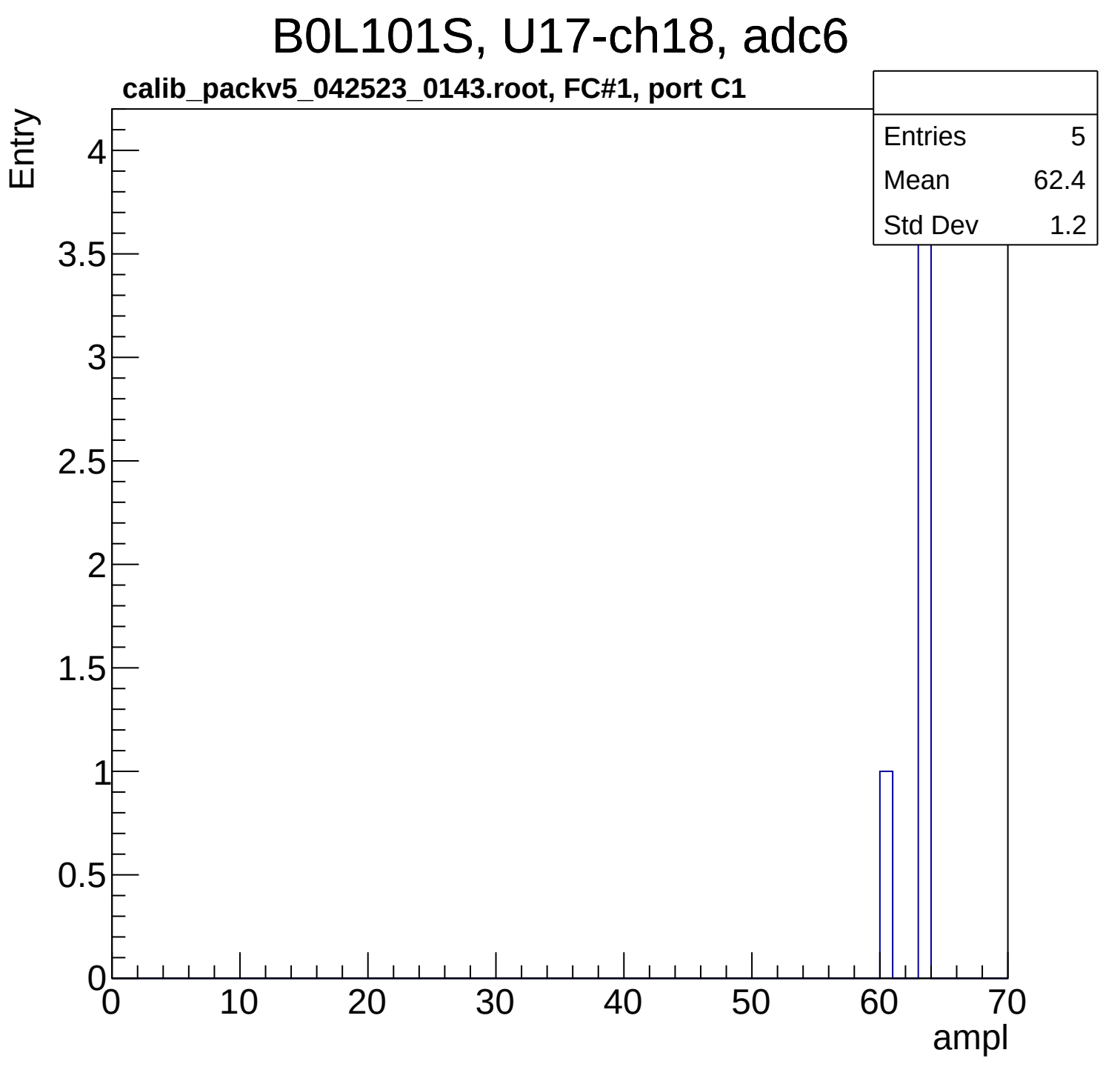

B0L101S, U17-ch18, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 37.79
Std Dev 3.644
Gaus mean : 38.5232
Gaus Width: 3.6910
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

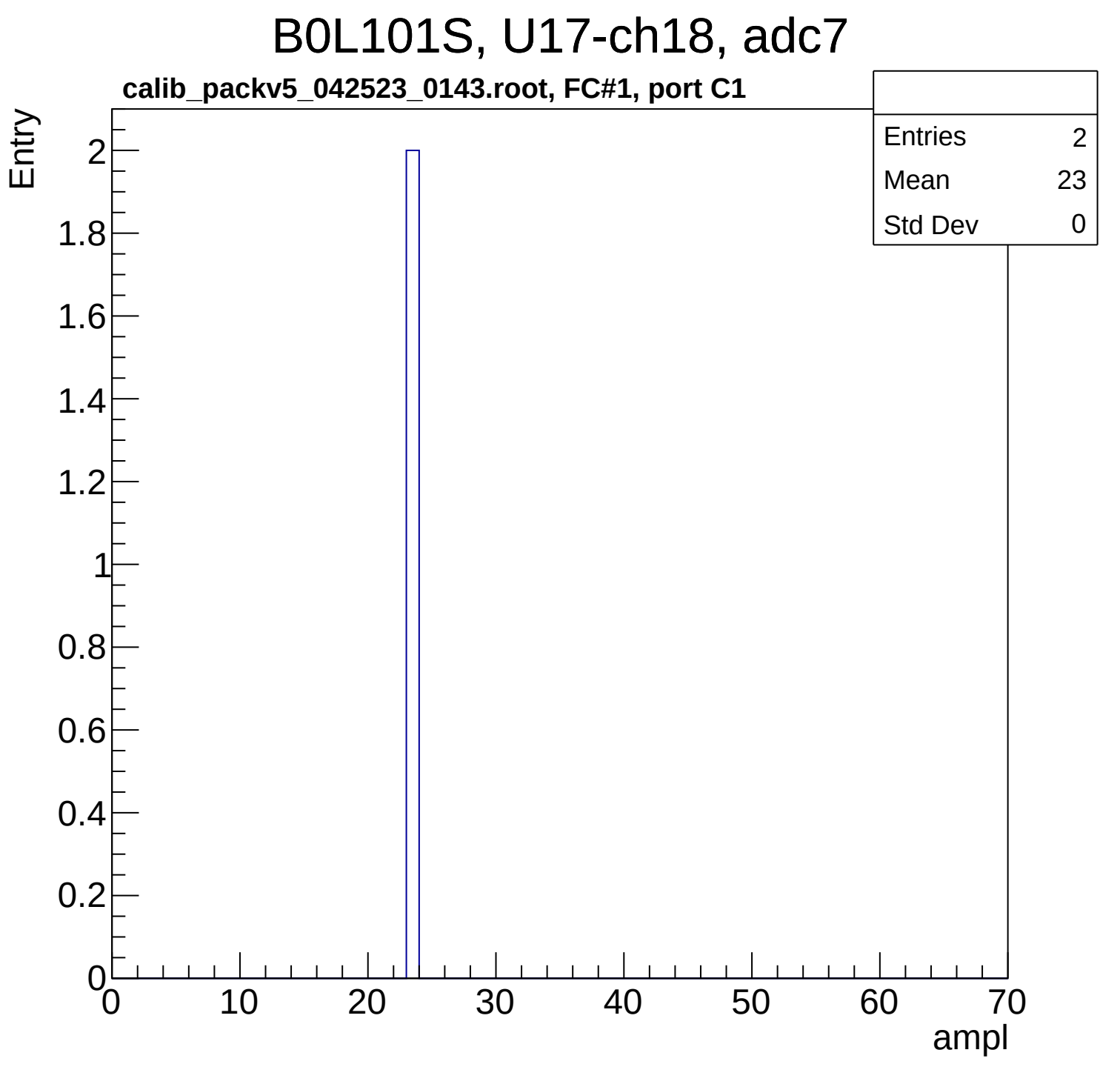

B0L101S, U17-ch18, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 44.63
Std Dev 3.537
Gaus mean : 45.2220
Gaus Width: 3.5733
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

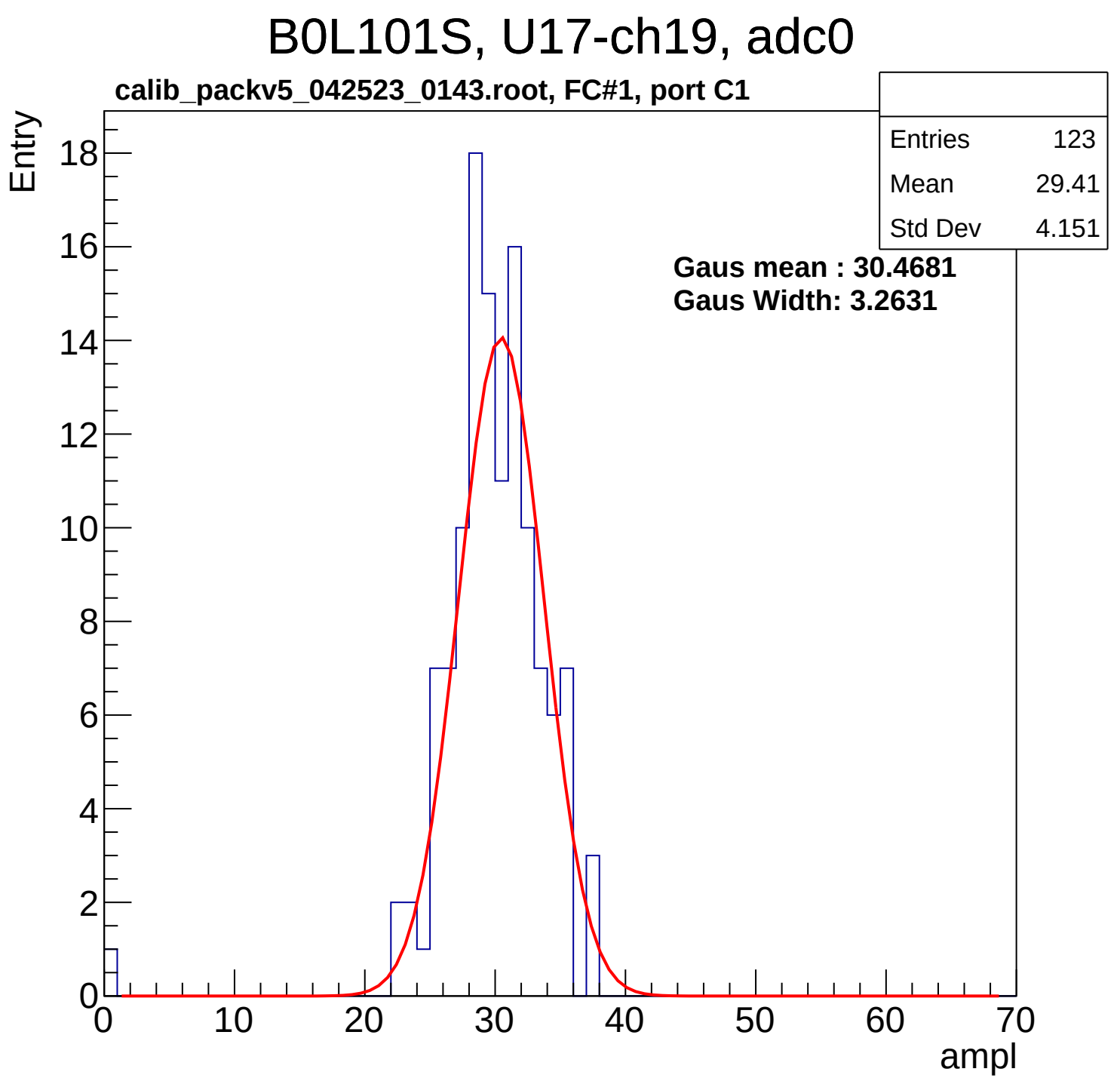

B0L101S, U17-ch18, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 50.77
Std Dev 3.564
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

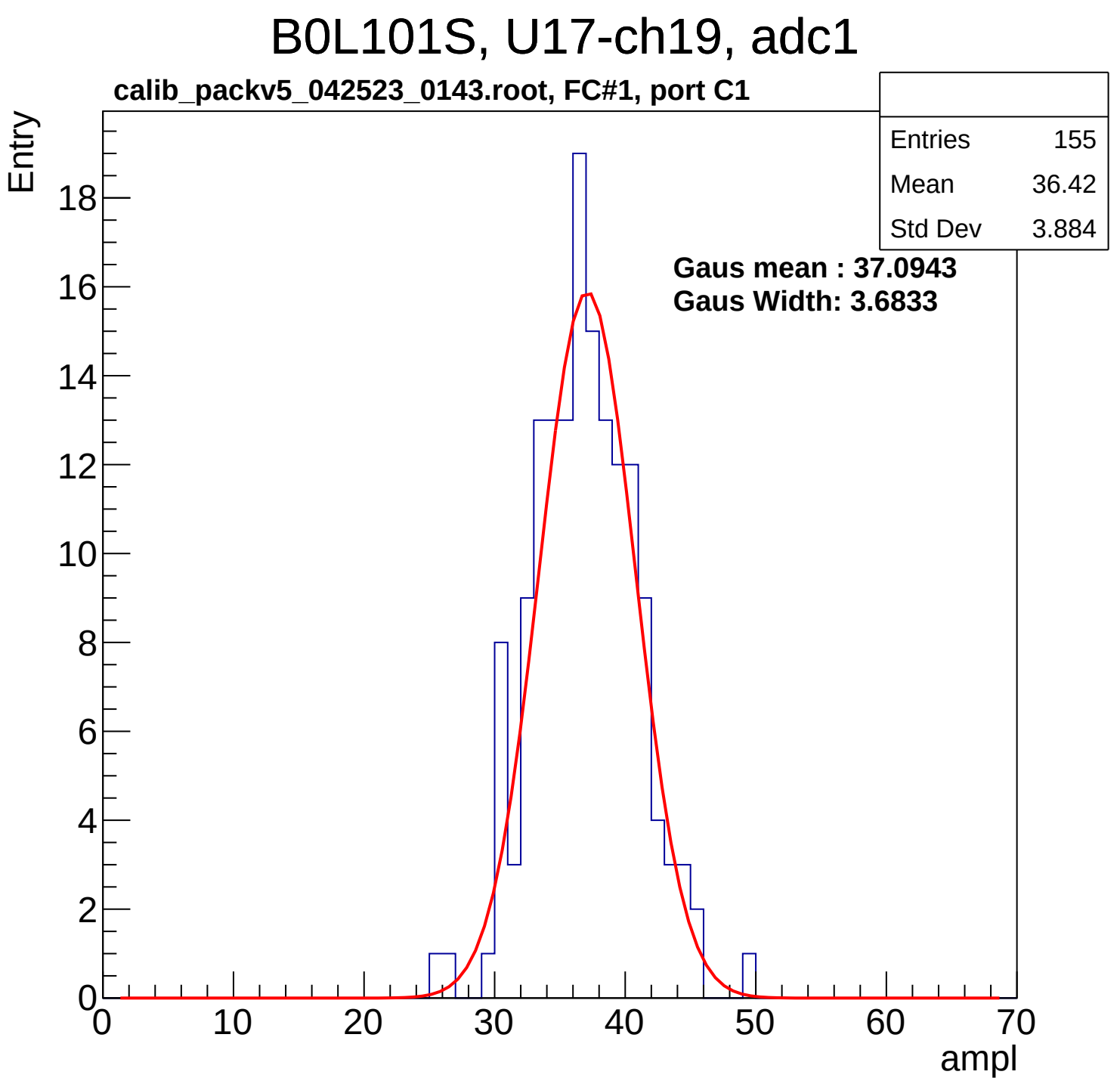

B0L101S, U17-ch18, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 57.08
Std Dev 3.203
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

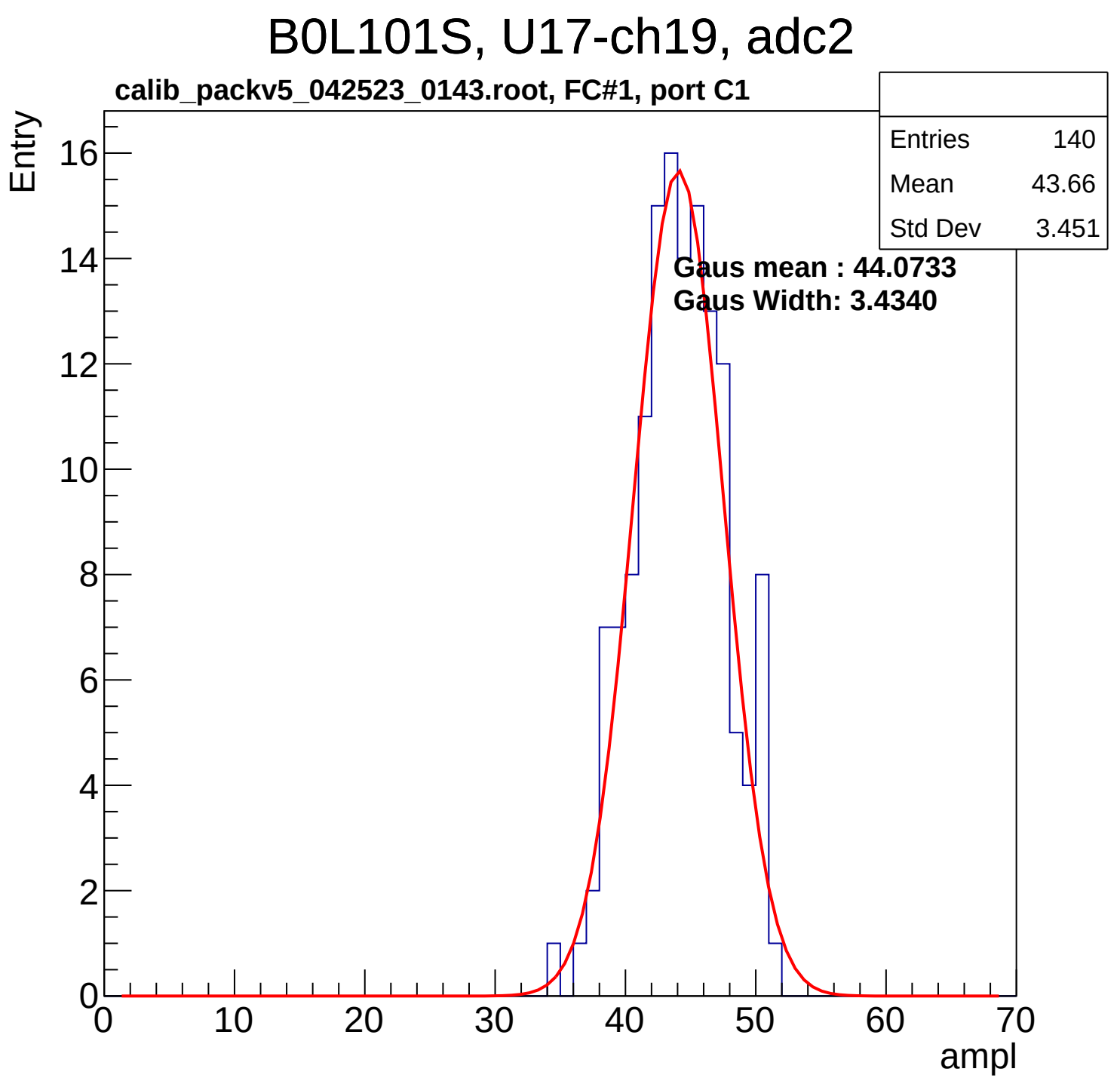

B0L101S, U17-ch18, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 65
Mean 58.78
Std Dev 10.63
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

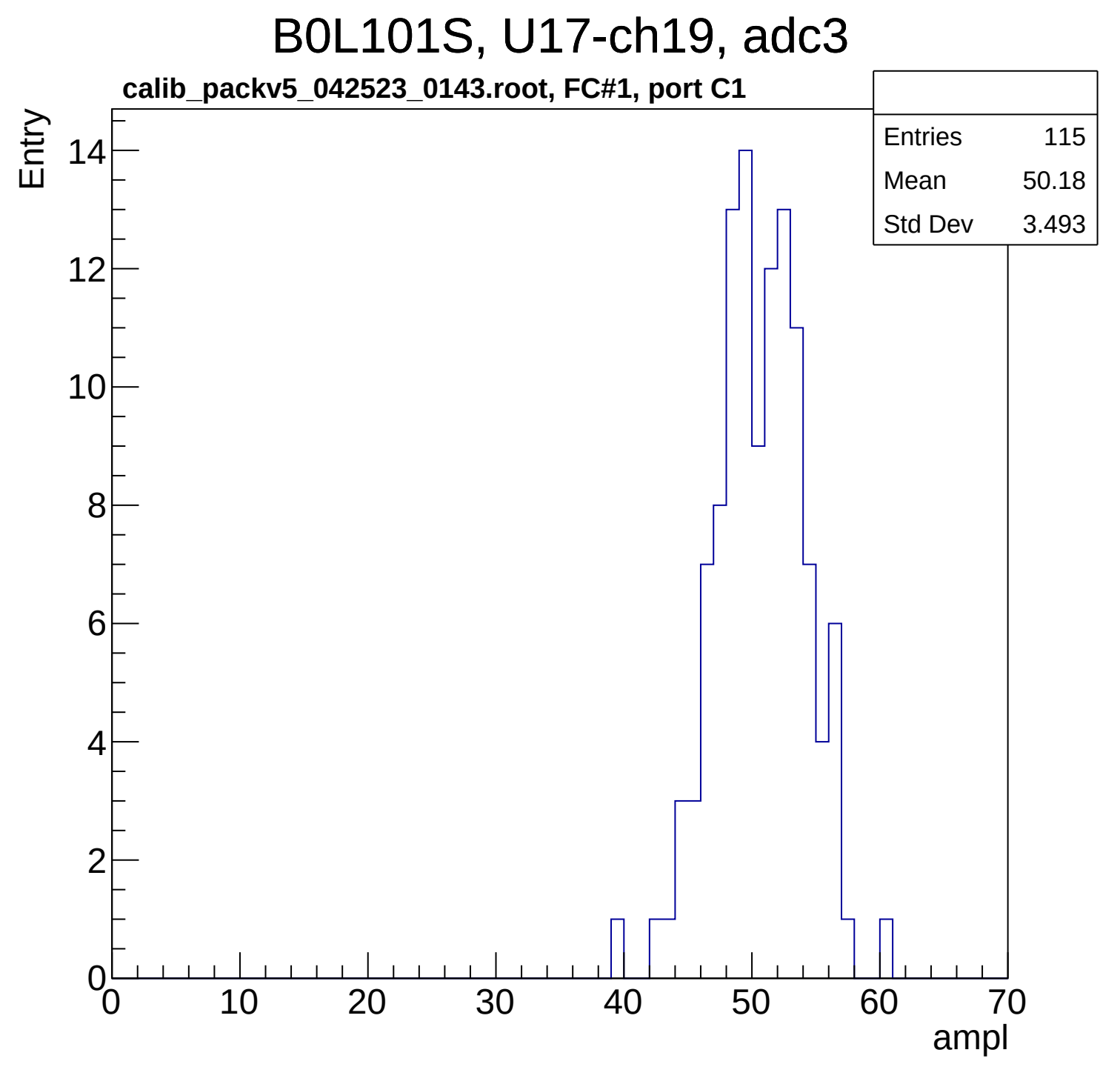

B0L101S, U17-ch18, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 62.4
Std Dev 1.2
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

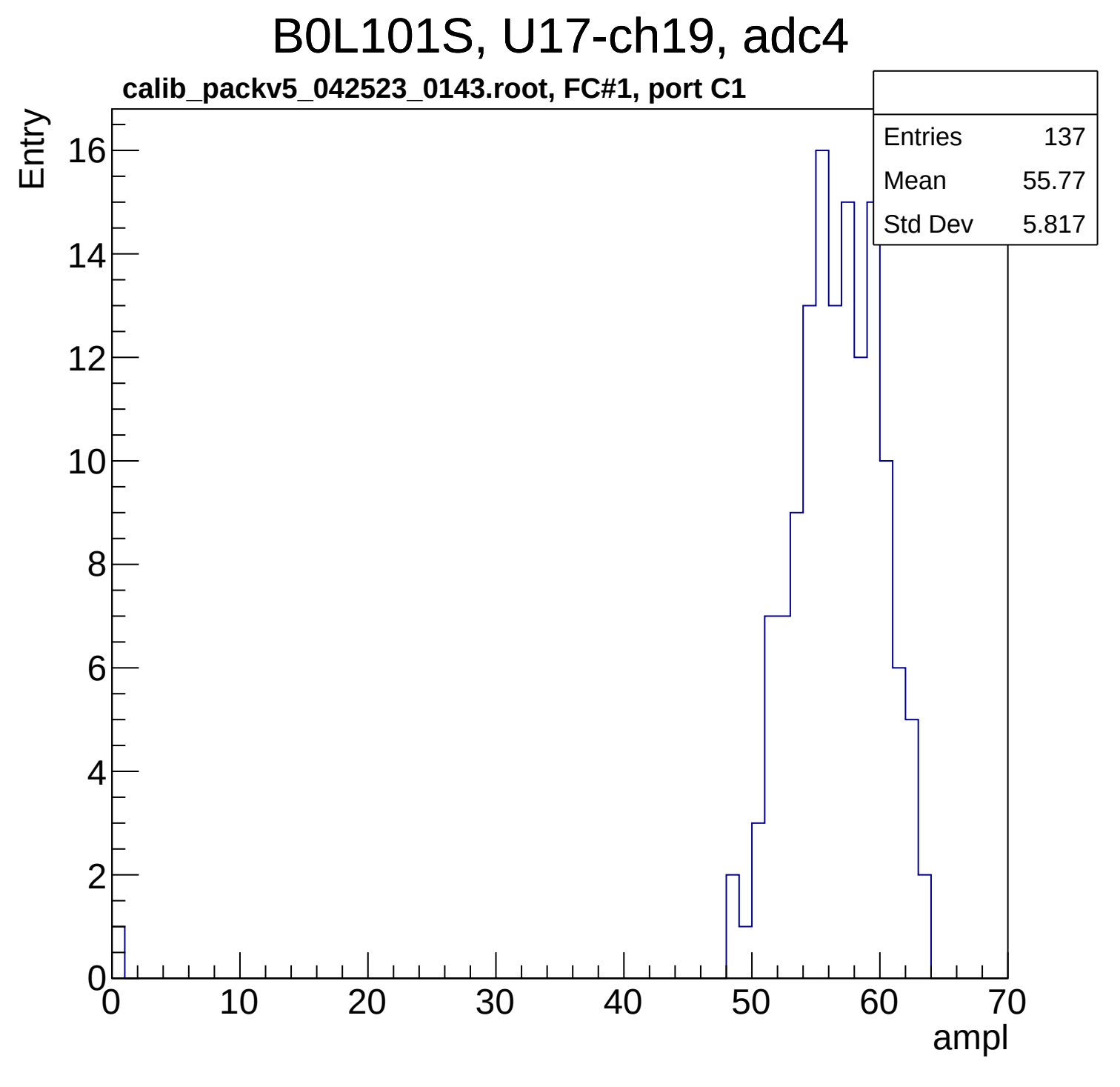

B0L101S, U17-ch18, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 23
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

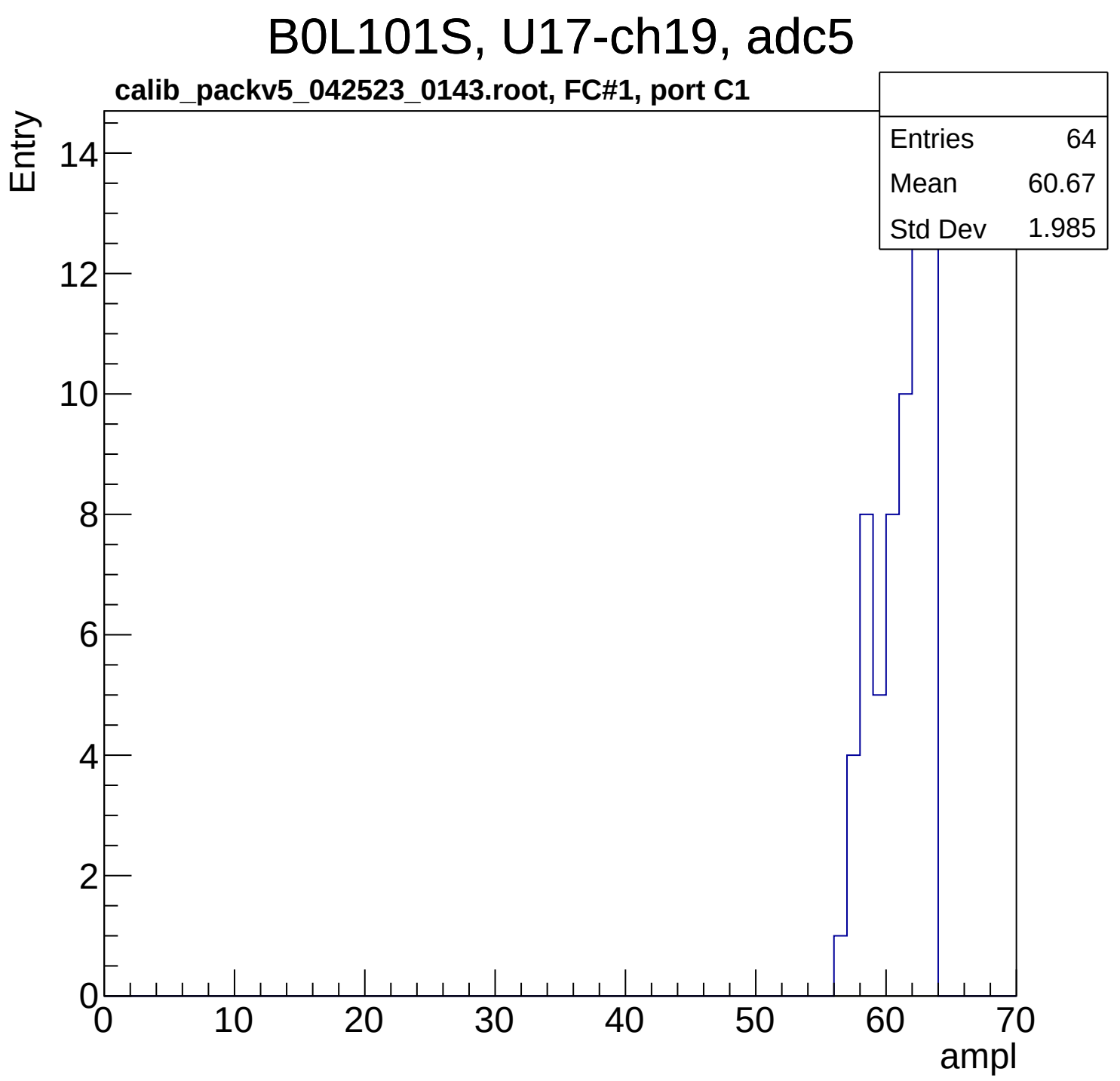

B0L101S, U17-ch19, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 29.41
Std Dev 4.151
Gaus mean : 30.4681
Gaus Width: 3.2631
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

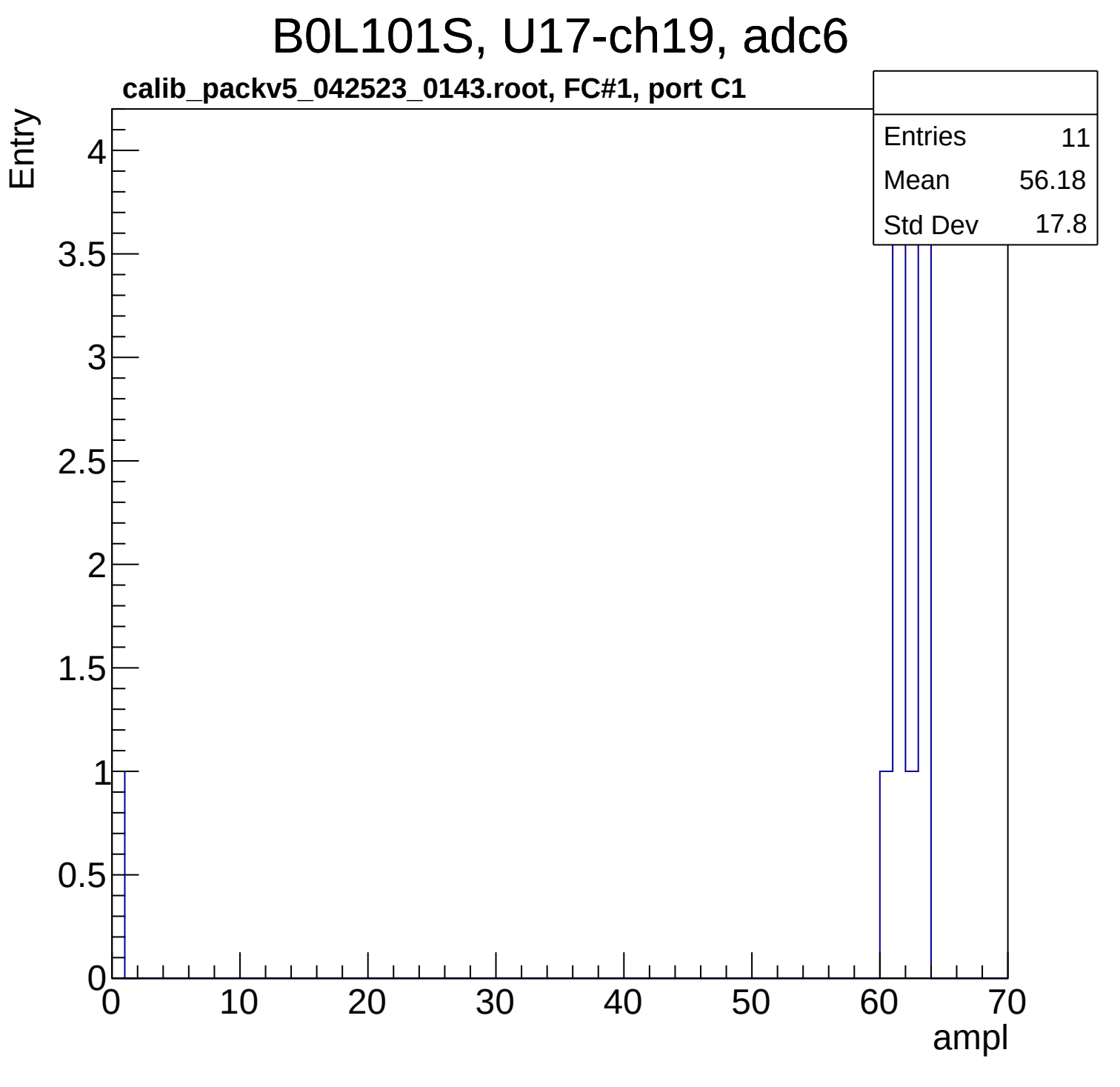

B0L101S, U17-ch19, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 36.42
Std Dev 3.884
Gaus mean : 37.0943
Gaus Width: 3.6833
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

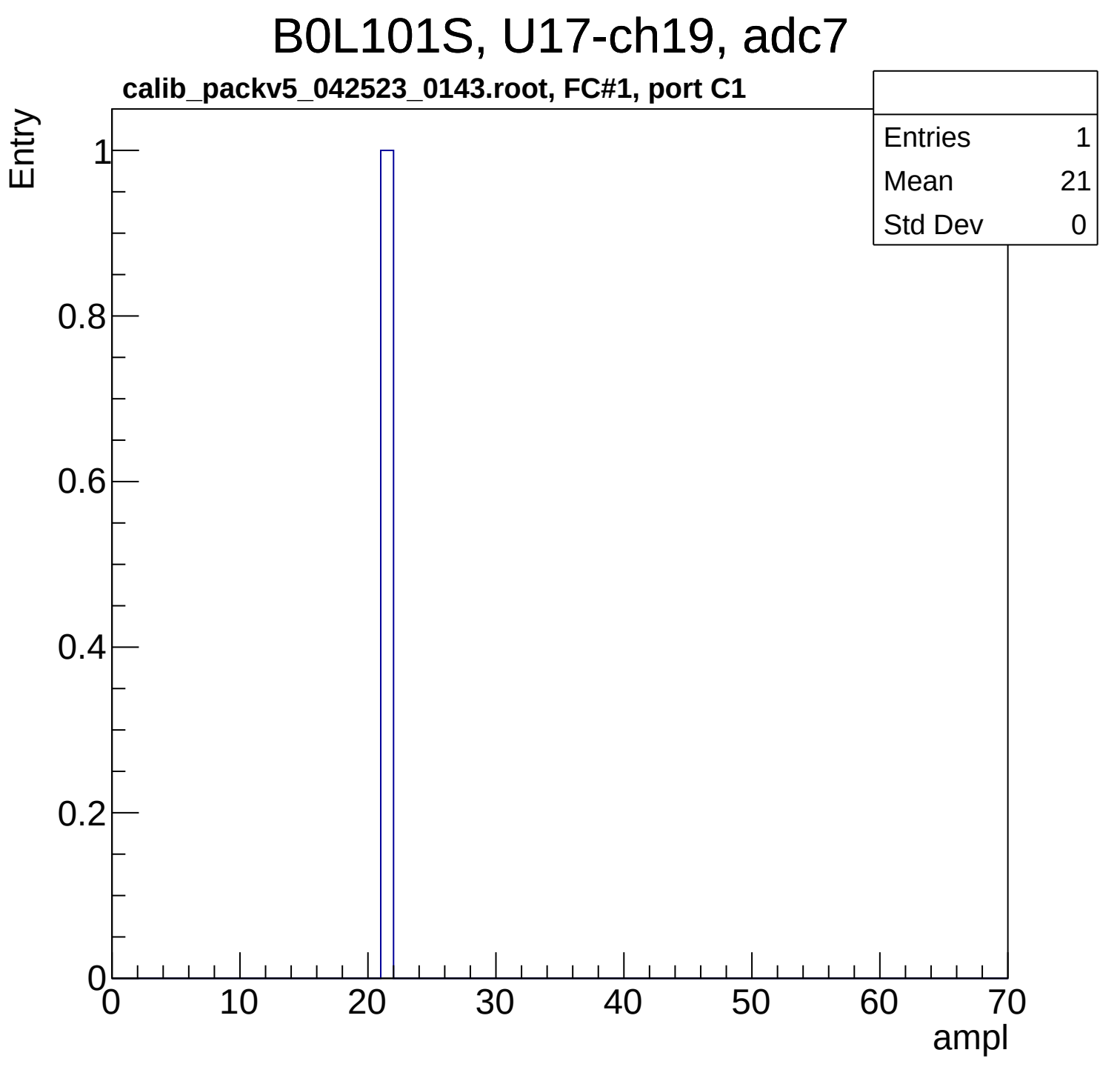

B0L101S, U17-ch19, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 43.66
Std Dev 3.451
Gaus mean : 44.0733
Gaus Width: 3.4340
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

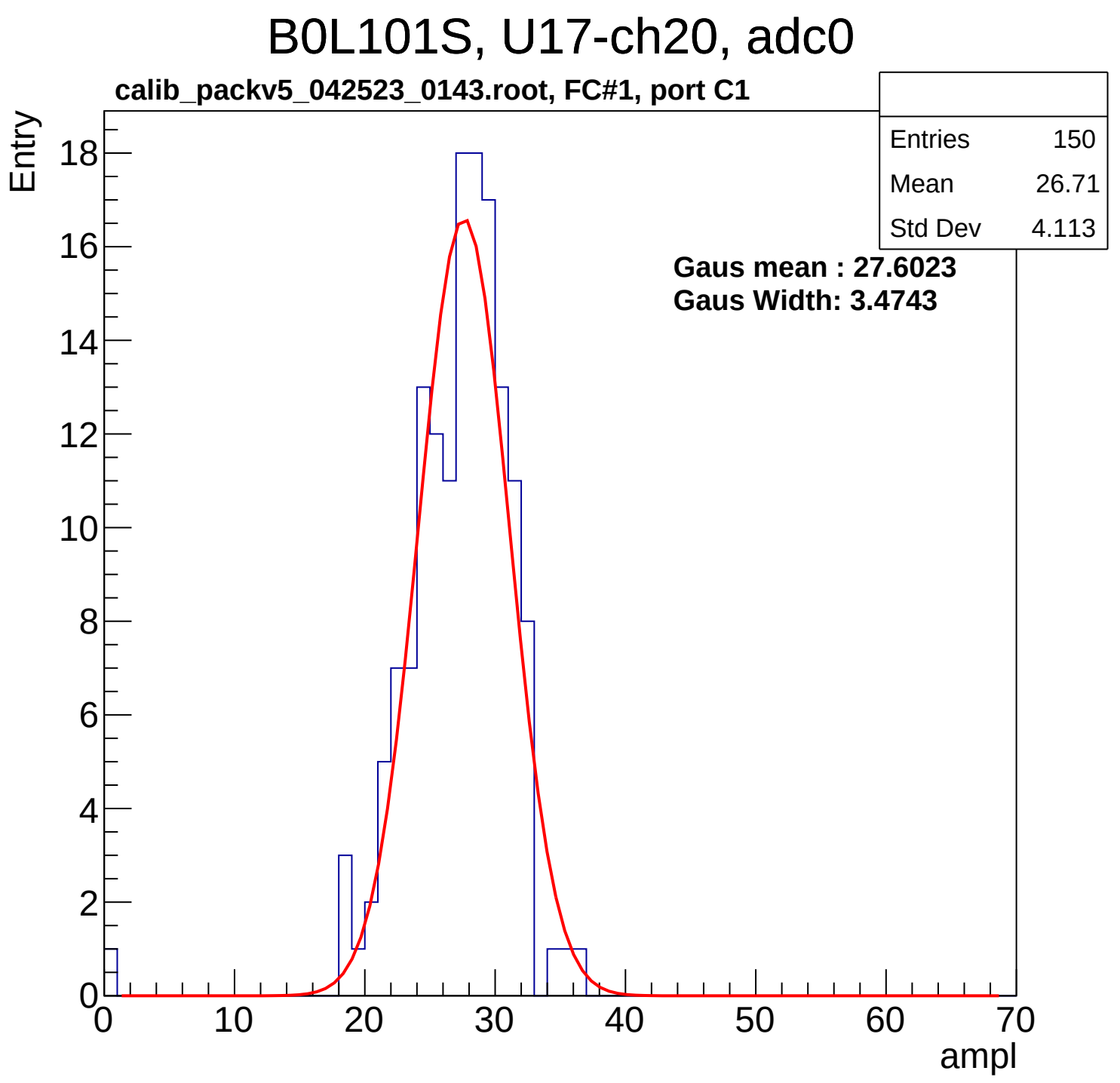

B0L101S, U17-ch19, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 50.18
Std Dev 3.493
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

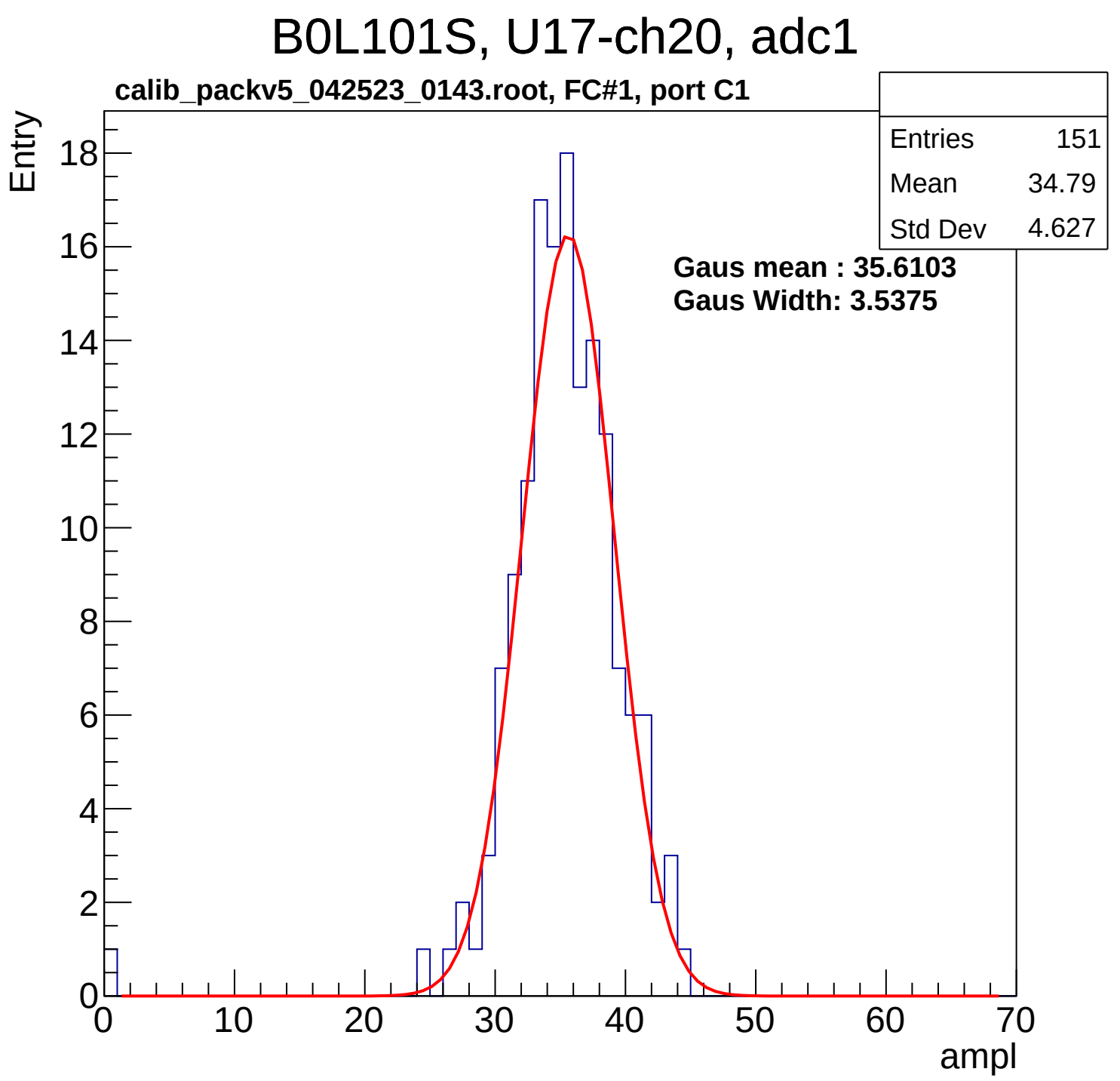

B0L101S, U17-ch19, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 55.77
Std Dev 5.817
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

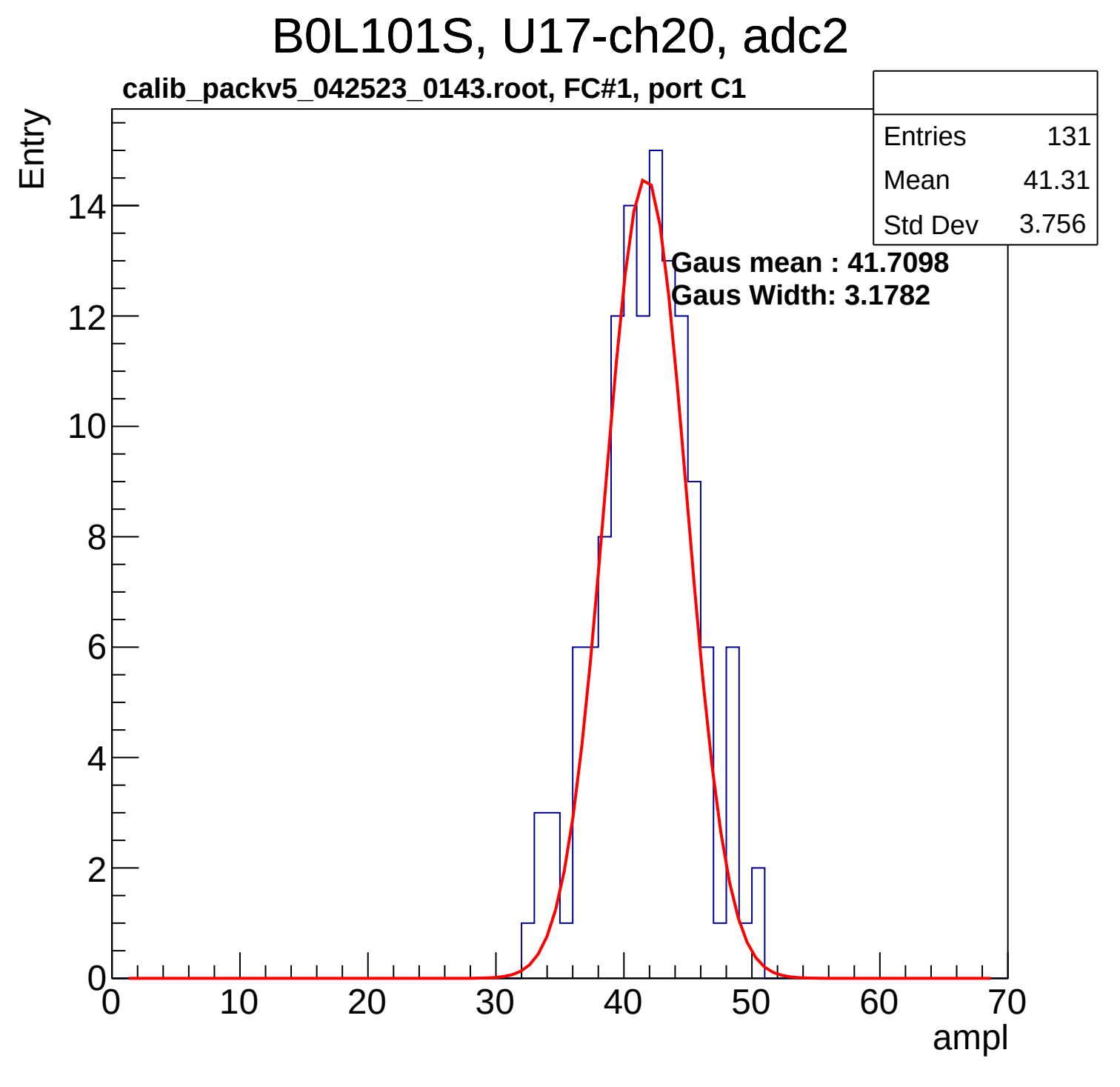

B0L101S, U17-ch19, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 64
Mean 60.67
Std Dev 1.985
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

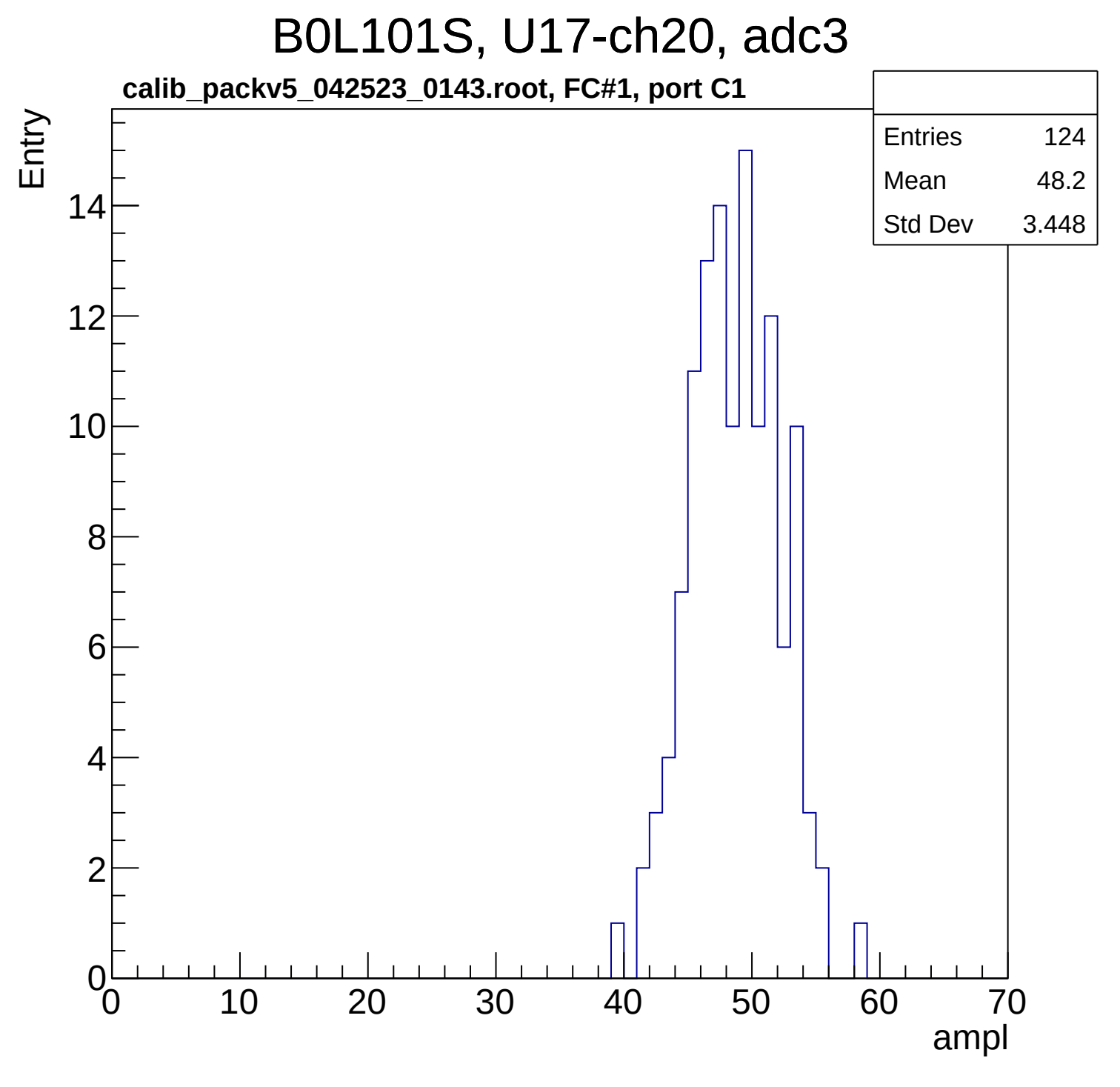

B0L101S, U17-ch19, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 56.18
Std Dev 17.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

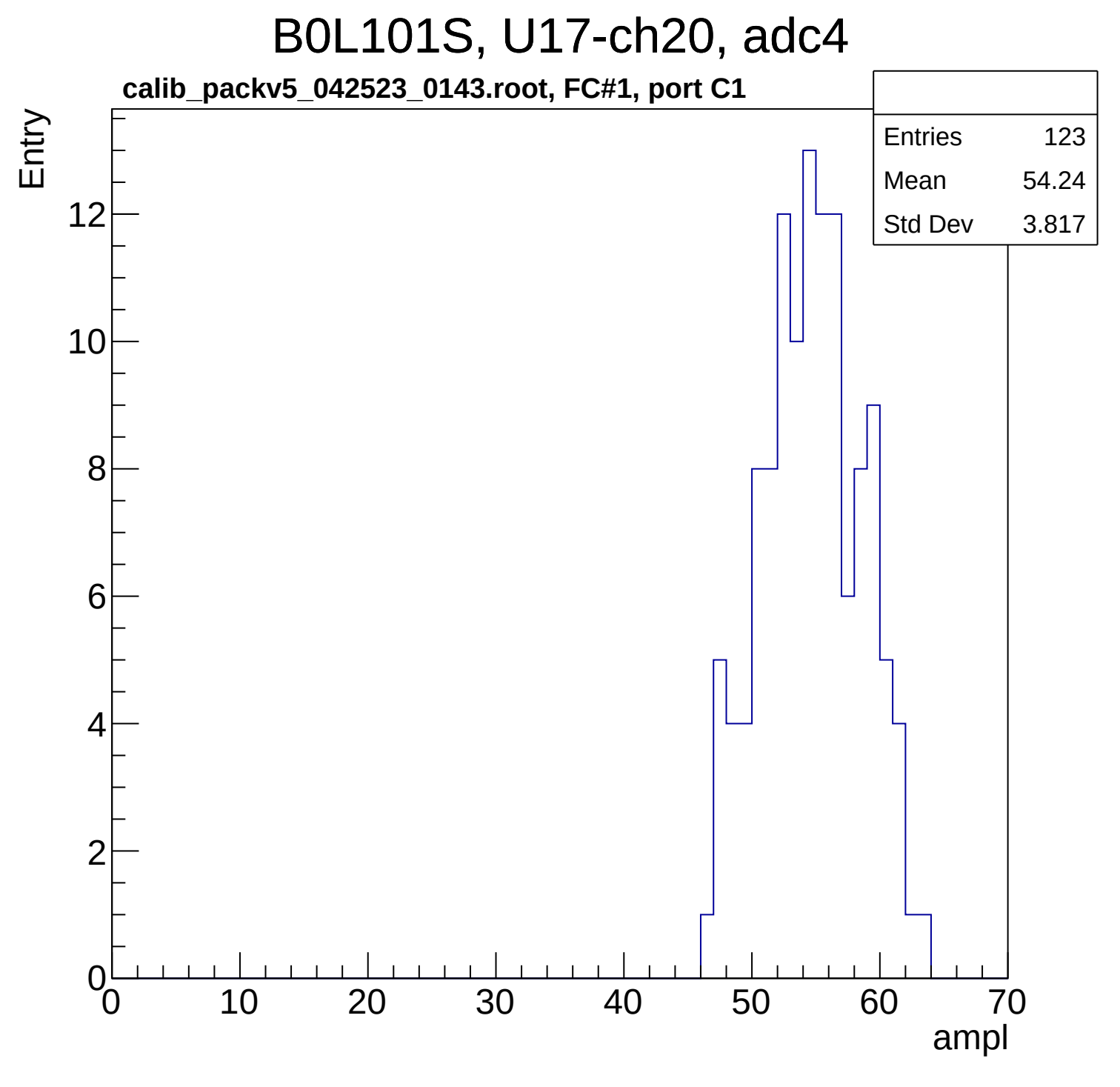

B0L101S, U17-ch19, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

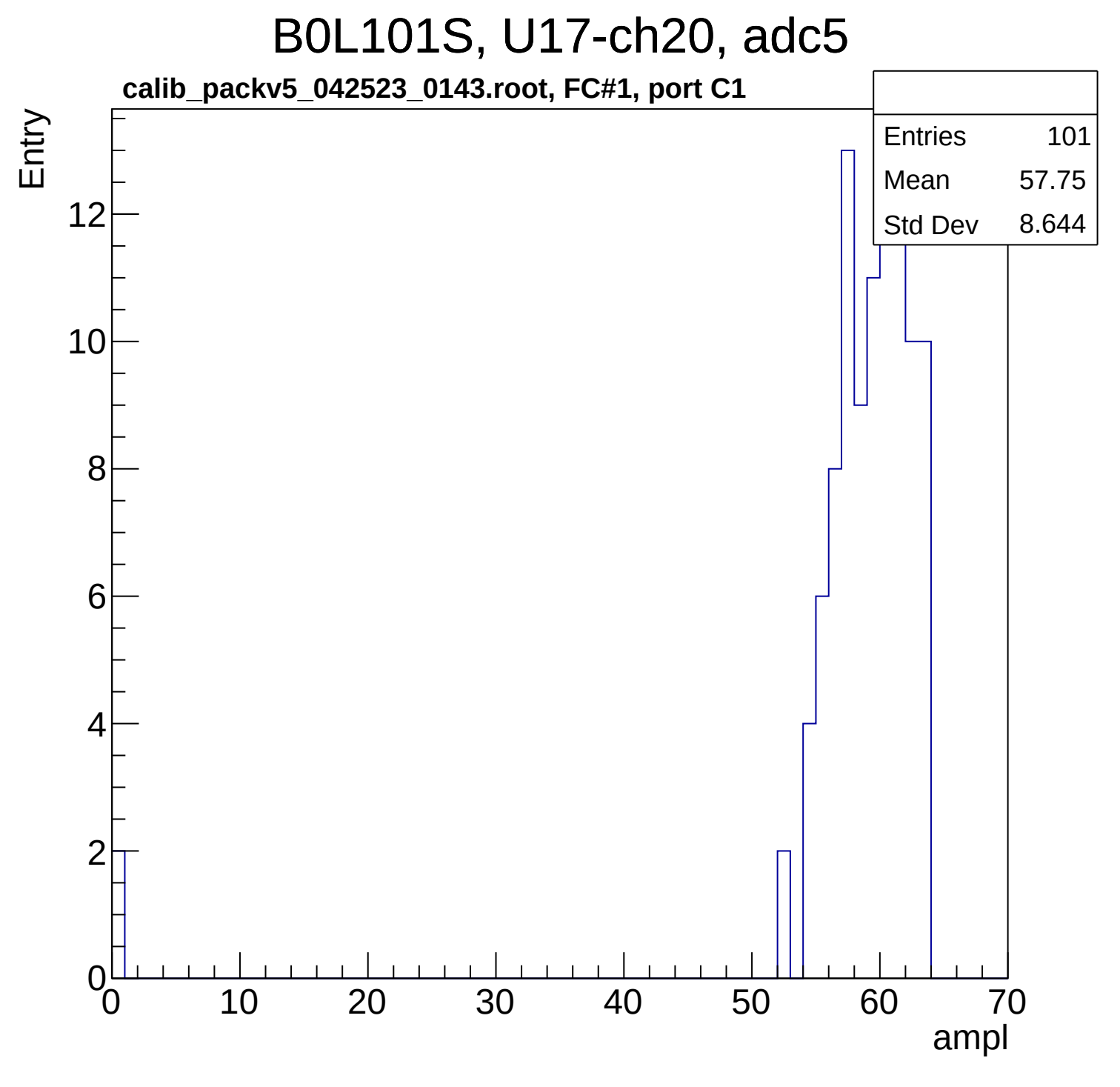

B0L101S, U17-ch20, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 26.71
Std Dev 4.113
Gaus mean : 27.6023
Gaus Width: 3.4743
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

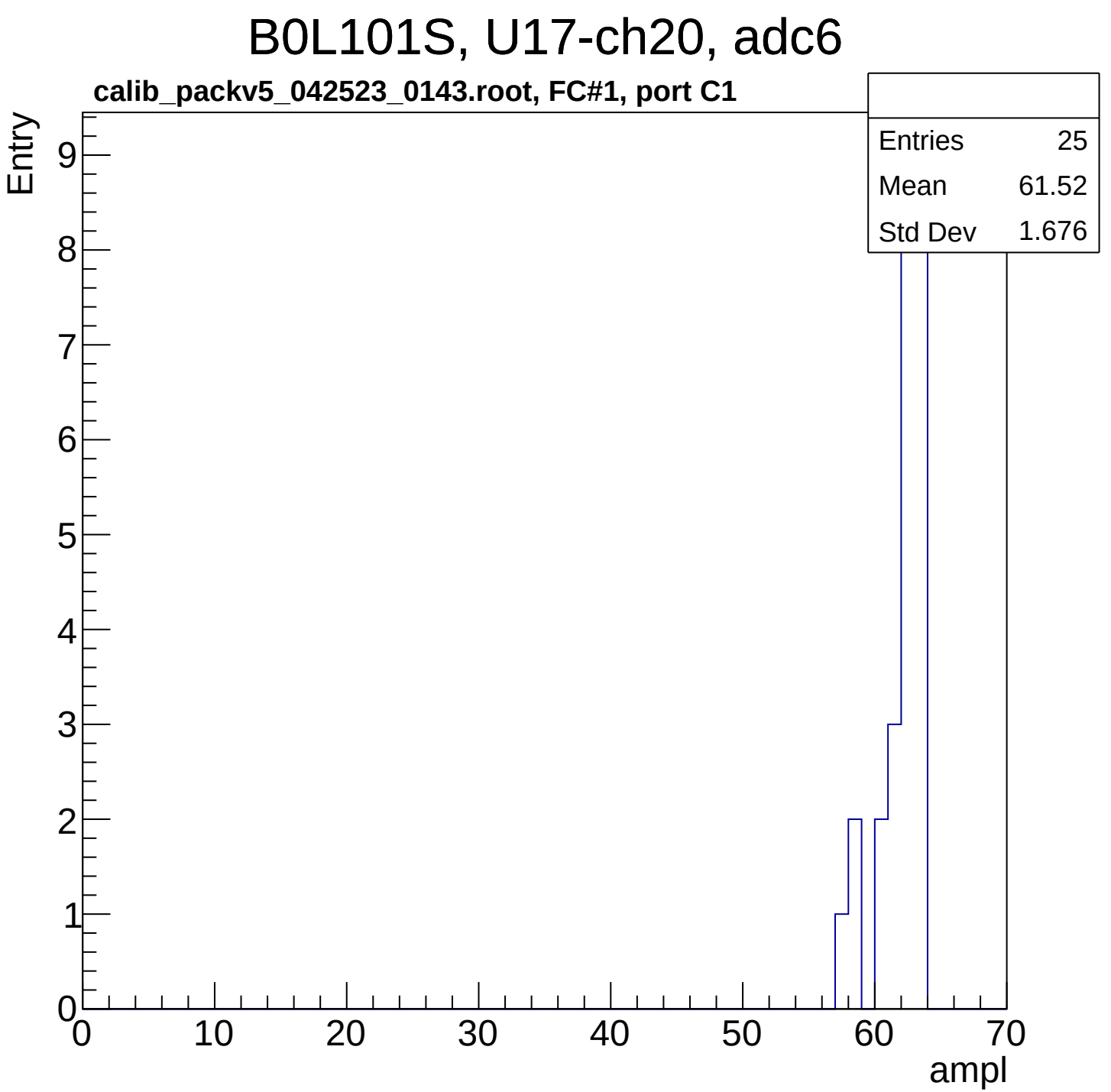

B0L101S, U17-ch20, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 34.79
Std Dev 4.627
Gaus mean : 35.6103
Gaus Width: 3.5375
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

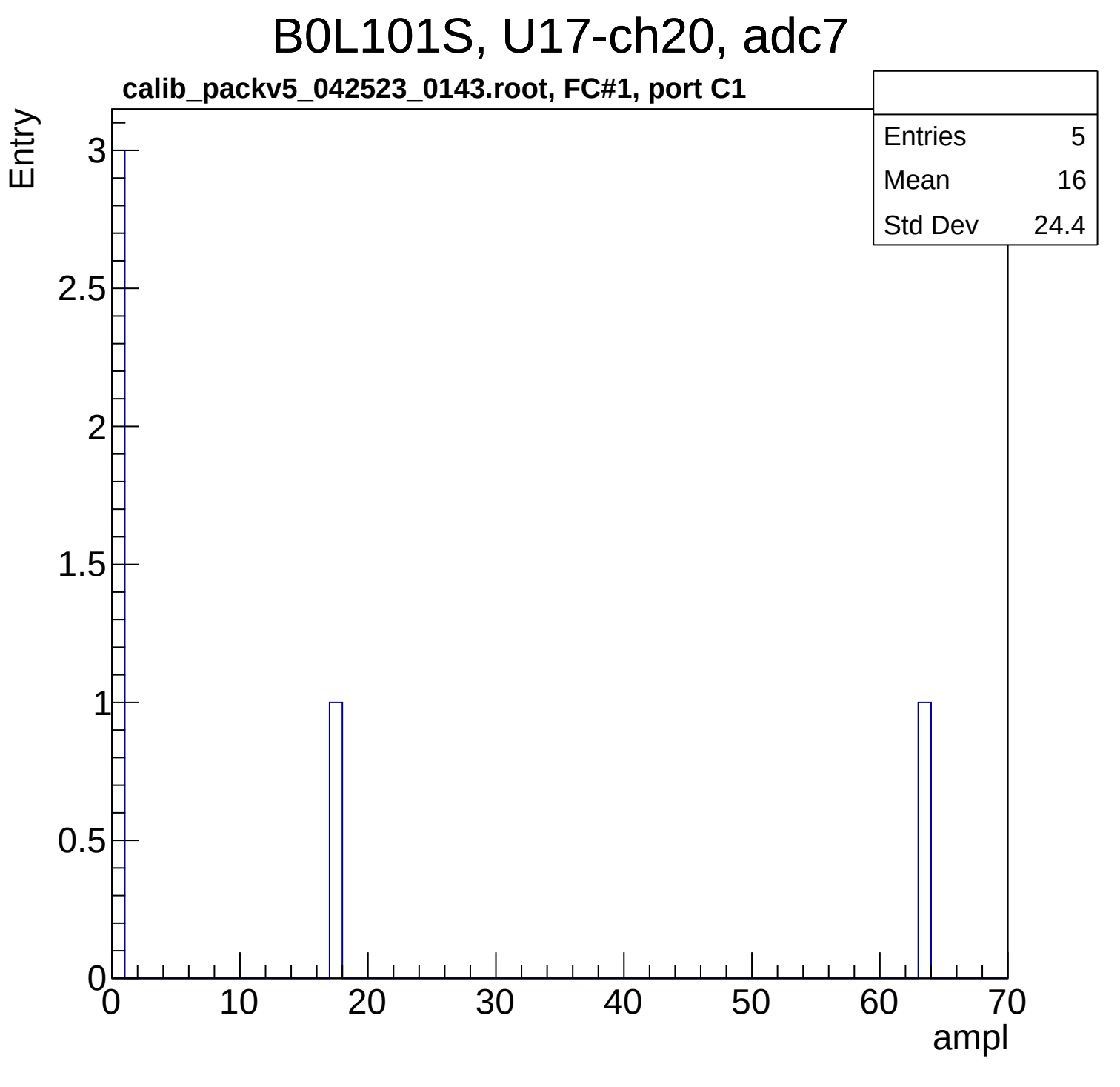

B0L101S, U17-ch20, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 41.31
Std Dev 3.756
Gaus mean : 41.7098
Gaus Width: 3.1782
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

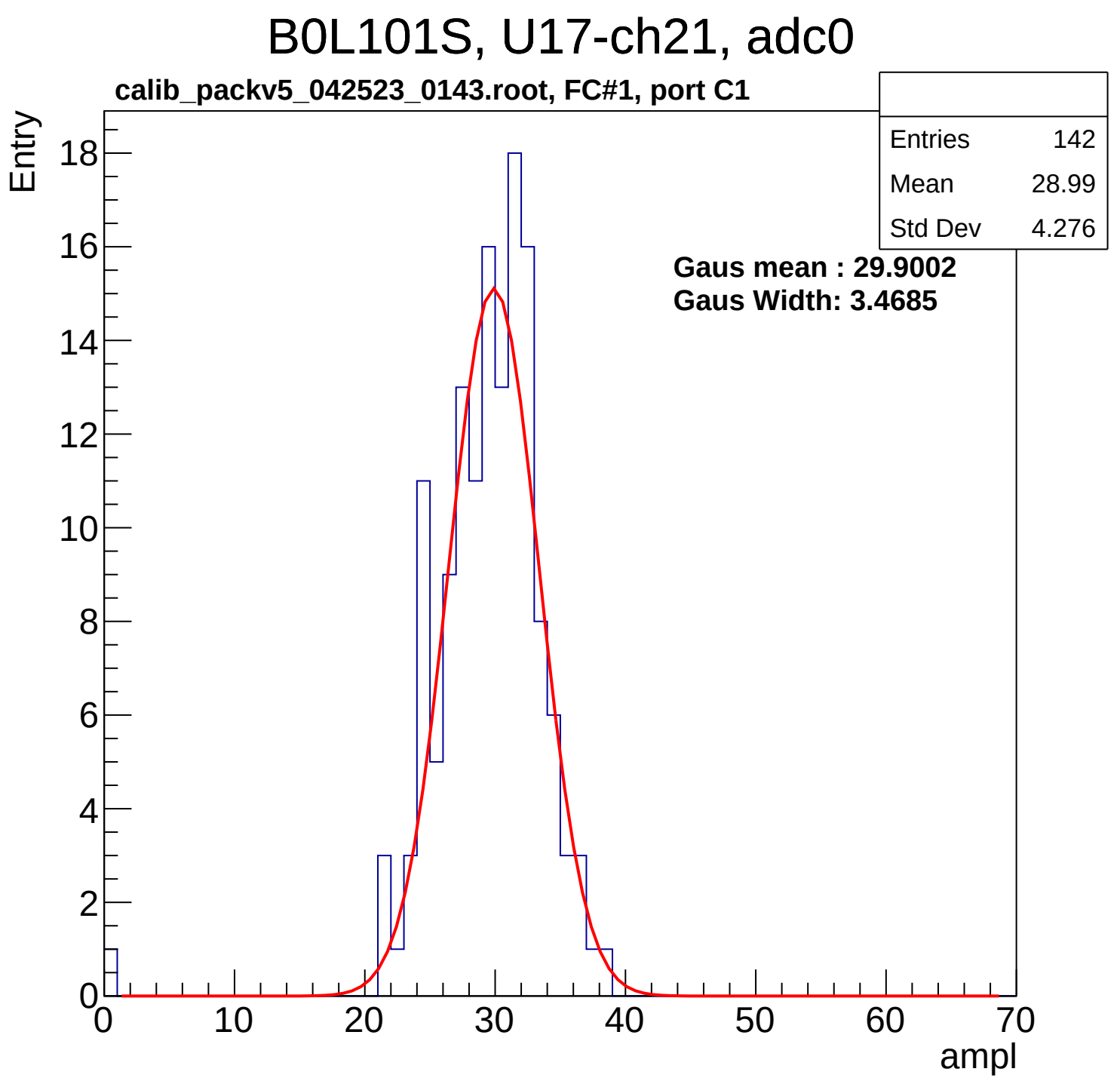

B0L101S, U17-ch20, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 48.2
Std Dev 3.448
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

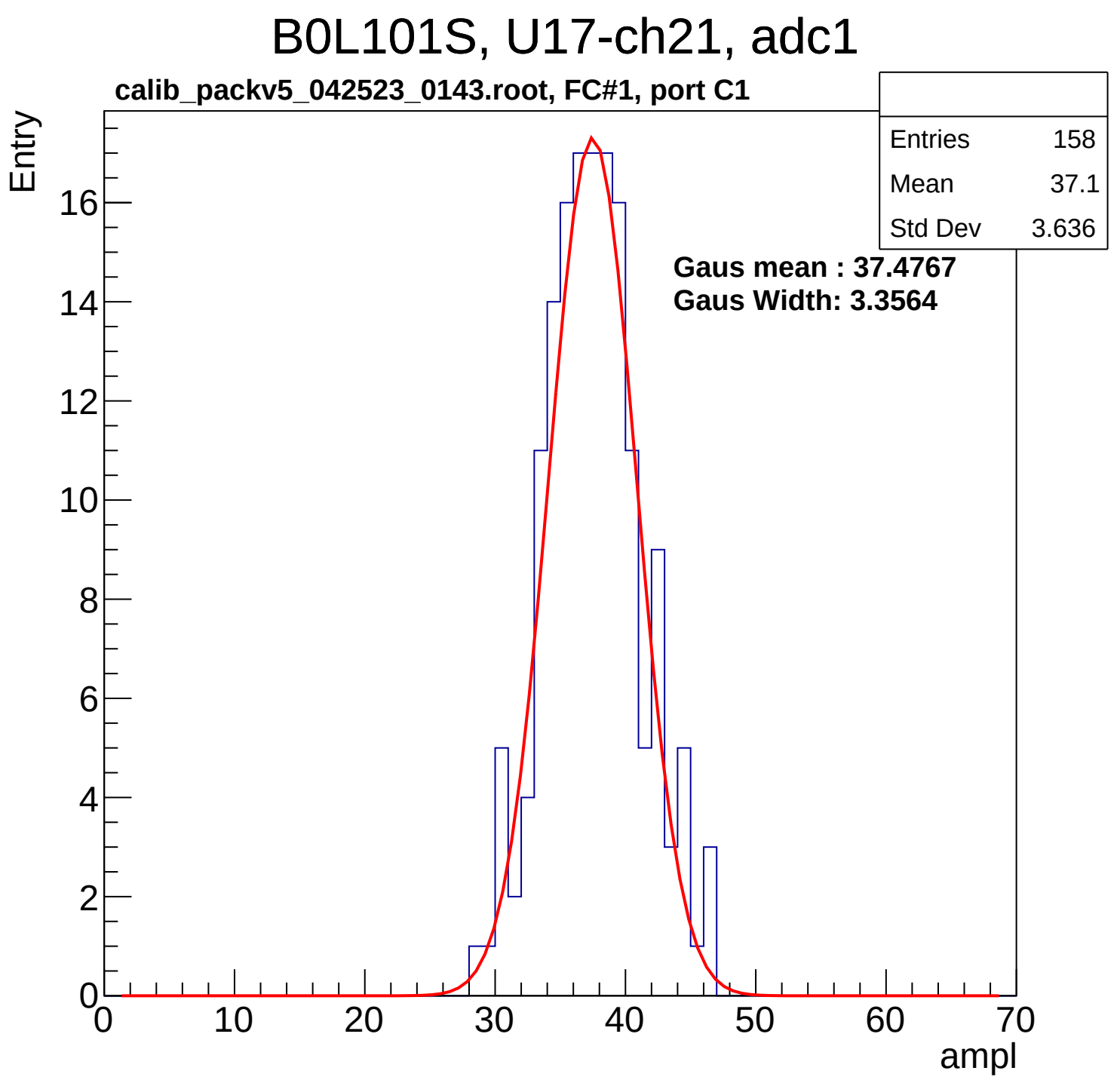

B0L101S, U17-ch20, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 54.24
Std Dev 3.817
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

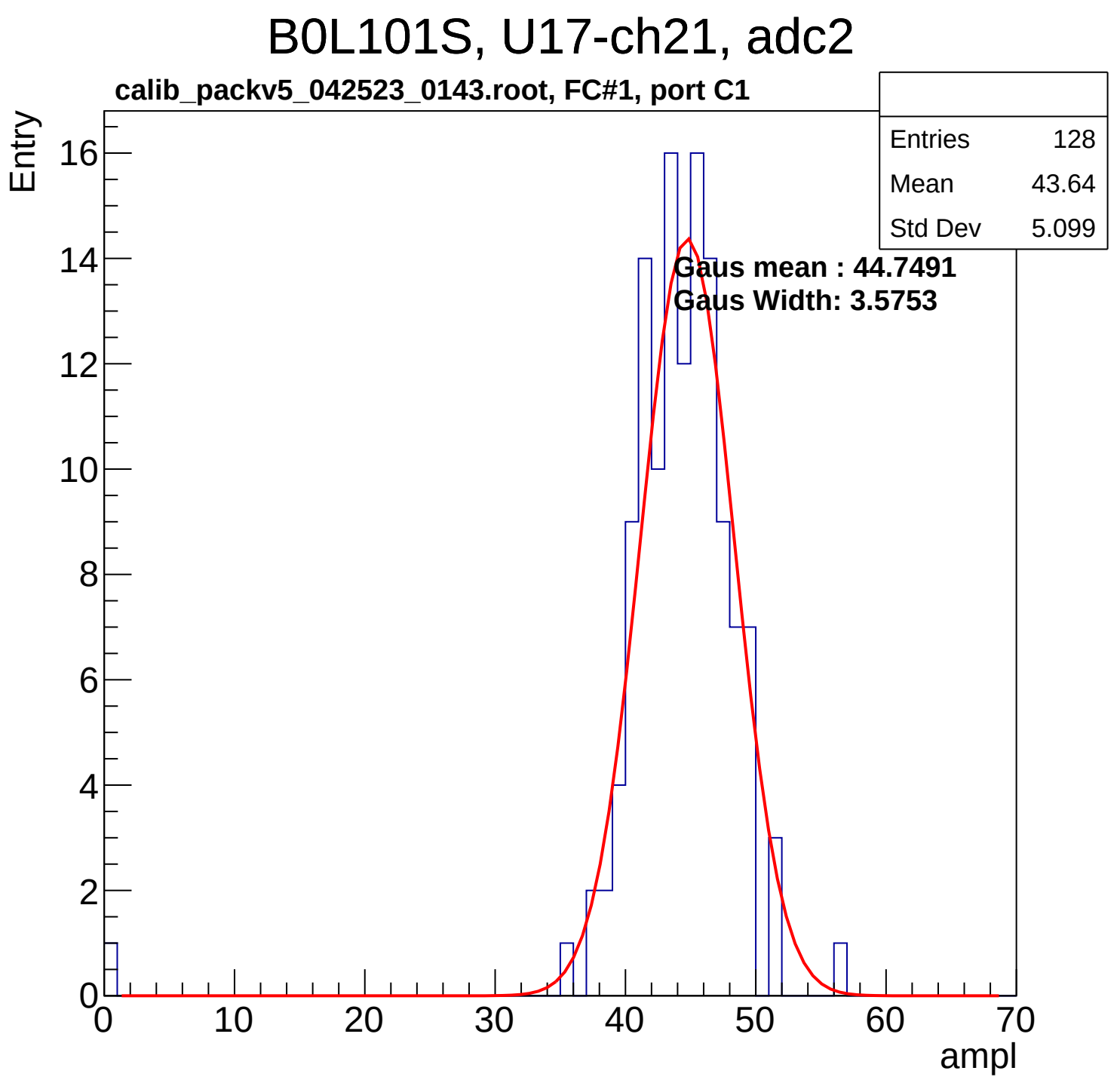

B0L101S, U17-ch20, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 57.75
Std Dev 8.644
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

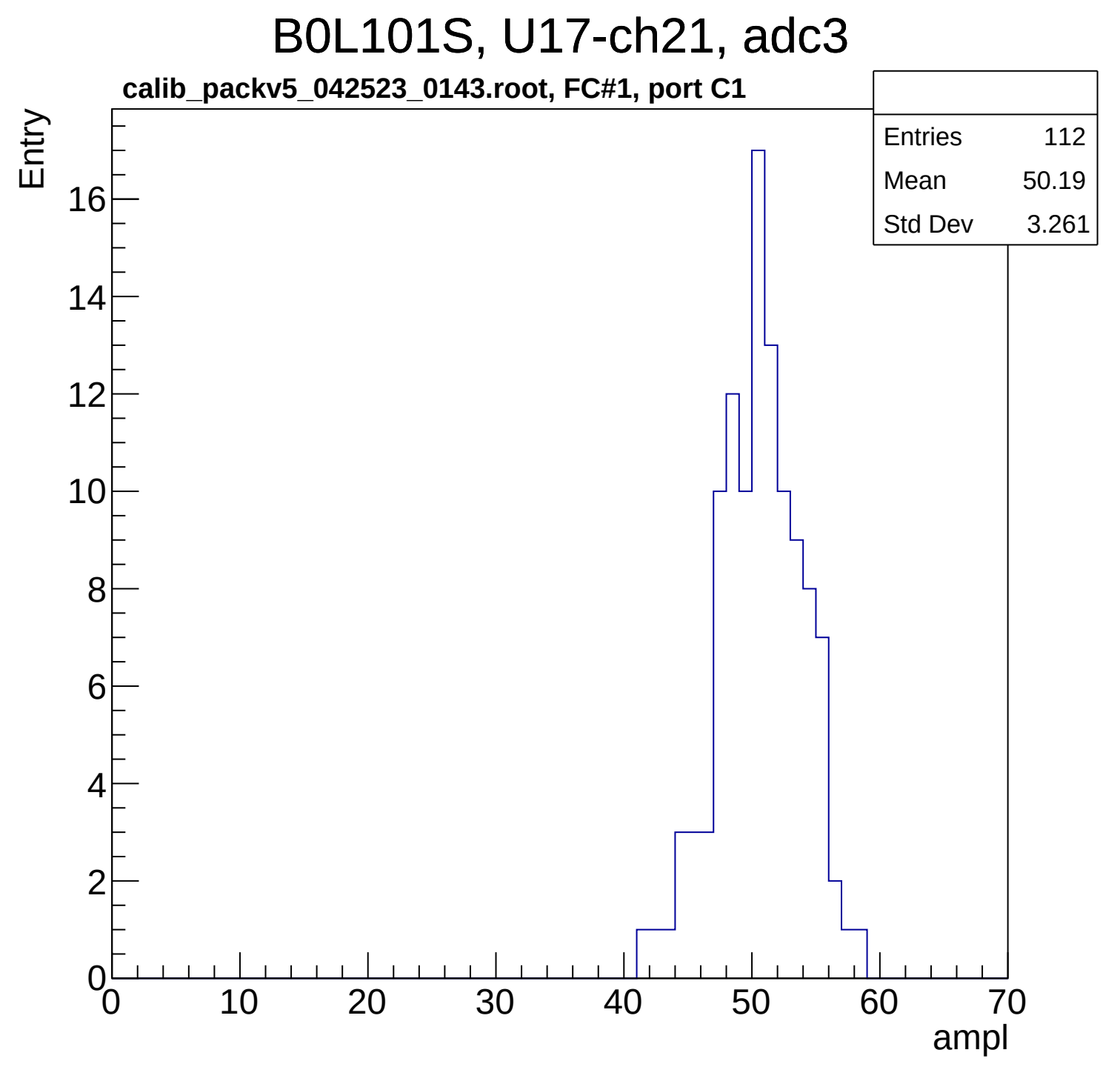

B0L101S, U17-ch20, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 25
Mean 61.52
Std Dev 1.676
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

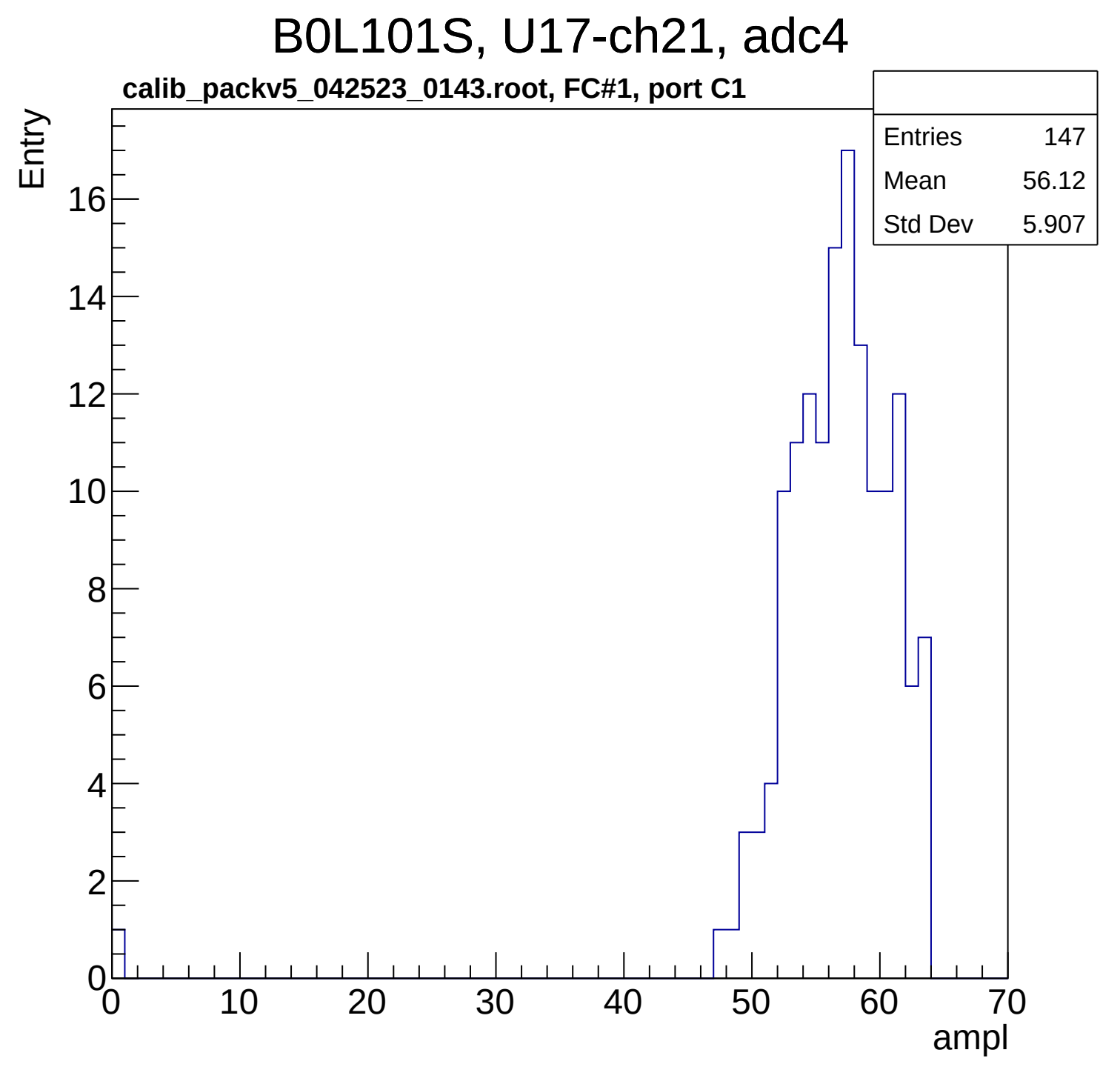

B0L101S, U17-ch20, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 16
Std Dev 24.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

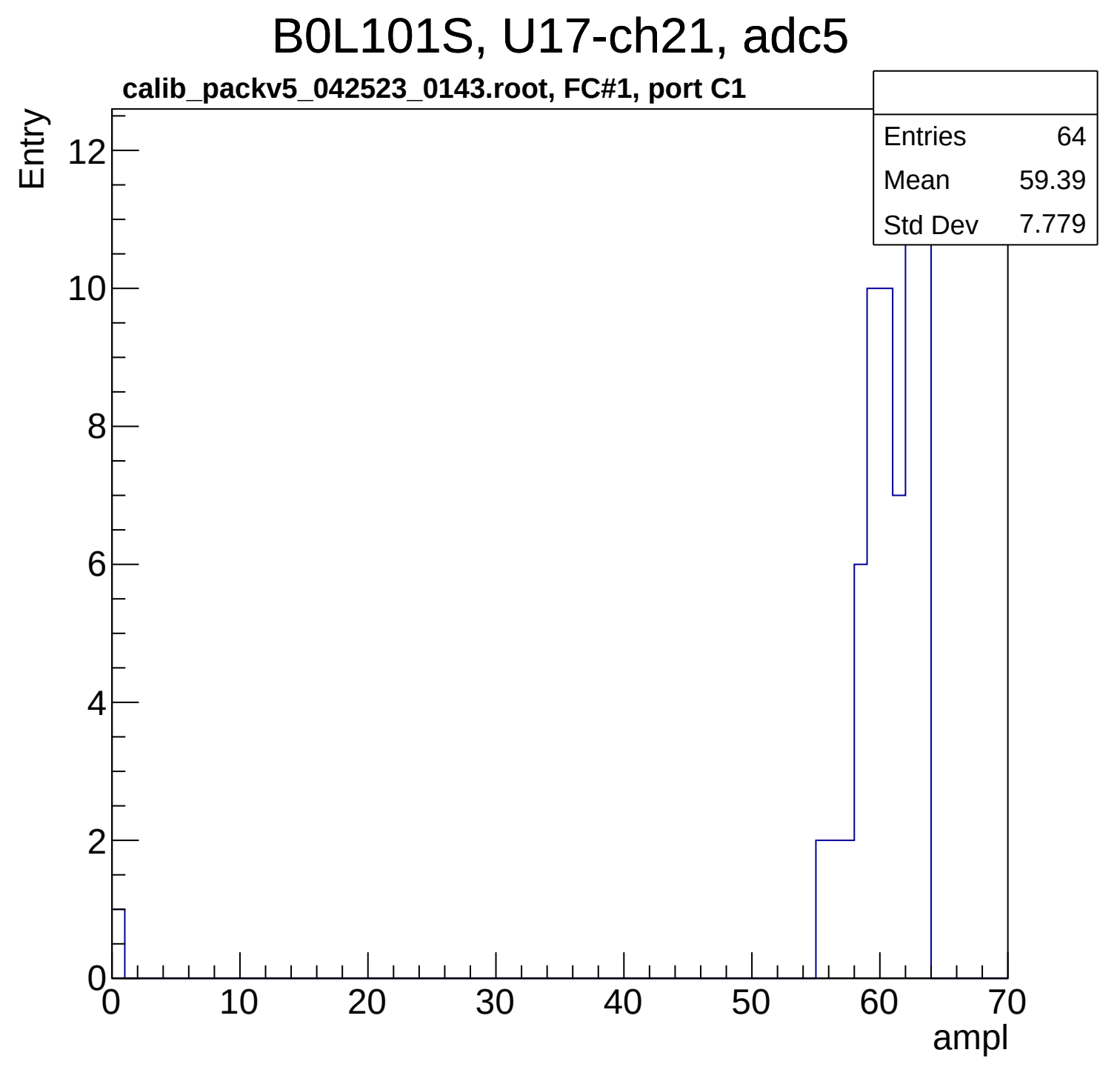

B0L101S, U17-ch21, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 28.99
Std Dev 4.276
Gaus mean : 29.9002
Gaus Width: 3.4685
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

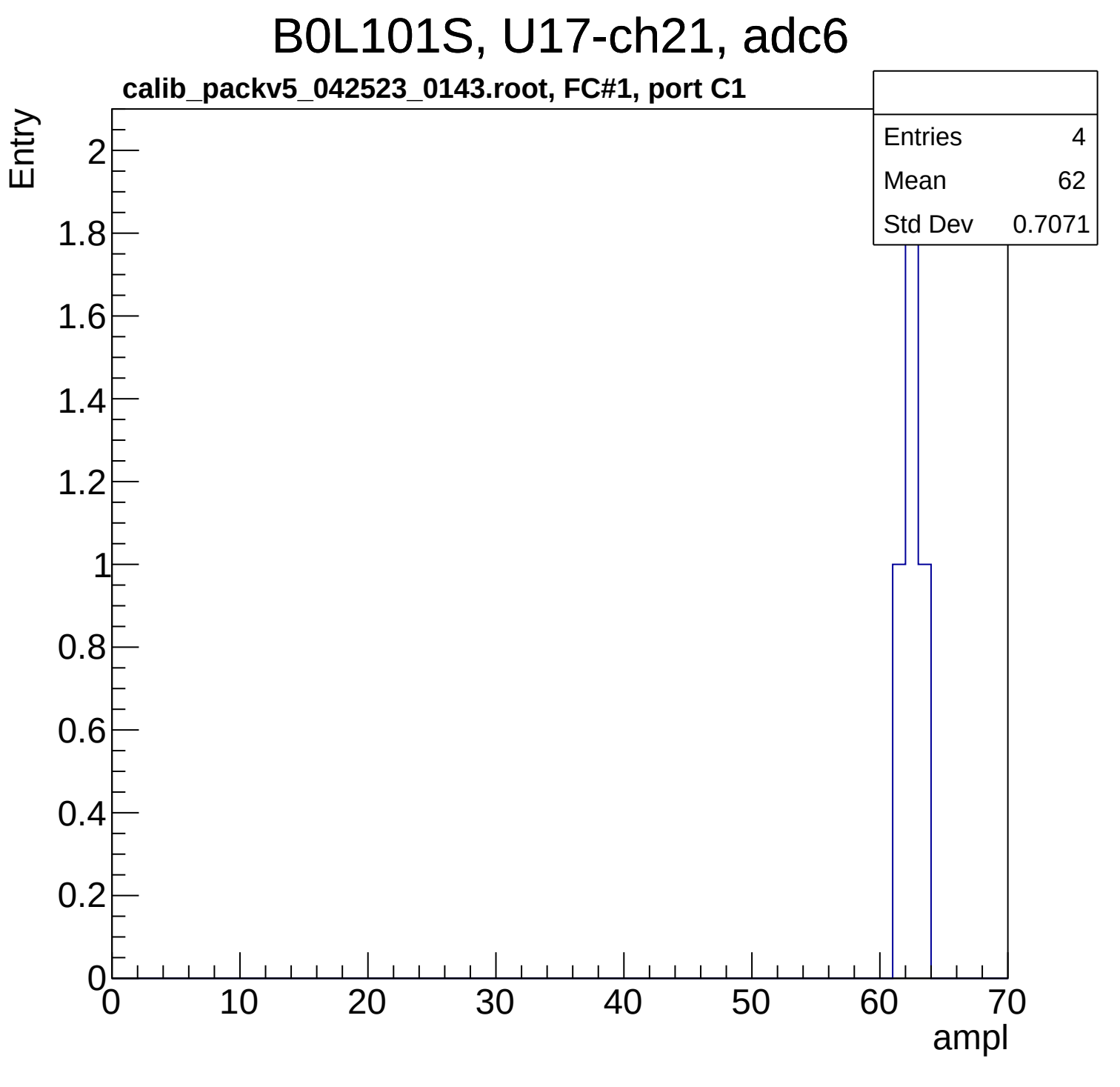

B0L101S, U17-ch21, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 158
Mean 37.1
Std Dev 3.636
Gaus mean : 37.4767
Gaus Width: 3.3564
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

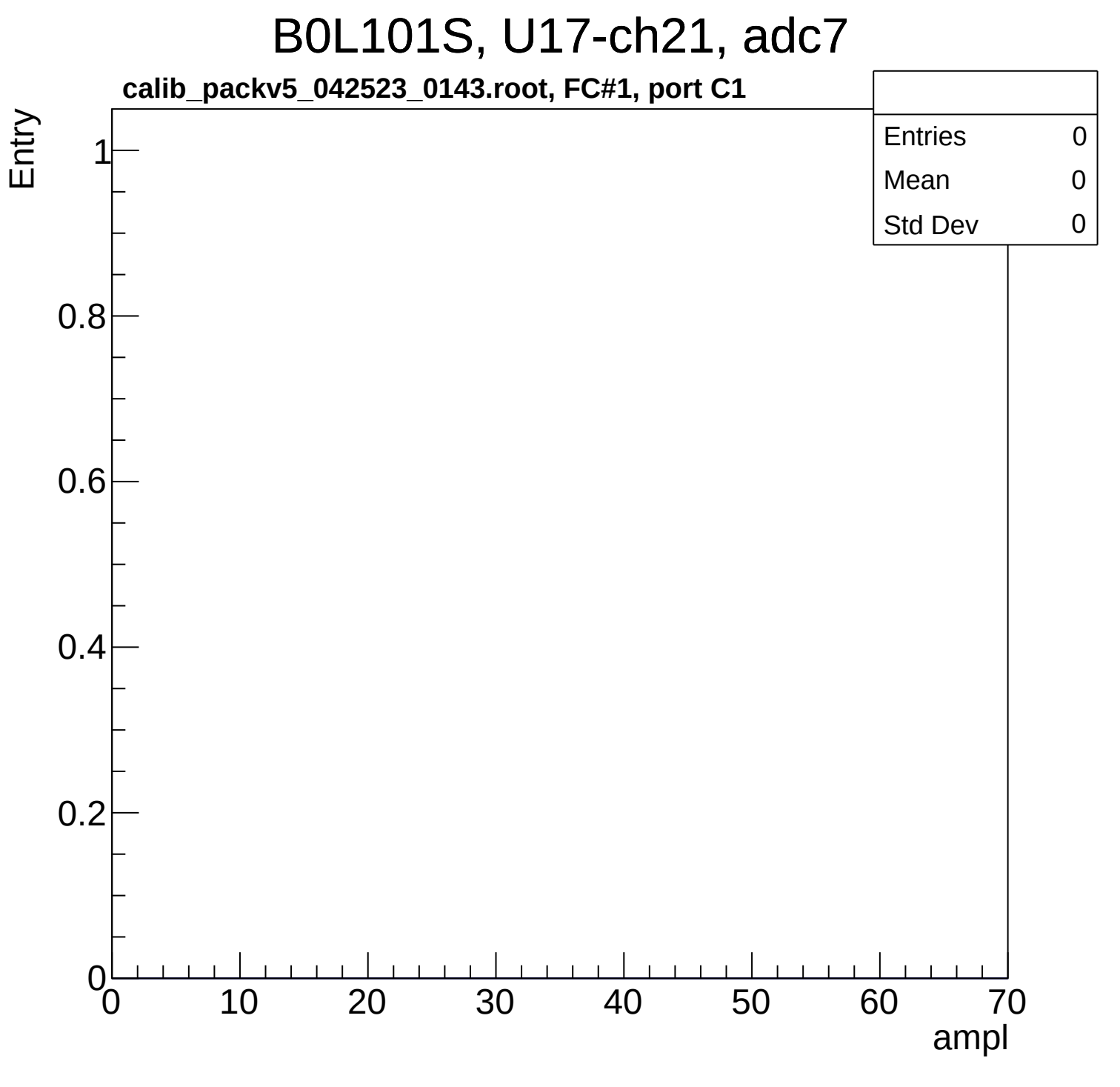

B0L101S, U17-ch21, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 43.64
Std Dev 5.099
Gaus mean : 44.7491
Gaus Width: 3.5753
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

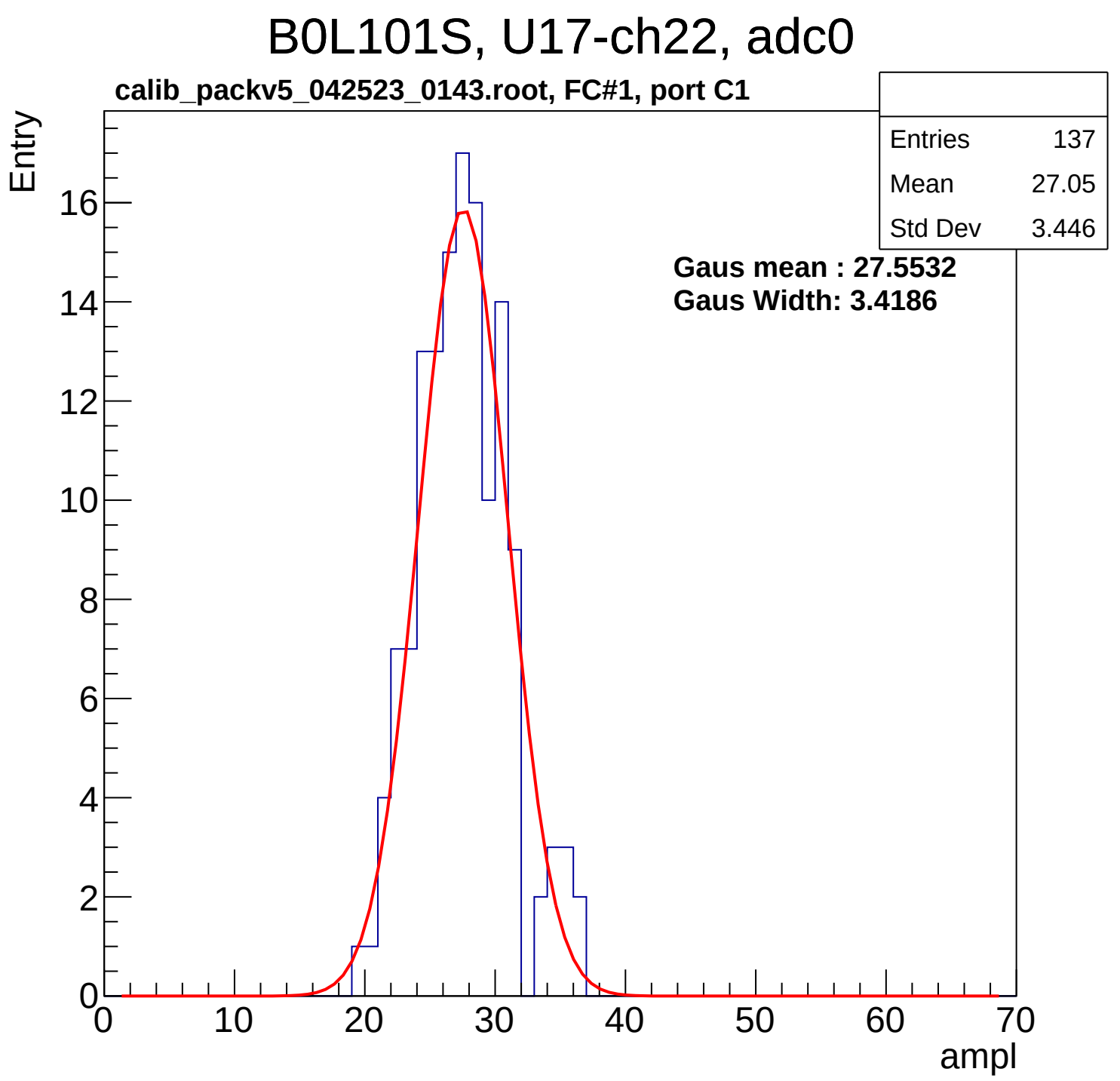

B0L101S, U17-ch21, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 50.19
Std Dev 3.261
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

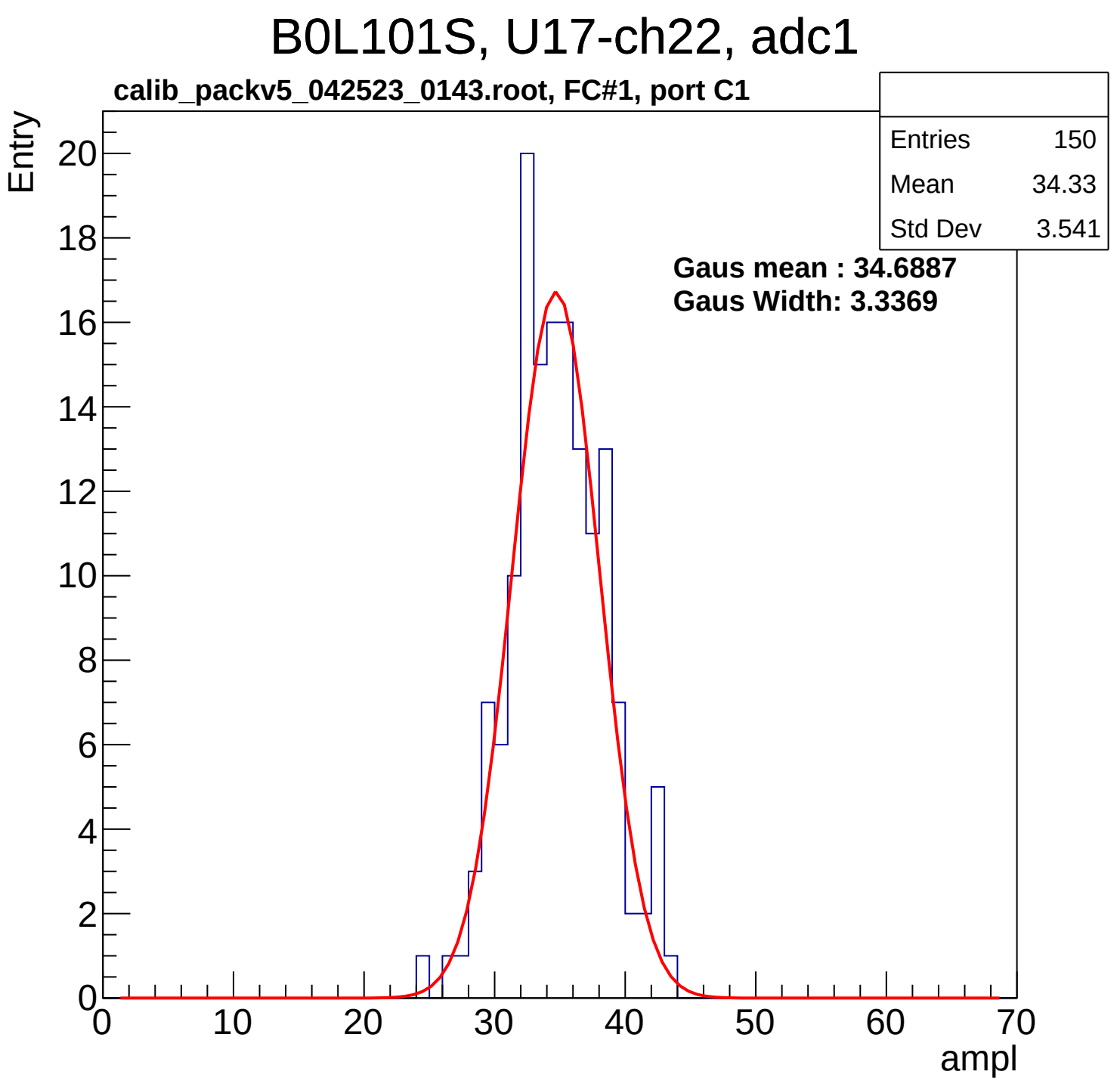

B0L101S, U17-ch21, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 56.12
Std Dev 5.907
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

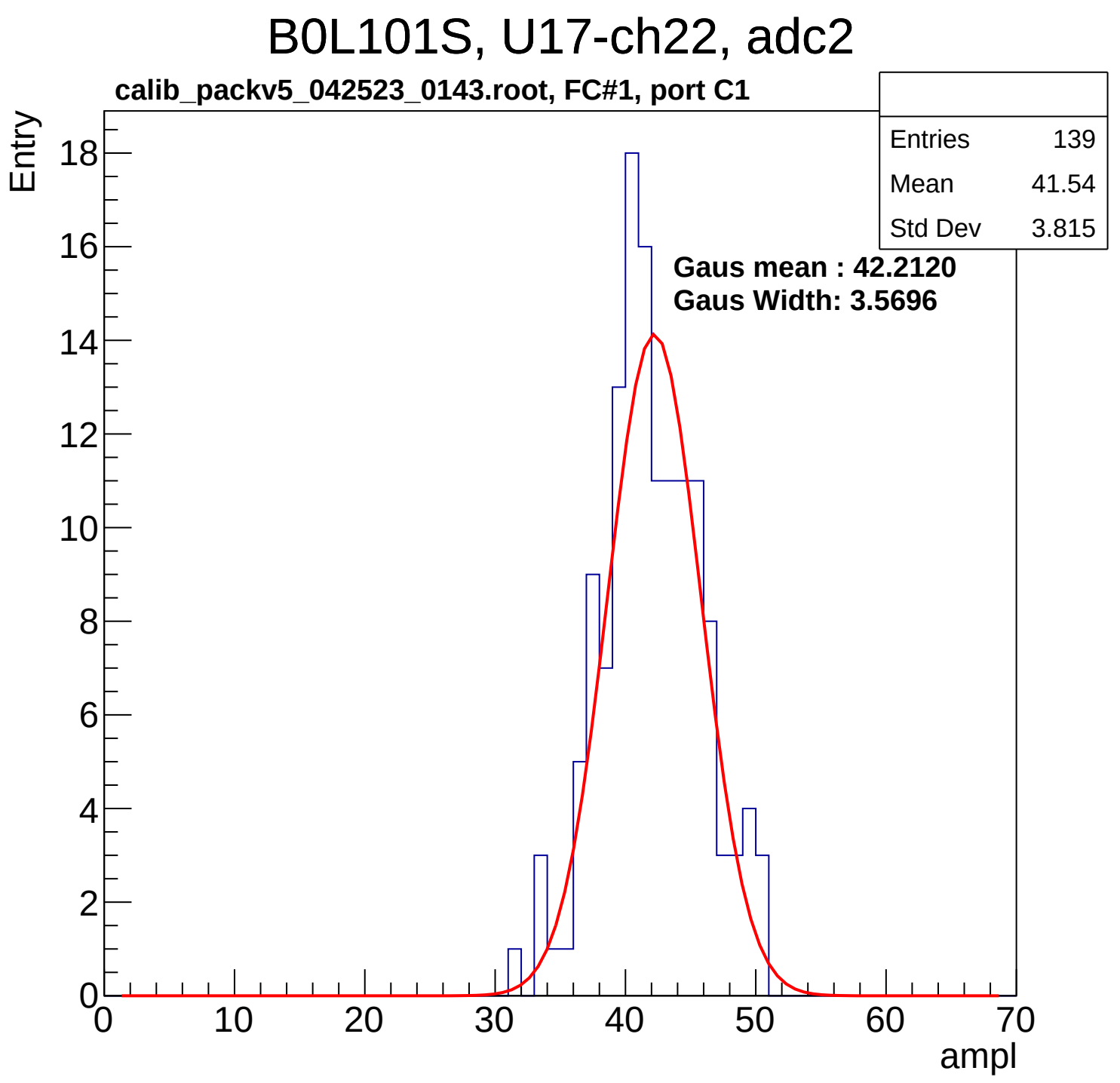

B0L101S, U17-ch21, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 64
Mean 59.39
Std Dev 7.779
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

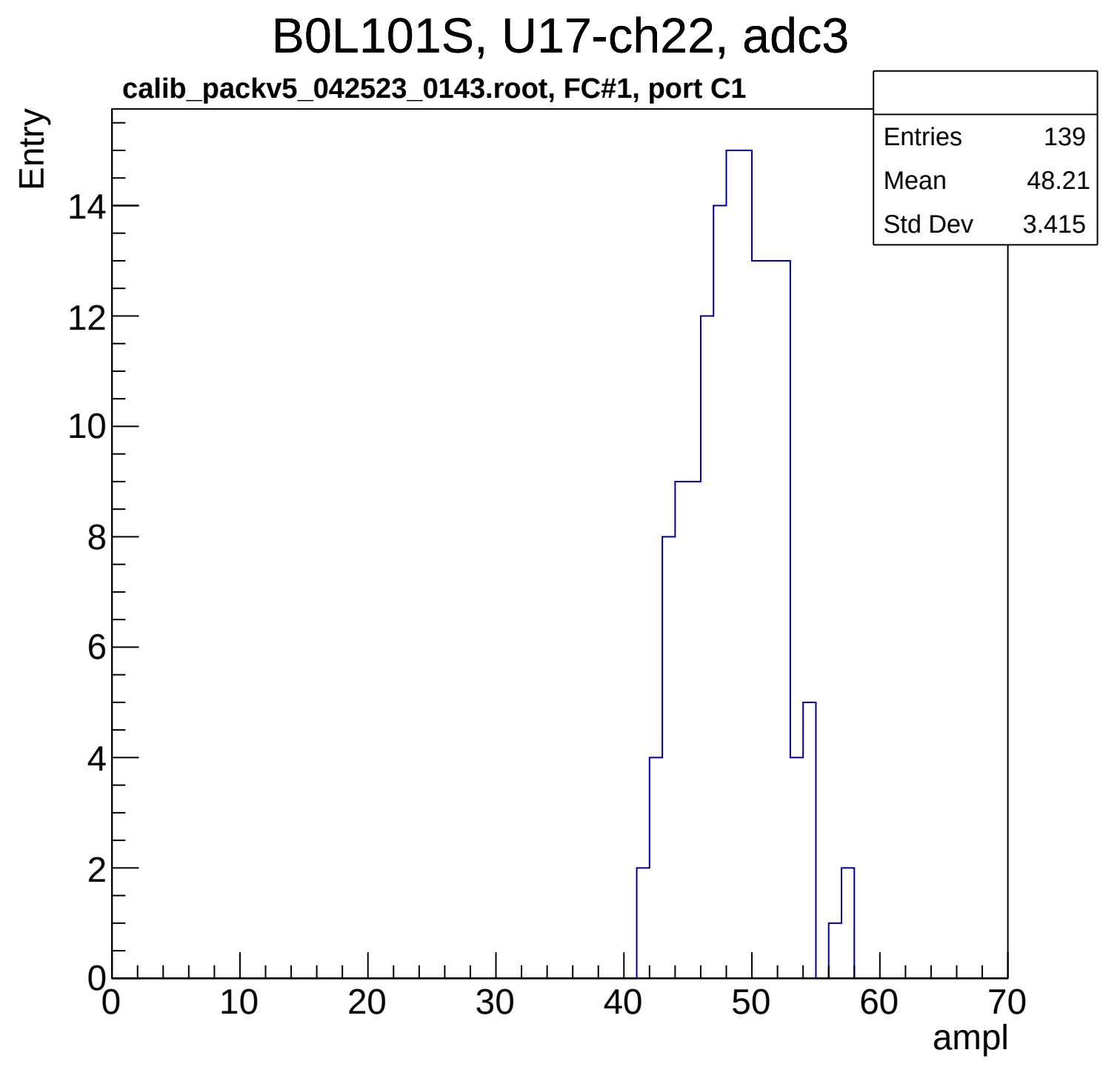

B0L101S, U17-ch21, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

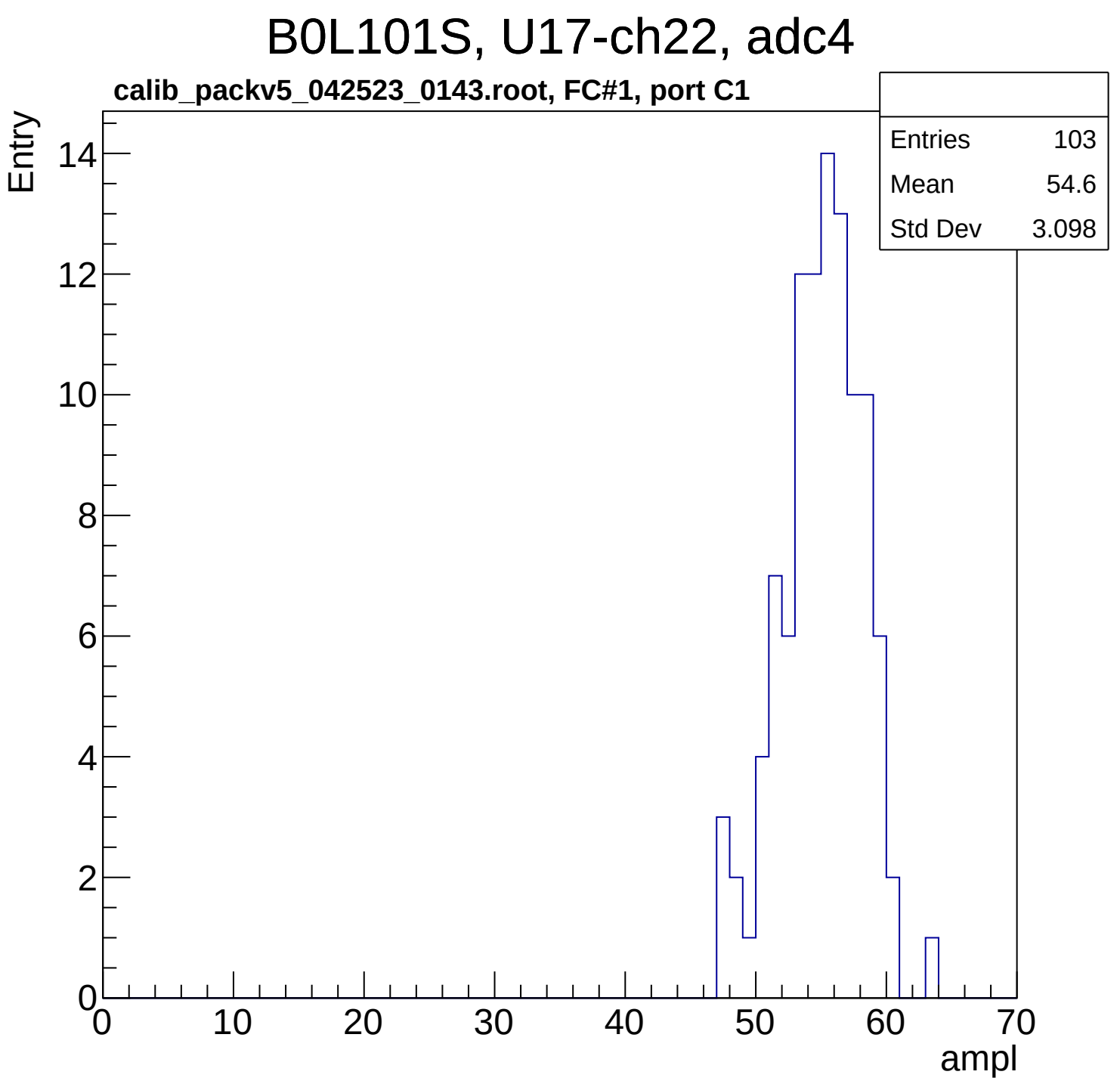

B0L101S, U17-ch21, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

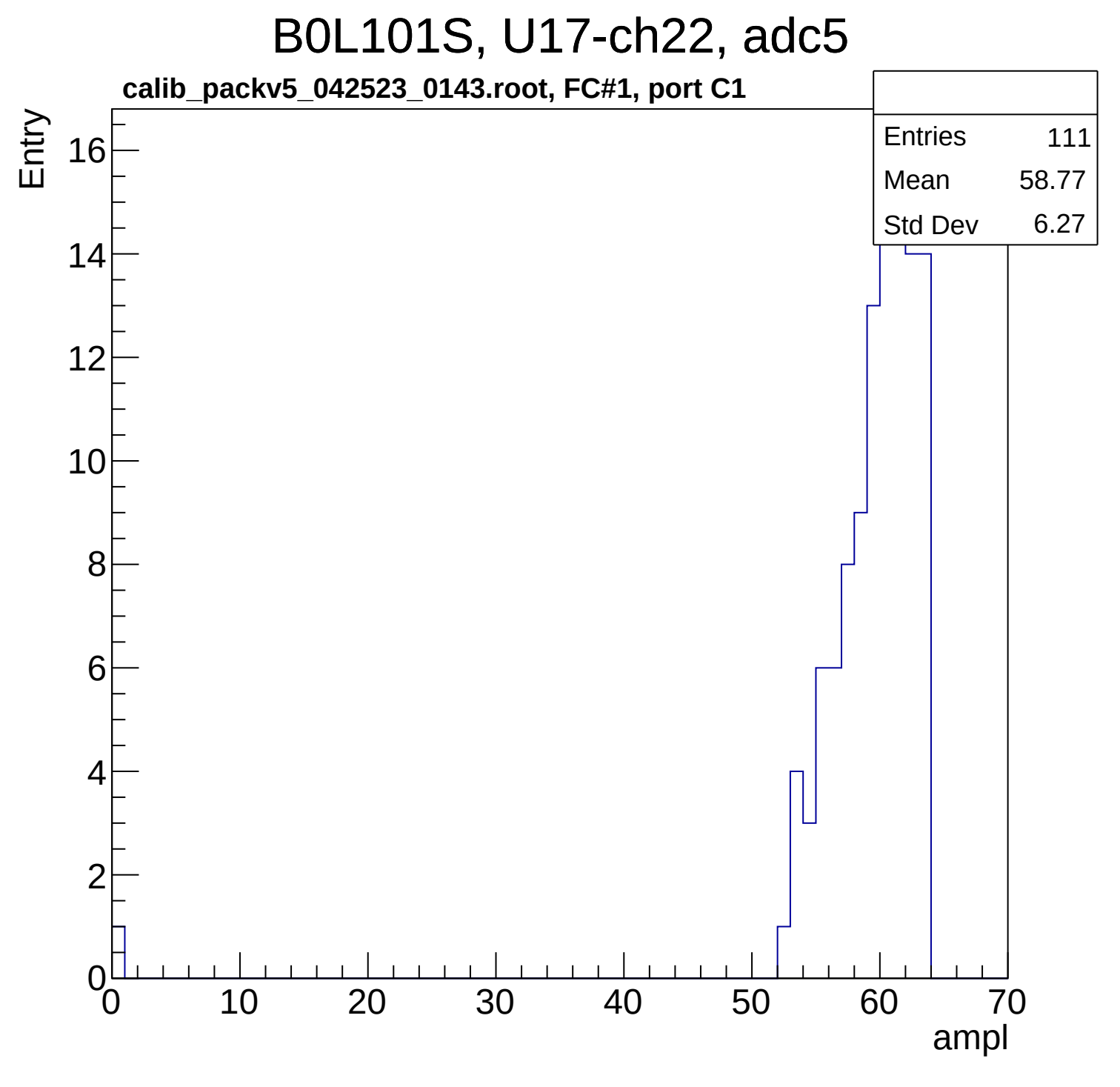

B0L101S, U17-ch22, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 27.05
Std Dev 3.446
Gaus mean : 27.5532
Gaus Width: 3.4186
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

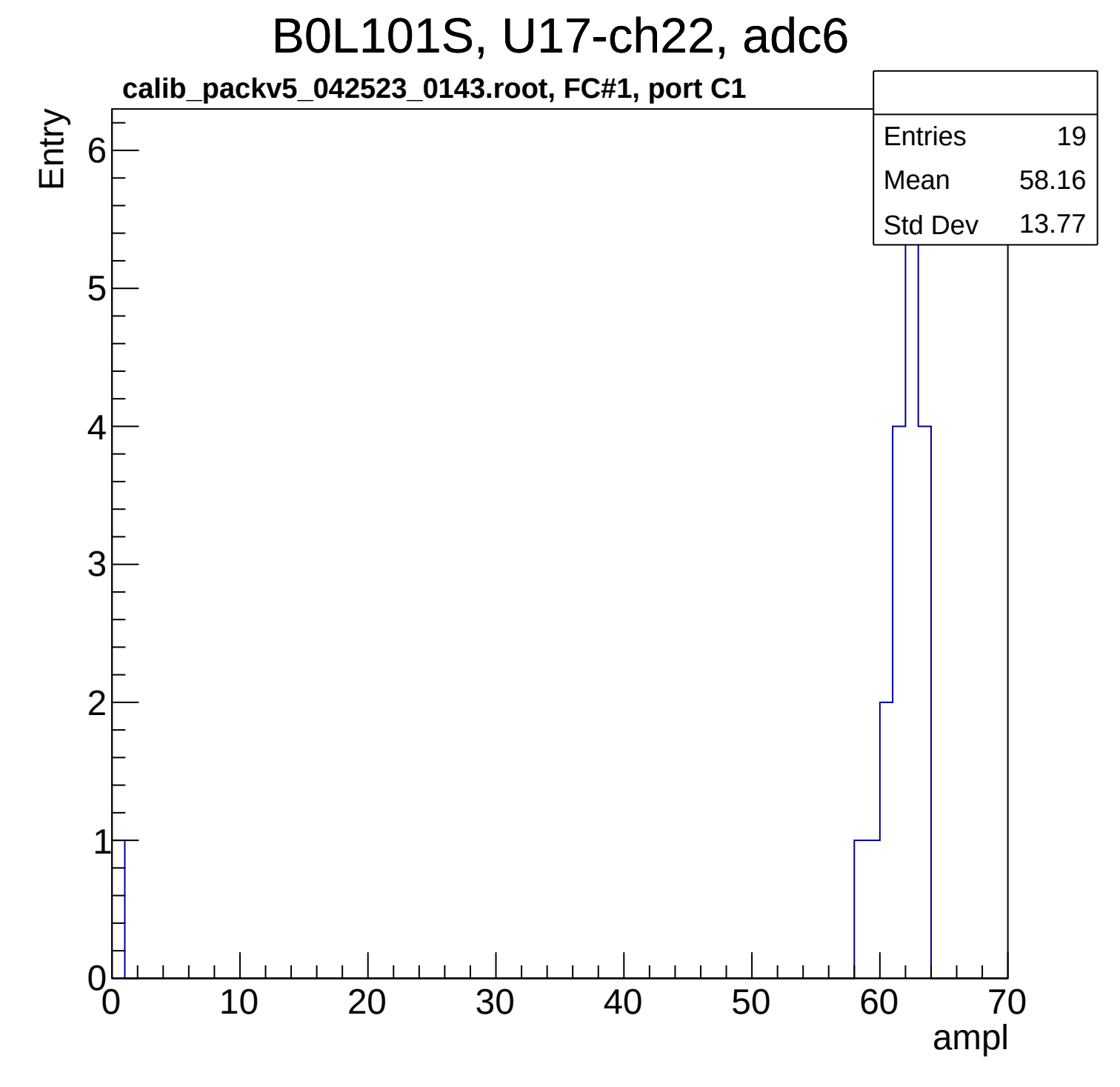

B0L101S, U17-ch22, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 34.33
Std Dev 3.541
Gaus mean : 34.6887
Gaus Width: 3.3369
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

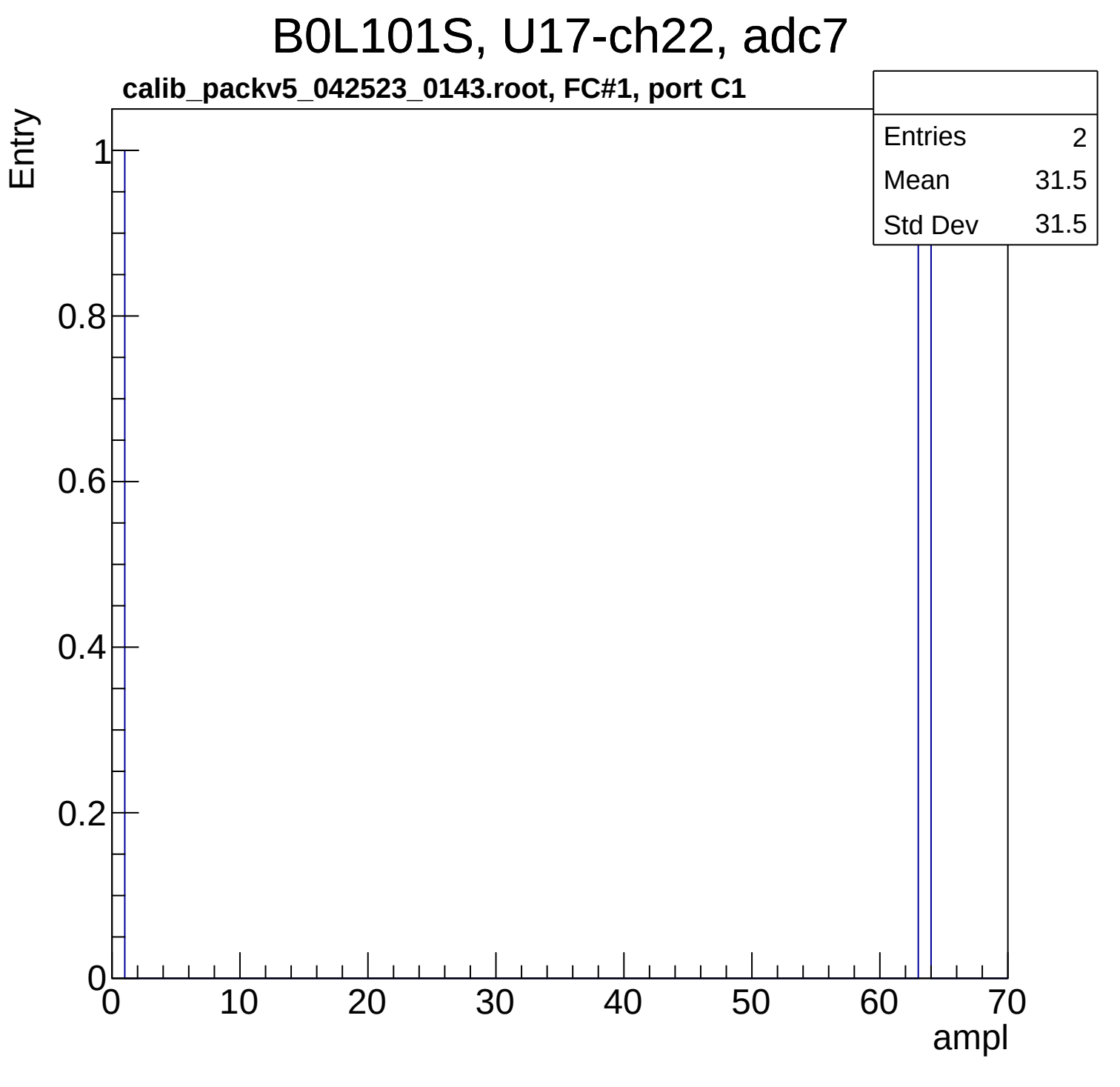

B0L101S, U17-ch22, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 41.54
Std Dev 3.815
Gaus mean : 42.2120
Gaus Width: 3.5696
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

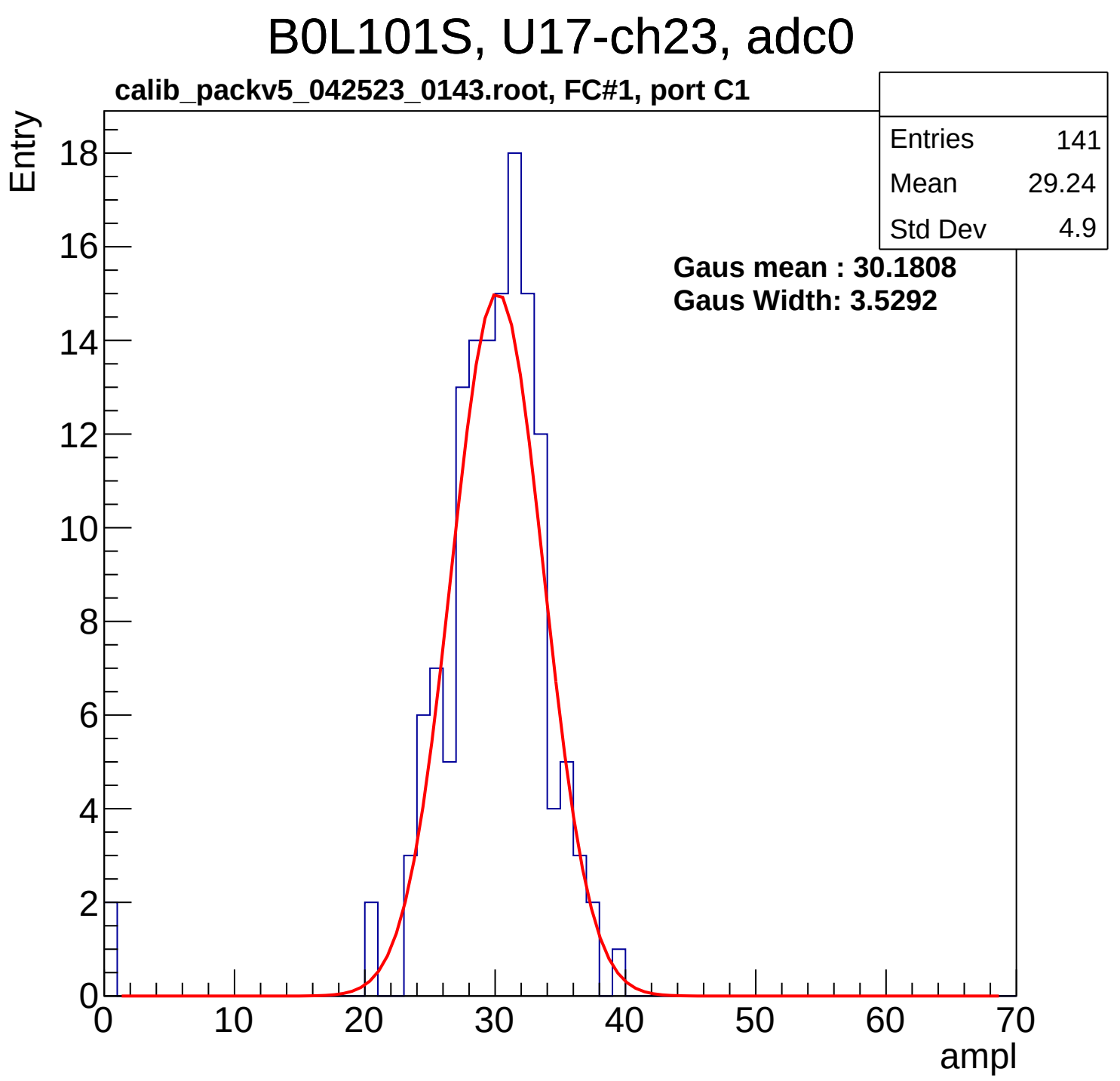

B0L101S, U17-ch22, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 48.21
Std Dev 3.415
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

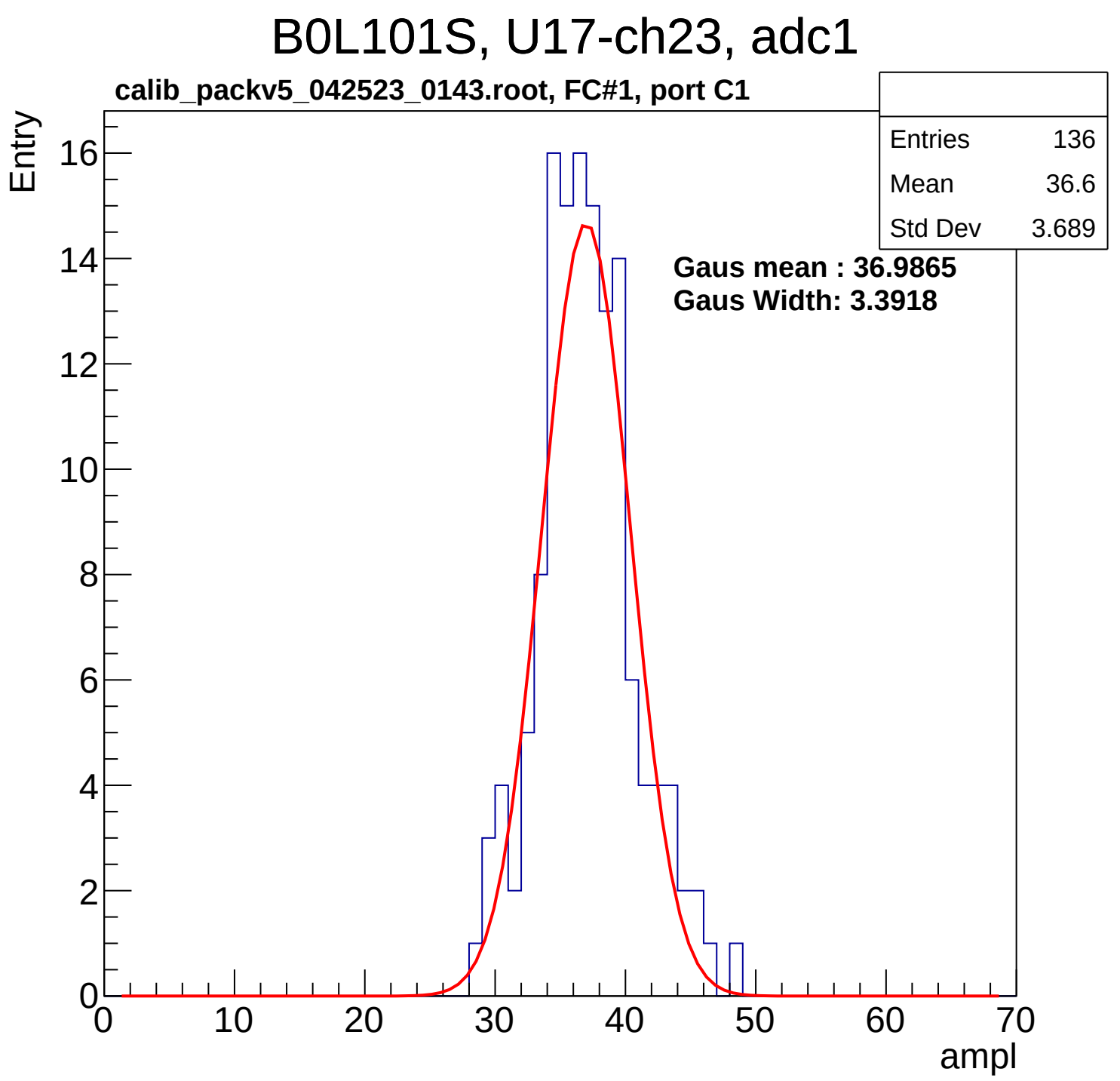

B0L101S, U17-ch22, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 54.6
Std Dev 3.098
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

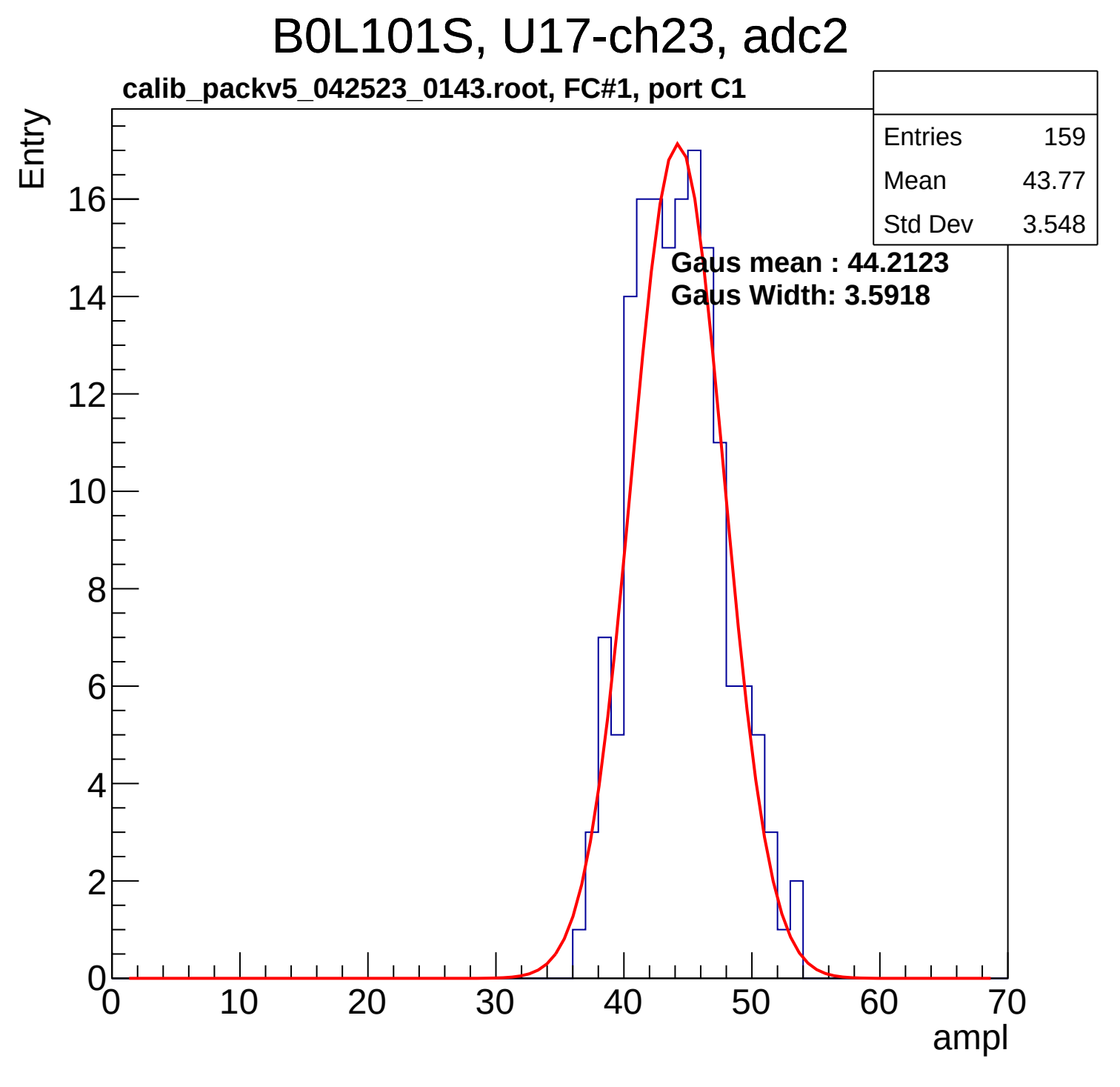

B0L101S, U17-ch22, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 58.77
Std Dev 6.27
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

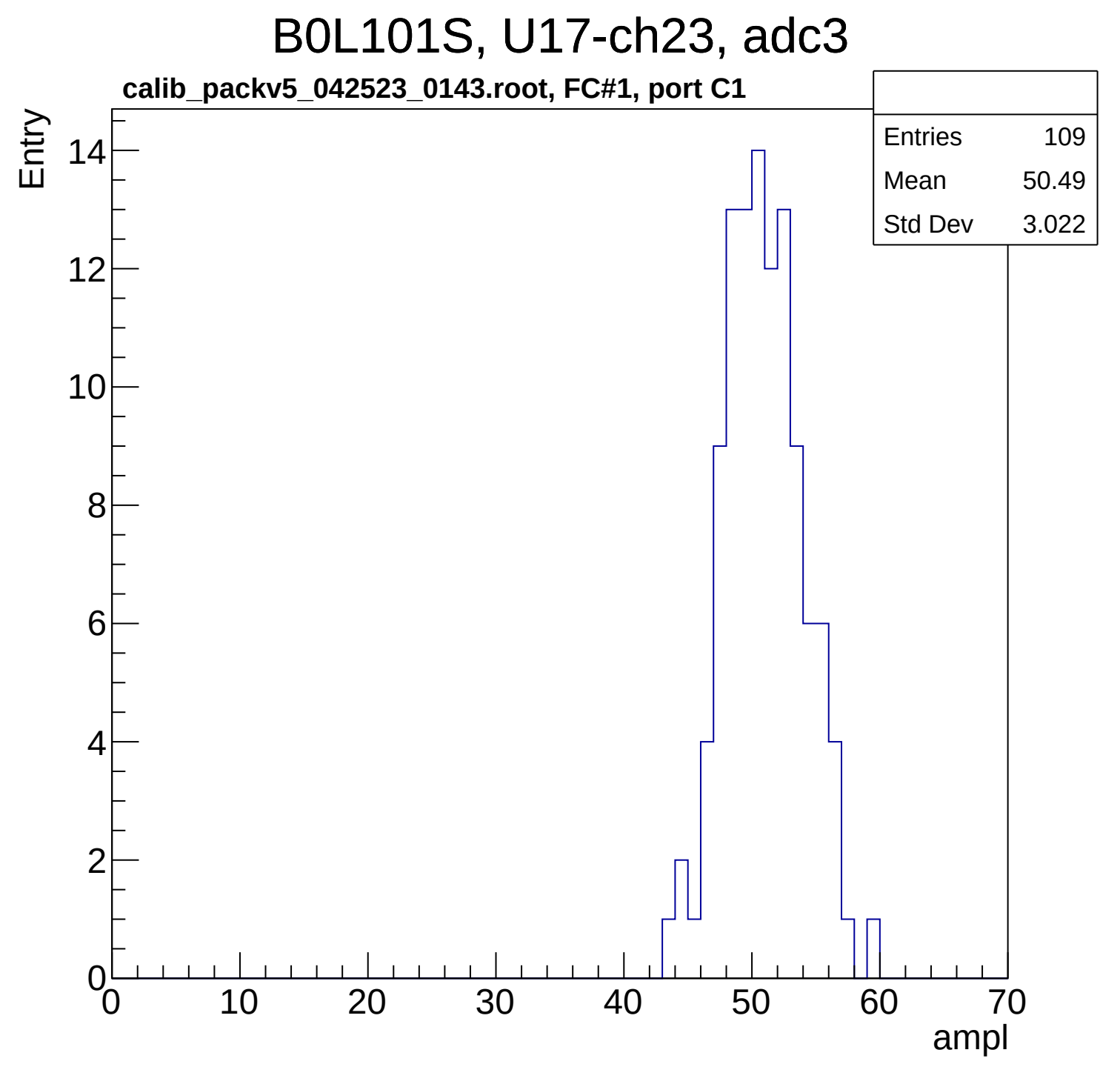

B0L101S, U17-ch22, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 19
Mean 58.16
Std Dev 13.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

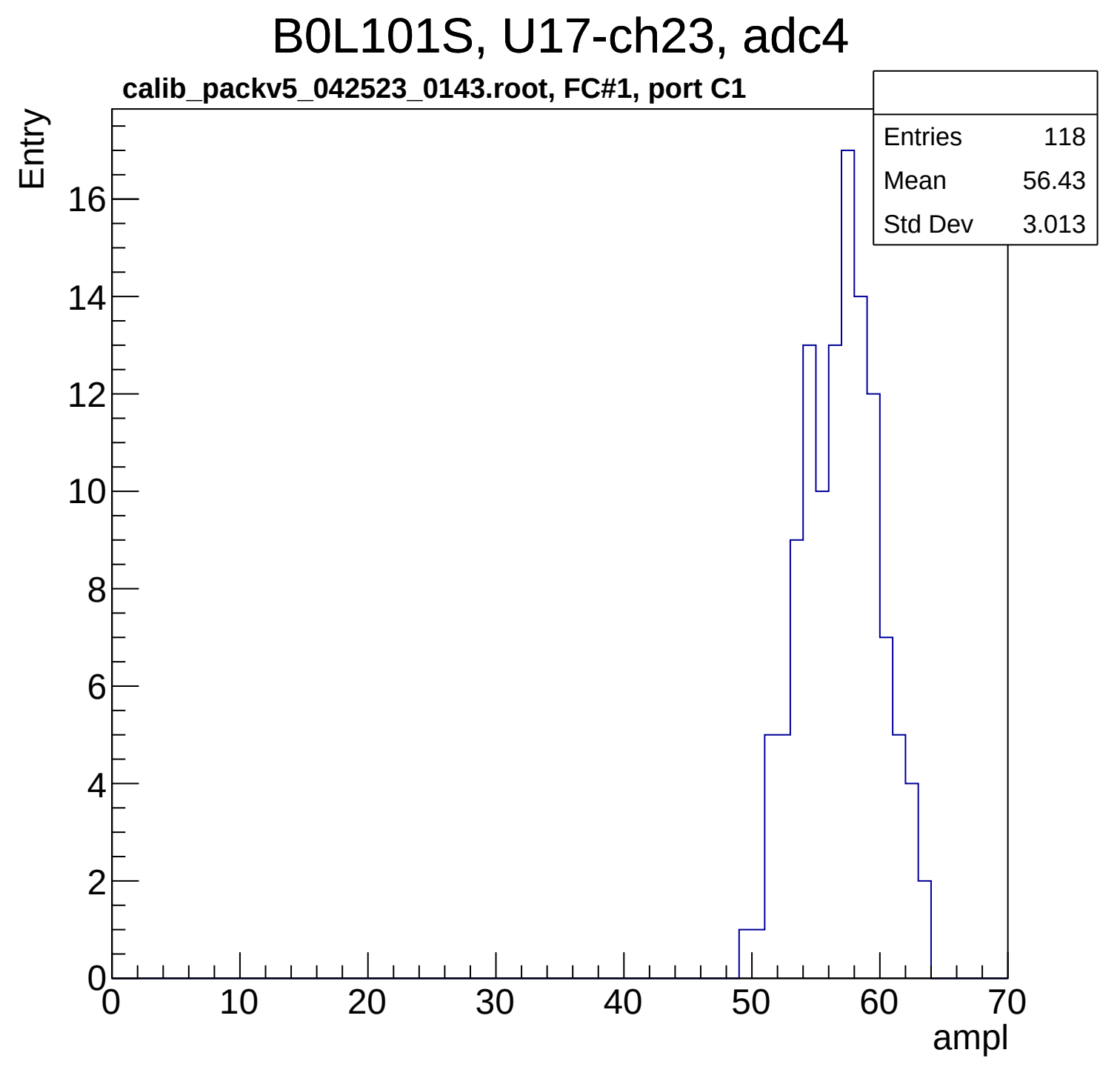

B0L101S, U17-ch22, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

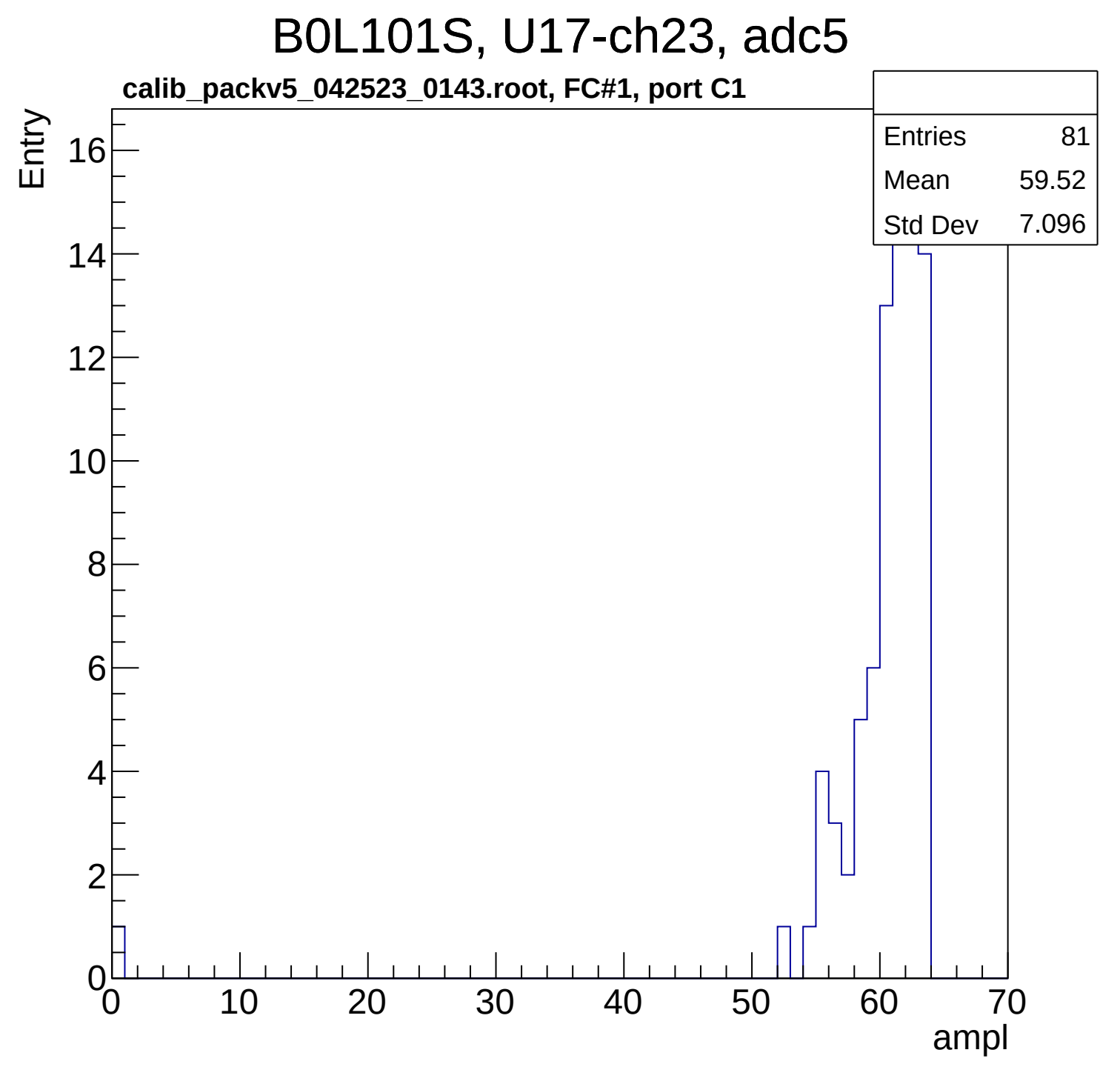

B0L101S, U17-ch23, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 29.24
Std Dev 4.9
Gaus mean : 30.1808
Gaus Width: 3.5292
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

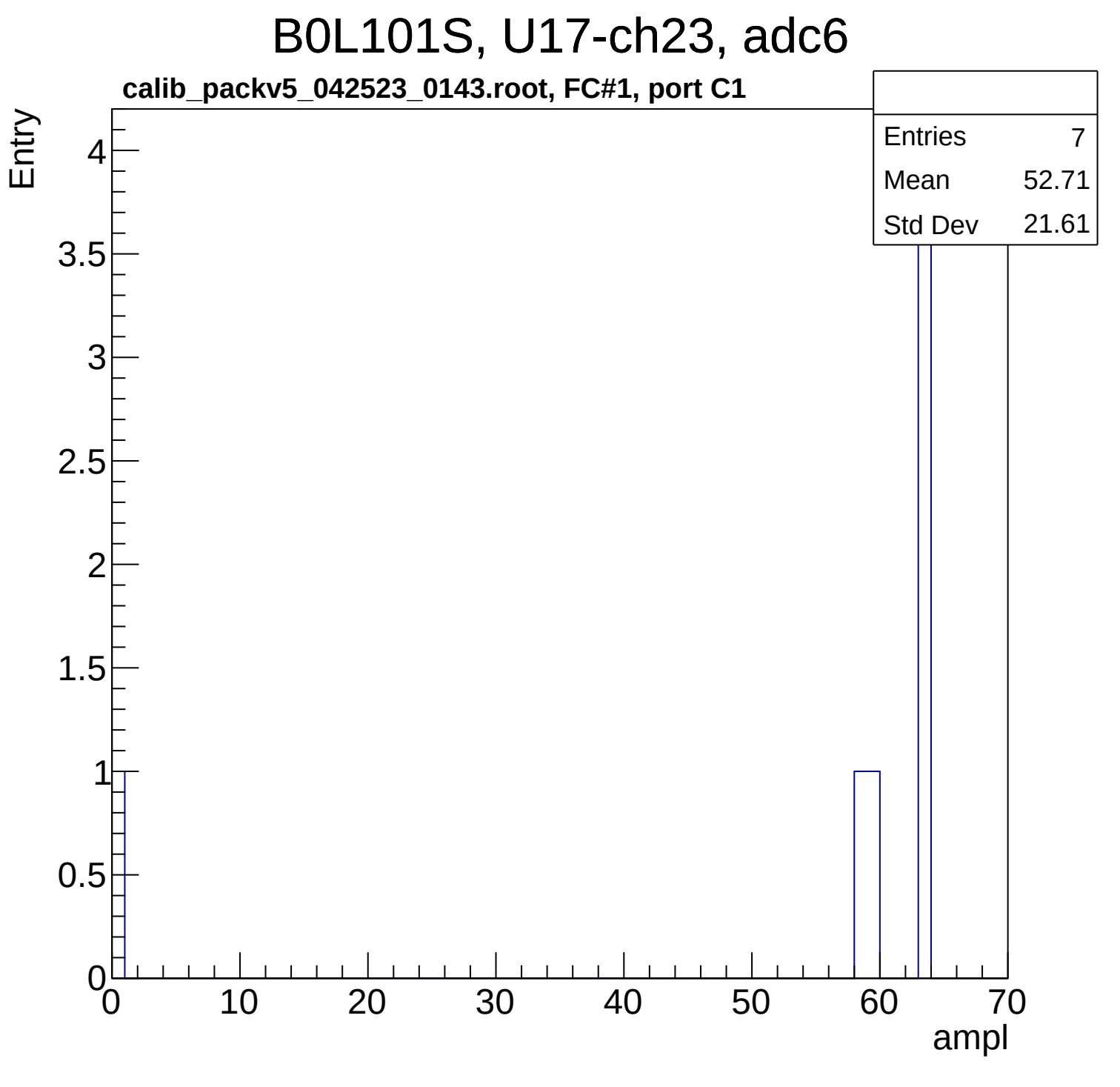

B0L101S, U17-ch23, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 36.6
Std Dev 3.689
Gaus mean : 36.9865
Gaus Width: 3.3918
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

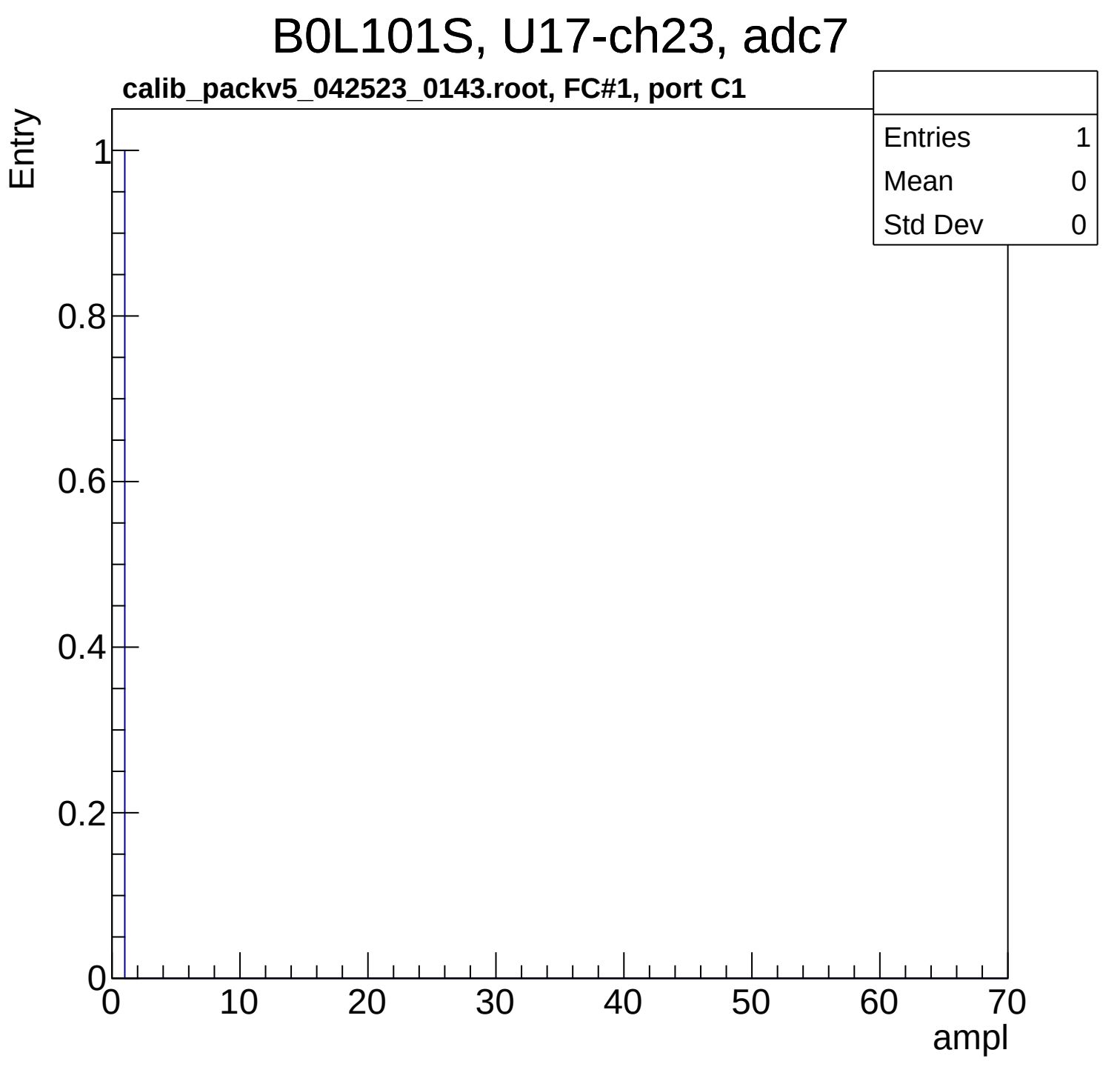

B0L101S, U17-ch23, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 43.77
Std Dev 3.548
Gaus mean : 44.2123
Gaus Width: 3.5918
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

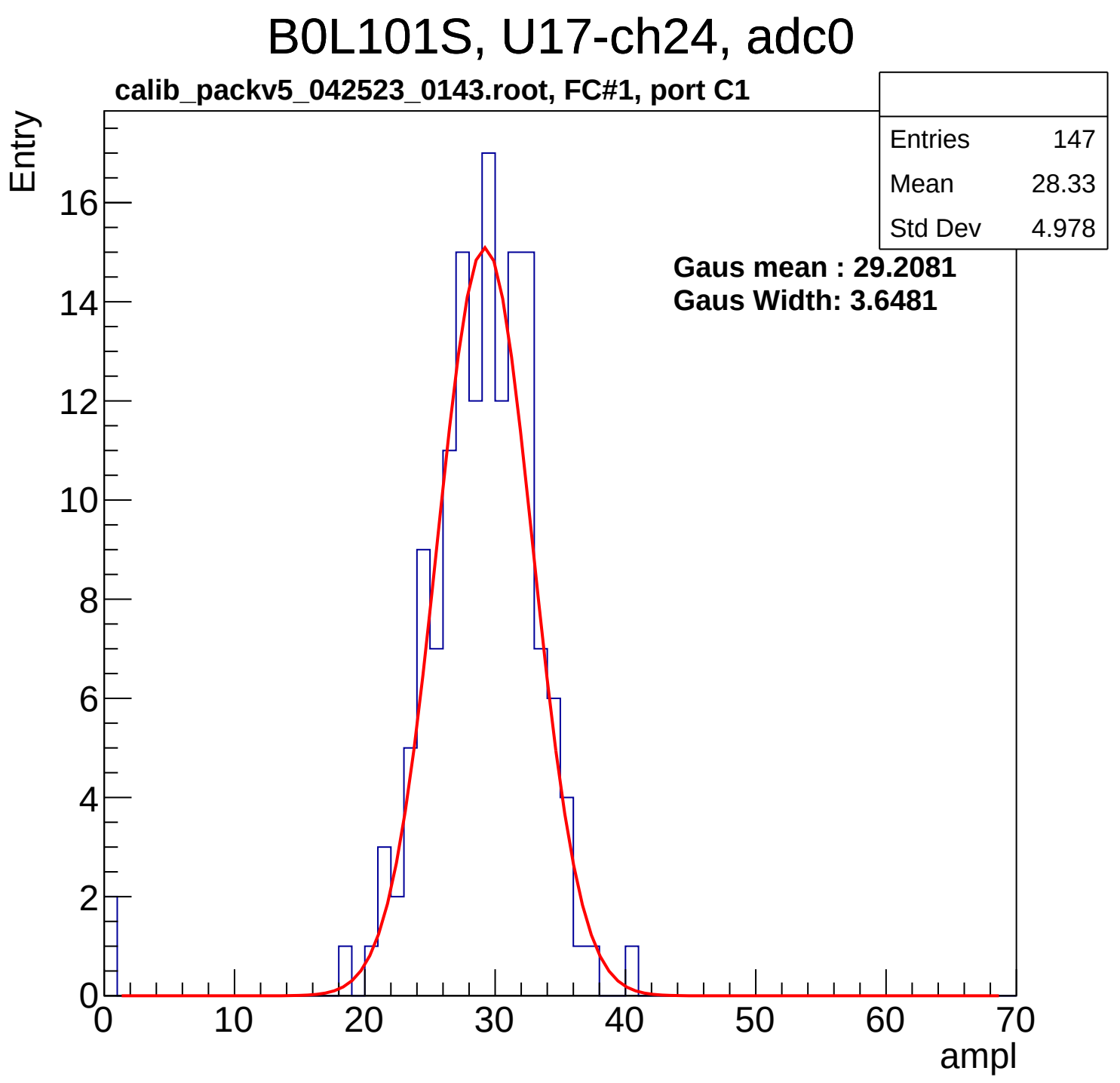

B0L101S, U17-ch23, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 50.49
Std Dev 3.022
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

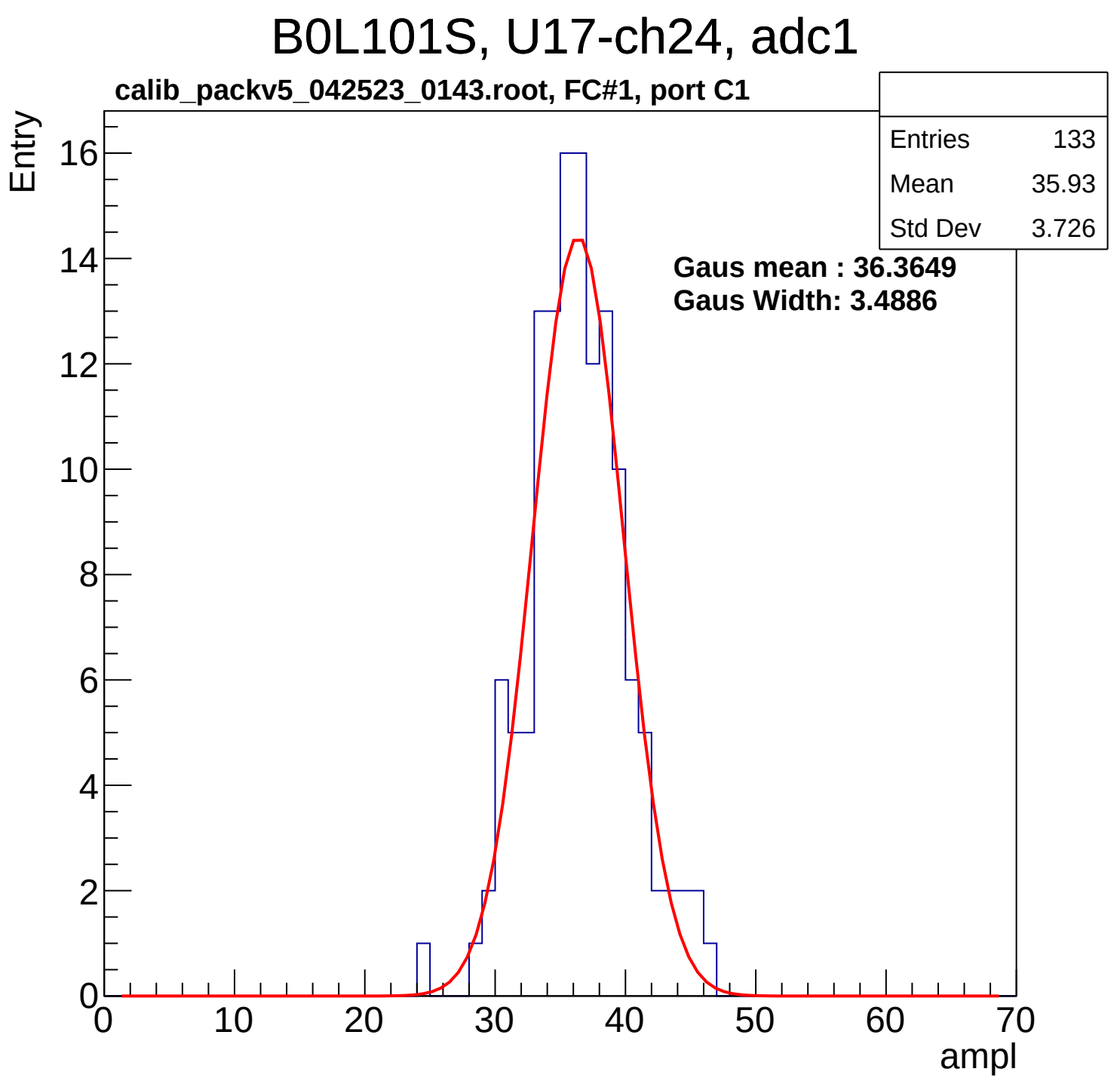

B0L101S, U17-ch23, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 56.43
Std Dev 3.013
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

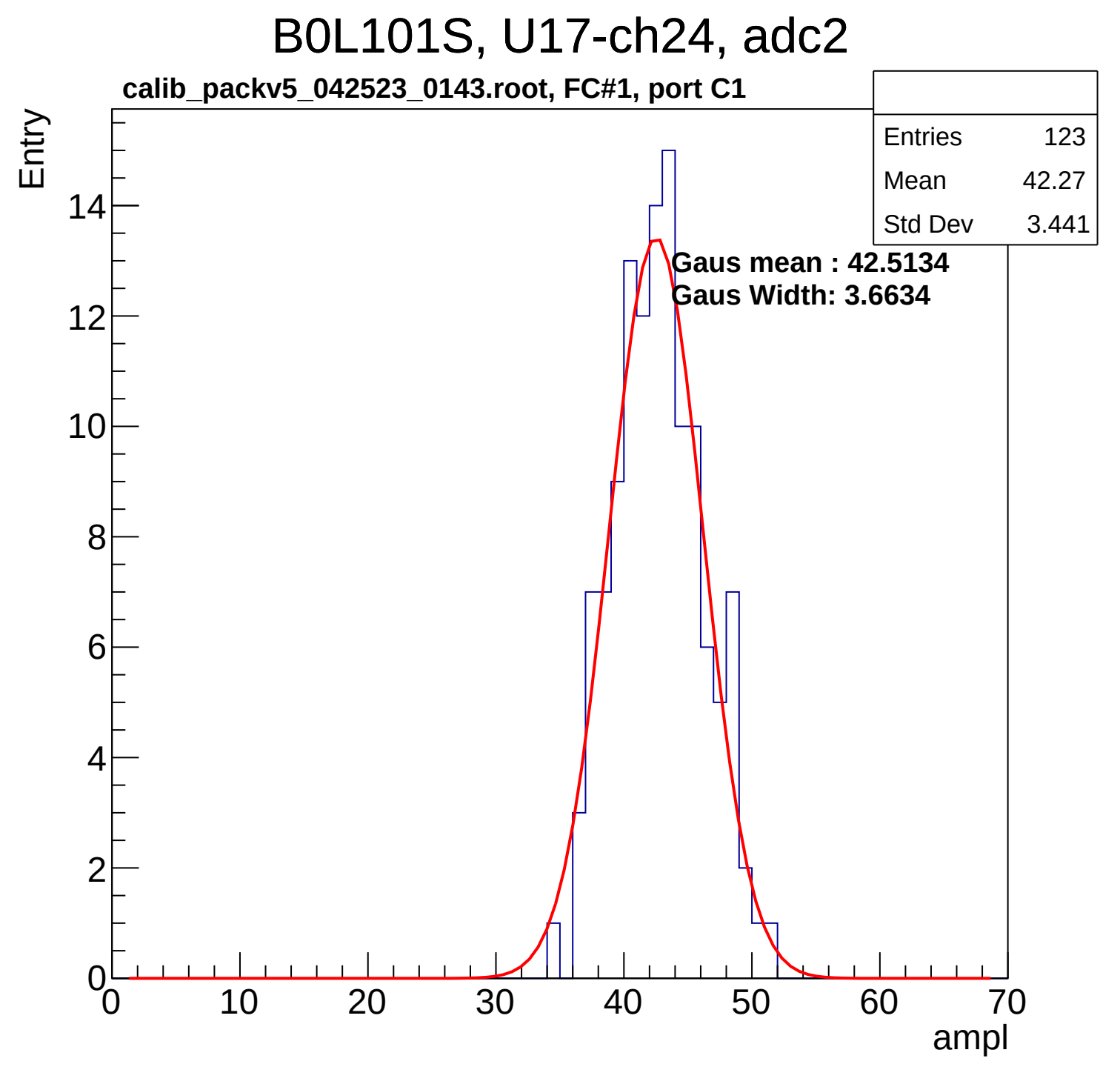

B0L101S, U17-ch23, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.52
Std Dev 7.096
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

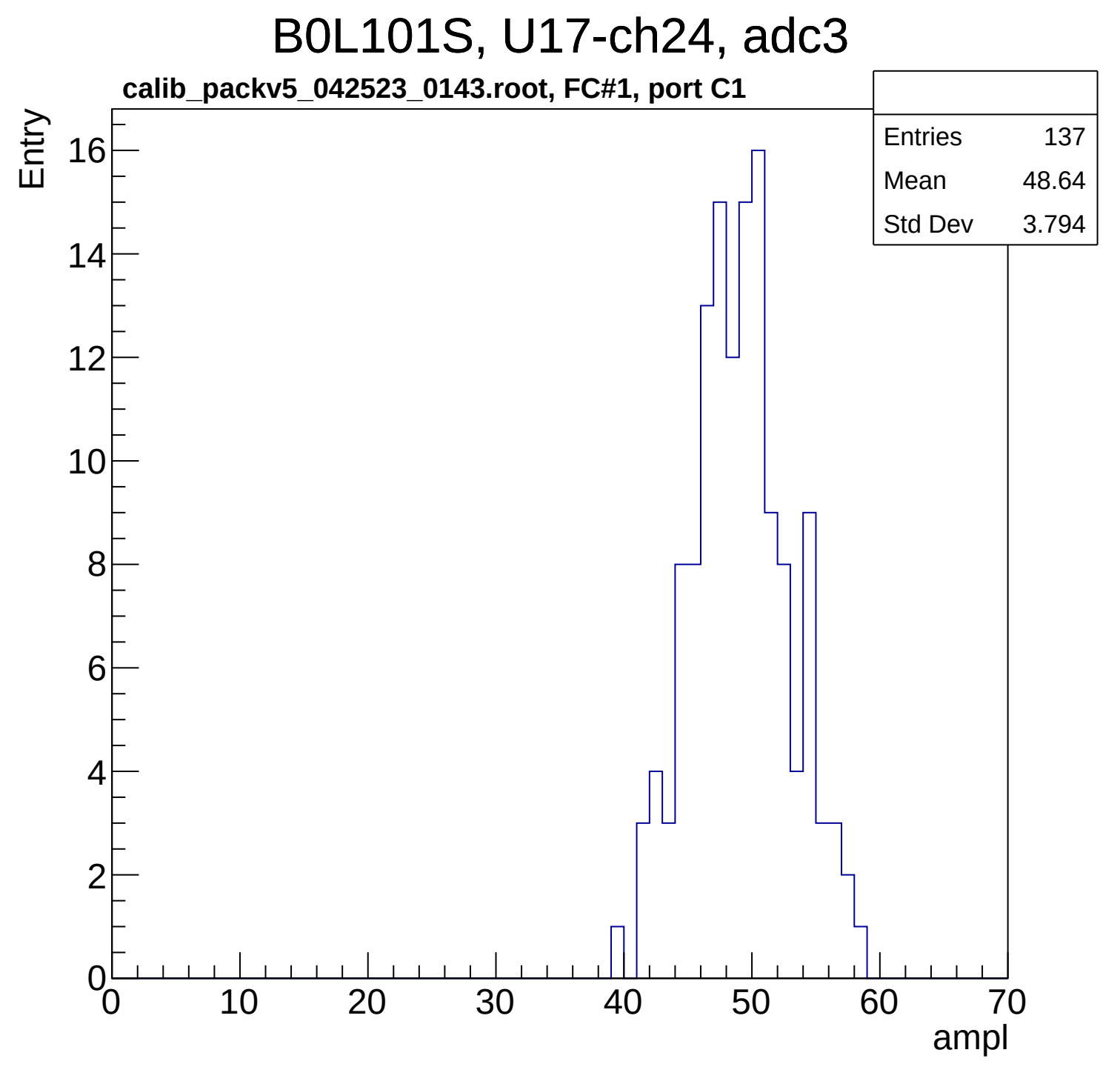

B0L101S, U17-ch23, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 7
Mean 52.71
Std Dev 21.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

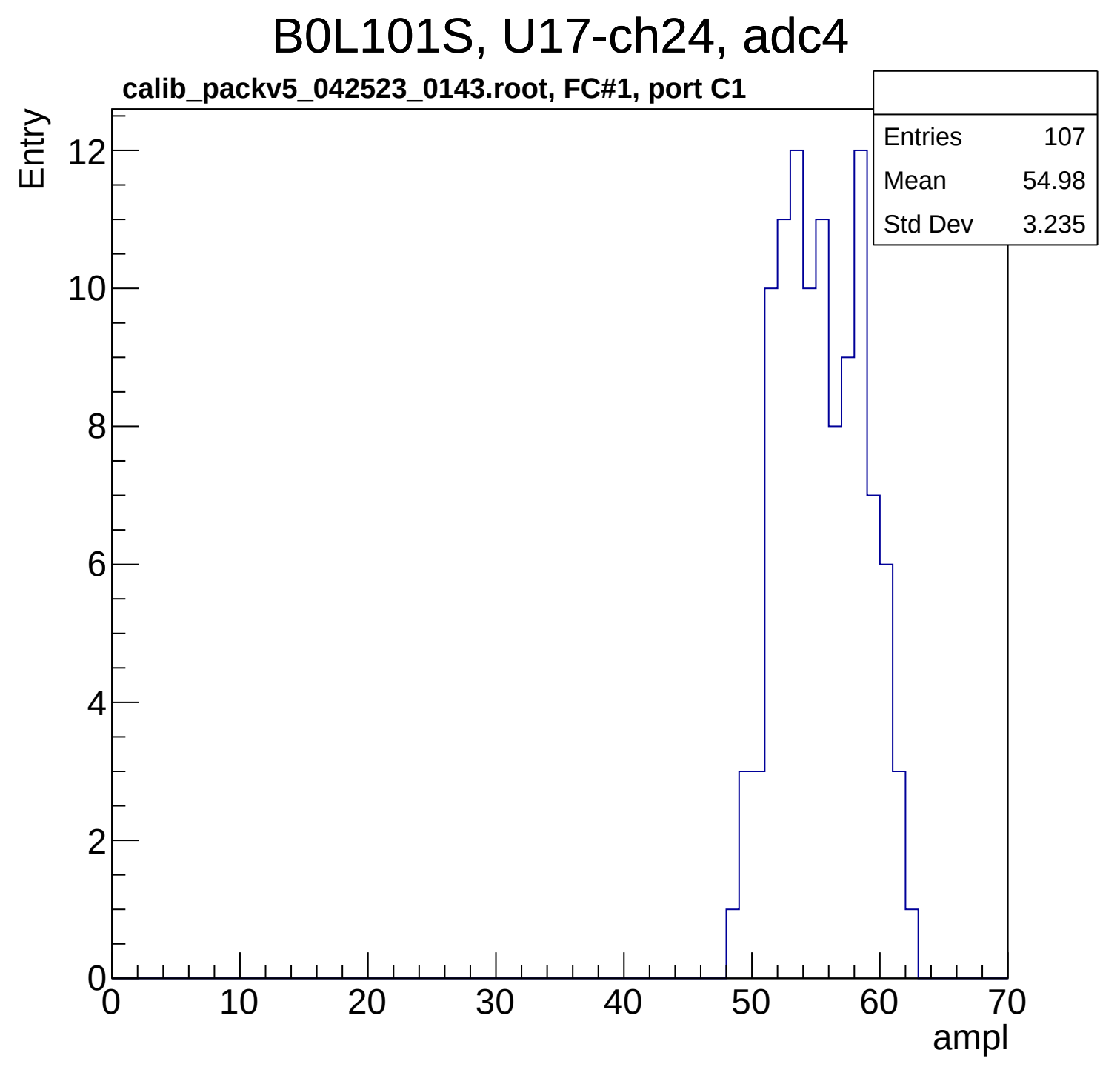

B0L101S, U17-ch23, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

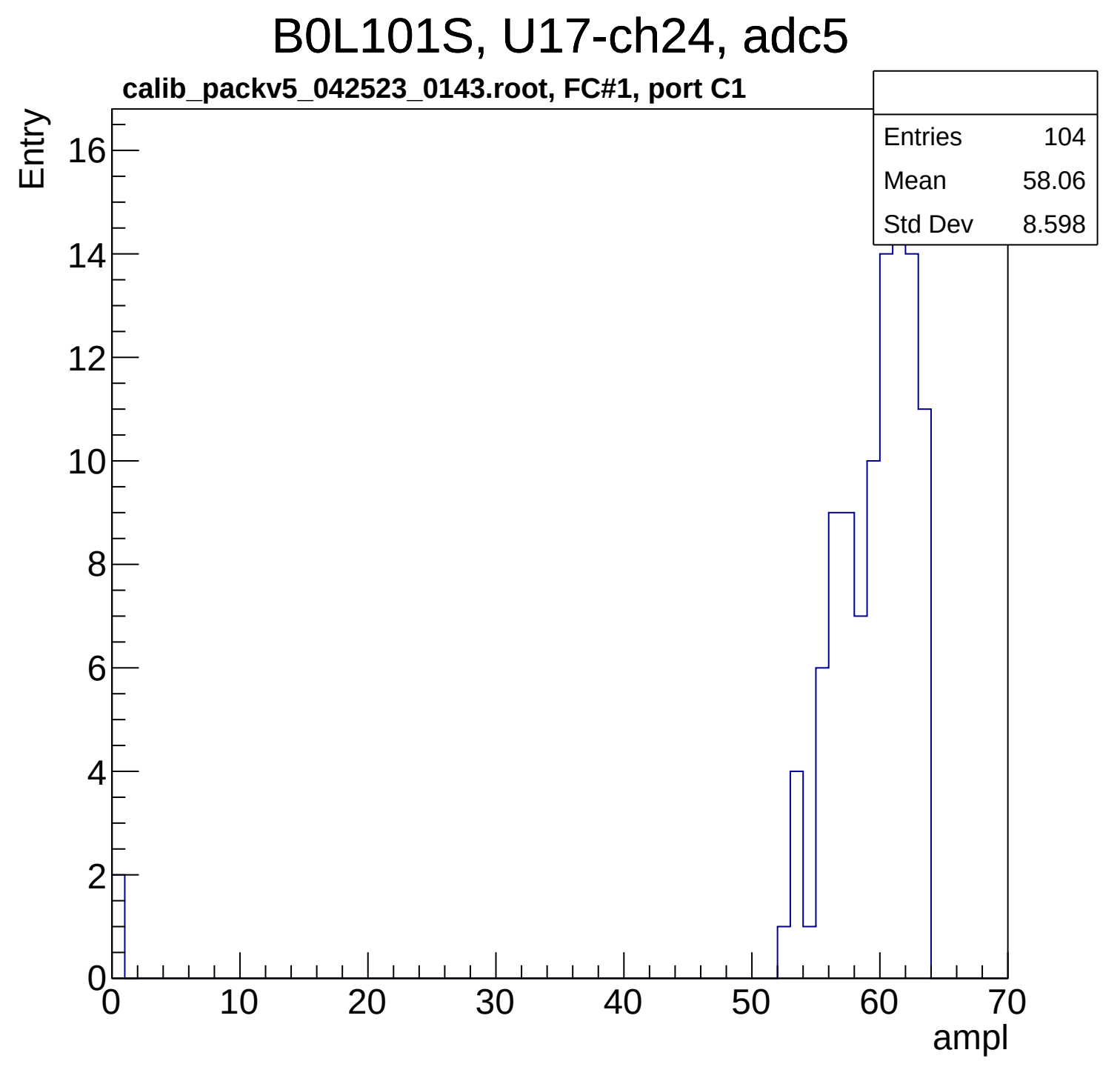

B0L101S, U17-ch24, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 28.33
Std Dev 4.978
Gaus mean : 29.2081
Gaus Width: 3.6481
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

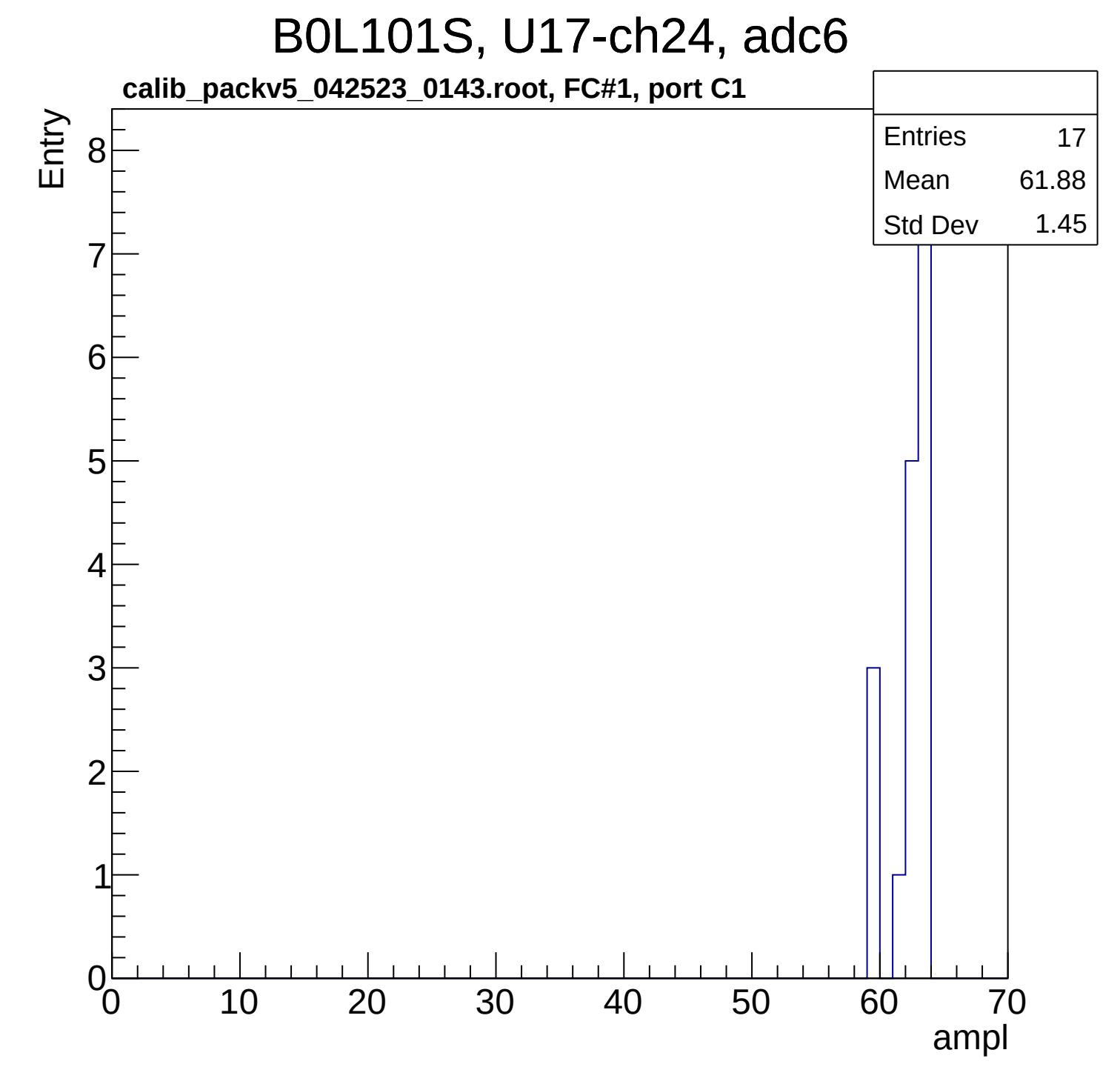

B0L101S, U17-ch24, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 35.93
Std Dev 3.726
Gaus mean : 36.3649
Gaus Width: 3.4886
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

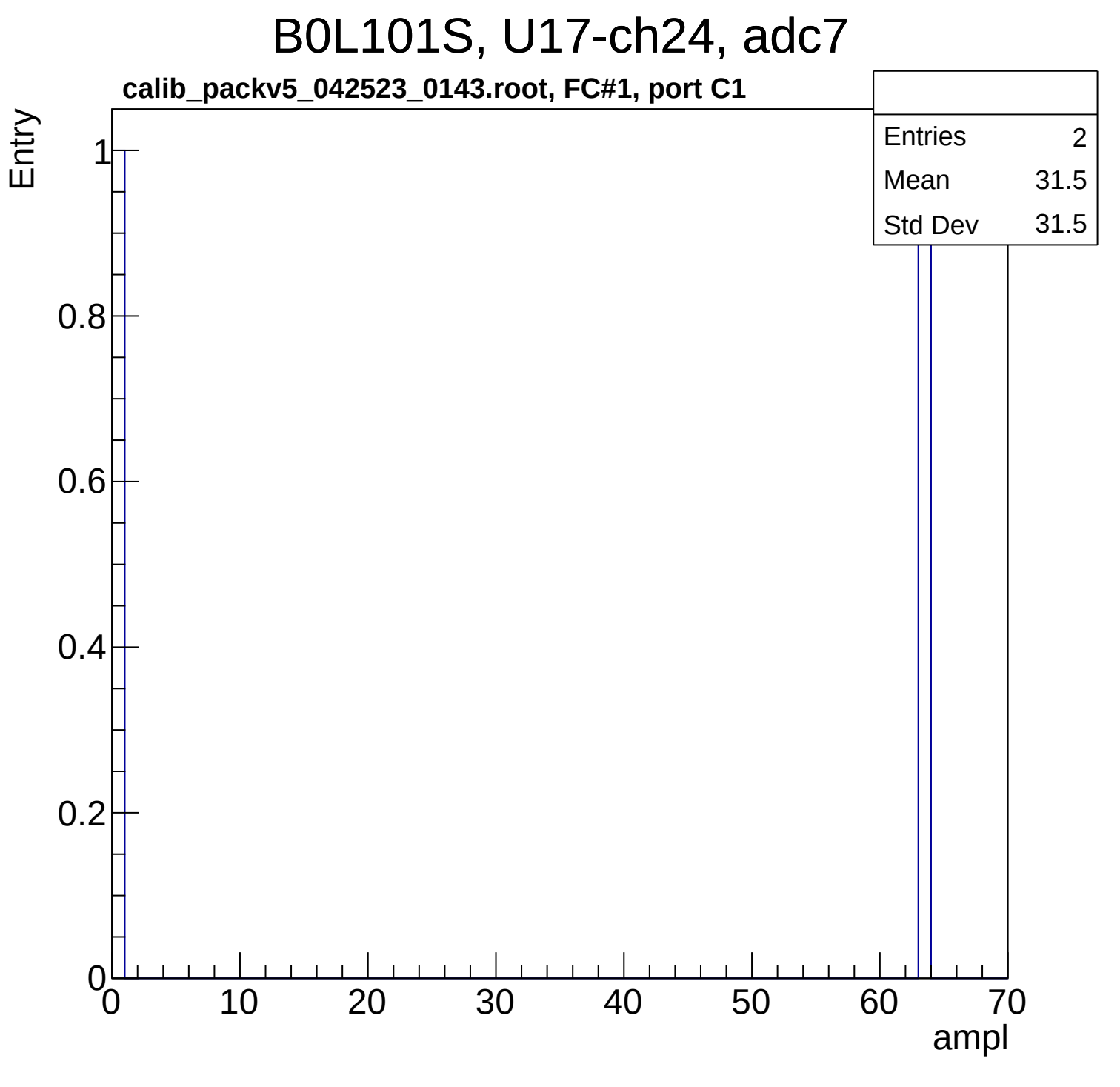

B0L101S, U17-ch24, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 42.27
Std Dev 3.441
Gaus mean : 42.5134
Gaus Width: 3.6634
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

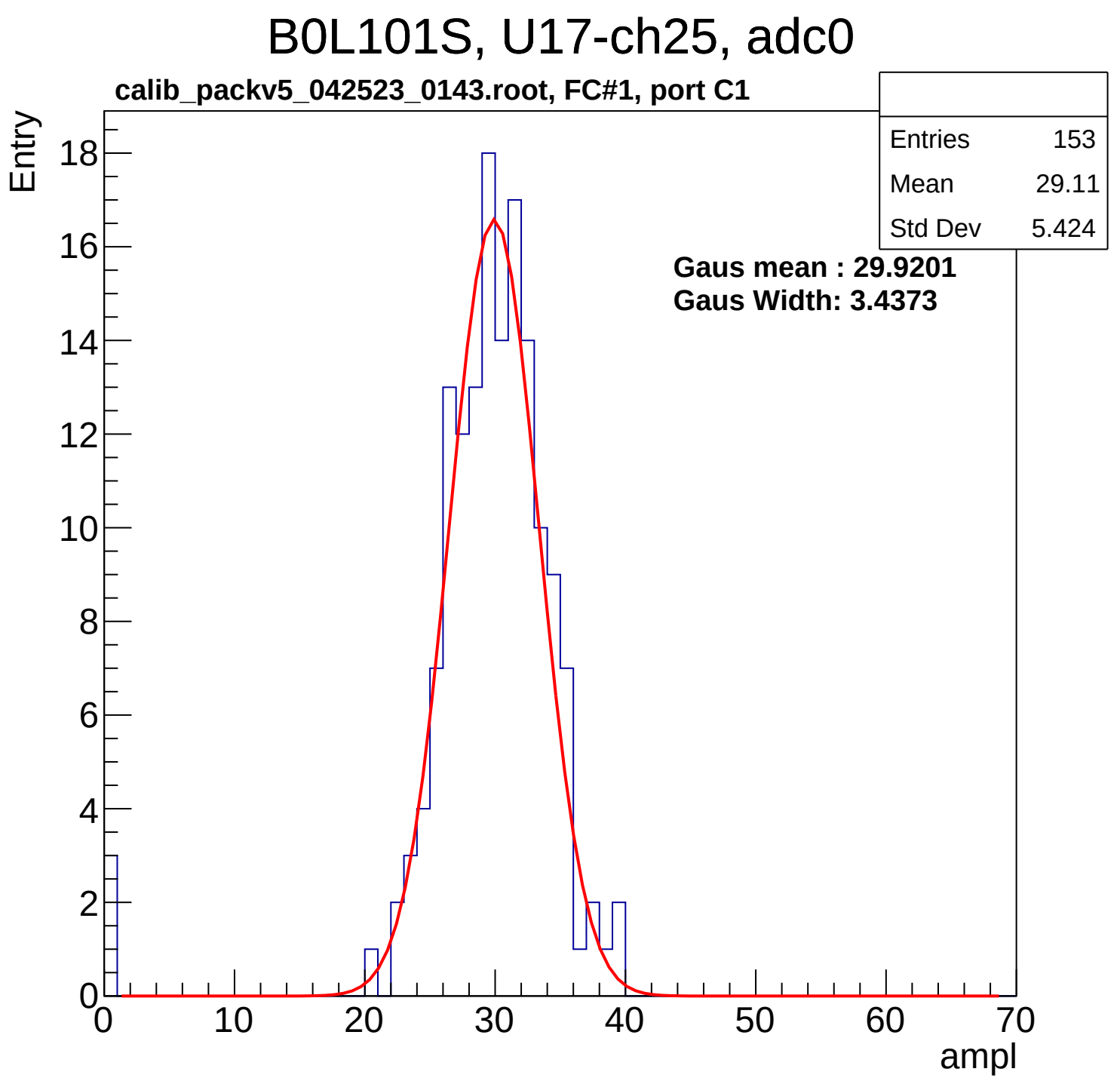

B0L101S, U17-ch24, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 48.64
Std Dev 3.794
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

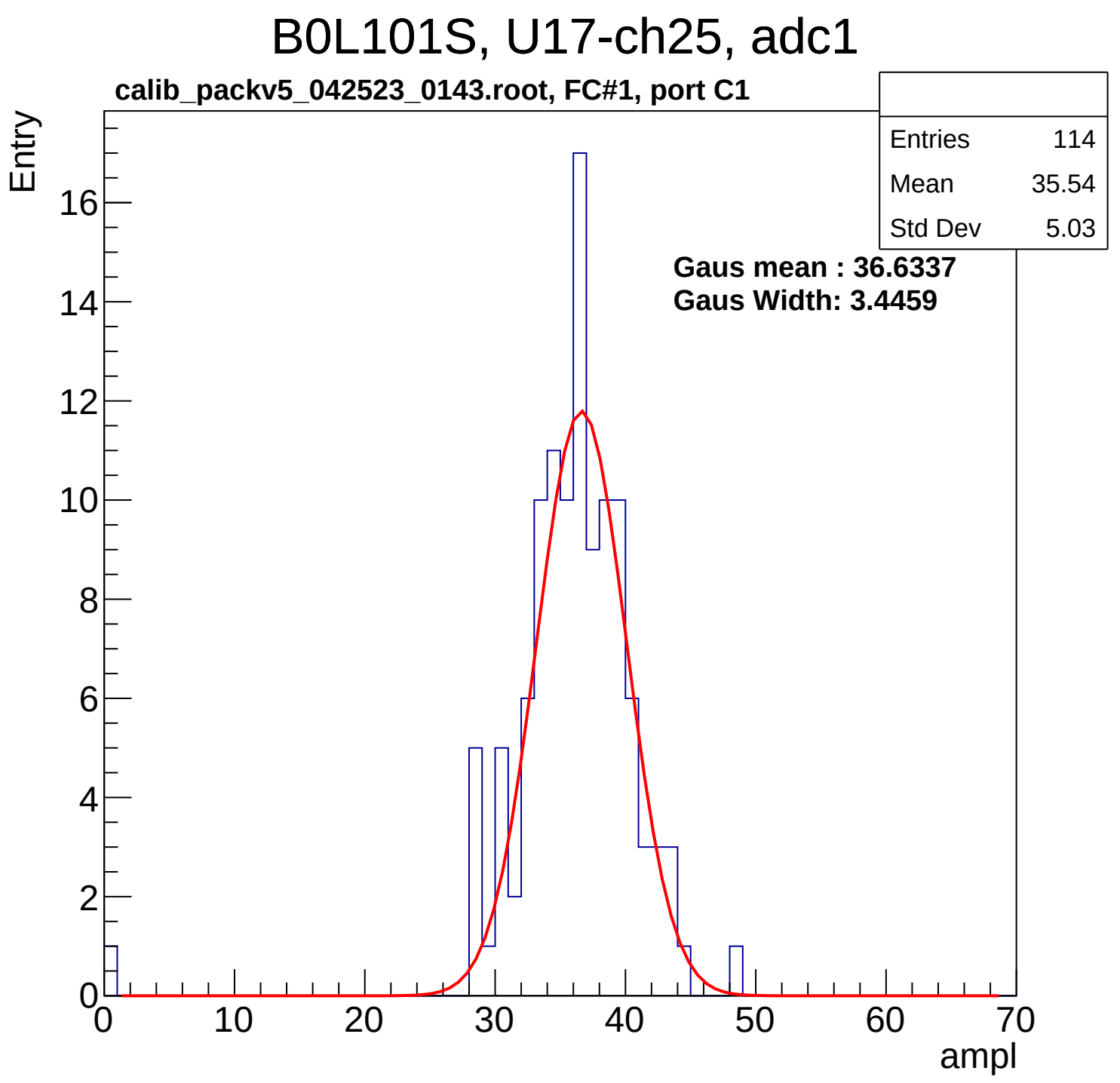

B0L101S, U17-ch24, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 107
Mean 54.98
Std Dev 3.235
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

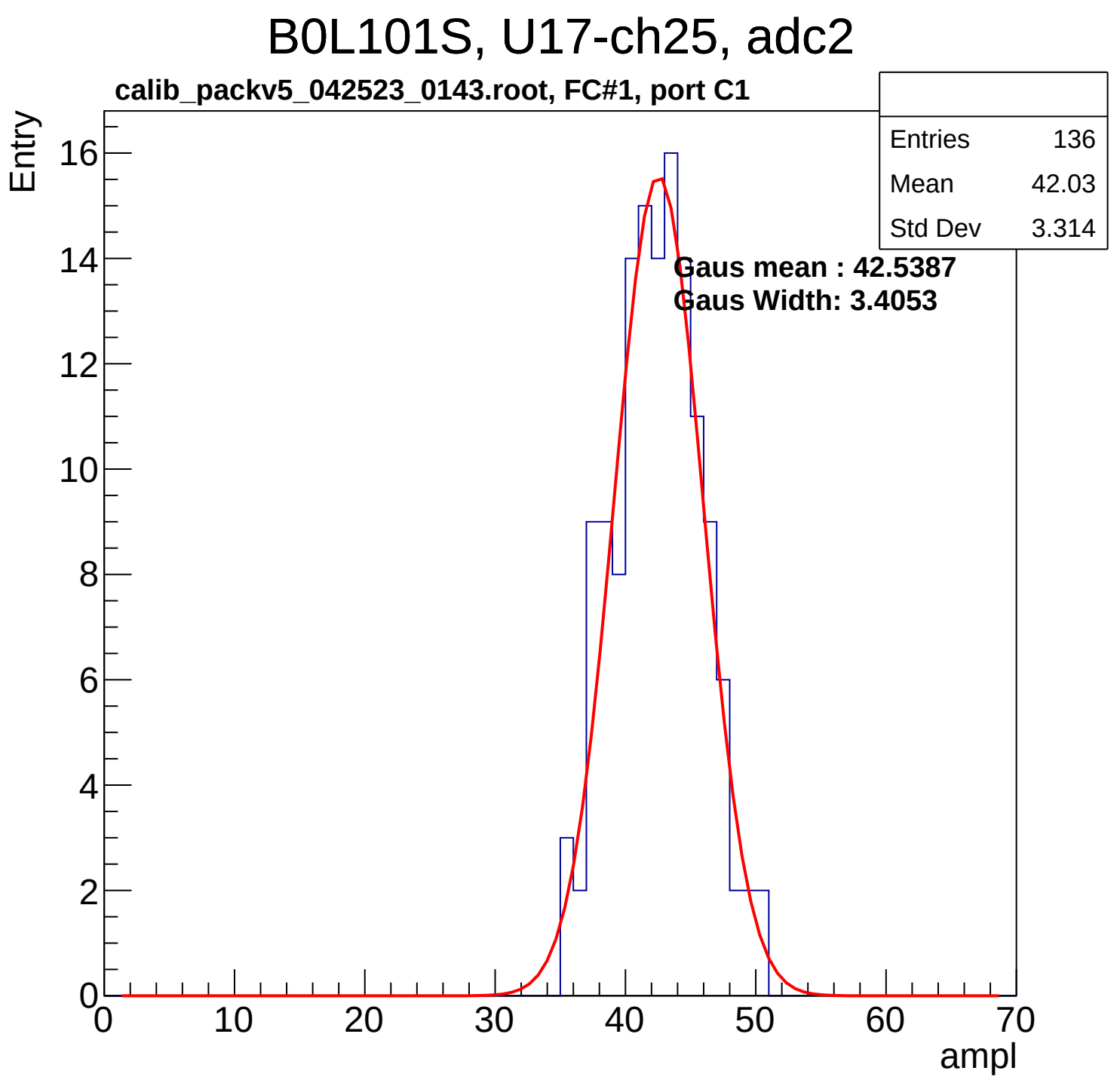

B0L101S, U17-ch24, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.06
Std Dev 8.598
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

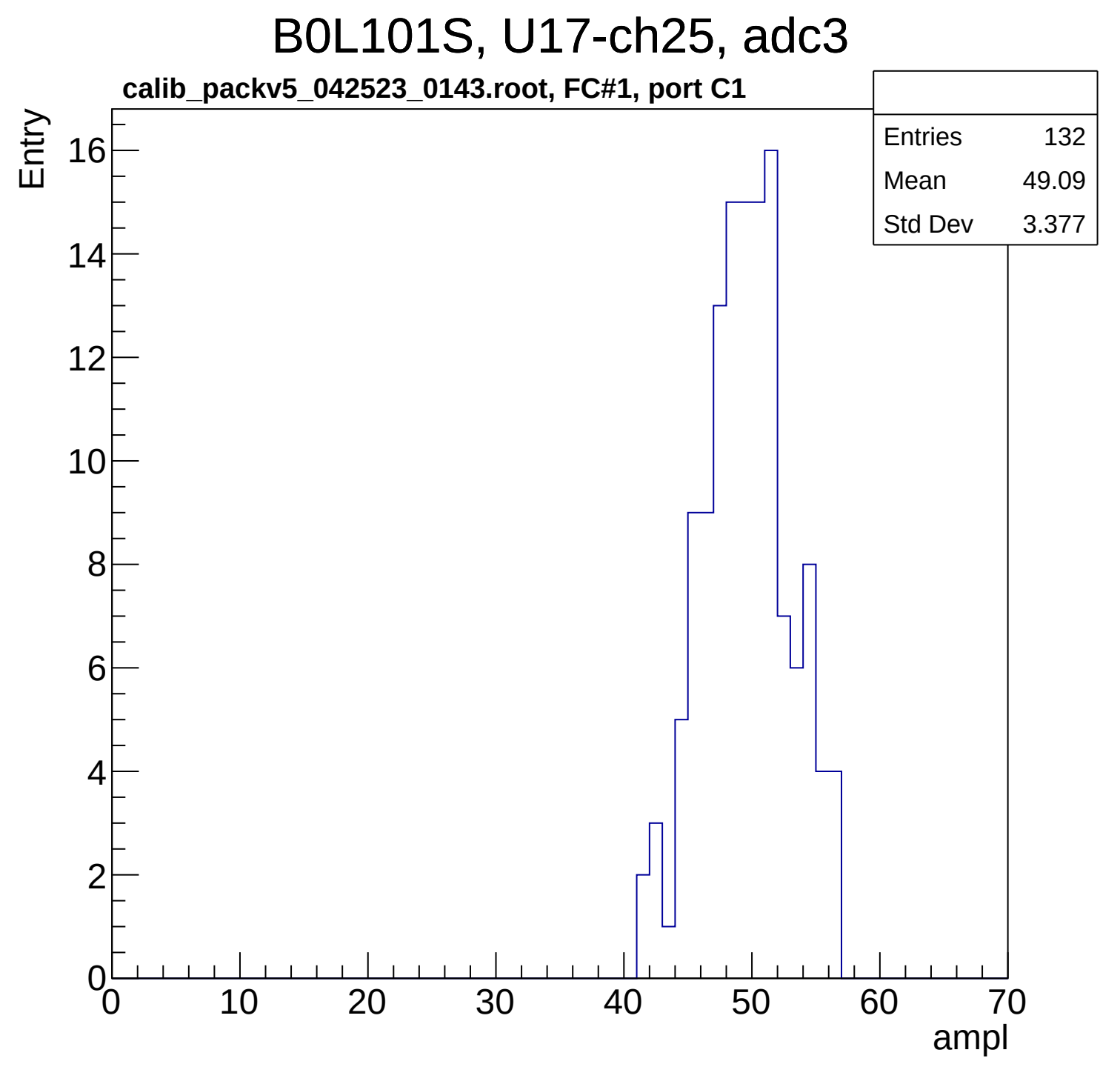

B0L101S, U17-ch24, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 17
Mean 61.88
Std Dev 1.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

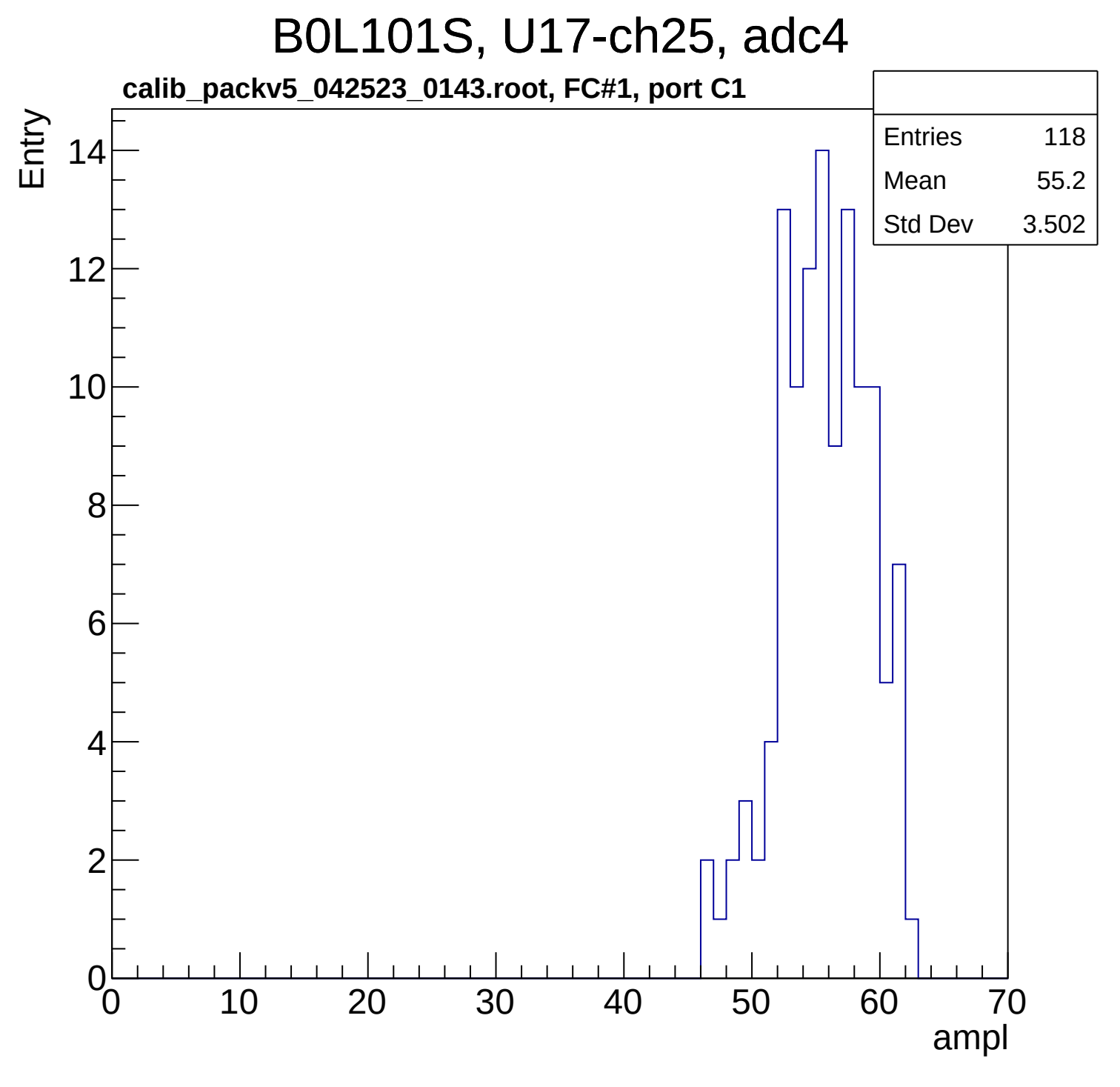

B0L101S, U17-ch24, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

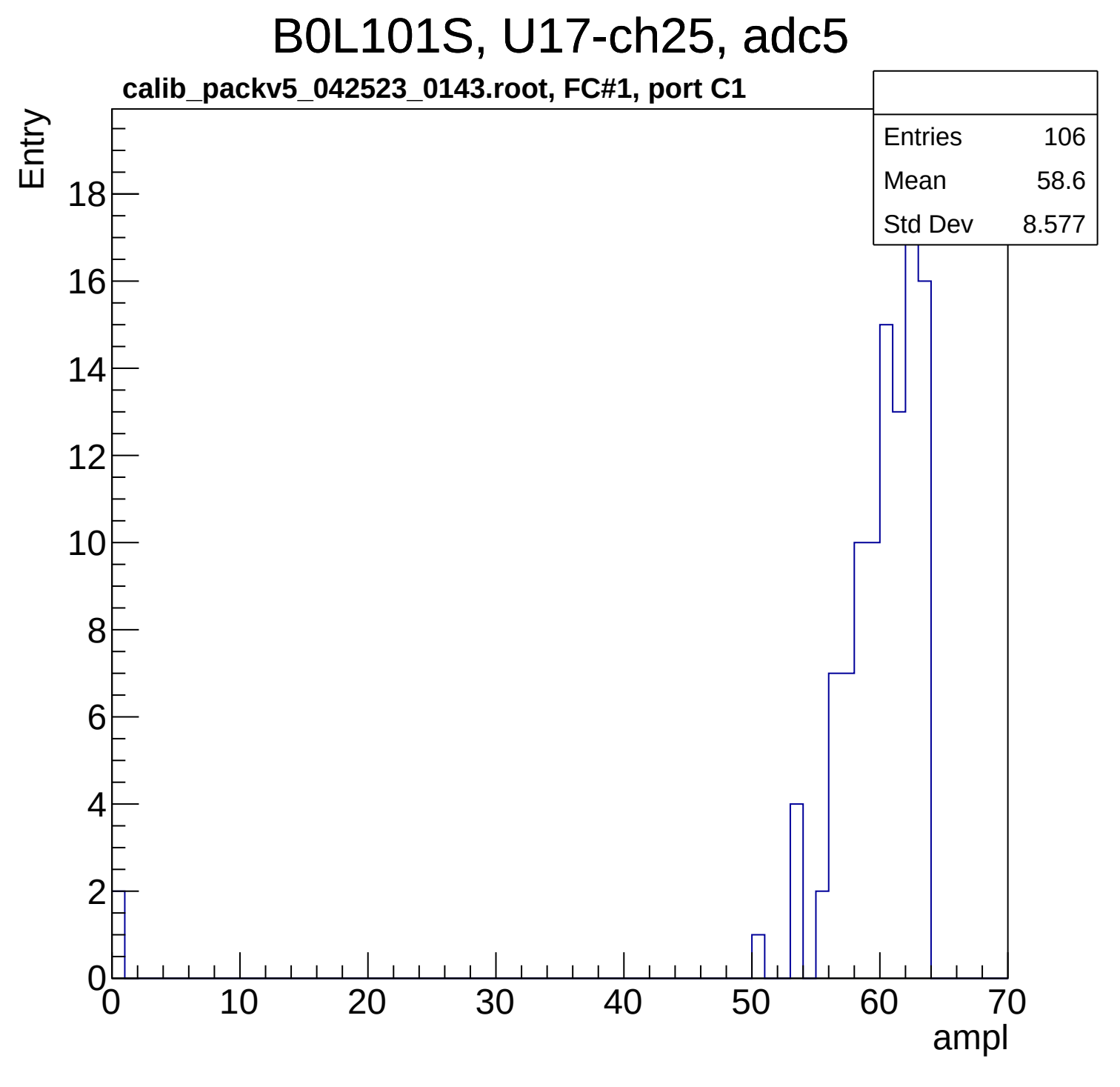

B0L101S, U17-ch25, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 29.11
Std Dev 5.424
Gaus mean : 29.9201
Gaus Width: 3.4373
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

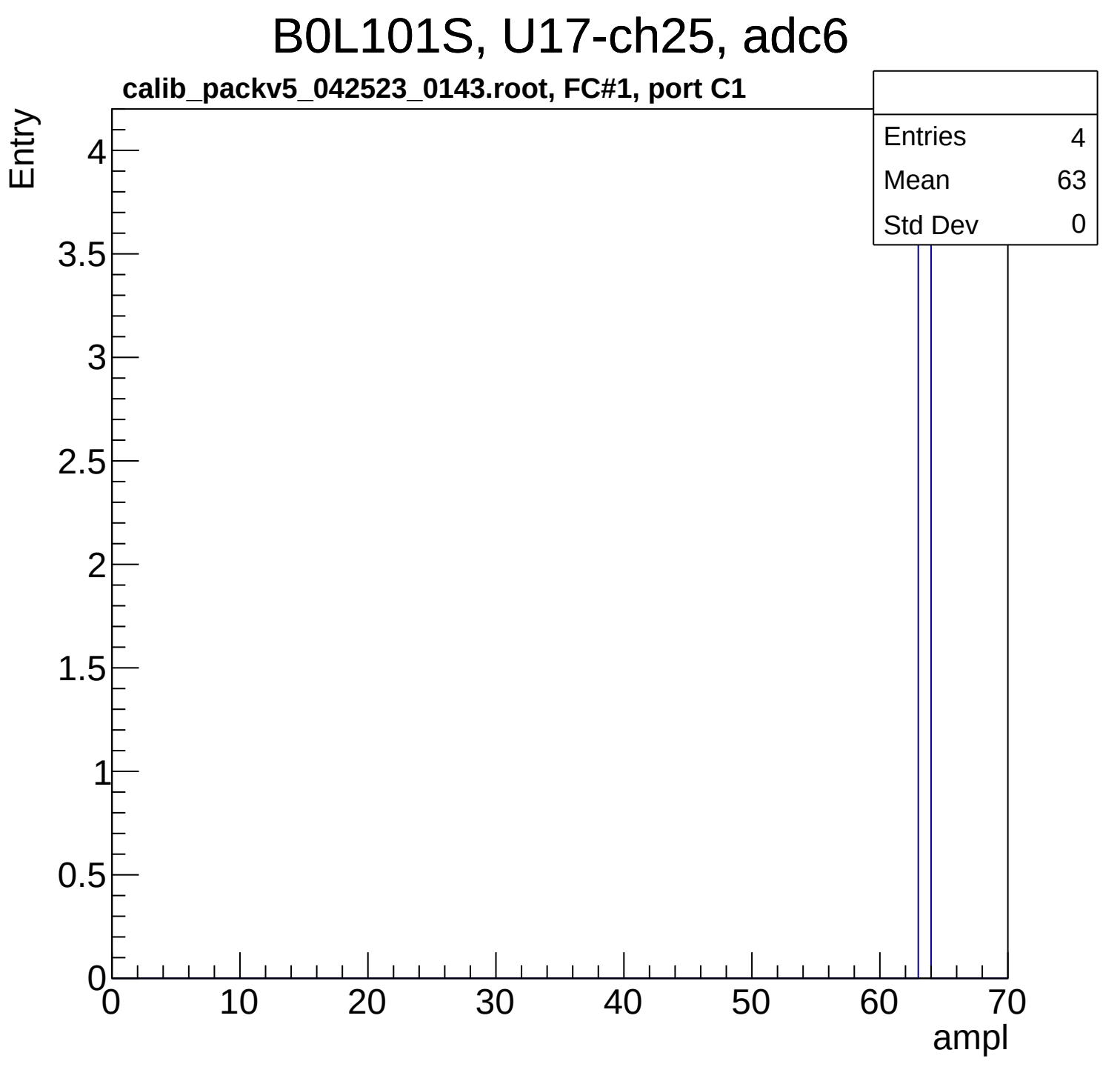

B0L101S, U17-ch25, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 35.54
Std Dev 5.03
Gaus mean : 36.6337
Gaus Width: 3.4459
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

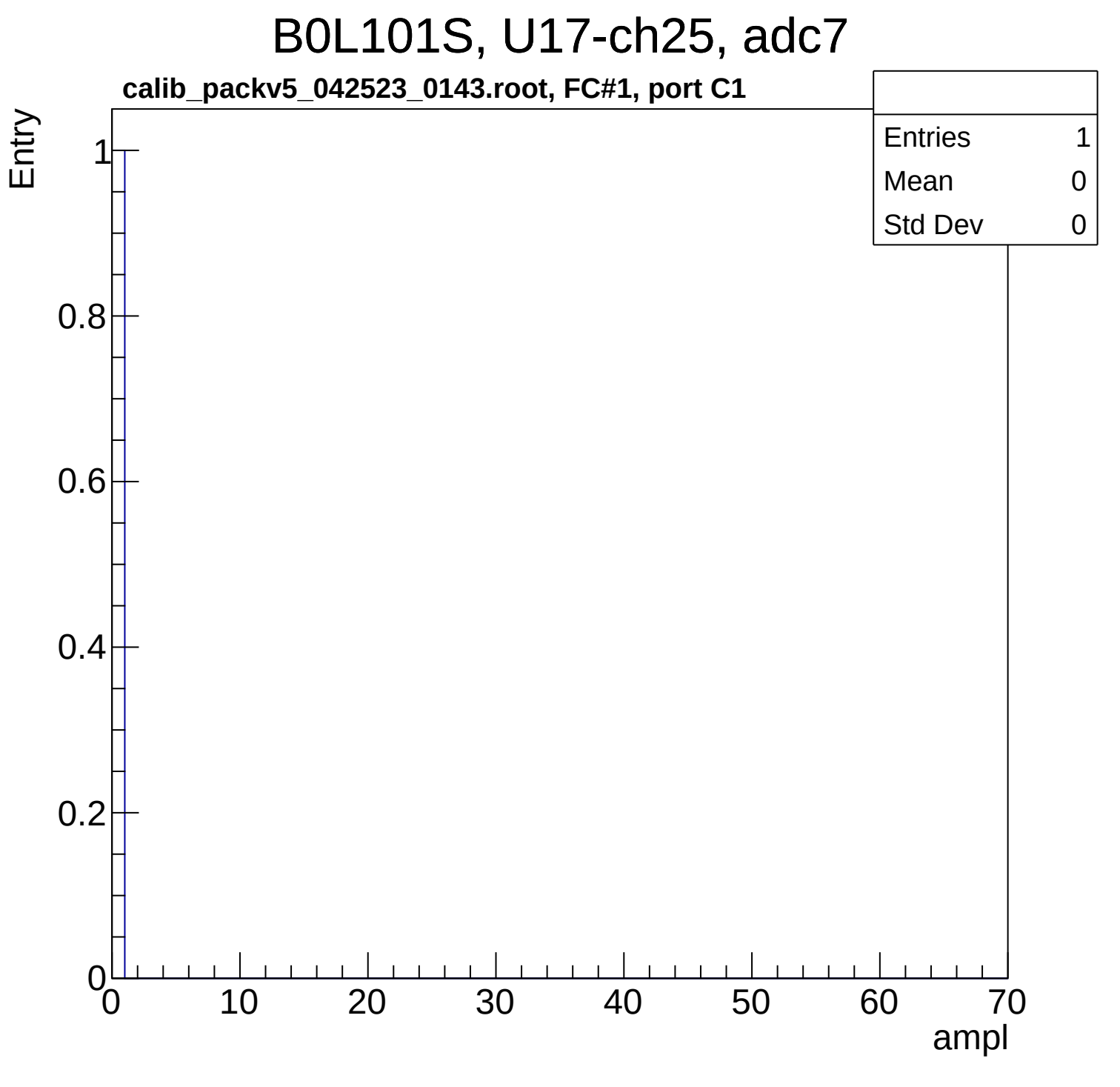

B0L101S, U17-ch25, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 42.03
Std Dev 3.314
Gaus mean : 42.5387
Gaus Width: 3.4053
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

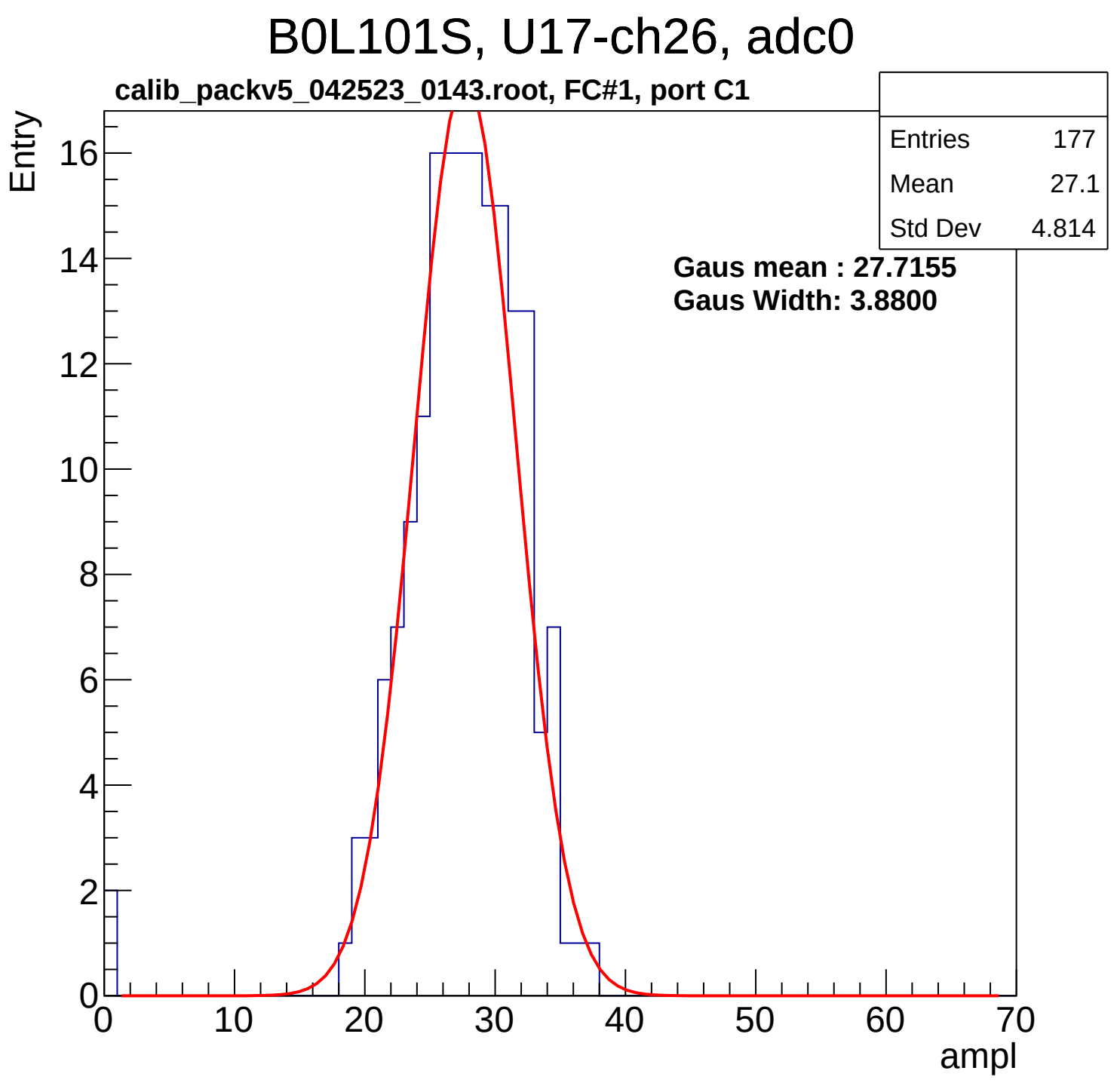

B0L101S, U17-ch25, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 49.09
Std Dev 3.377
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

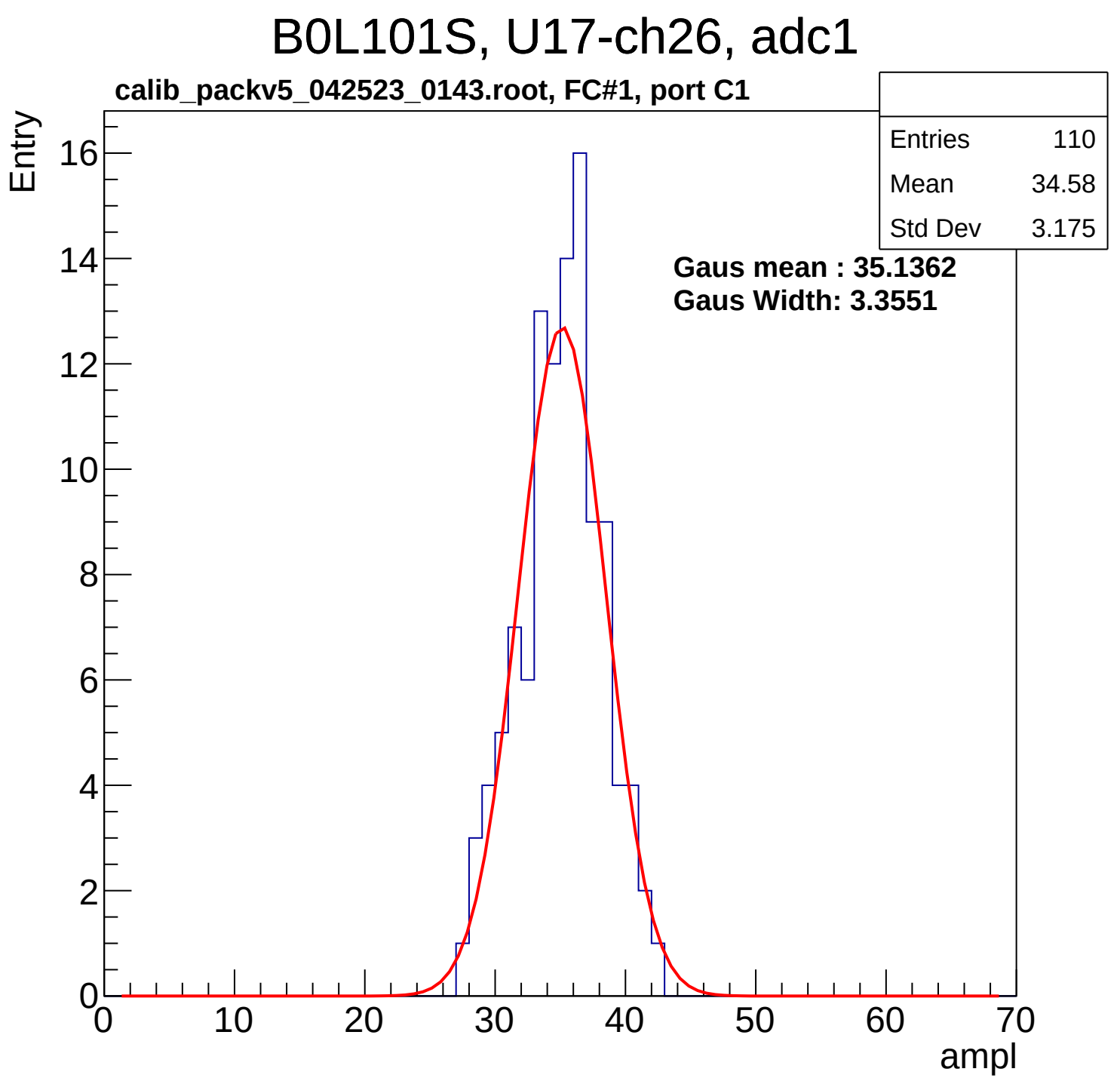

B0L101S, U17-ch25, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 55.2
Std Dev 3.502
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

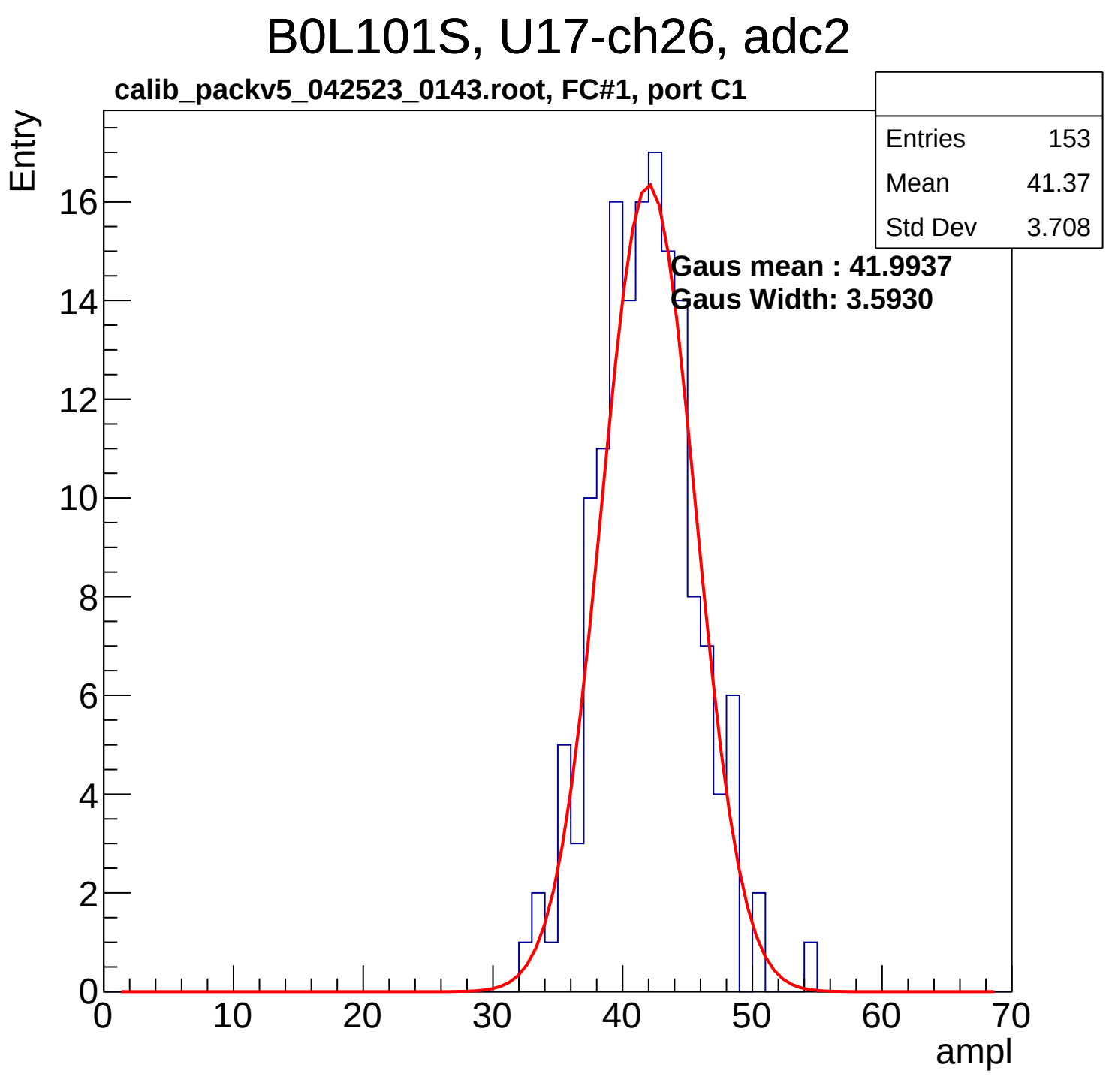

B0L101S, U17-ch25, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 106
Mean 58.6
Std Dev 8.577
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

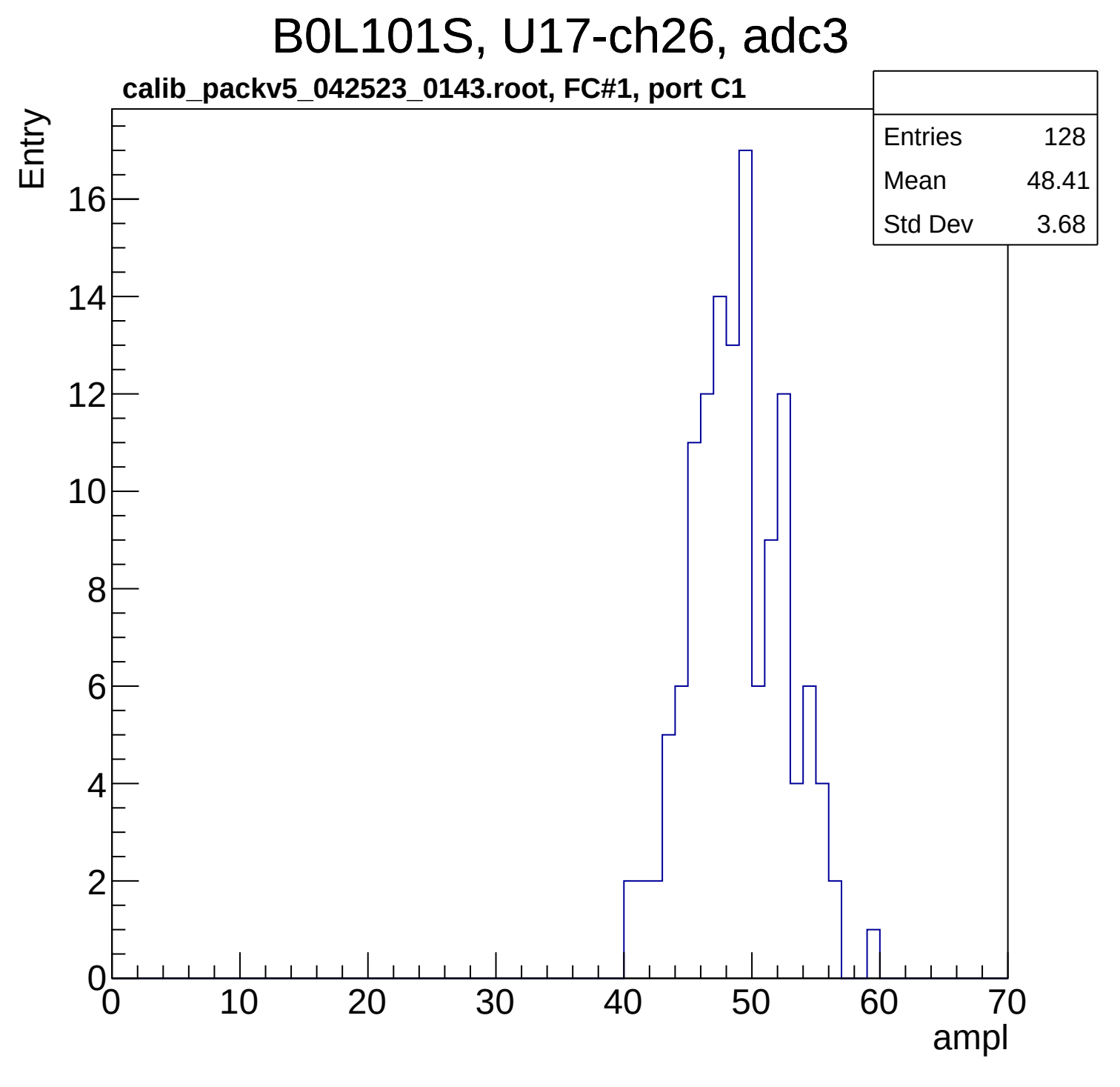

B0L101S, U17-ch25, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

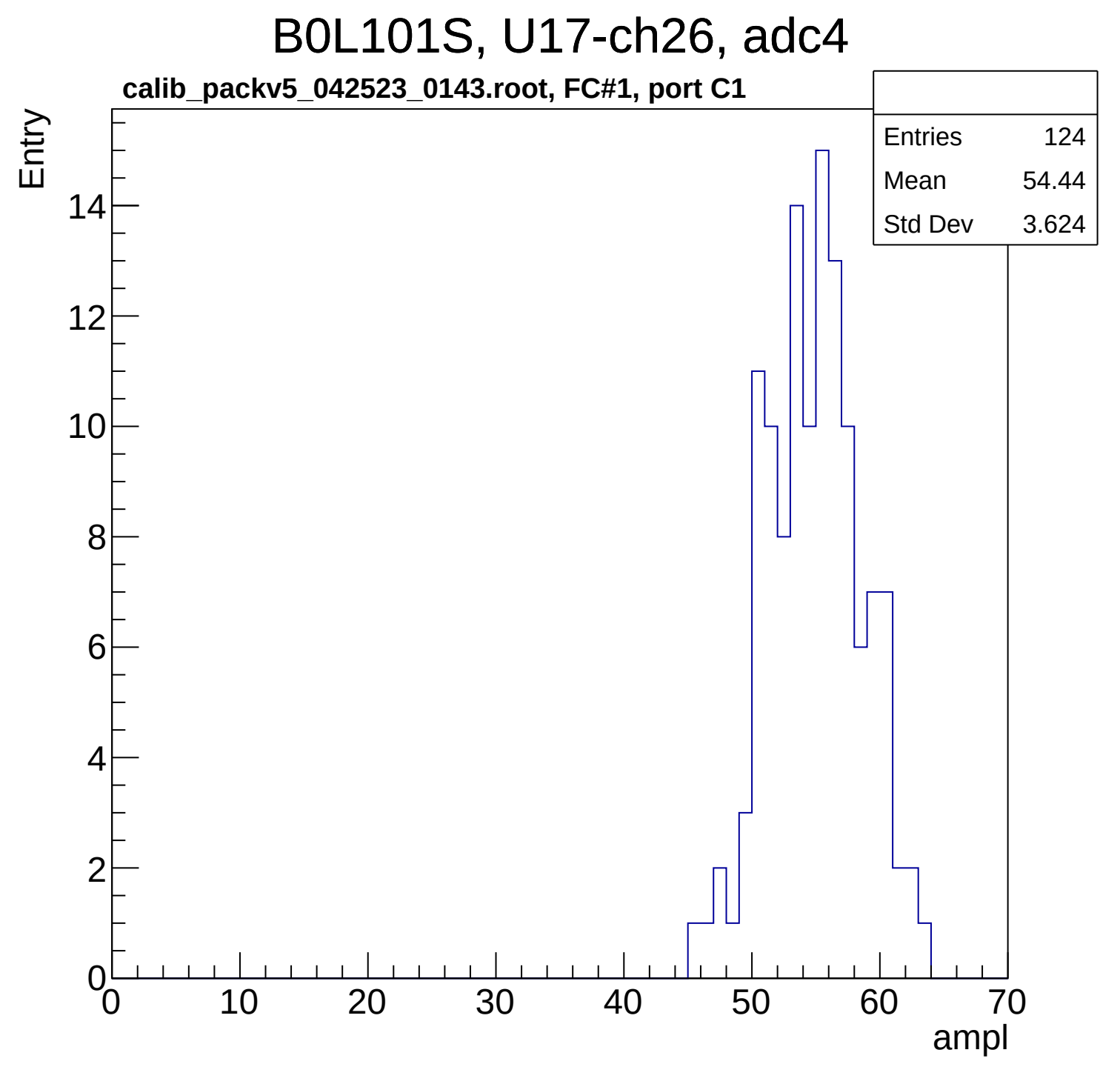

B0L101S, U17-ch25, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

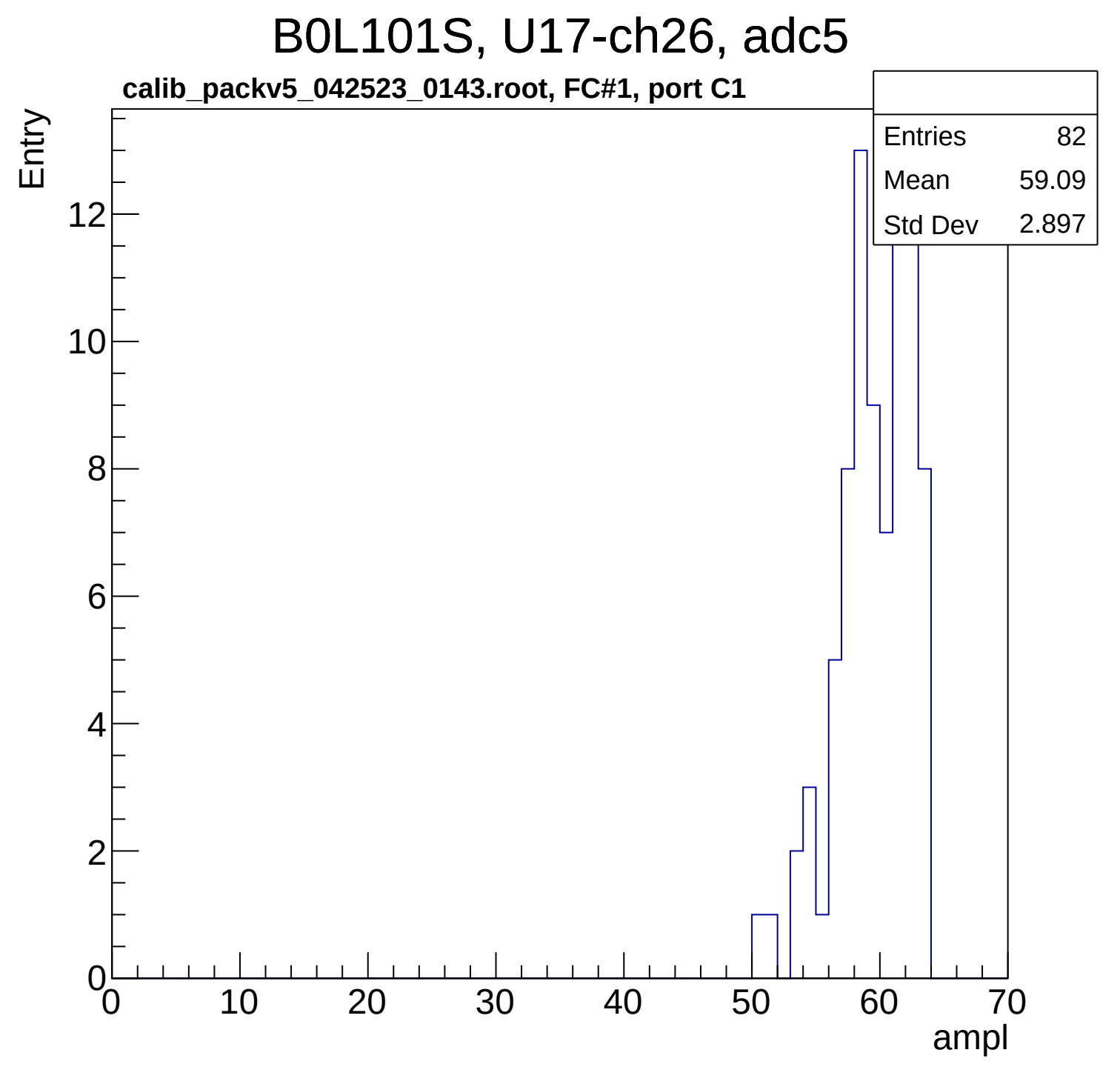

B0L101S, U17-ch26, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 177
Mean 27.1
Std Dev 4.814
Gaus mean : 27.7155
Gaus Width: 3.8800
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

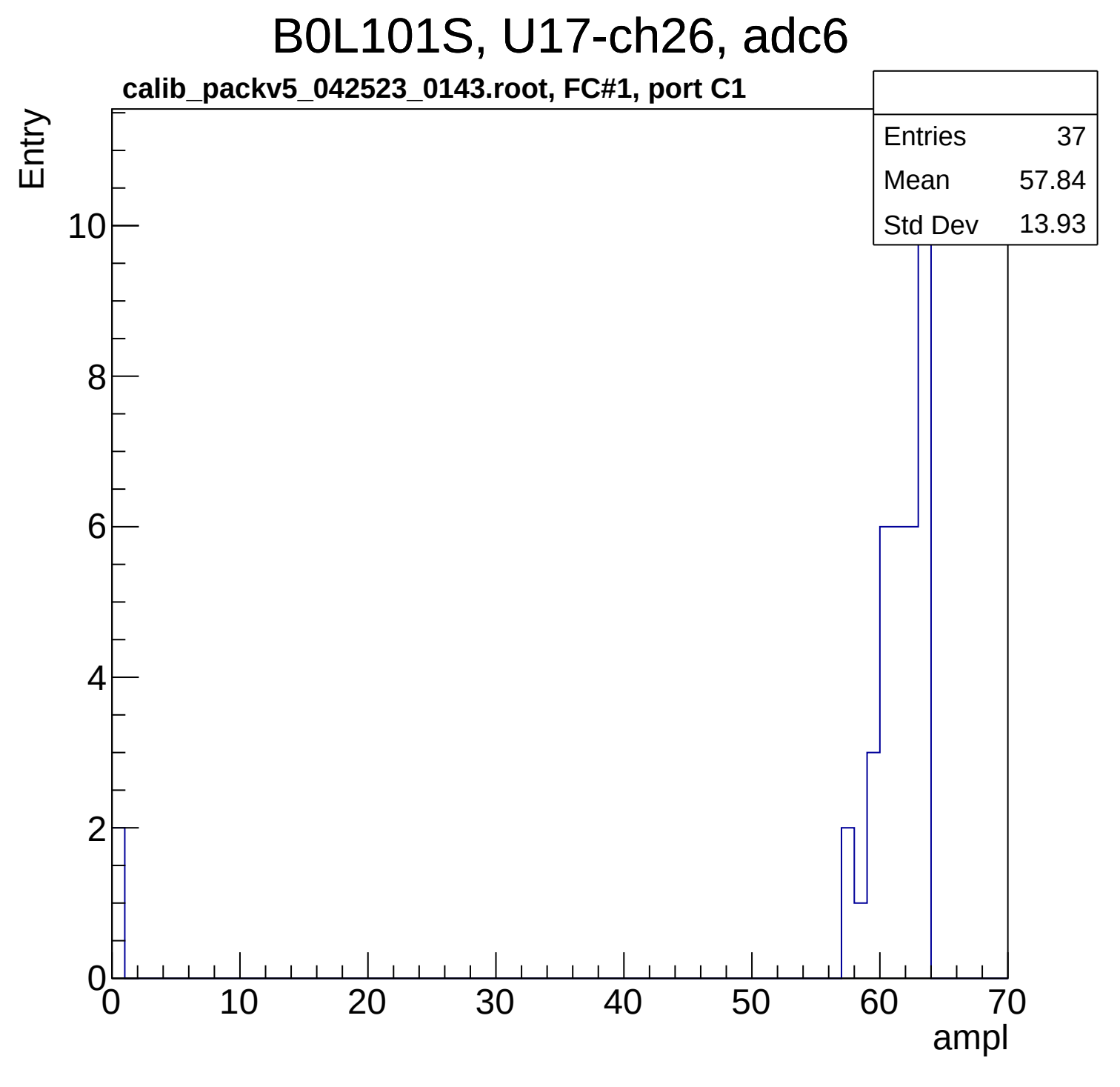

B0L101S, U17-ch26, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 34.58
Std Dev 3.175
Gaus mean : 35.1362
Gaus Width: 3.3551
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

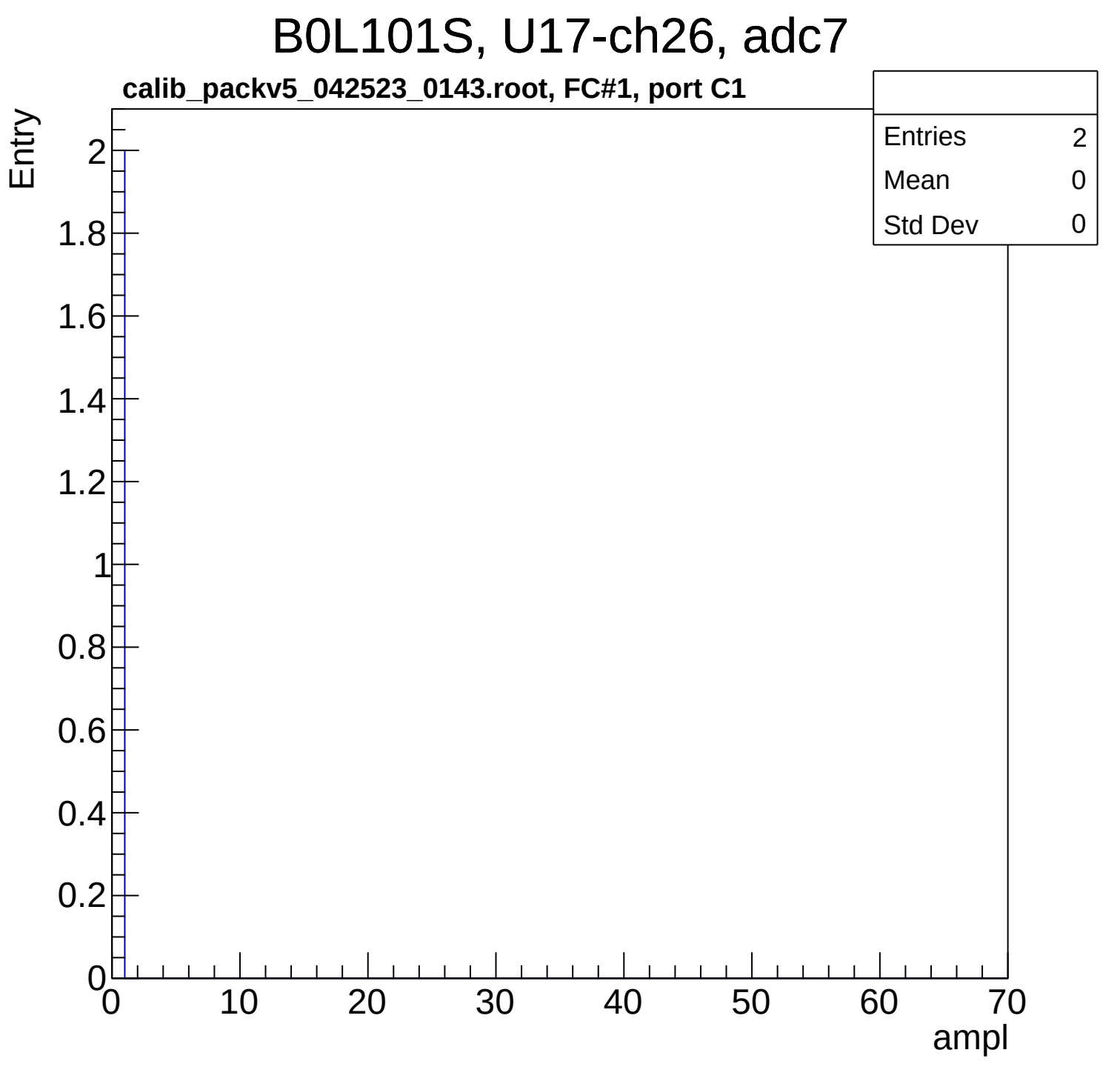

B0L101S, U17-ch26, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 41.37
Std Dev 3.708
Gaus mean : 41.9937
Gaus Width: 3.5930
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

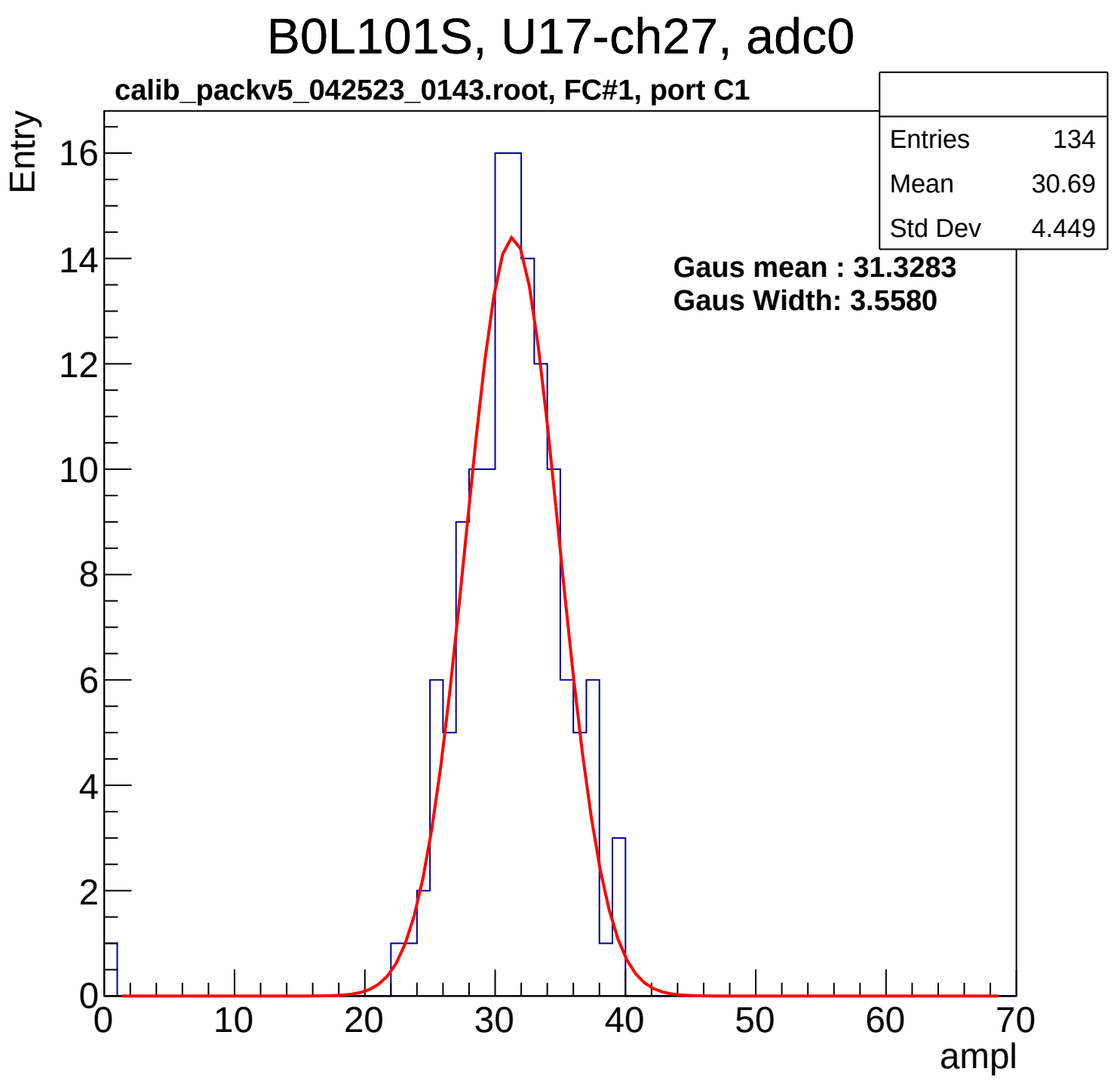

B0L101S, U17-ch26, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 48.41
Std Dev 3.68
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

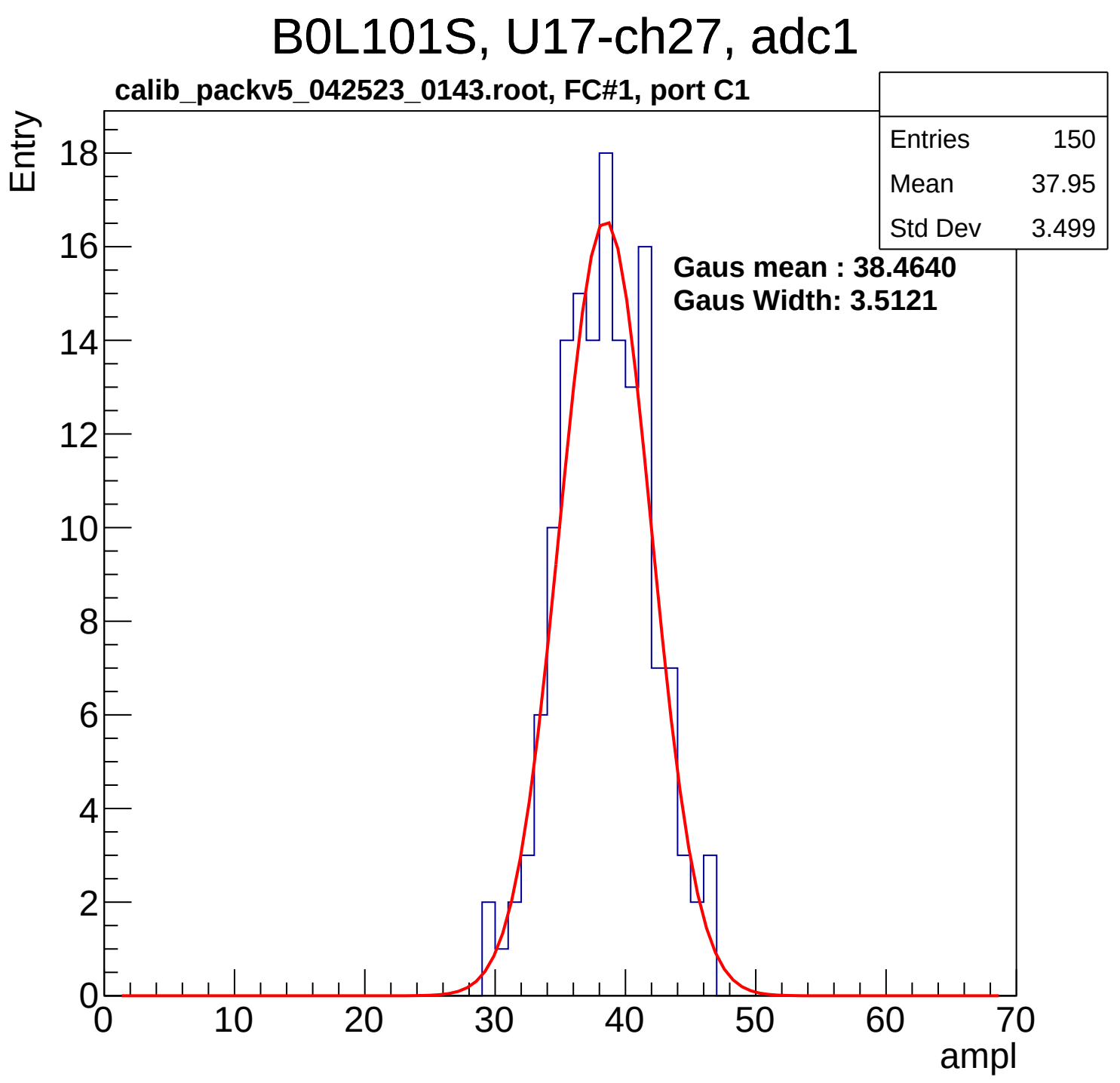

B0L101S, U17-ch26, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 54.44
Std Dev 3.624
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

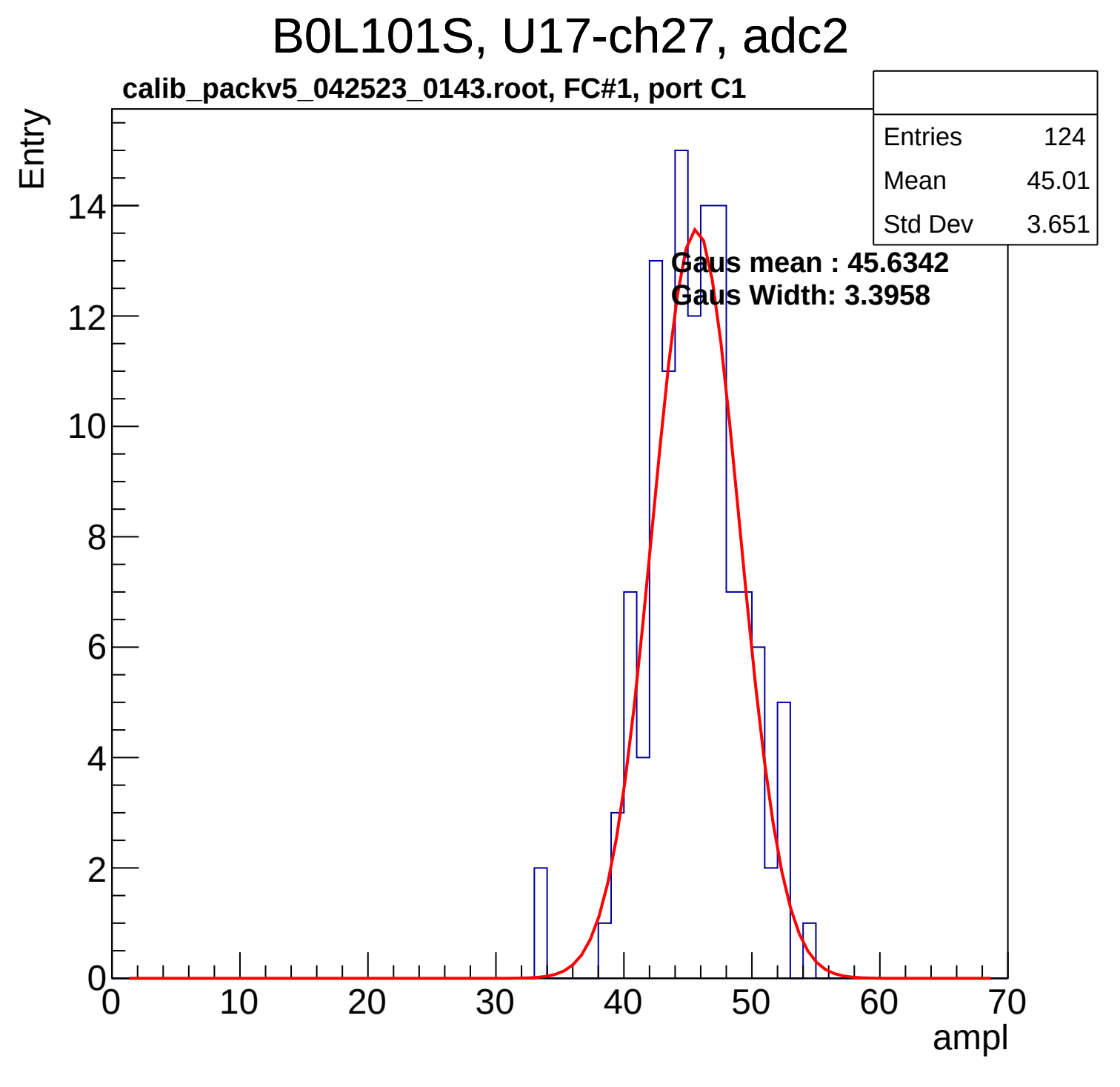

B0L101S, U17-ch26, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 82
Mean 59.09
Std Dev 2.897
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

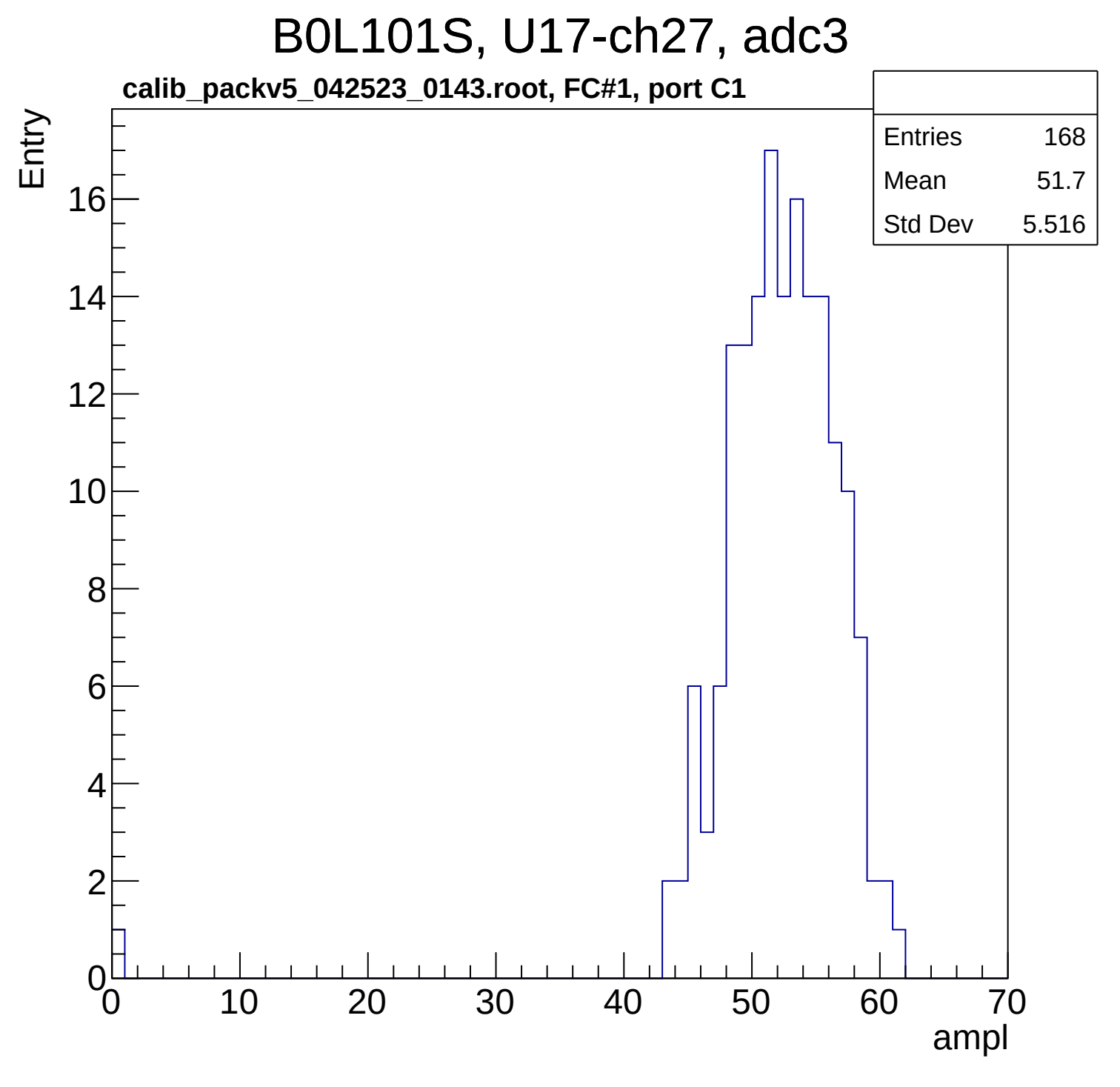

B0L101S, U17-ch26, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 37
Mean 57.84
Std Dev 13.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

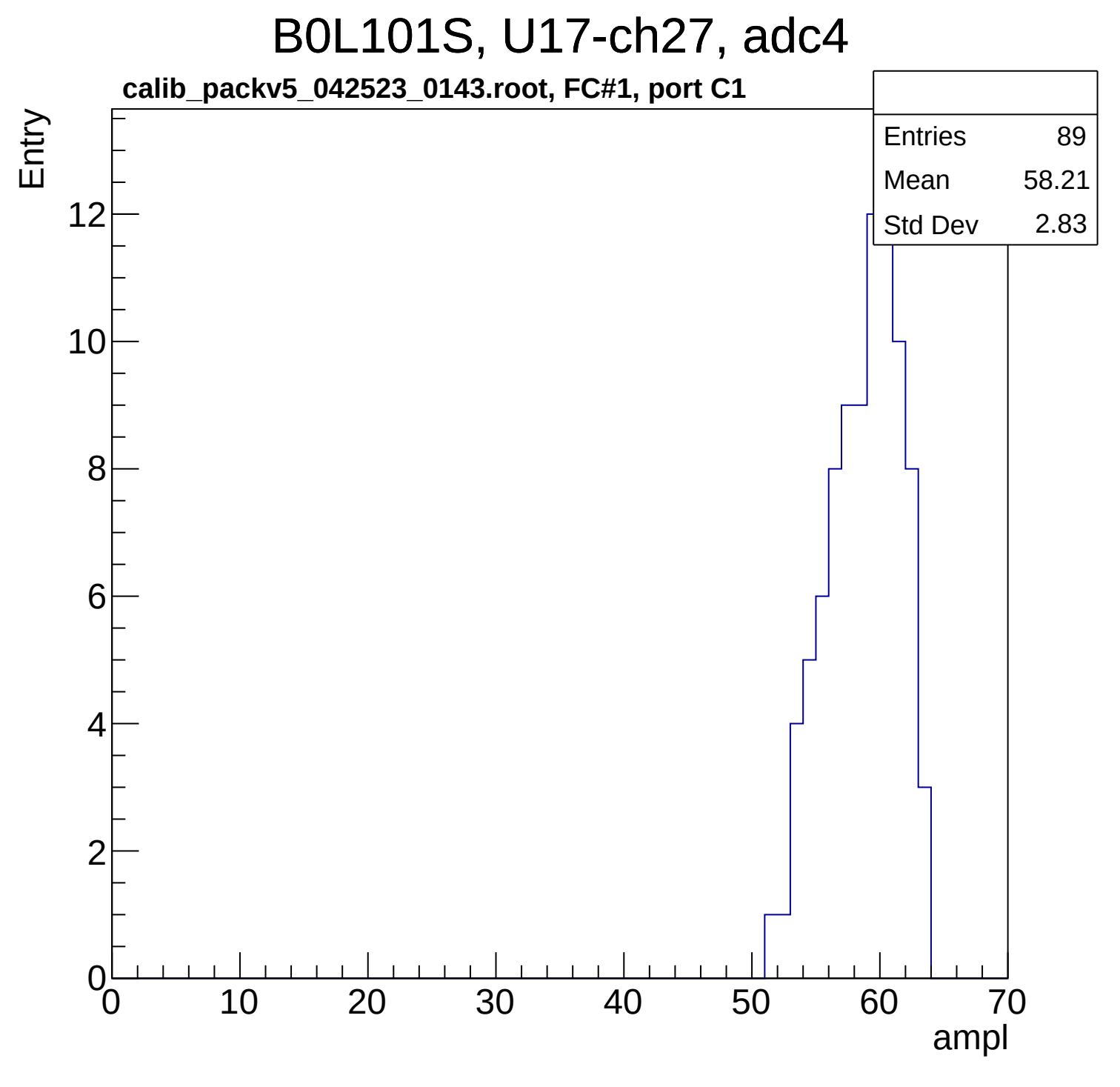

B0L101S, U17-ch26, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

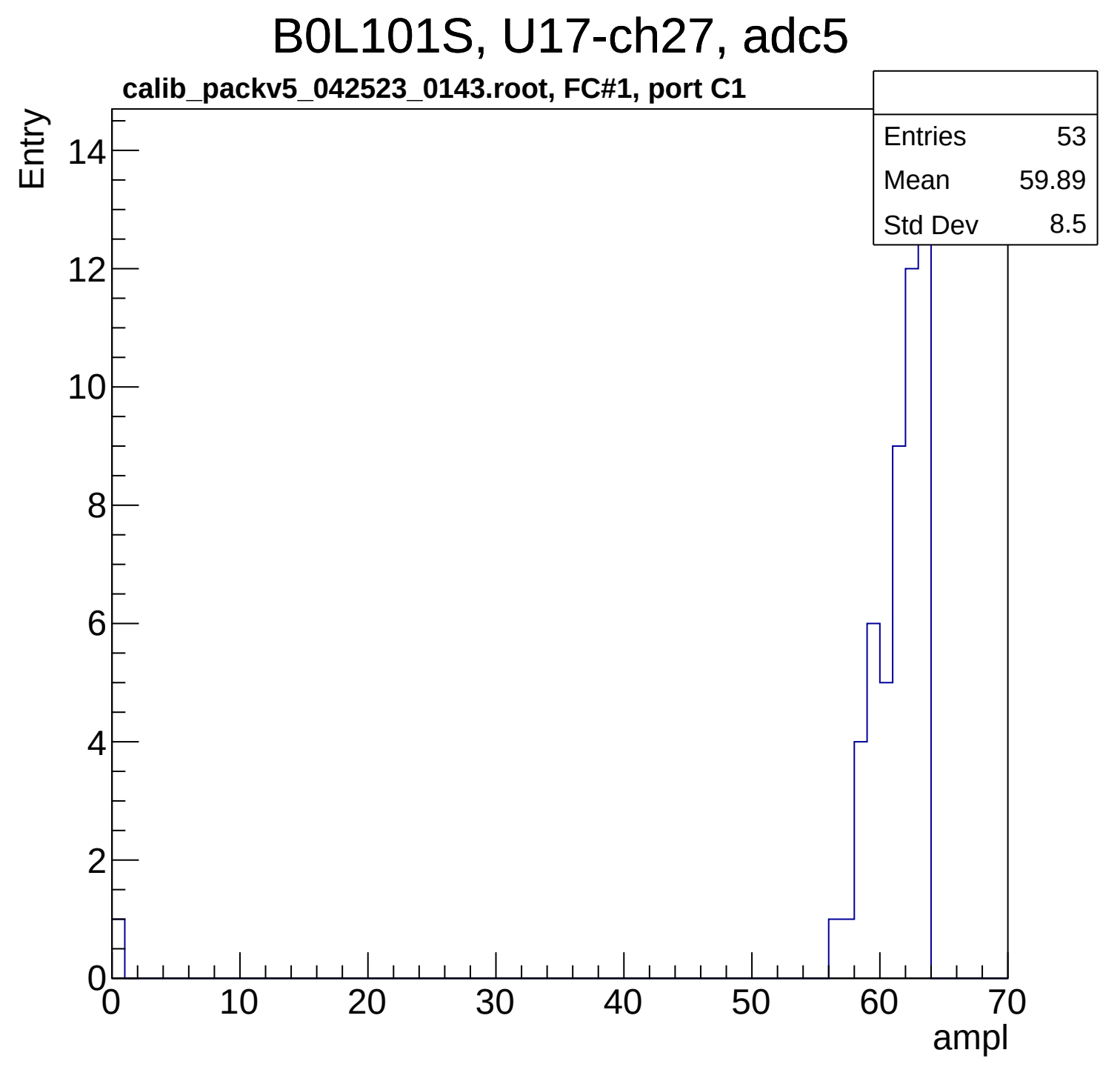

B0L101S, U17-ch27, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 30.69
Std Dev 4.449
Gaus mean : 31.3283
Gaus Width: 3.5580
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

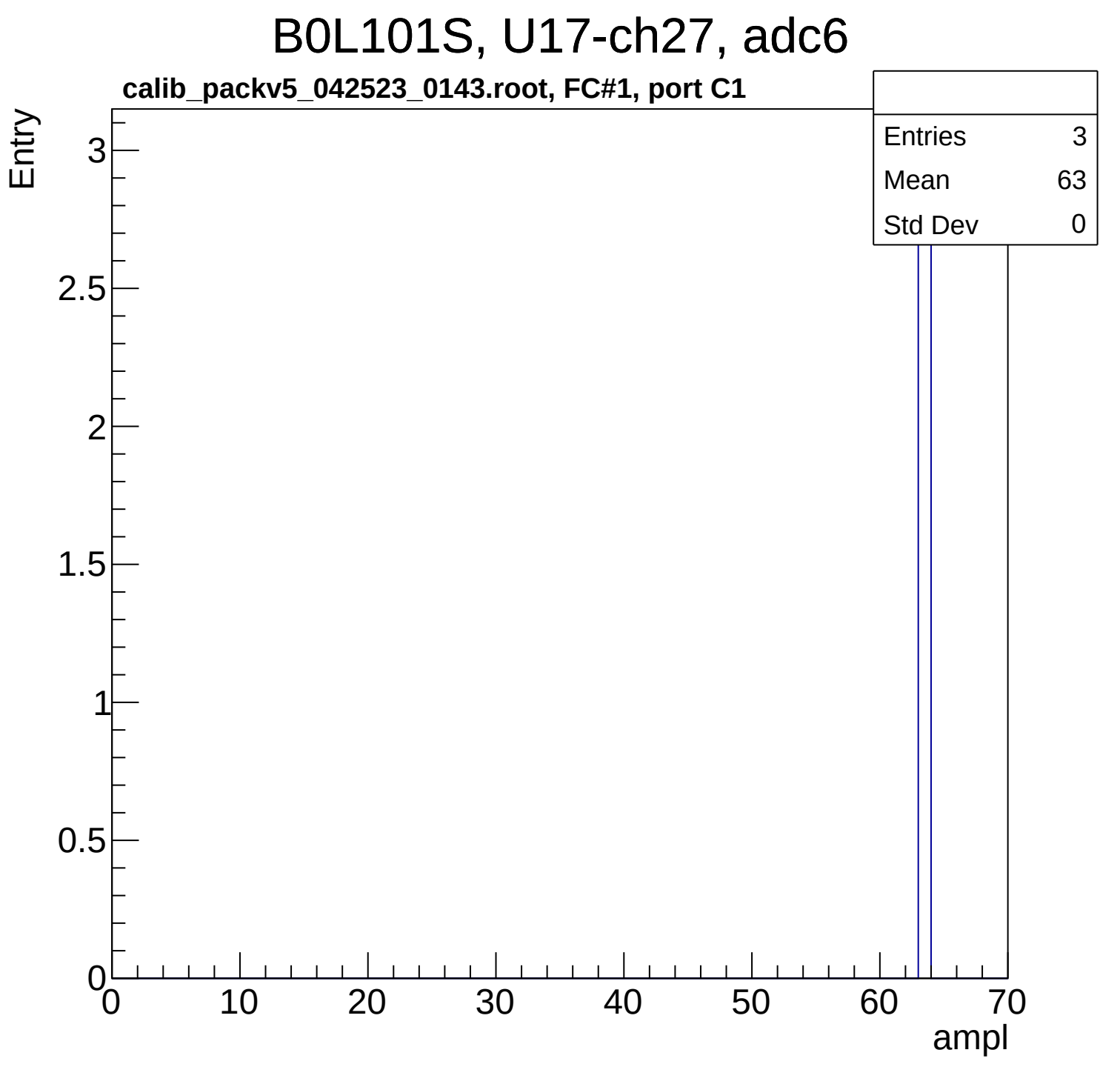

B0L101S, U17-ch27, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 37.95
Std Dev 3.499
Gaus mean : 38.4640
Gaus Width: 3.5121
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

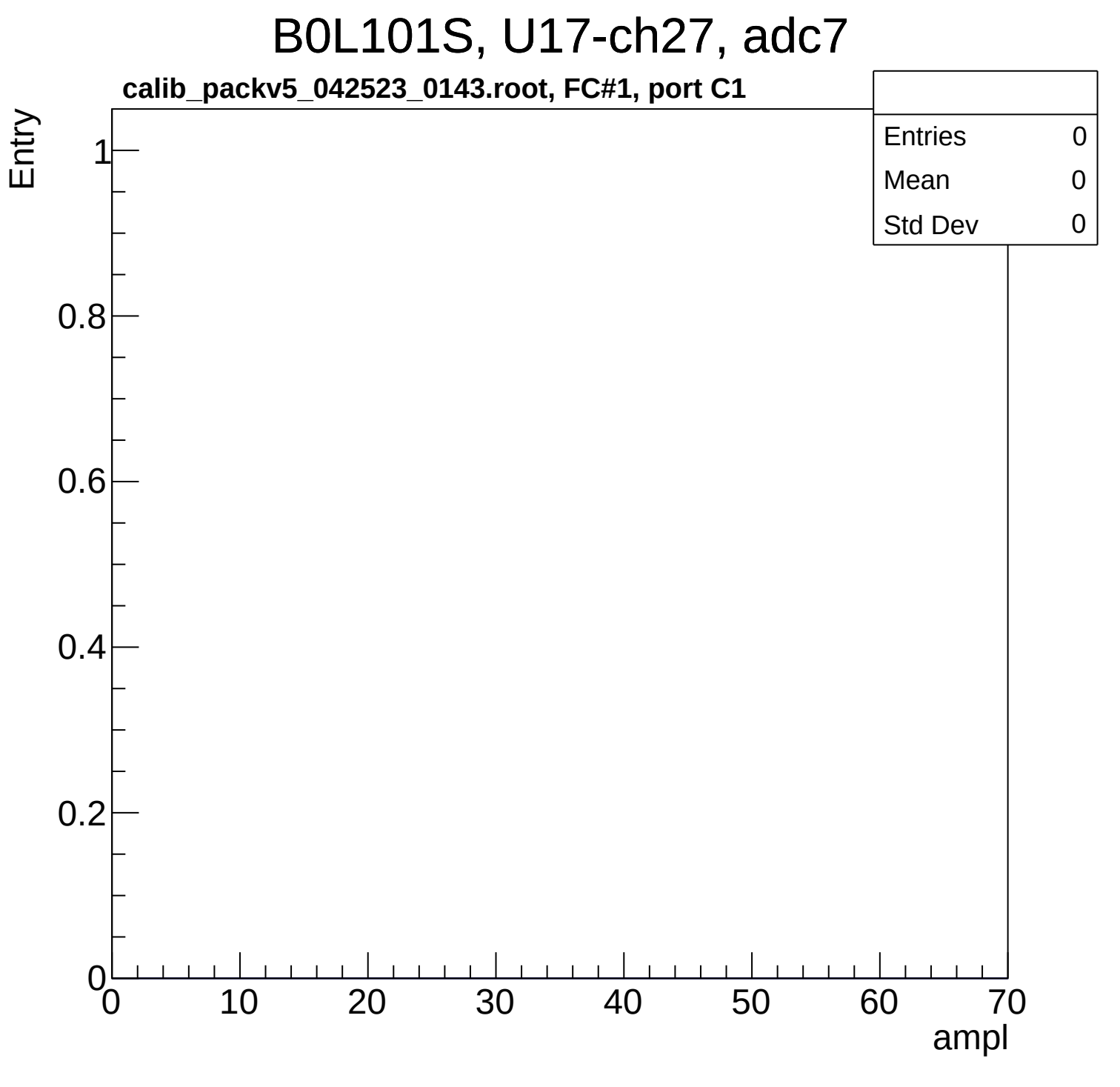

B0L101S, U17-ch27, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 45.01
Std Dev 3.651
Gaus mean : 45.6342
Gaus Width: 3.3958
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

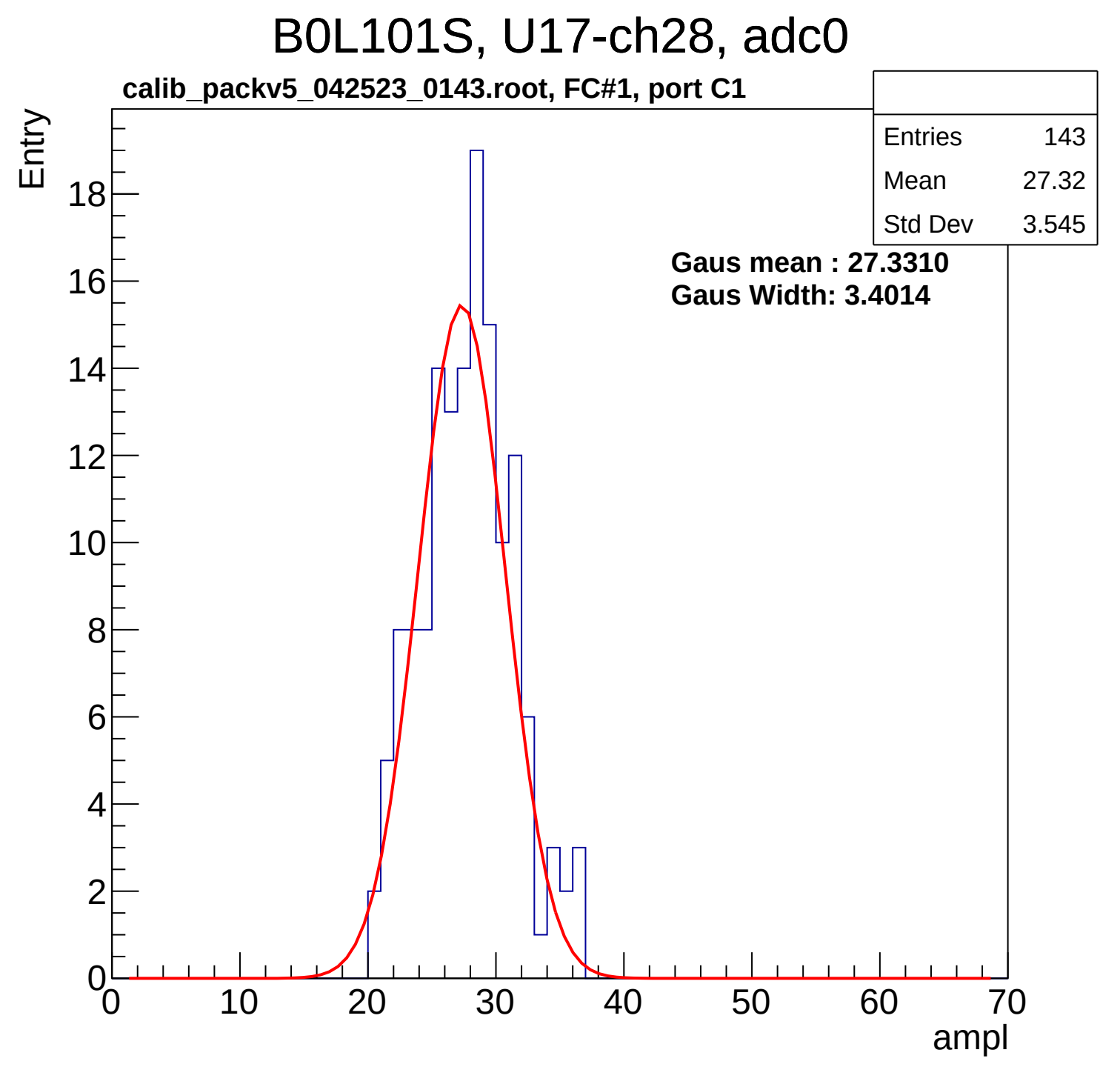

B0L101S, U17-ch27, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 168
Mean 51.7
Std Dev 5.516
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

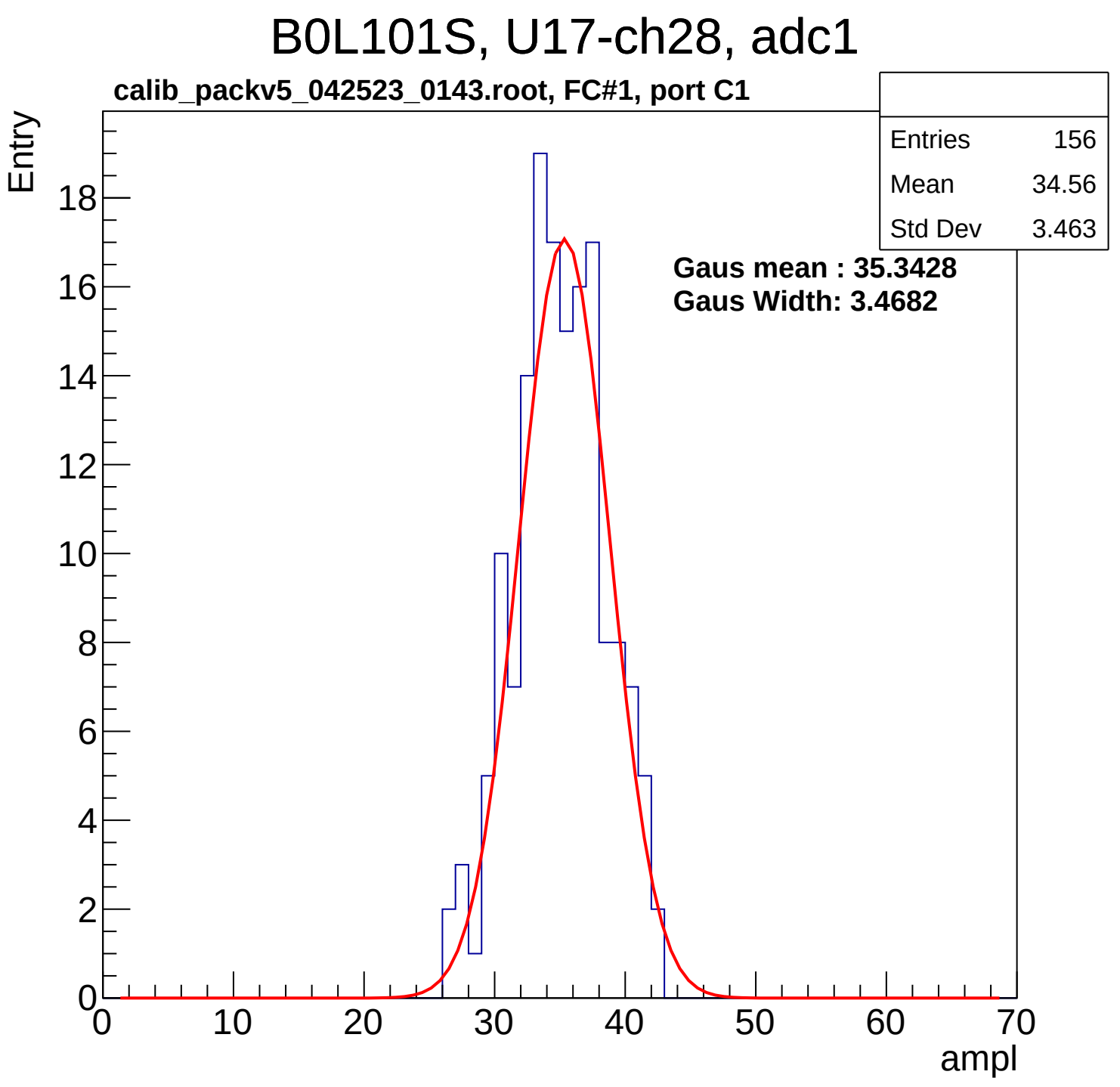

B0L101S, U17-ch27, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 89
Mean 58.21
Std Dev 2.83
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

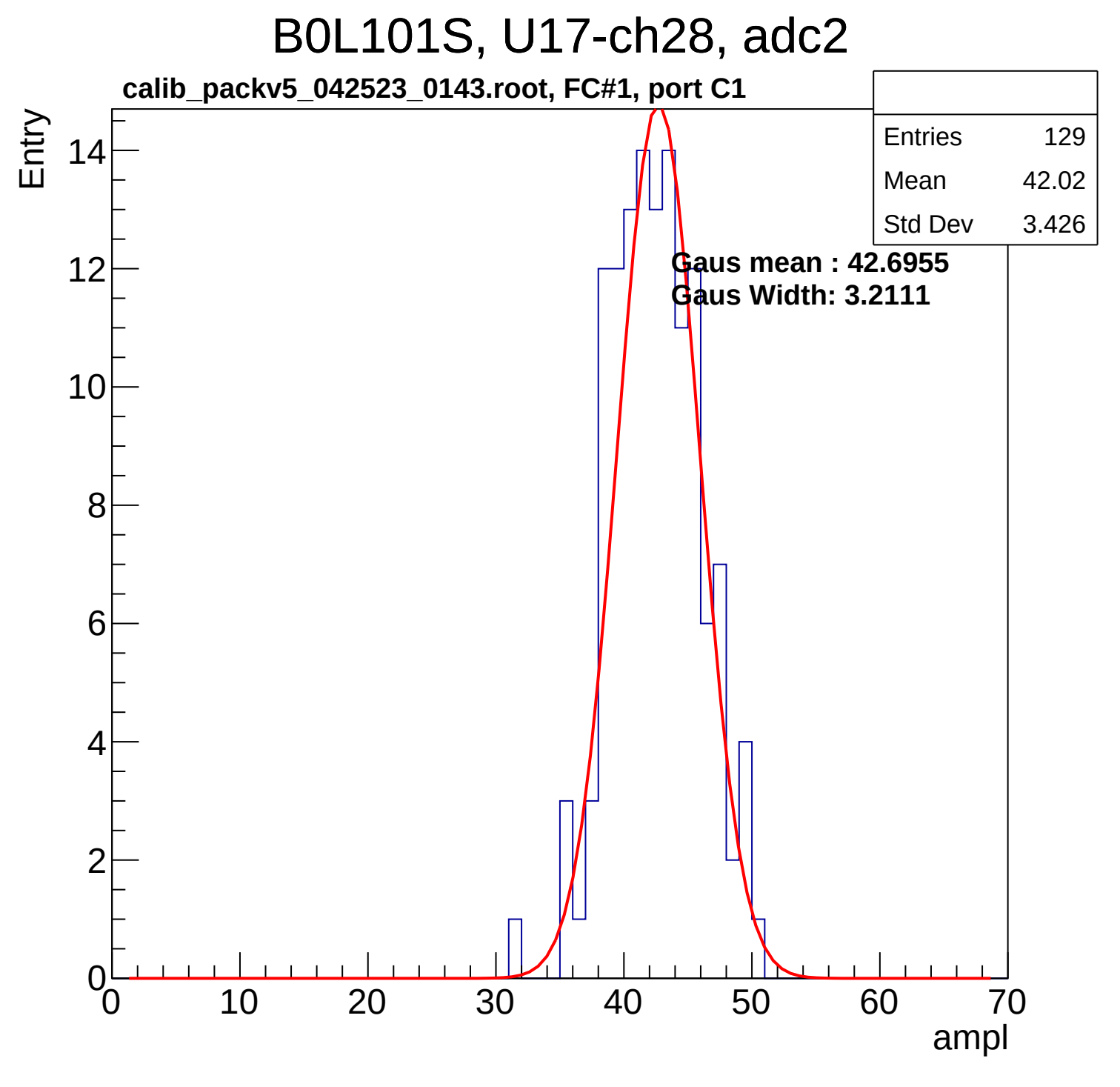

B0L101S, U17-ch27, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 53
Mean 59.89
Std Dev 8.5
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

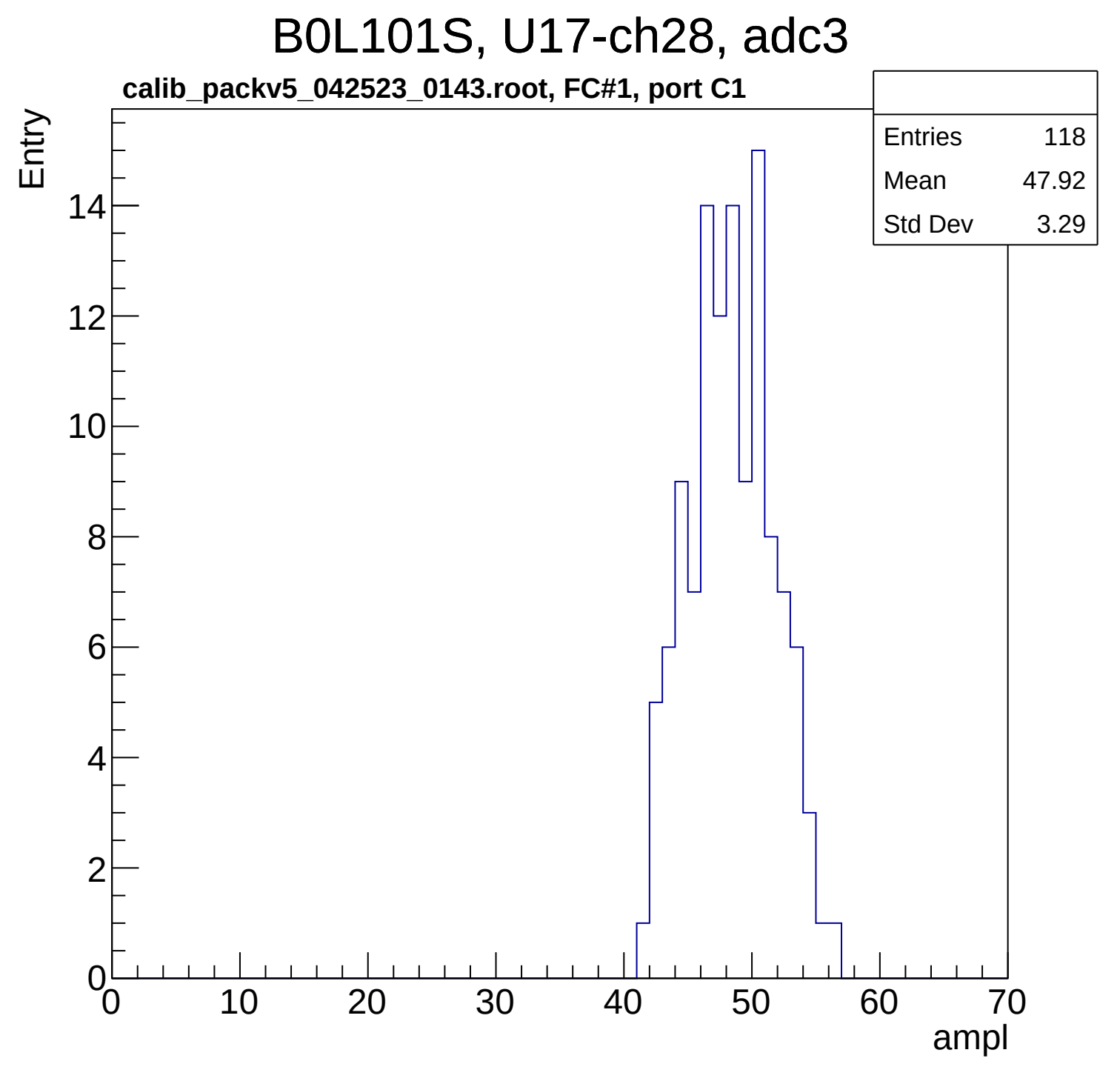

B0L101S, U17-ch27, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

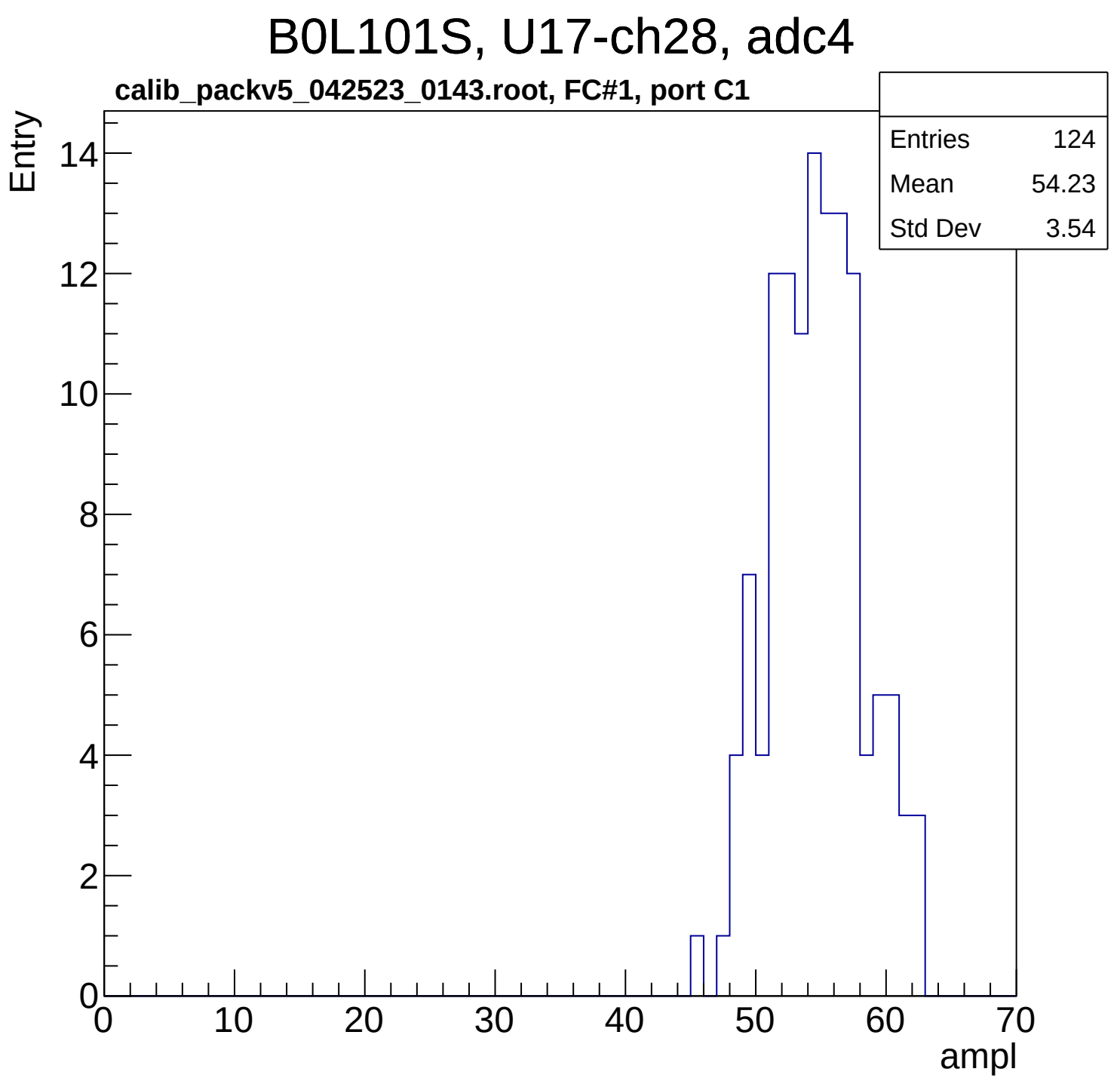

B0L101S, U17-ch27, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

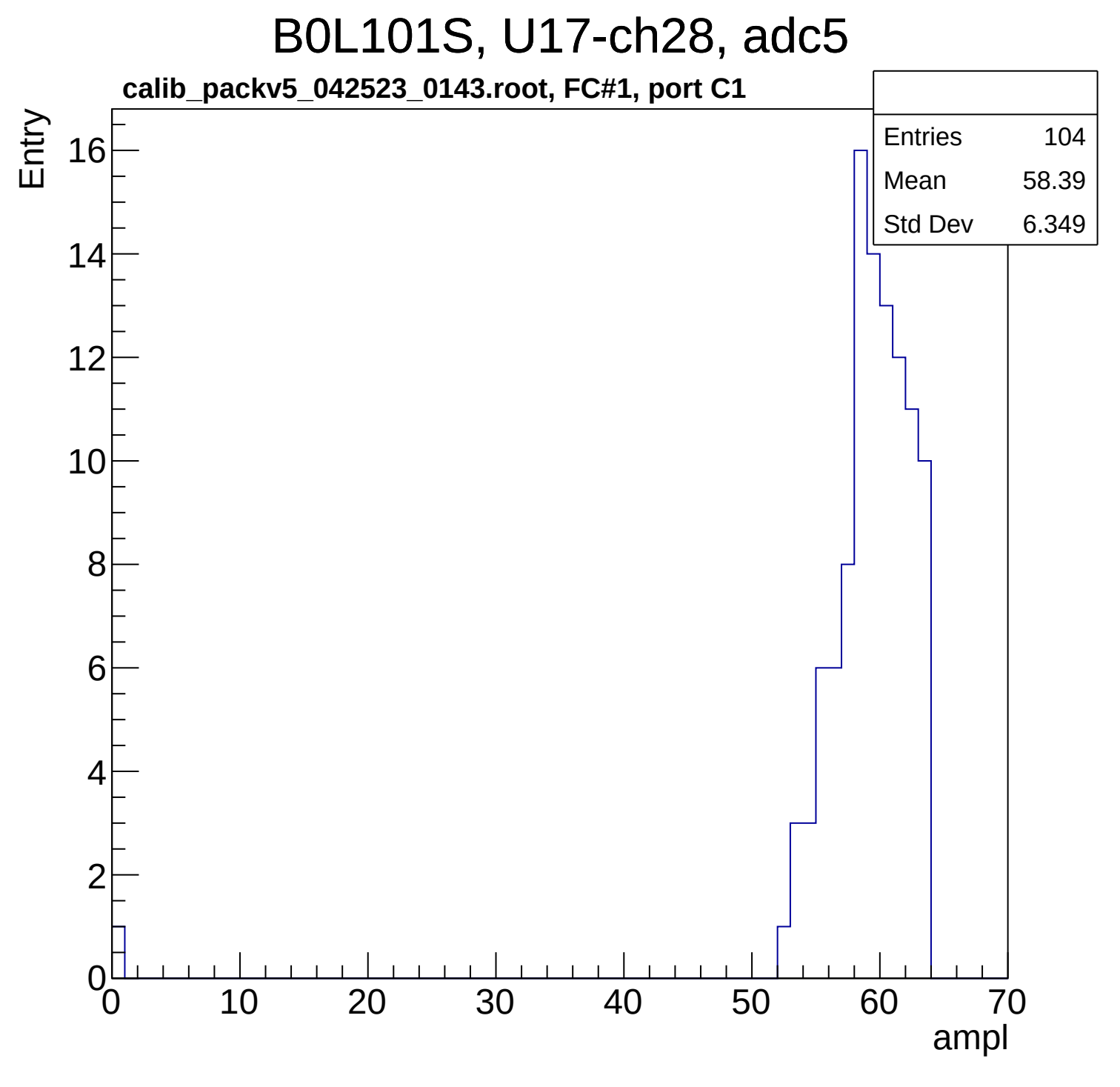

B0L101S, U17-ch28, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 27.32
Std Dev 3.545
Gaus mean : 27.3310
Gaus Width: 3.4014
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

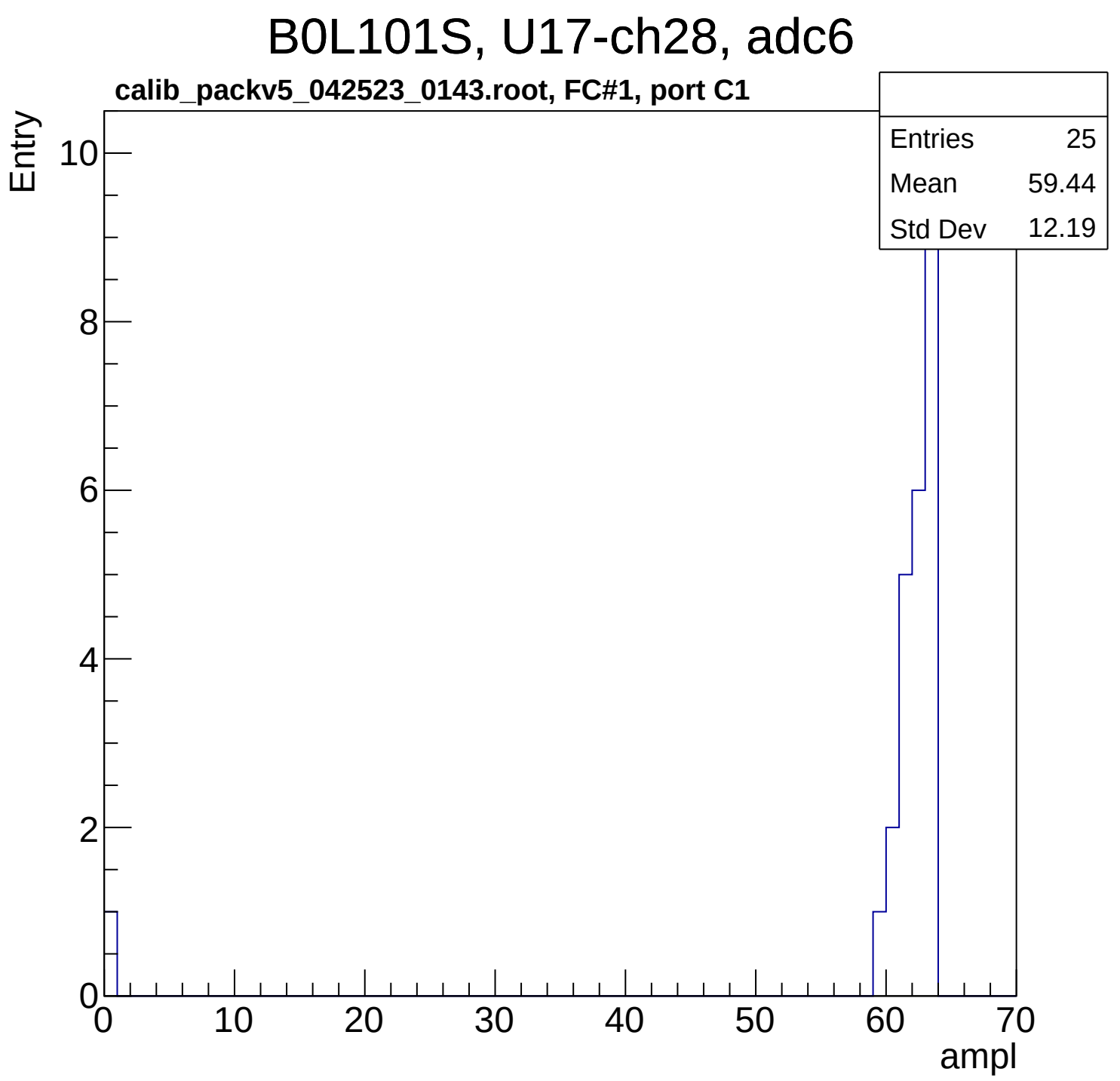

B0L101S, U17-ch28, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 34.56
Std Dev 3.463
Gaus mean : 35.3428
Gaus Width: 3.4682
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

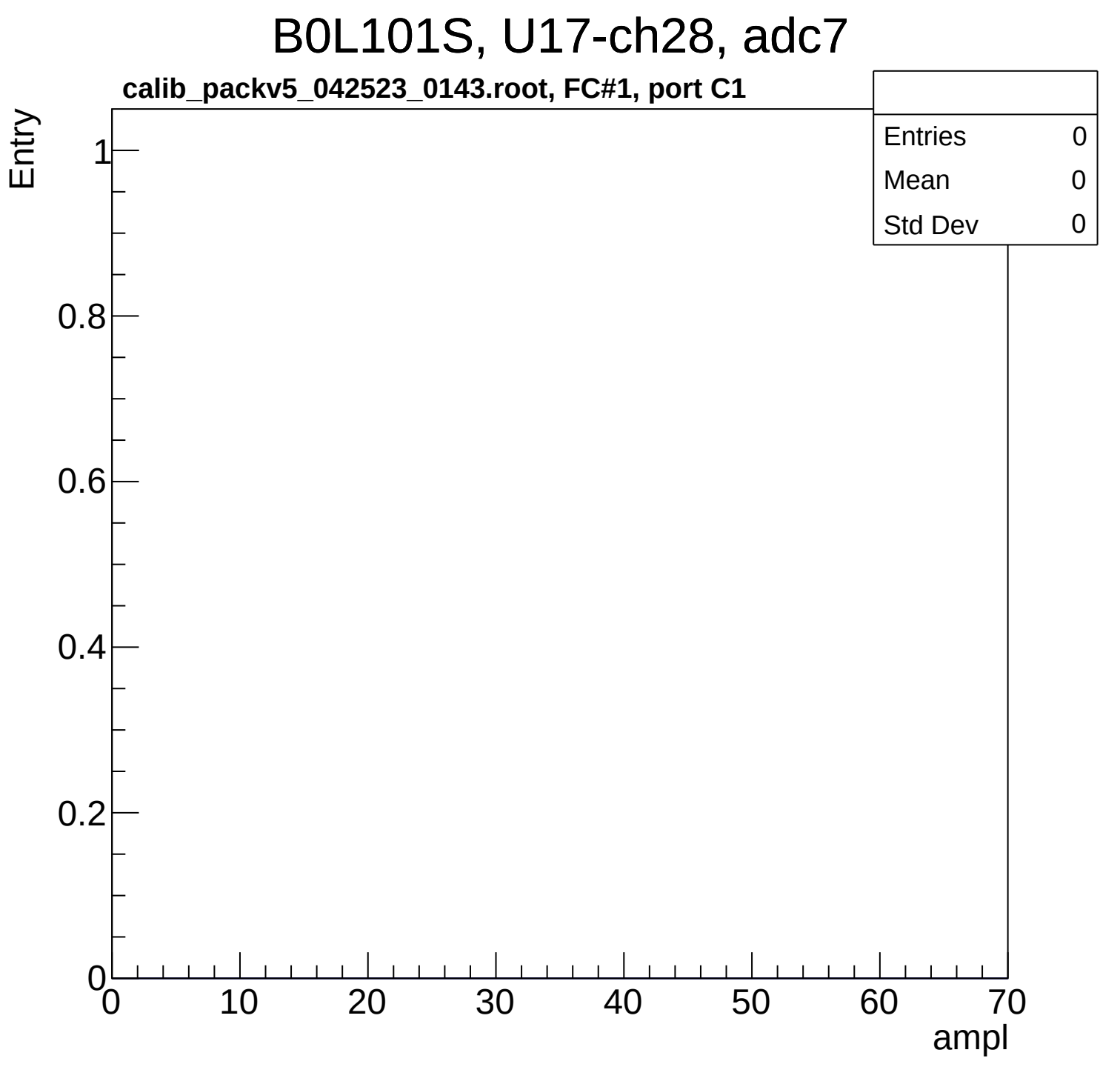

B0L101S, U17-ch28, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 42.02
Std Dev 3.426
Gaus mean : 42.6955
Gaus Width: 3.2111
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

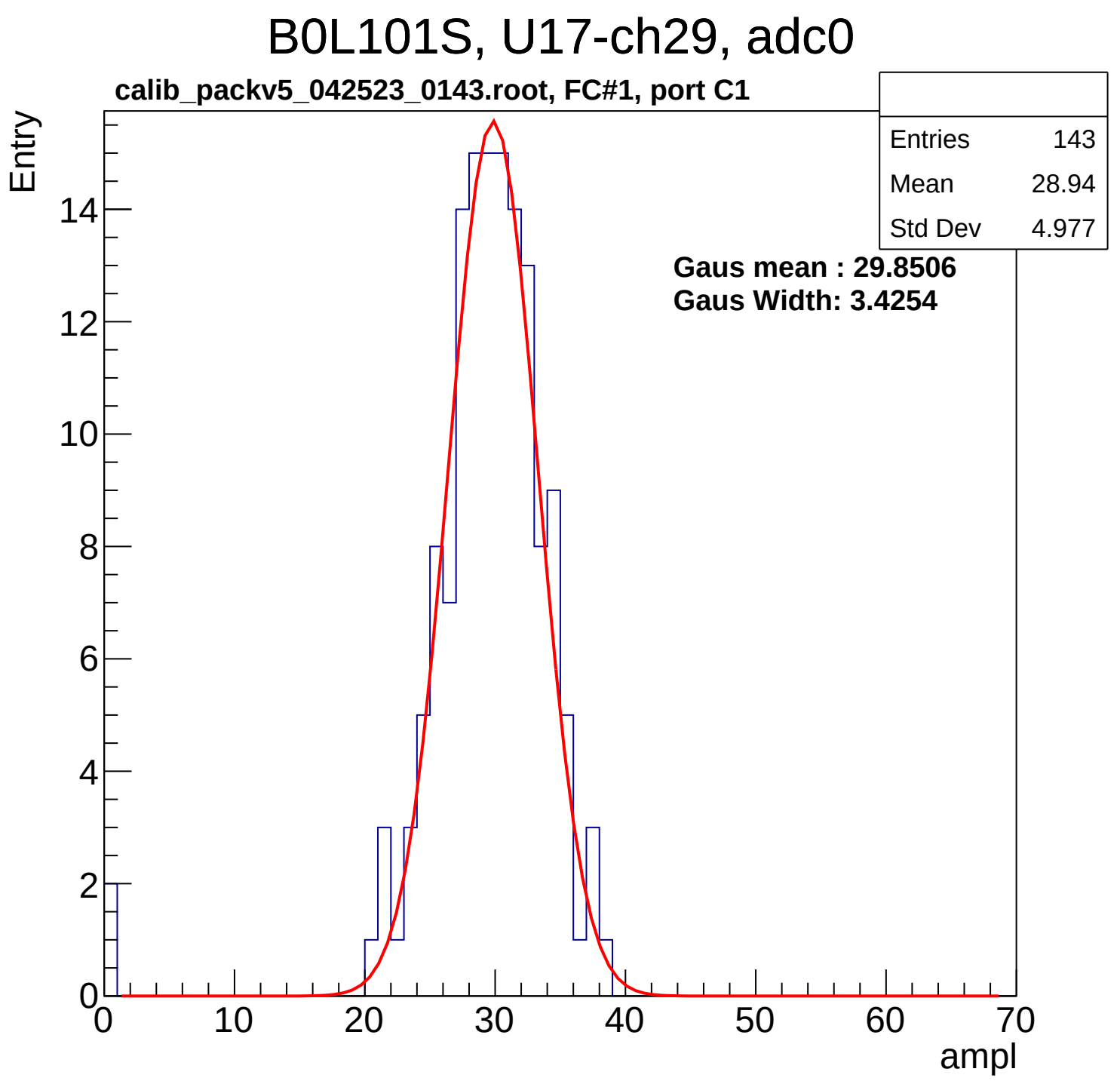

B0L101S, U17-ch28, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 47.92
Std Dev 3.29
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

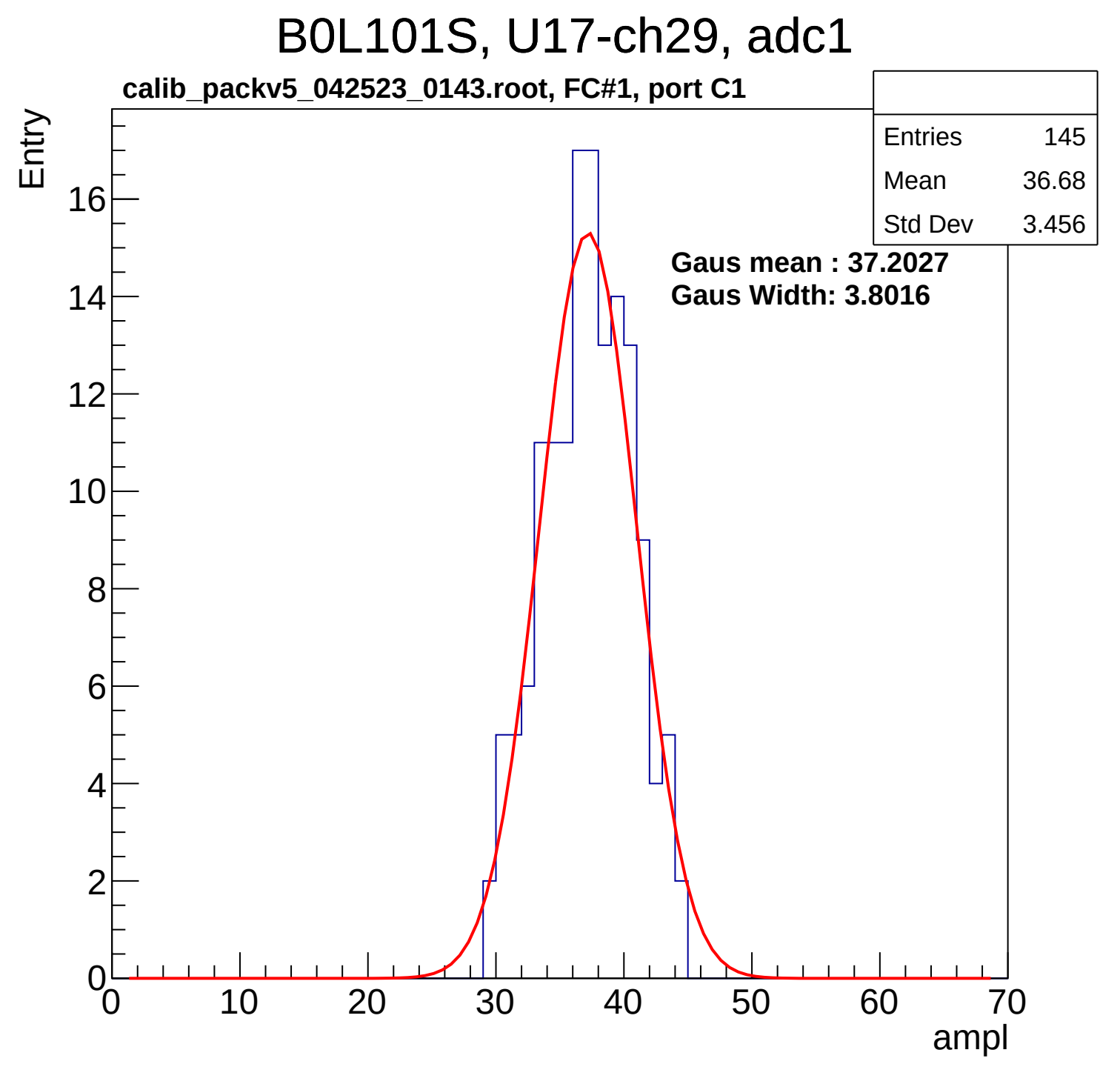

B0L101S, U17-ch28, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 54.23
Std Dev 3.54
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

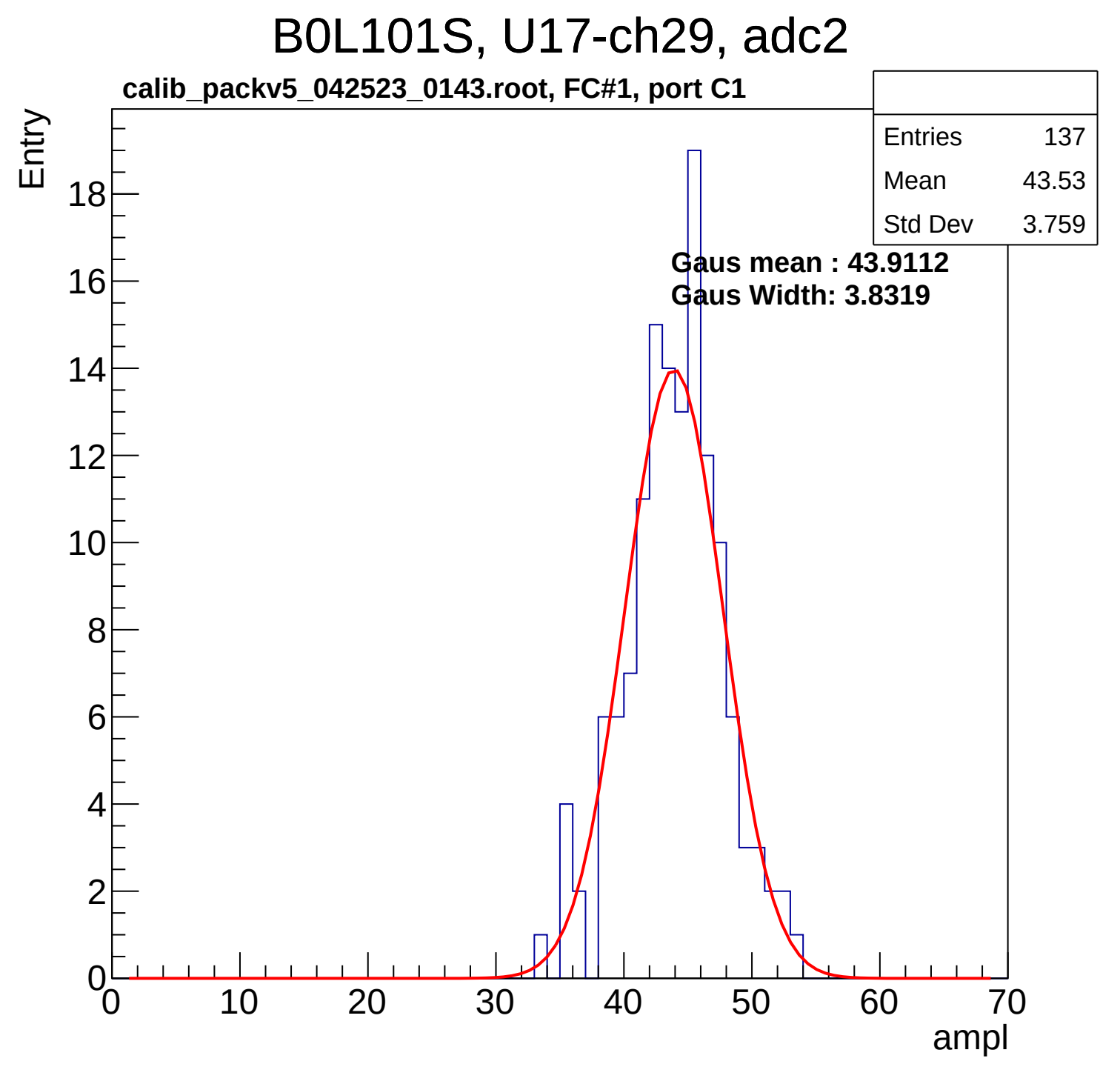

B0L101S, U17-ch28, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.39
Std Dev 6.349
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

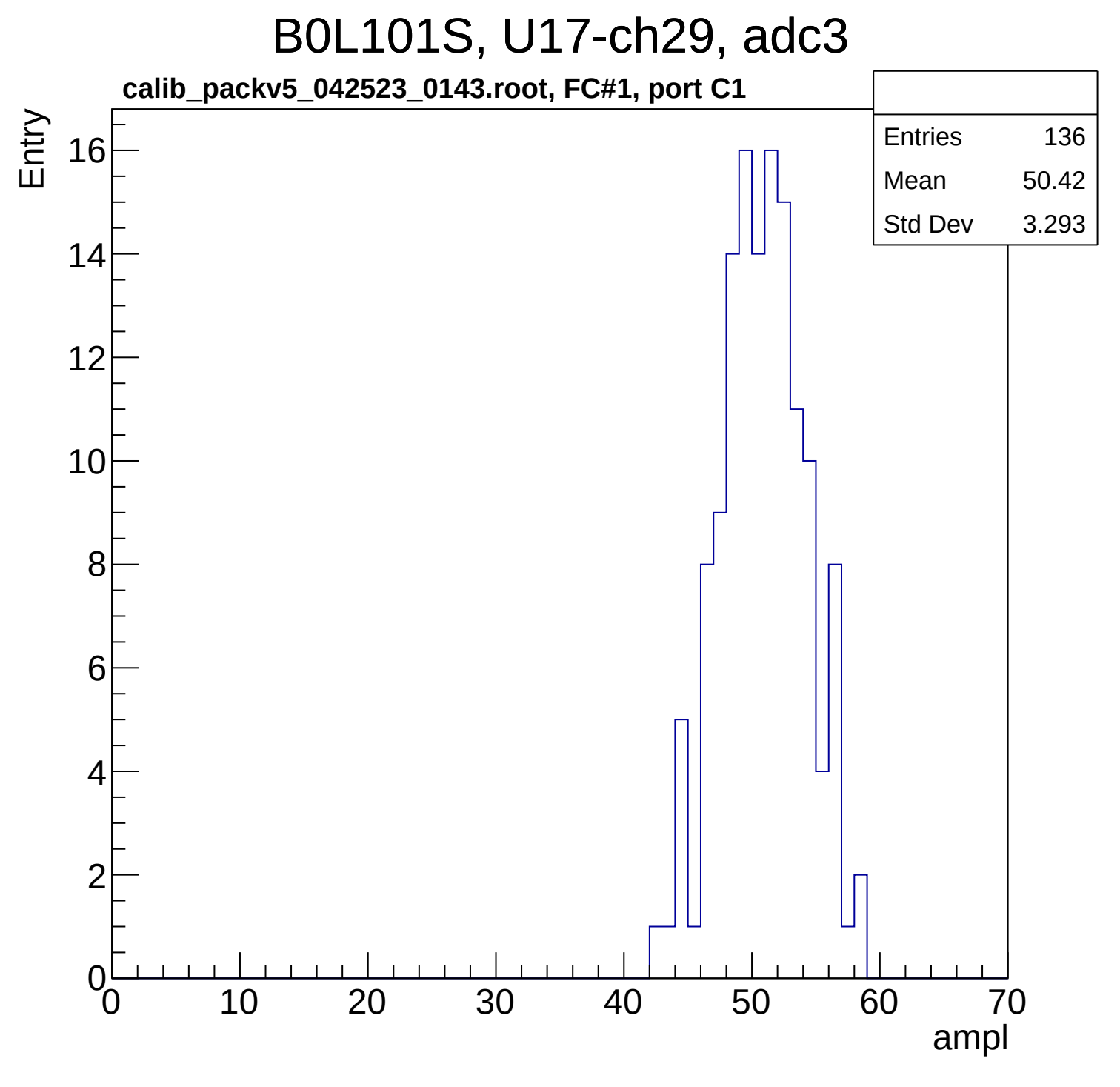

B0L101S, U17-ch28, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 25
Mean 59.44
Std Dev 12.19
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

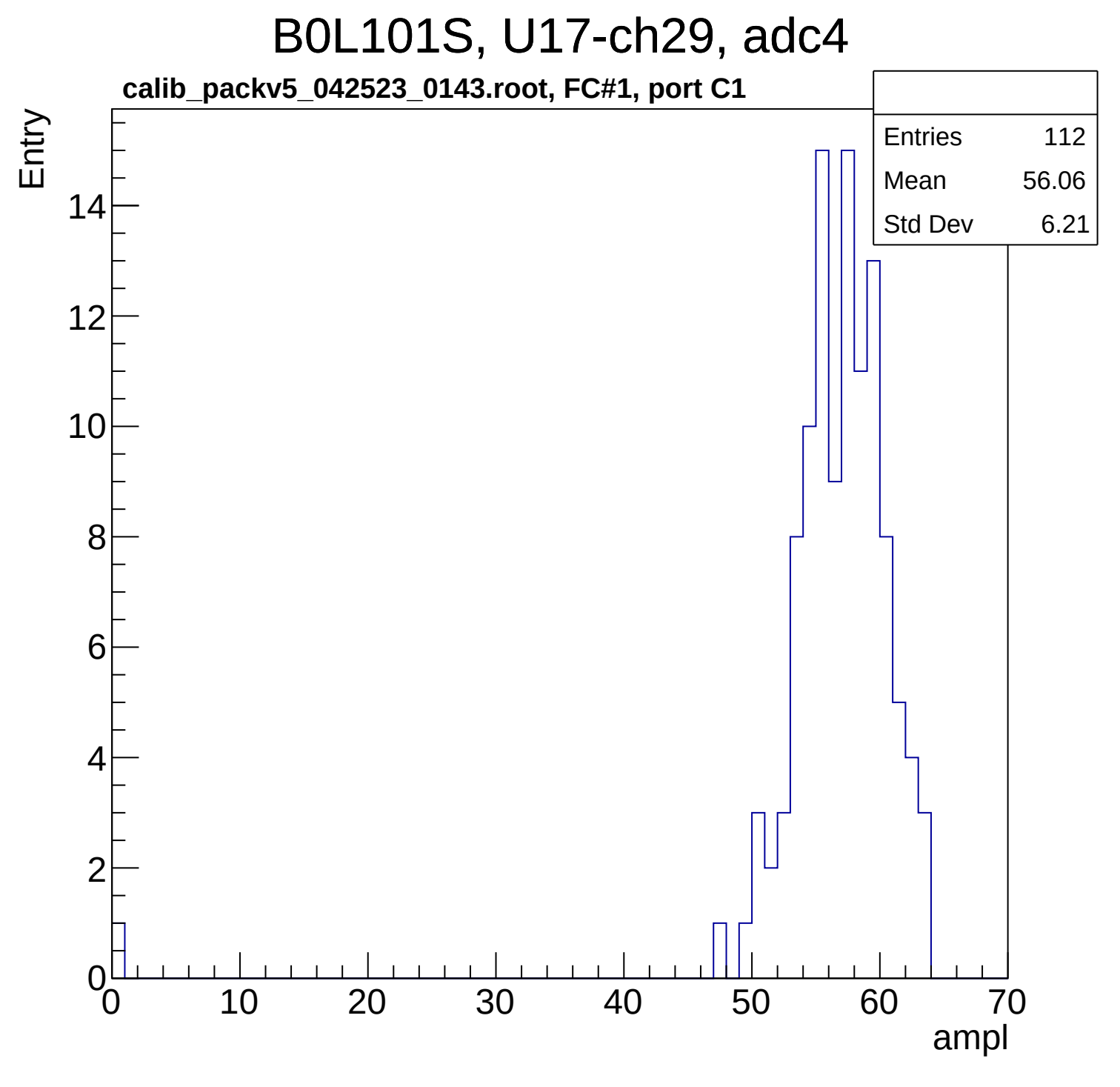

B0L101S, U17-ch28, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

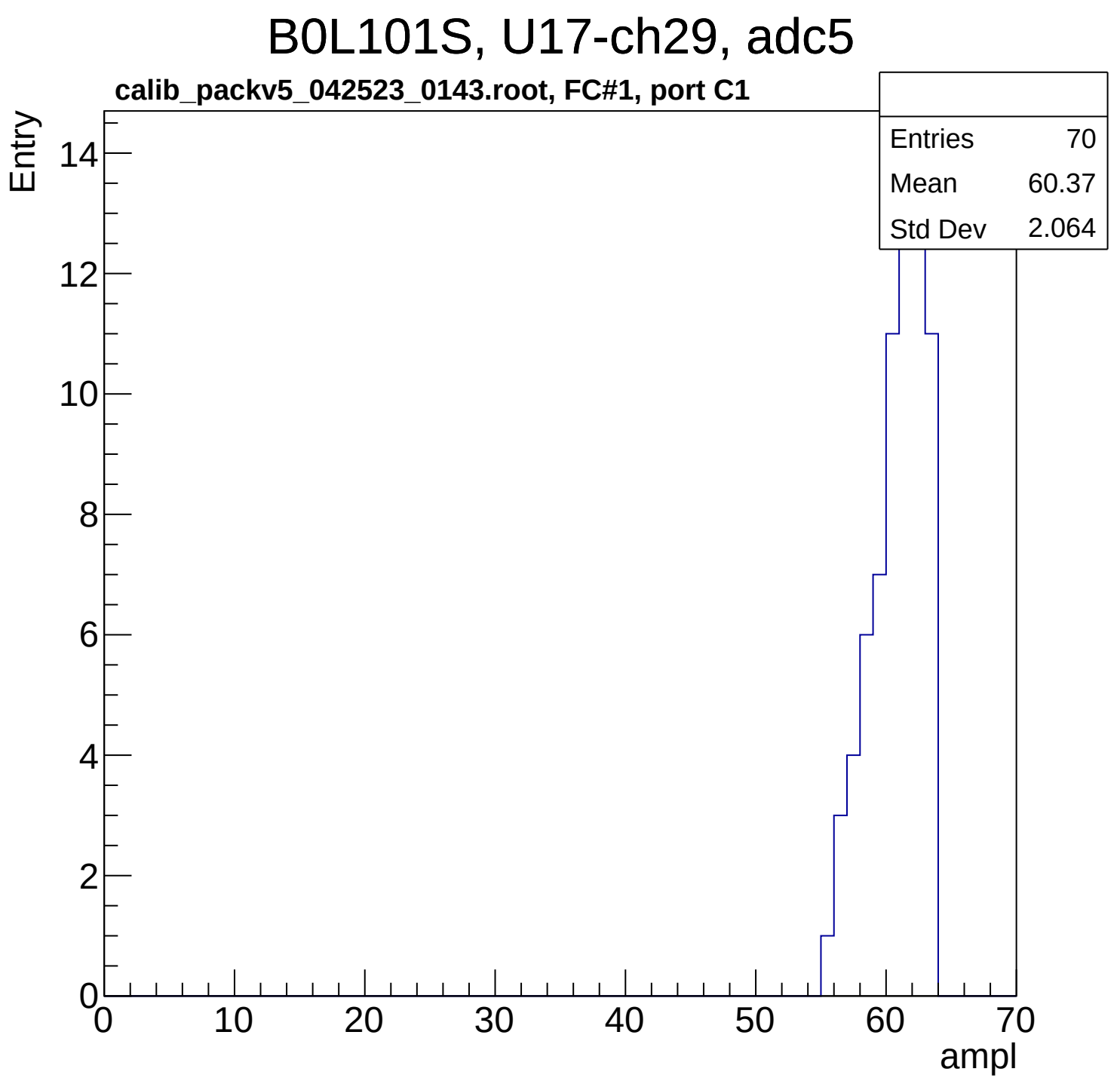

B0L101S, U17-ch29, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 28.94
Std Dev 4.977
Gaus mean : 29.8506
Gaus Width: 3.4254
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

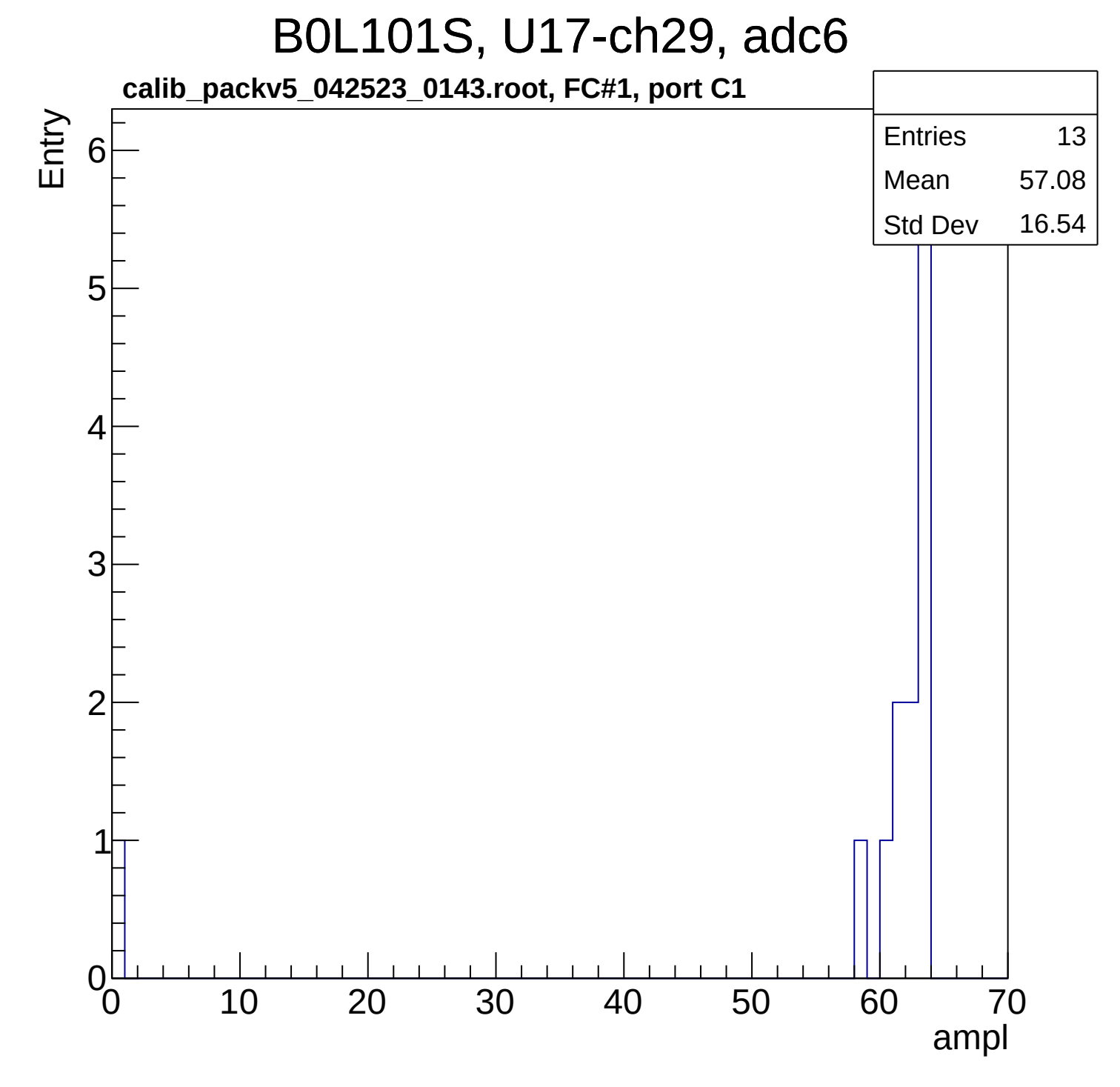

B0L101S, U17-ch29, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 36.68
Std Dev 3.456
Gaus mean : 37.2027
Gaus Width: 3.8016
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

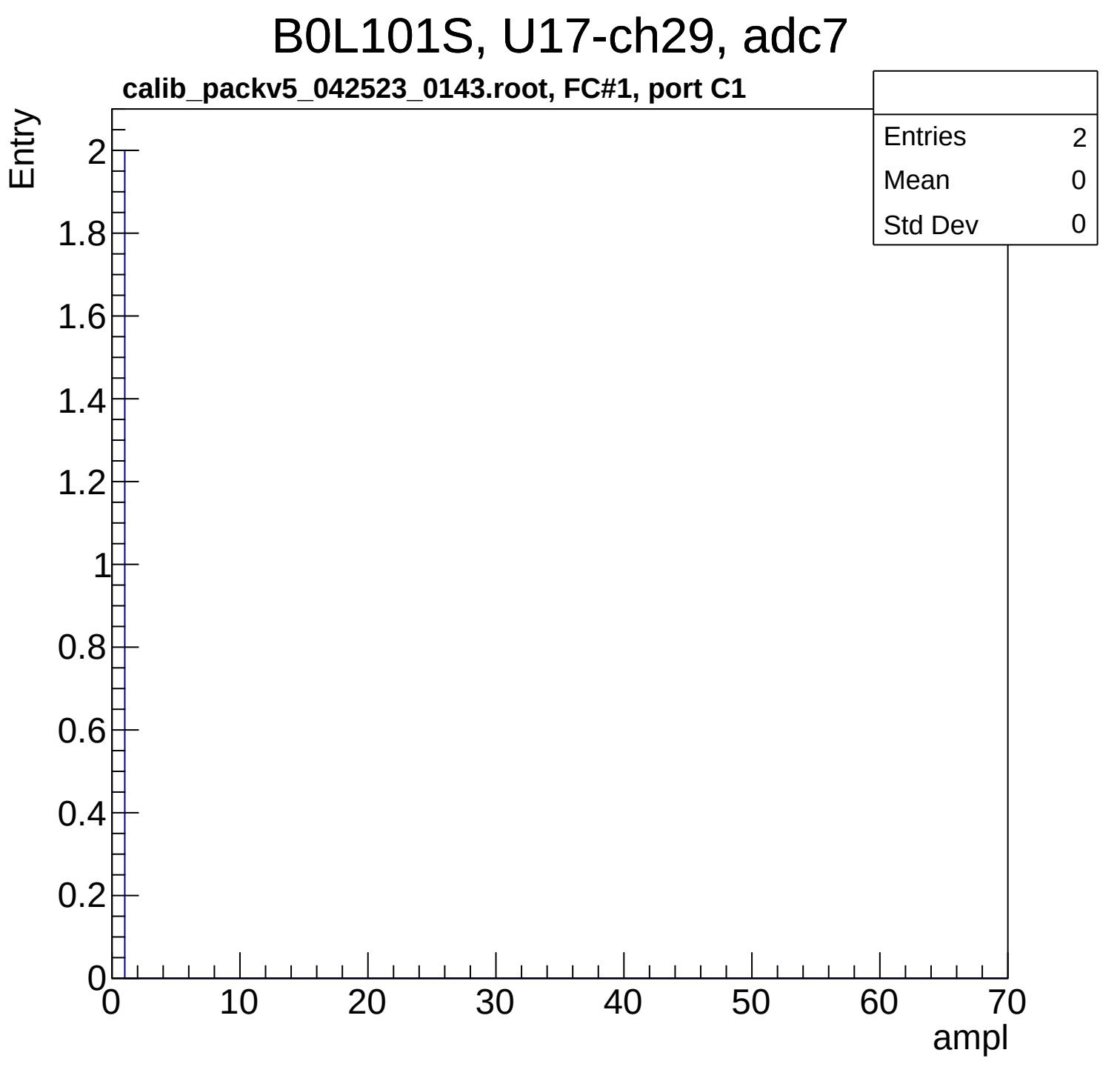

B0L101S, U17-ch29, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 43.53
Std Dev 3.759
Gaus mean : 43.9112
Gaus Width: 3.8319
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

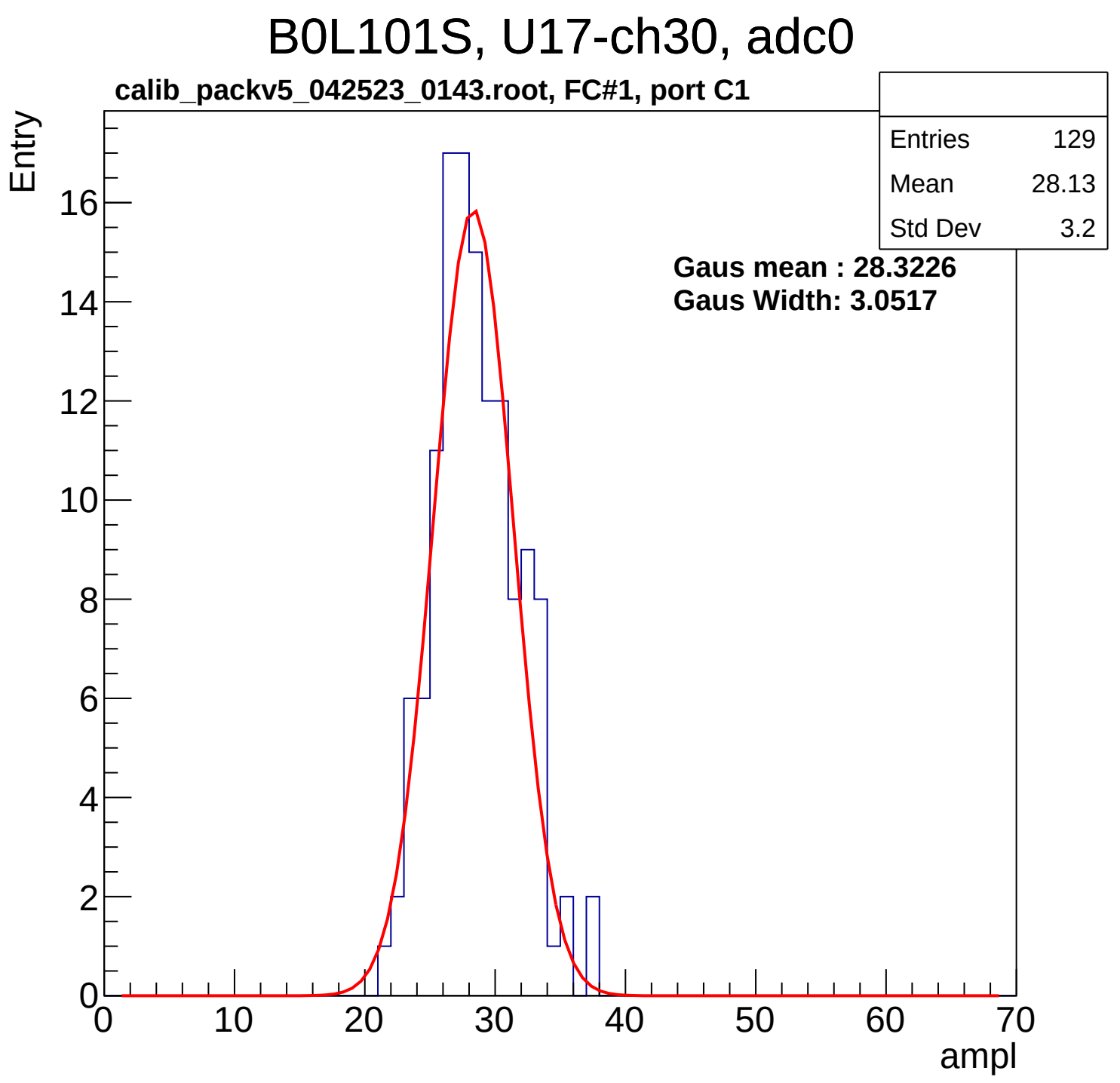

B0L101S, U17-ch29, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 50.42
Std Dev 3.293
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

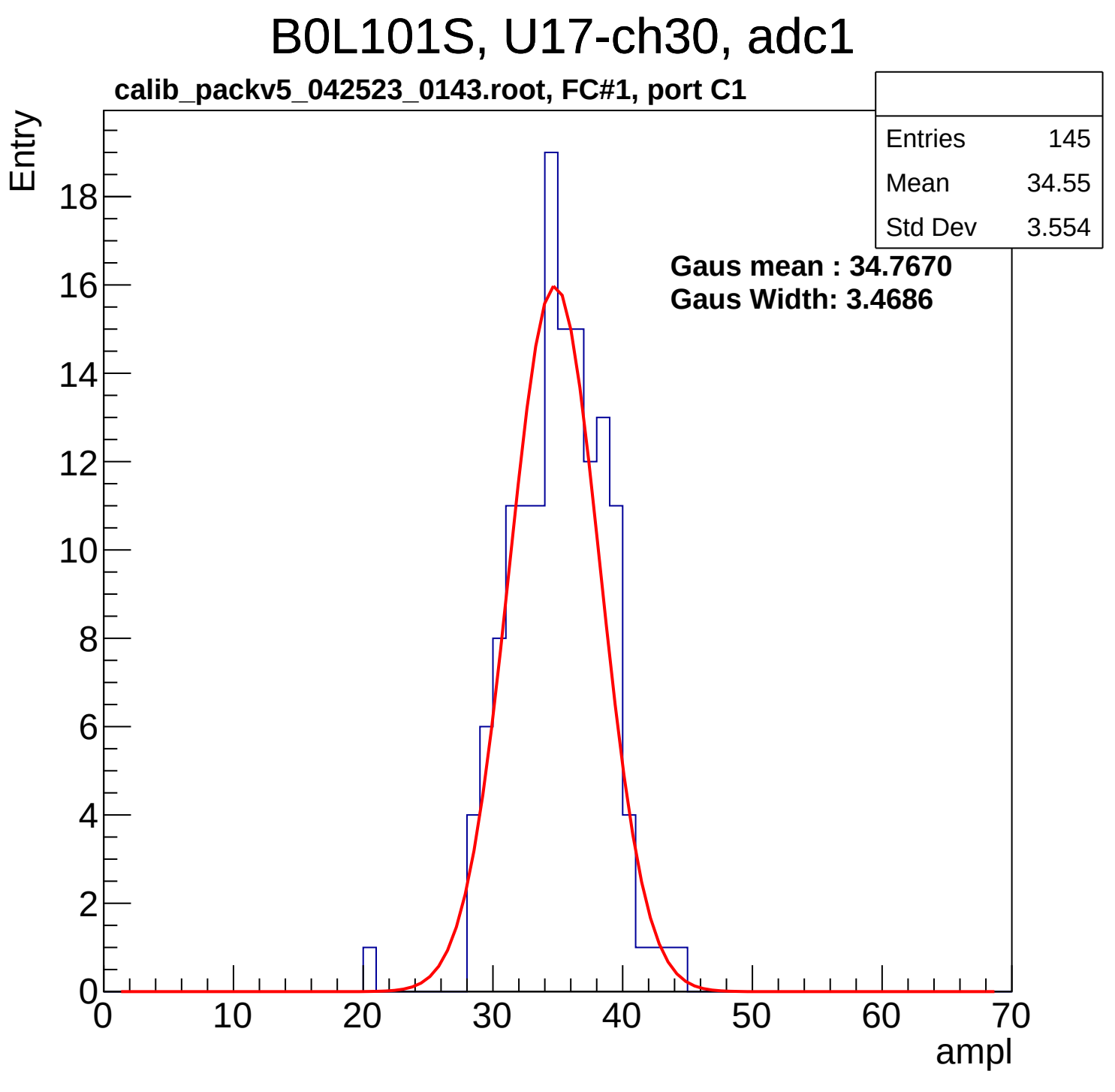

B0L101S, U17-ch29, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 56.06
Std Dev 6.21
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

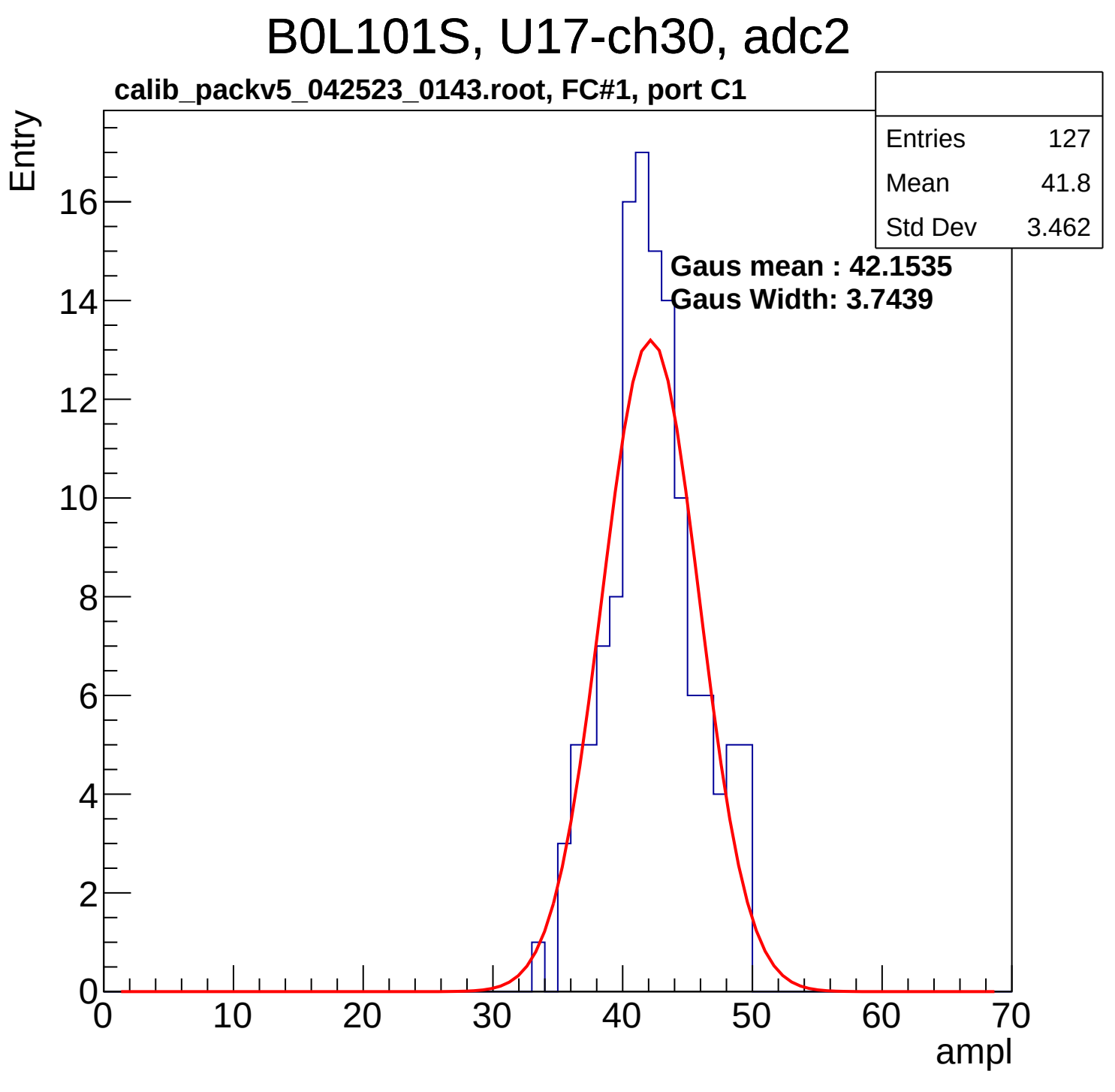

B0L101S, U17-ch29, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 70
Mean 60.37
Std Dev 2.064
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

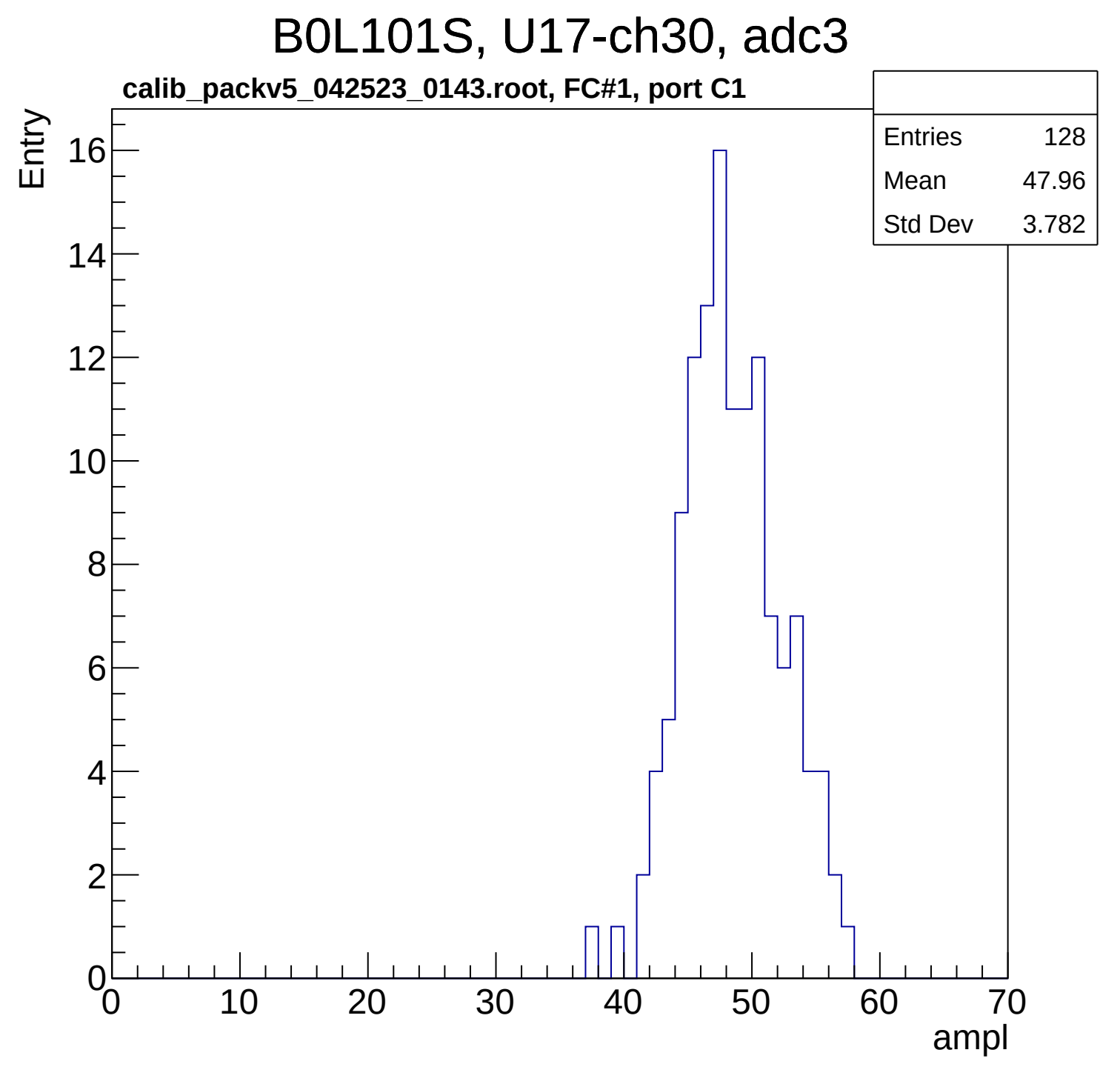

B0L101S, U17-ch29, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 13
Mean 57.08
Std Dev 16.54
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

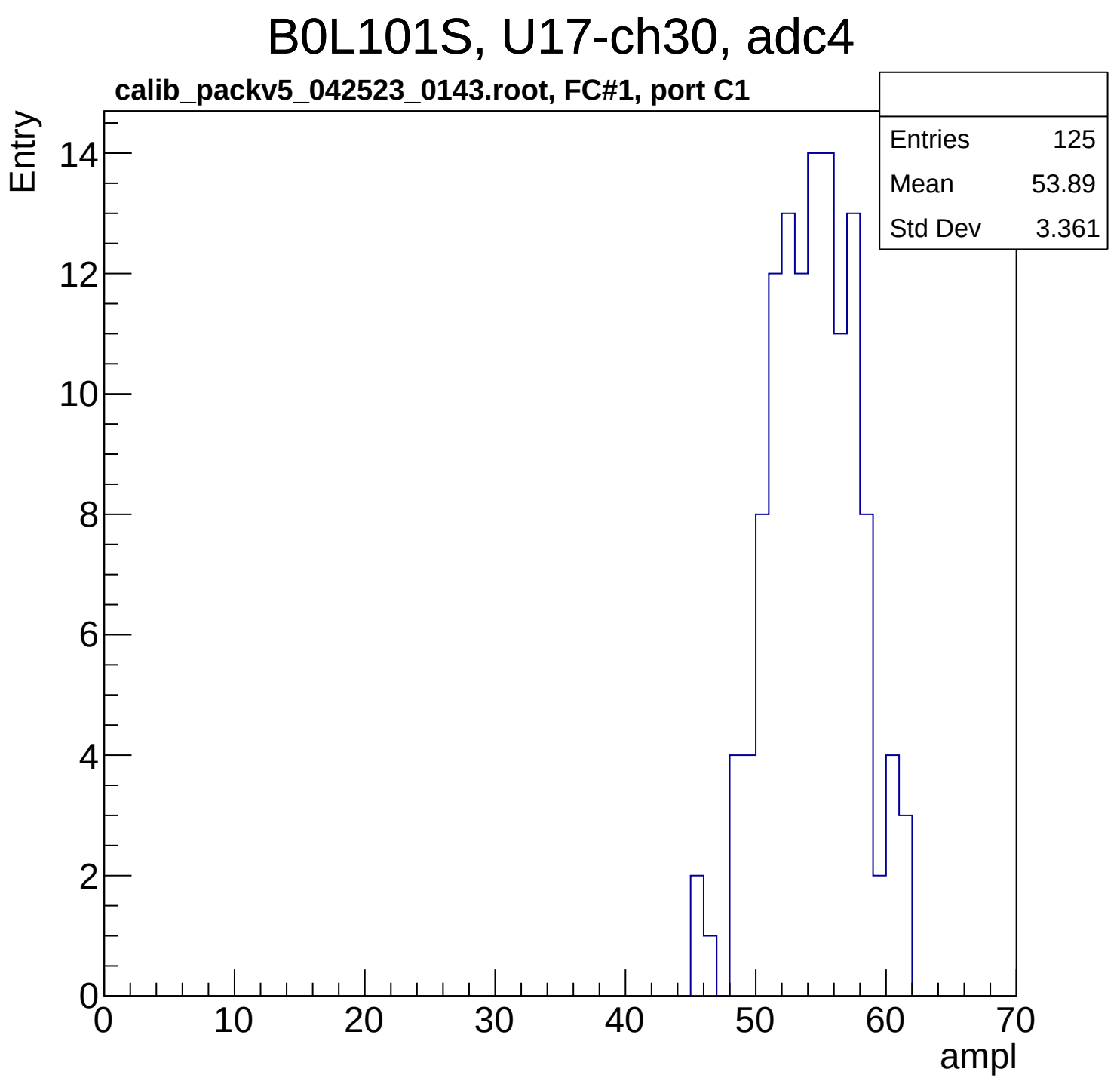

B0L101S, U17-ch29, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

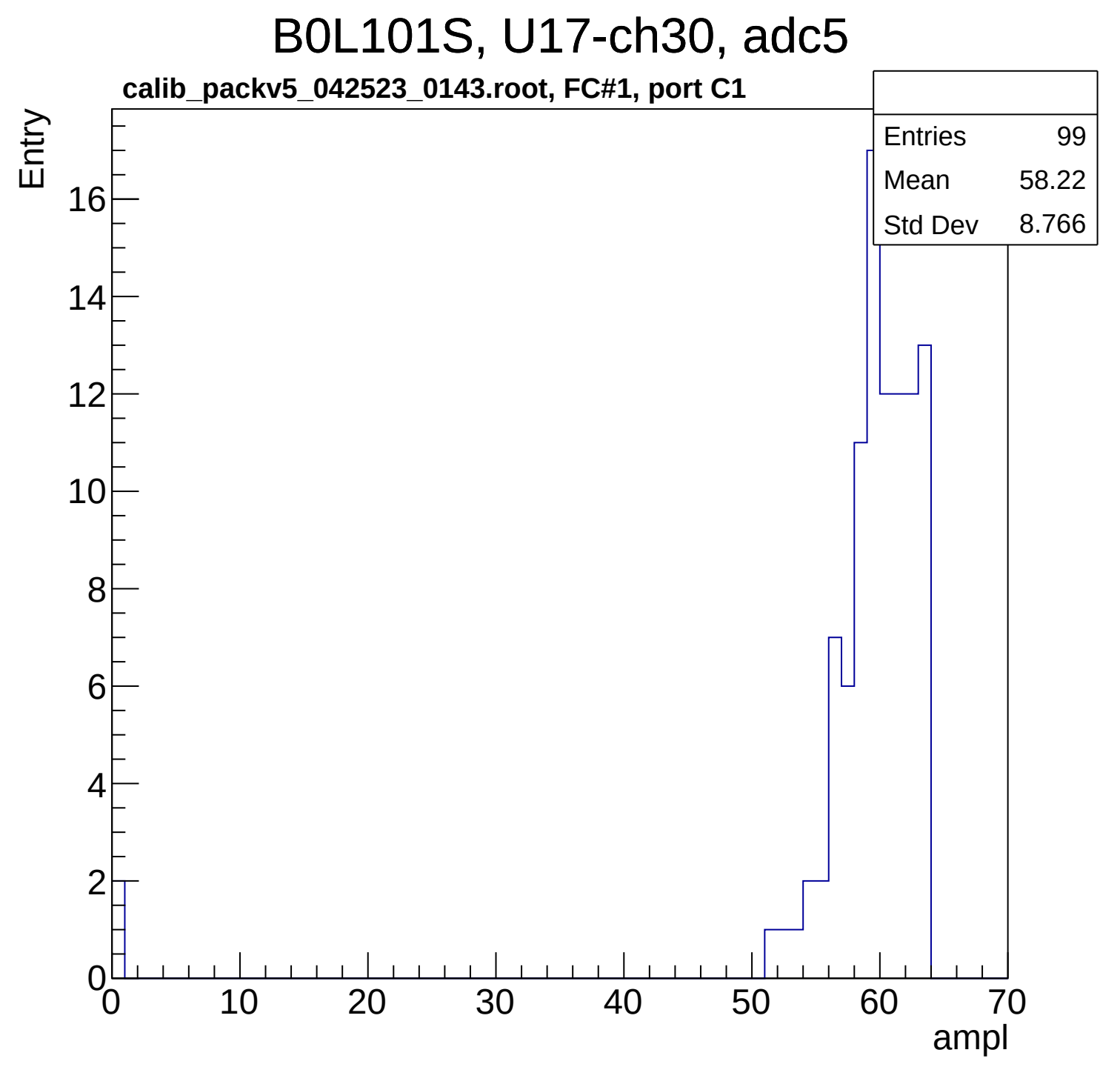

B0L101S, U17-ch30, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 28.13
Std Dev 3.2
Gaus mean : 28.3226
Gaus Width: 3.0517
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

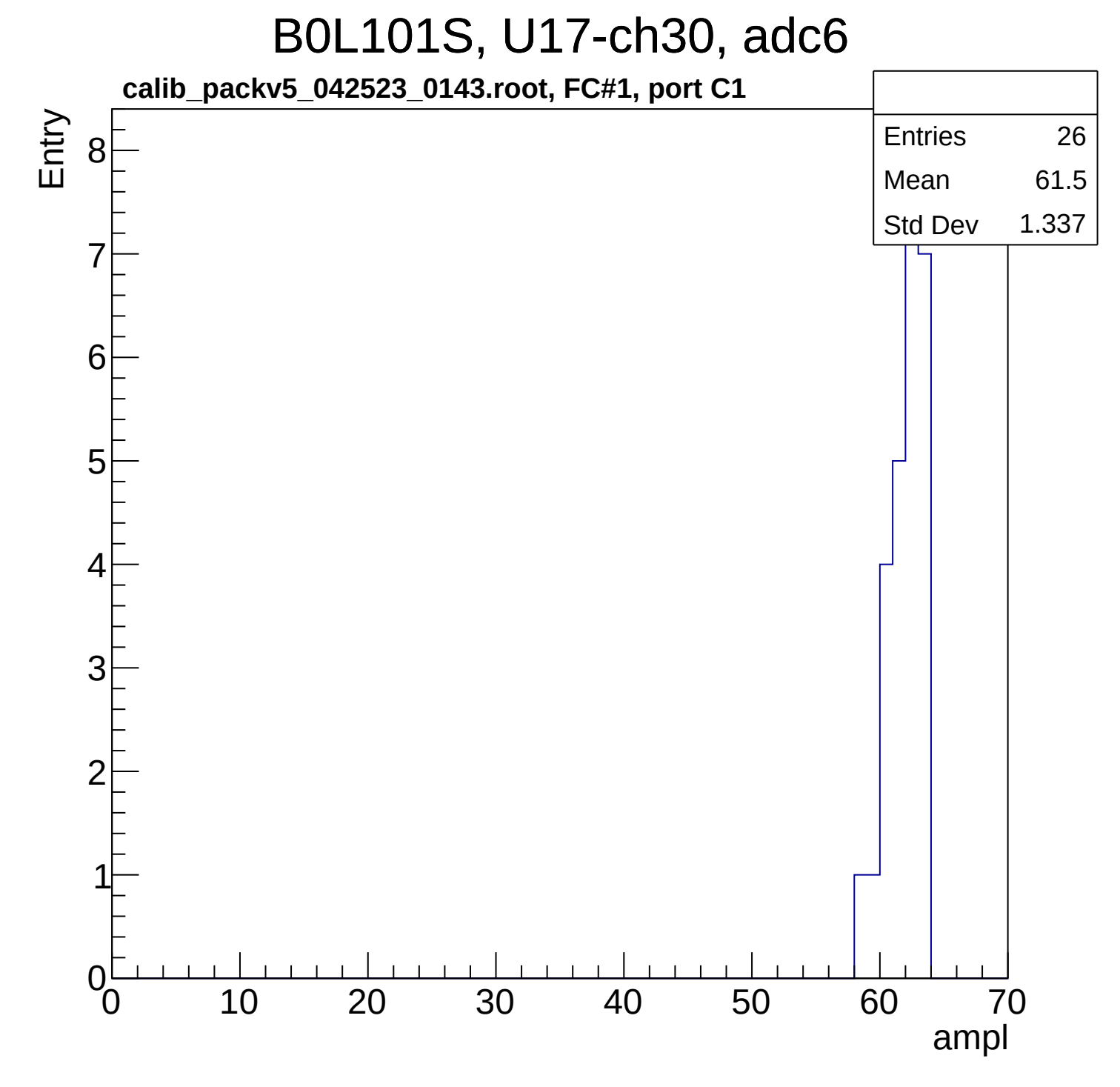

B0L101S, U17-ch30, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 34.55
Std Dev 3.554
Gaus mean : 34.7670
Gaus Width: 3.4686
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

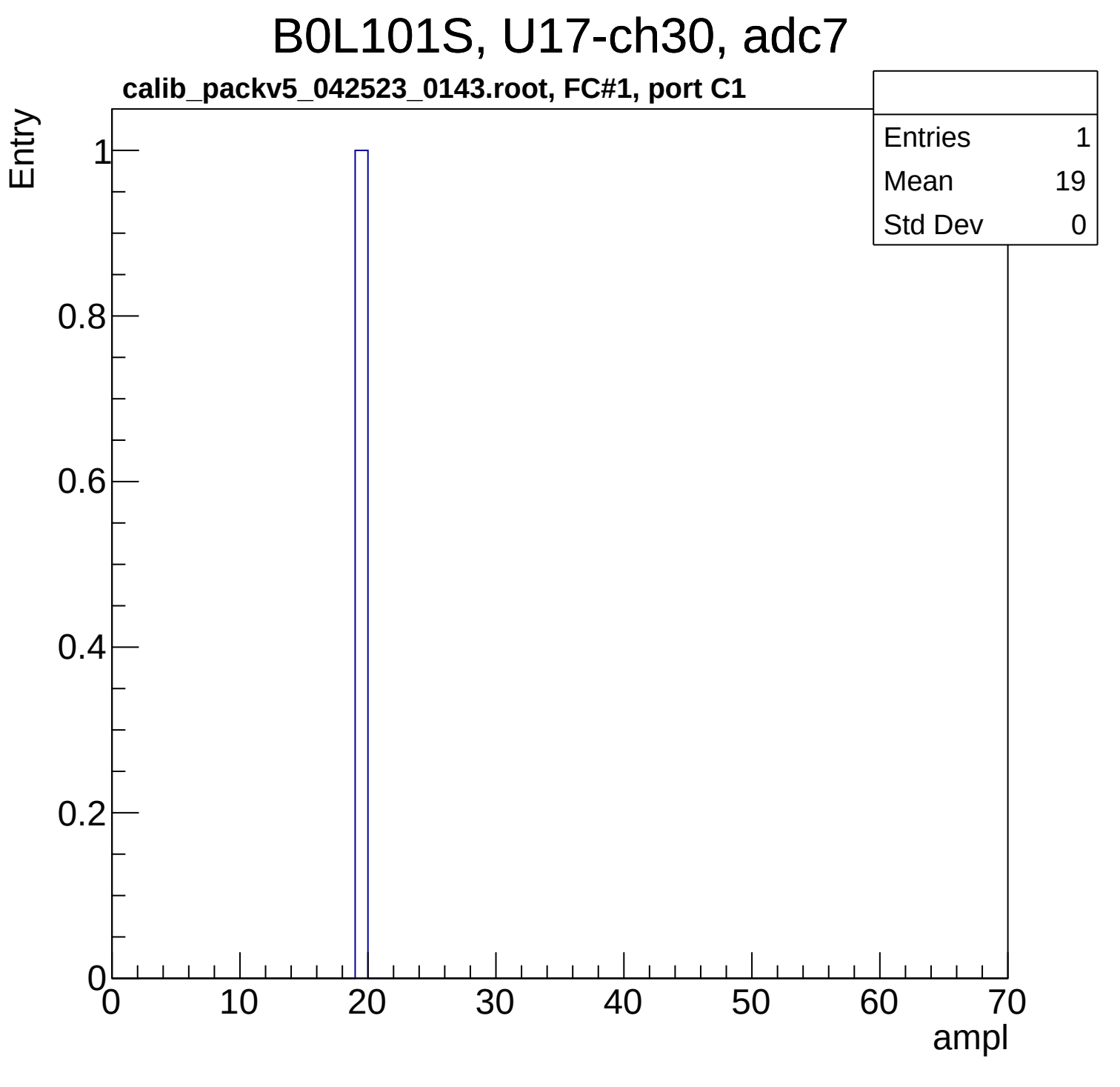

B0L101S, U17-ch30, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 41.8
Std Dev 3.462
Gaus mean : 42.1535
Gaus Width: 3.7439
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

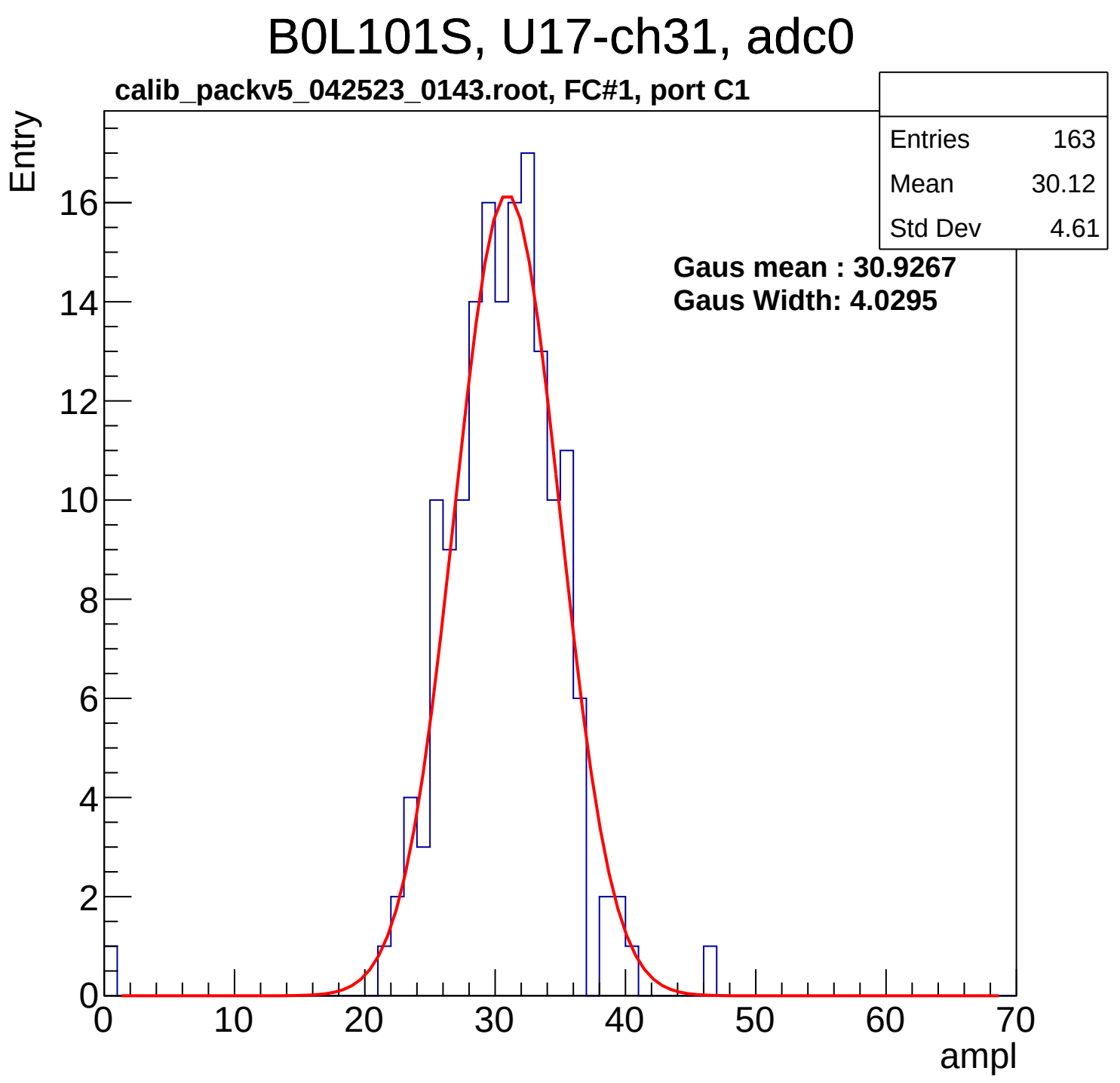

B0L101S, U17-ch30, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 47.96
Std Dev 3.782
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

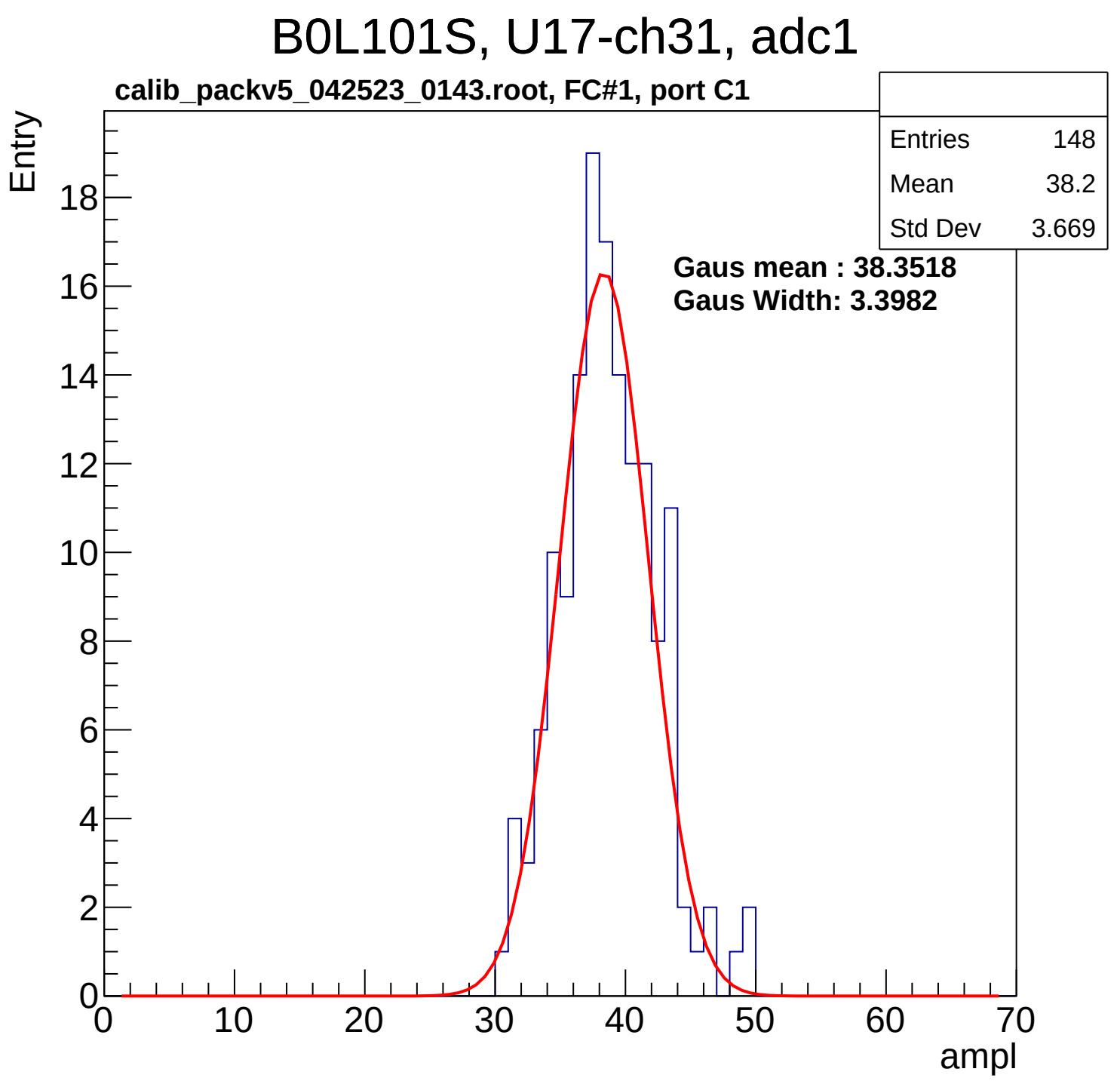

B0L101S, U17-ch30, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 53.89
Std Dev 3.361
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

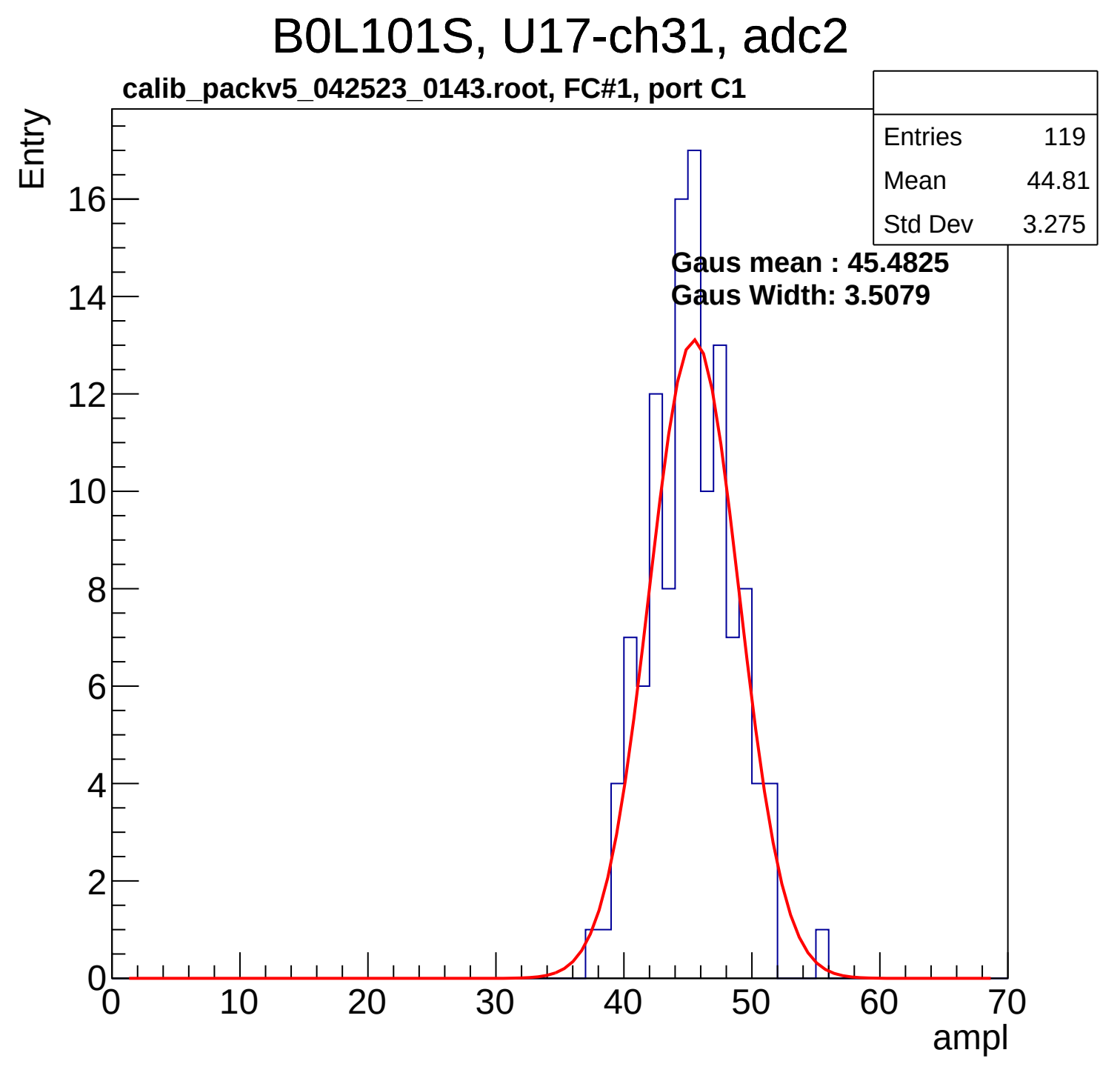

B0L101S, U17-ch30, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 99
Mean 58.22
Std Dev 8.766
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

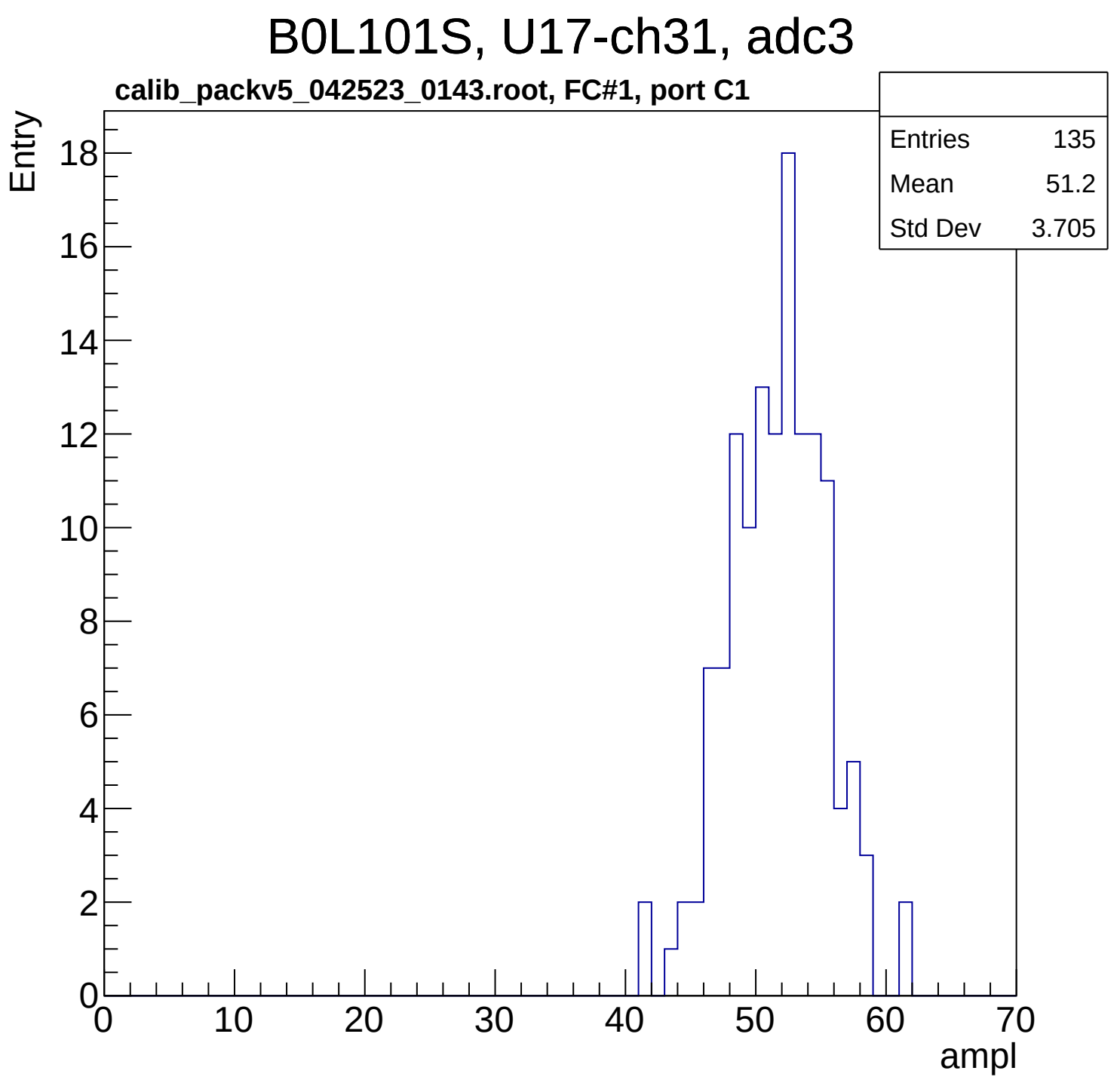

B0L101S, U17-ch30, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 26
Mean 61.5
Std Dev 1.337
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

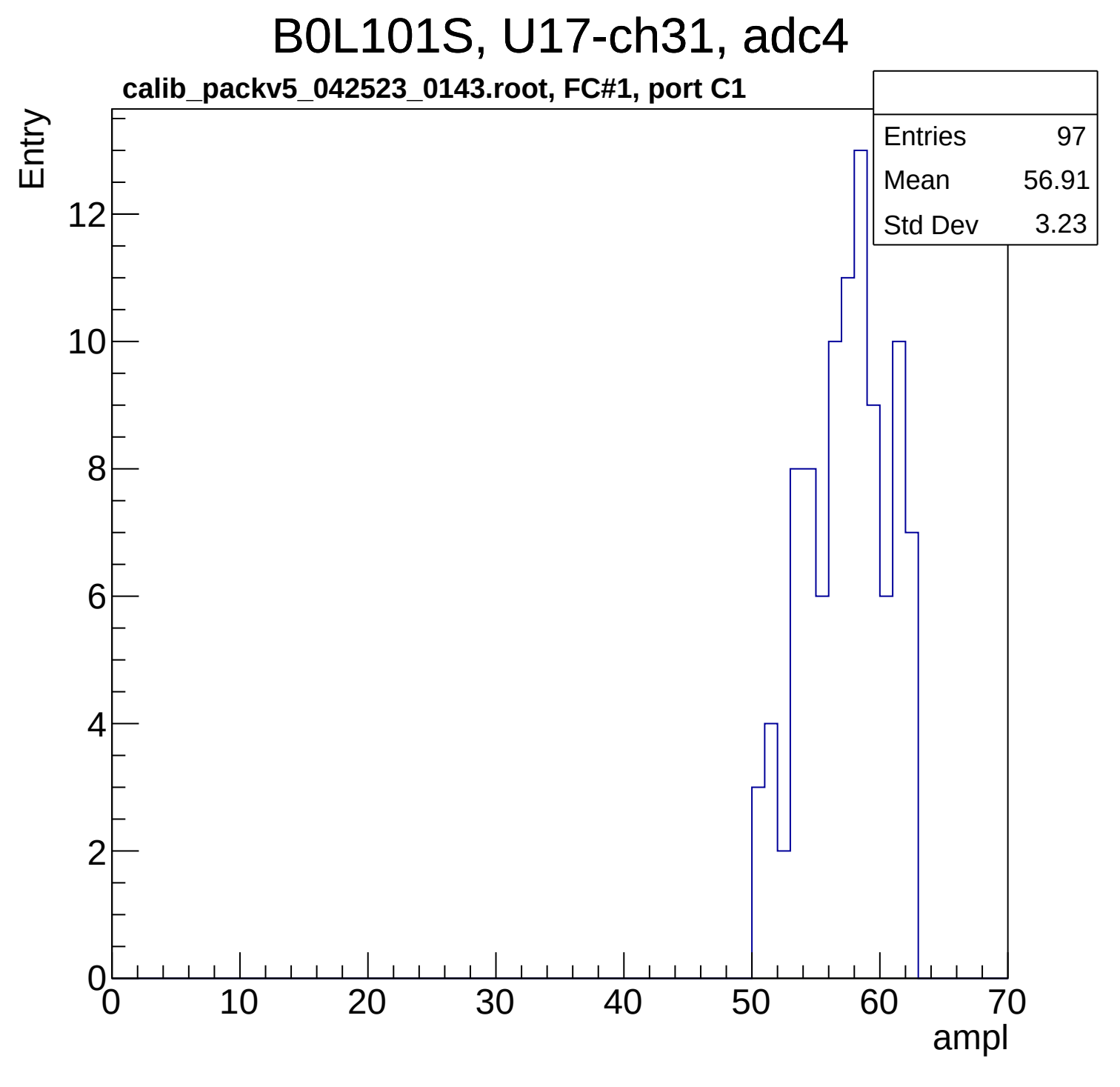

B0L101S, U17-ch30, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

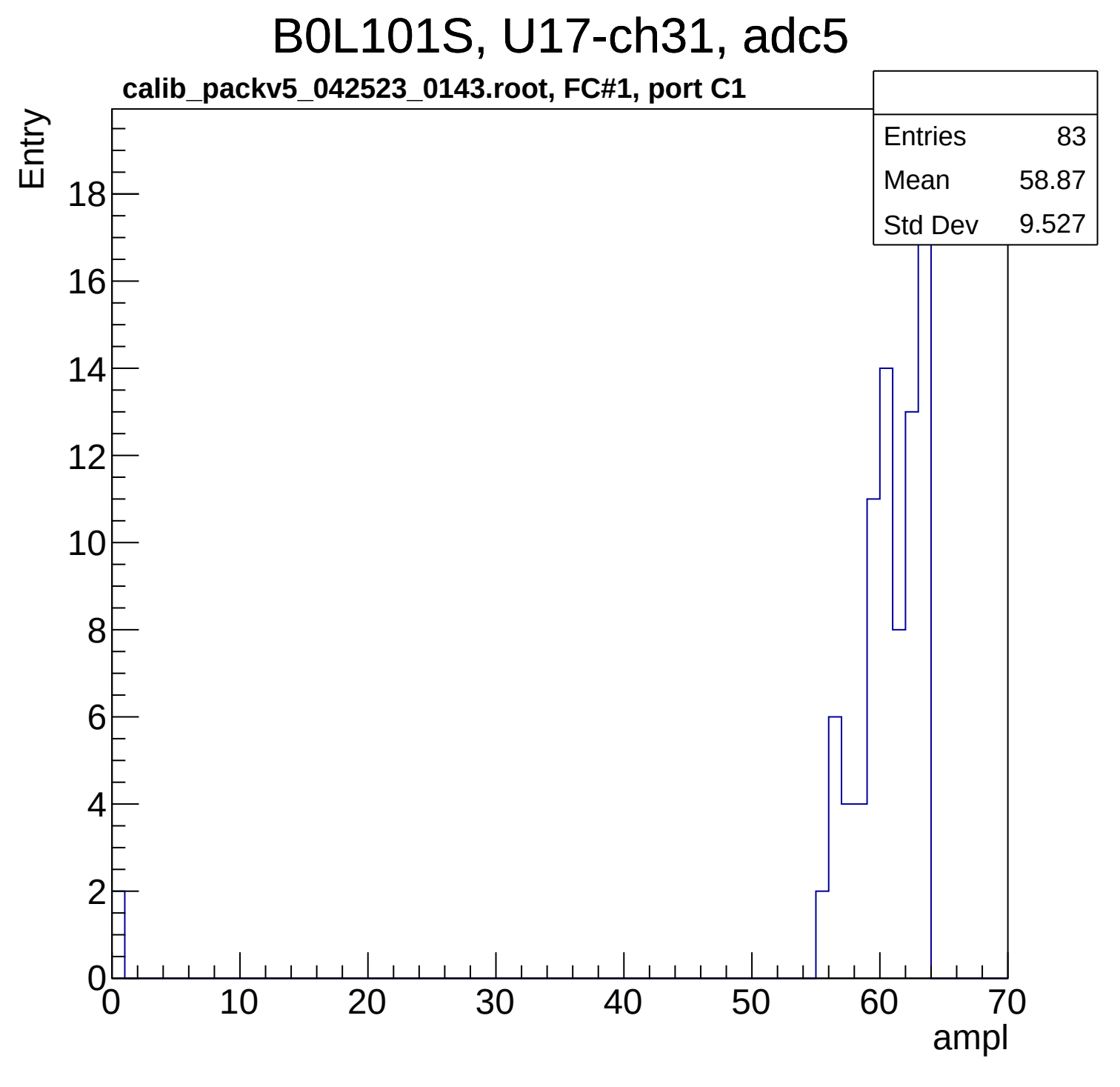

B0L101S, U17-ch31, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 30.12
Std Dev 4.61
Gaus mean : 30.9267
Gaus Width: 4.0295
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

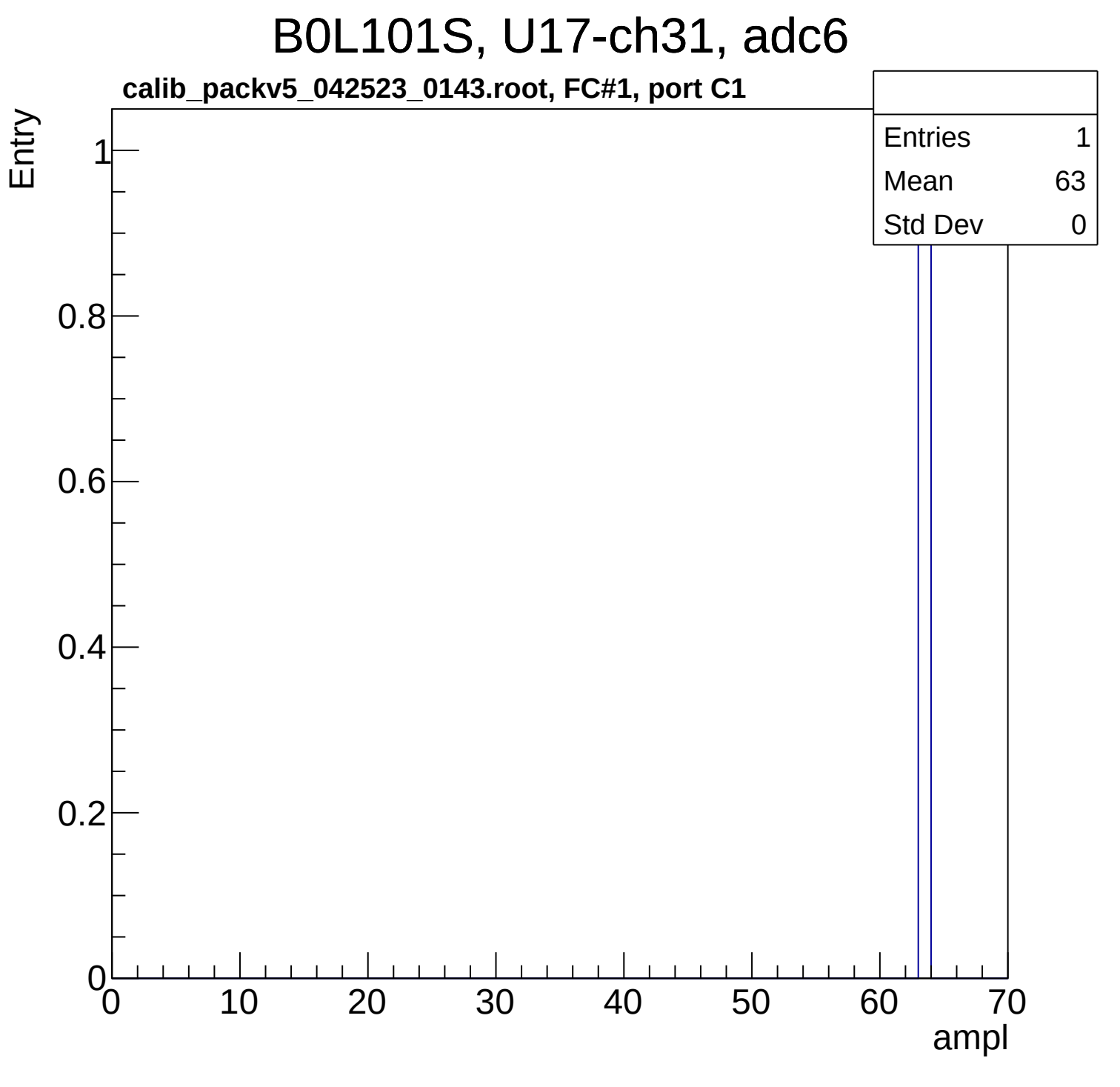

B0L101S, U17-ch31, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 38.2
Std Dev 3.669
Gaus mean : 38.3518
Gaus Width: 3.3982
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

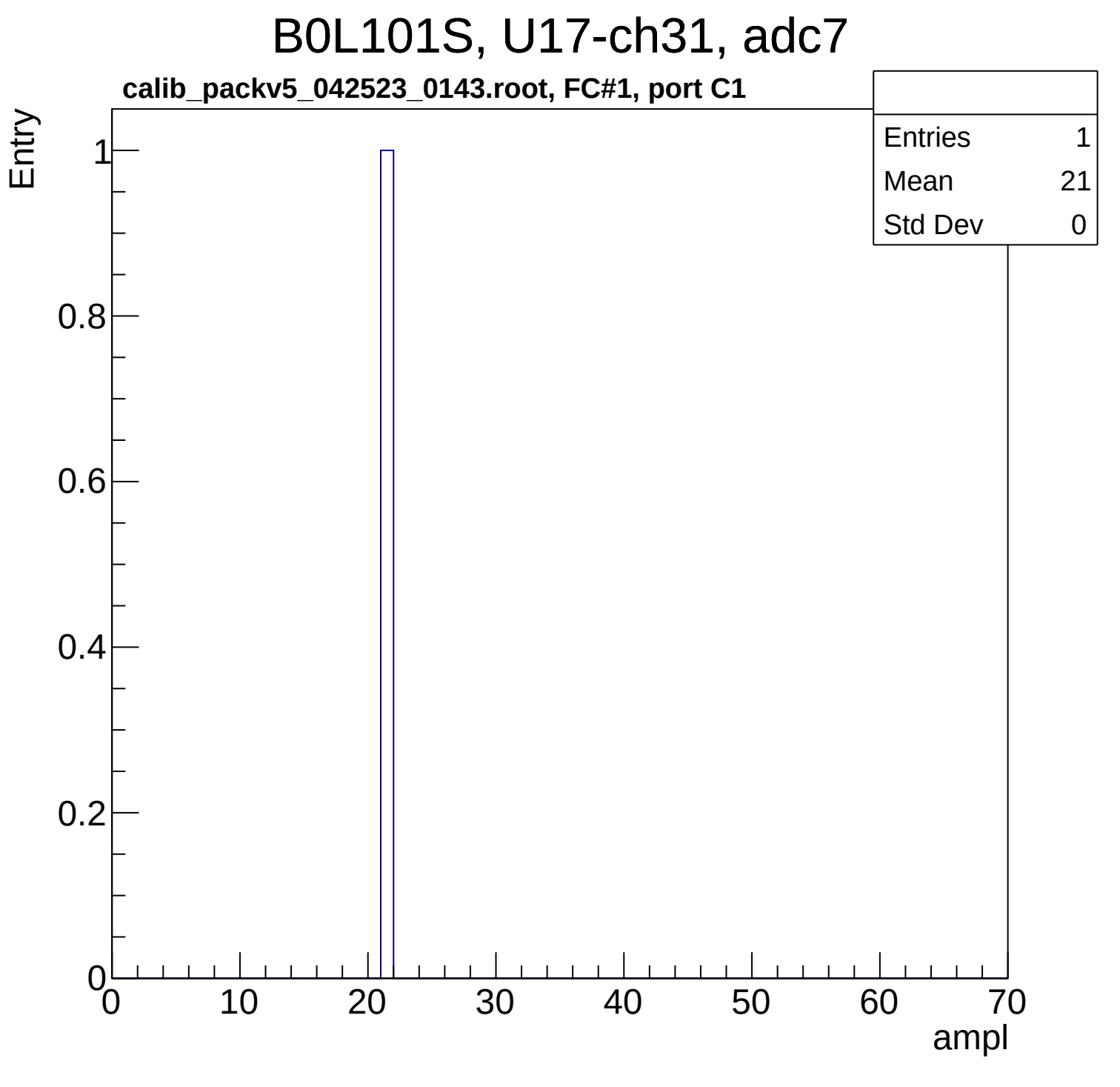

B0L101S, U17-ch31, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 44.81
Std Dev 3.275
Gaus mean : 45.4825
Gaus Width: 3.5079
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

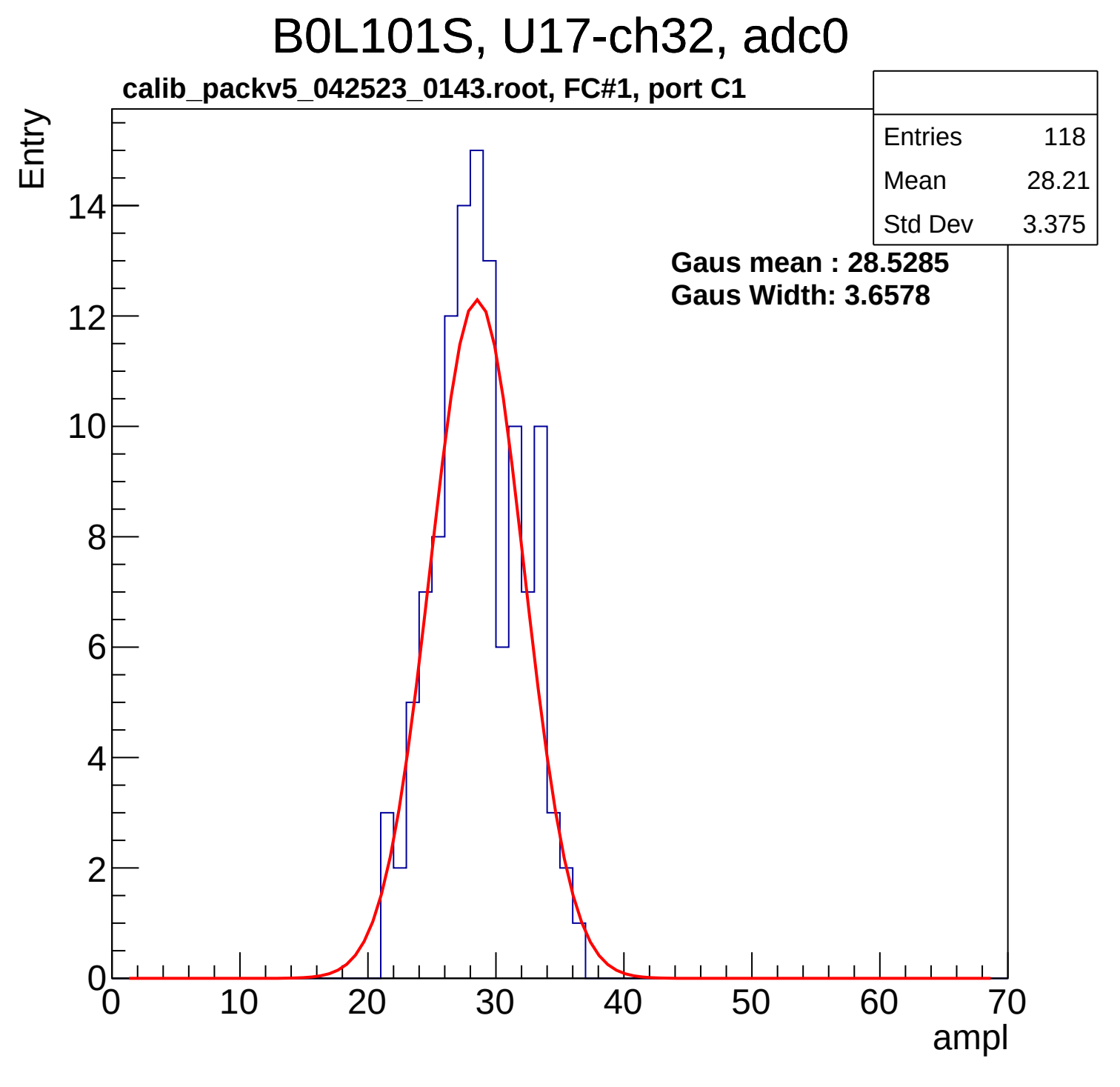

B0L101S, U17-ch31, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 51.2
Std Dev 3.705
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

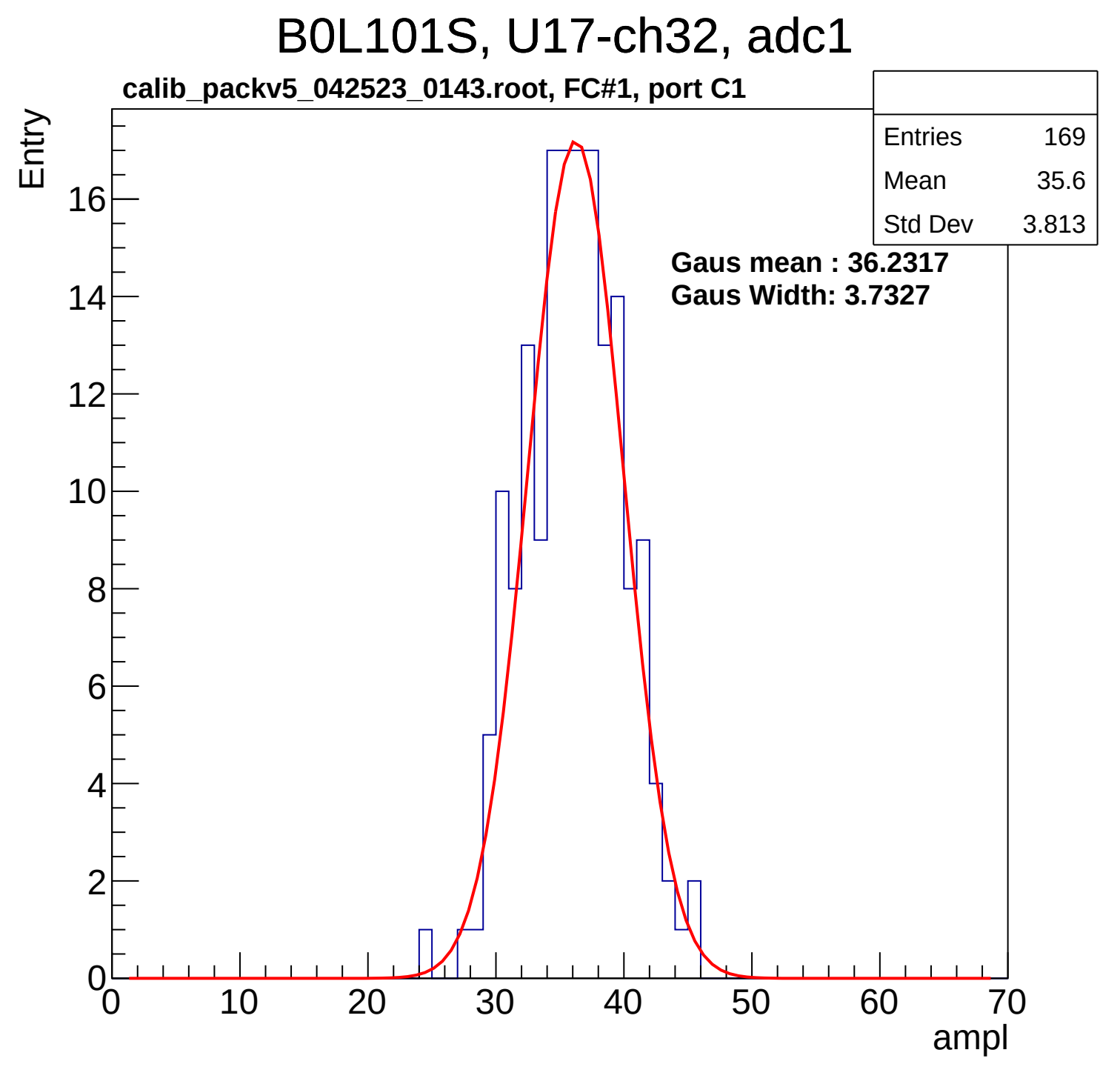

B0L101S, U17-ch31, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 97
Mean 56.91
Std Dev 3.23
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

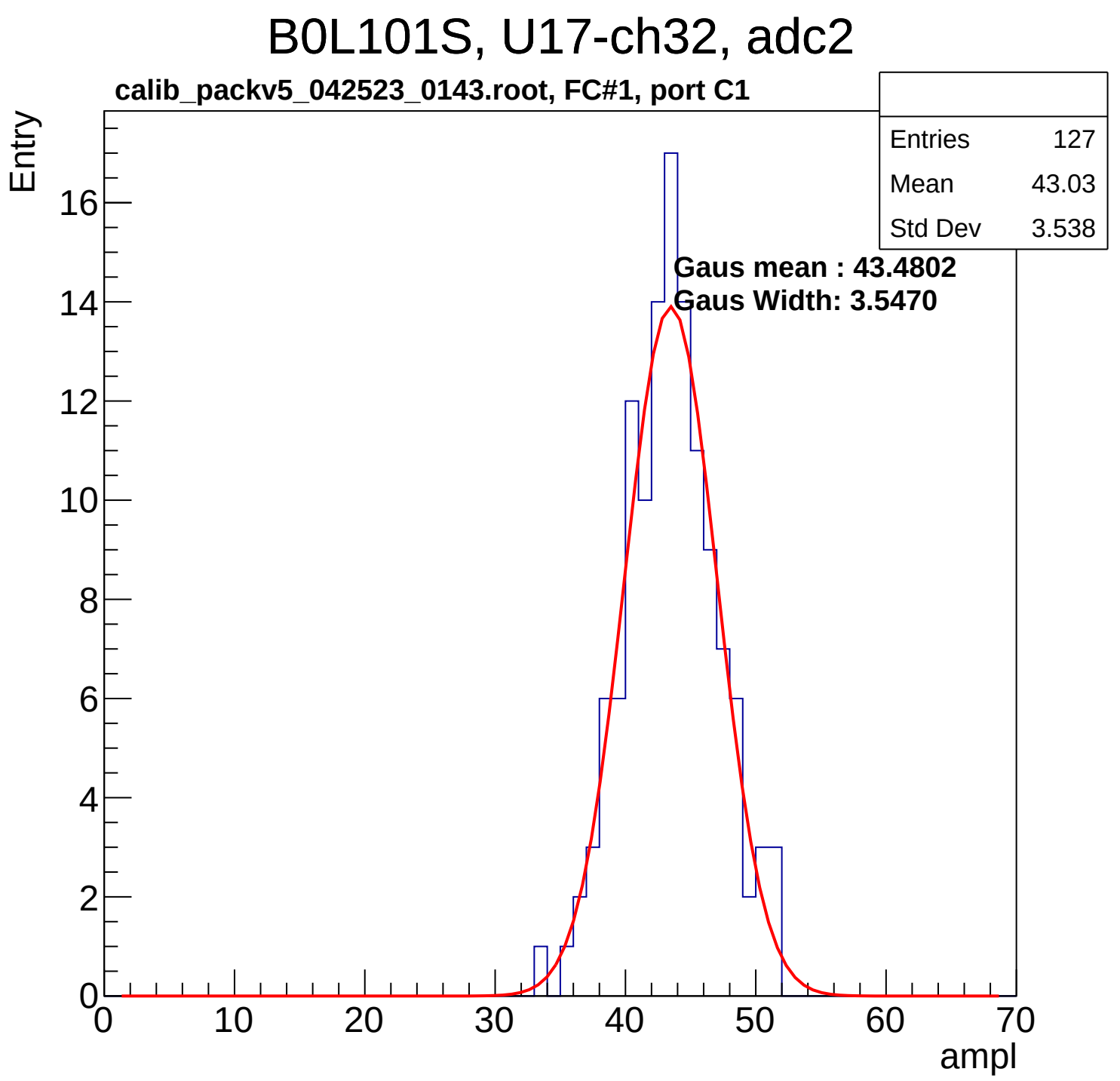

B0L101S, U17-ch31, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 83
Mean 58.87
Std Dev 9.527
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

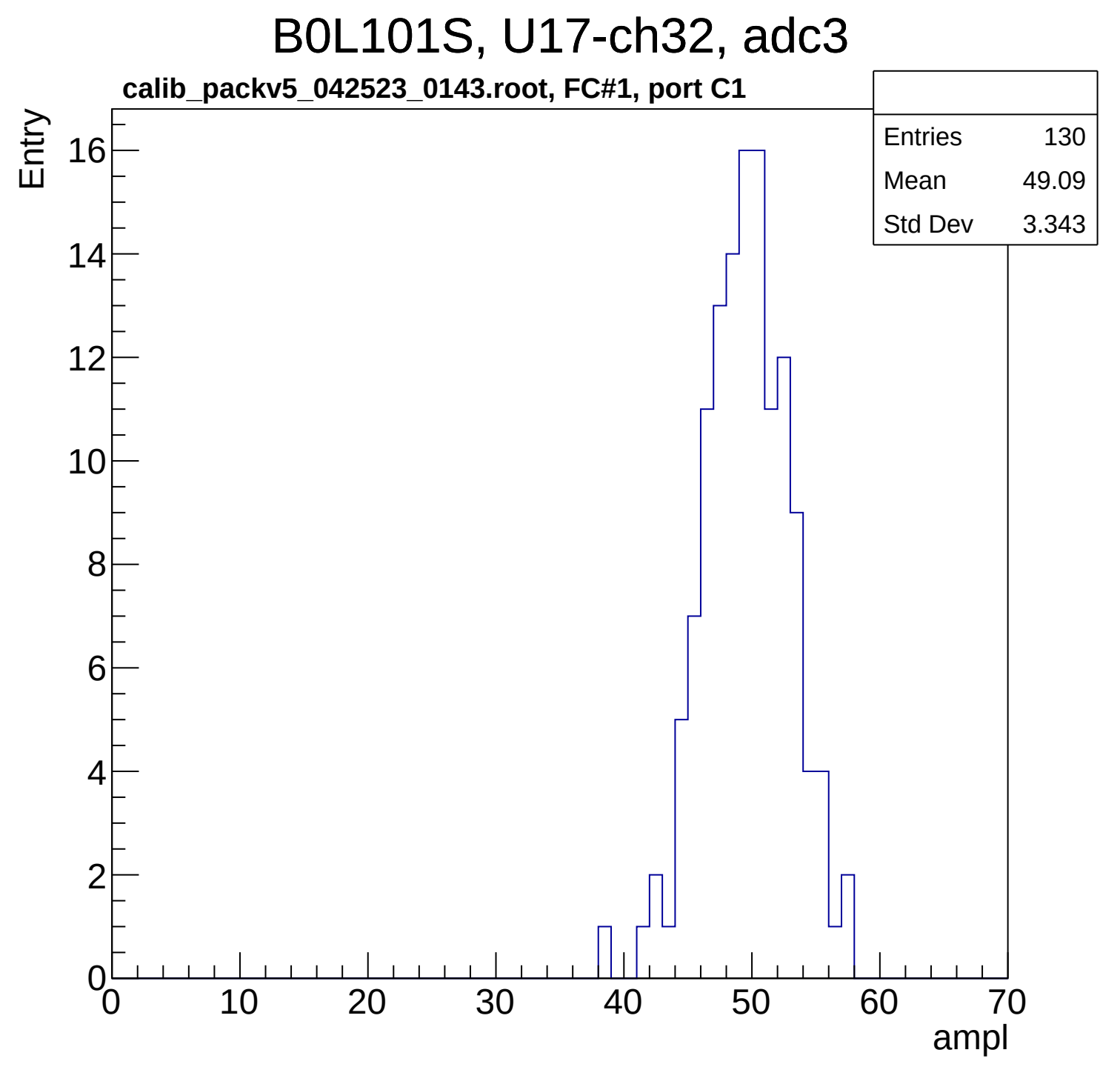

B0L101S, U17-ch31, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

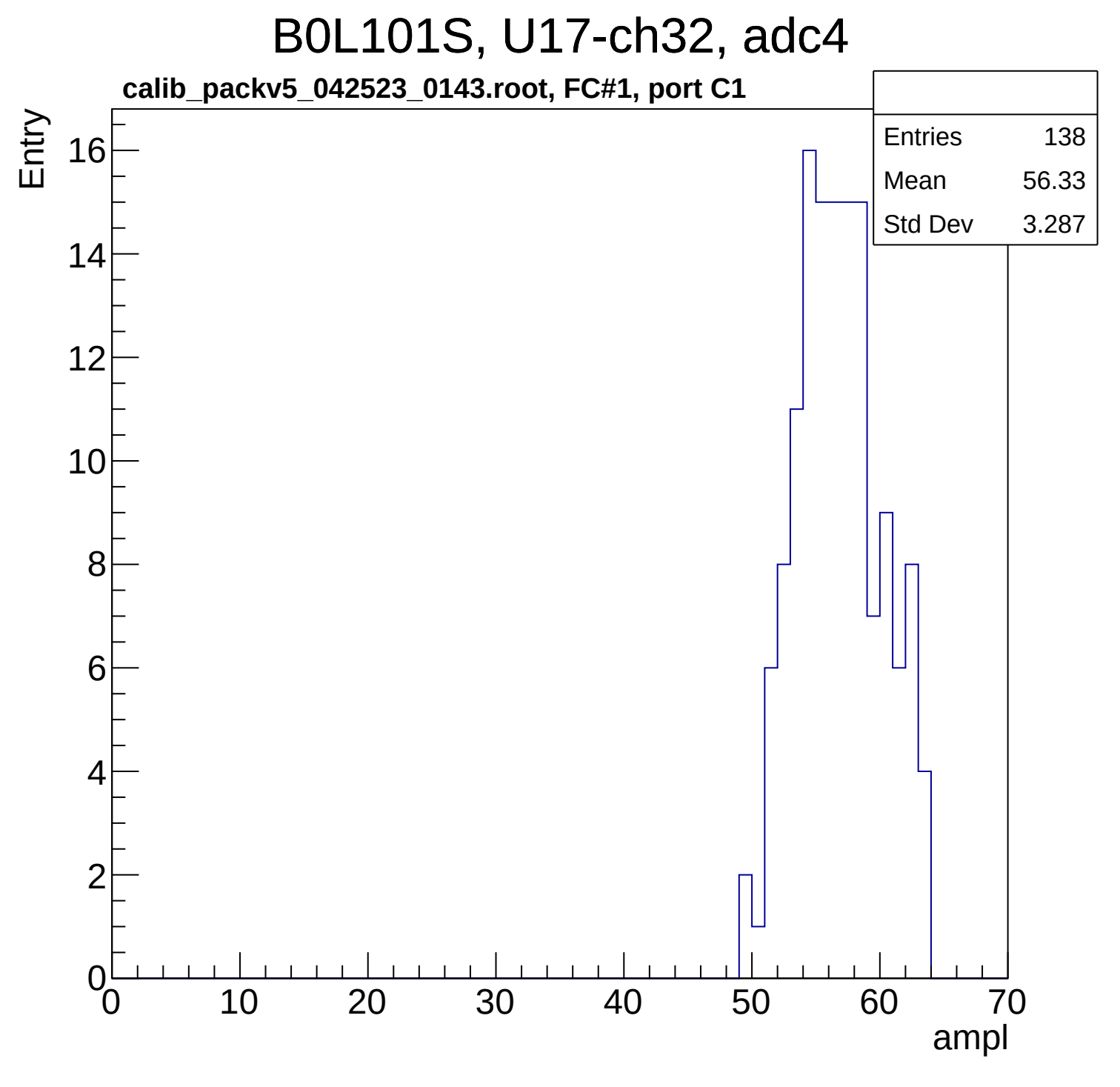

B0L101S, U17-ch31, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

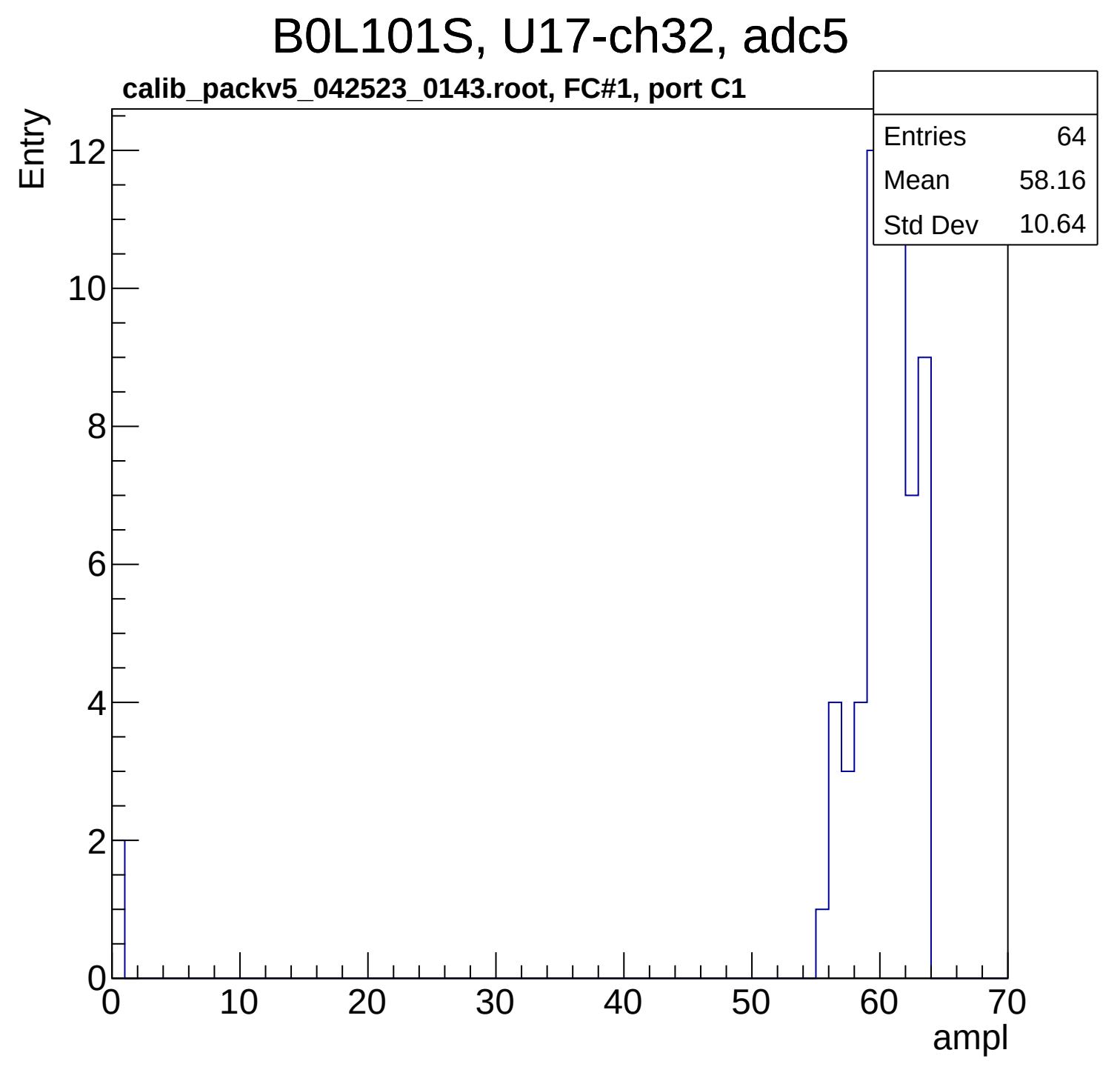

B0L101S, U17-ch32, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 28.21
Std Dev 3.375
Gaus mean : 28.5285
Gaus Width: 3.6578
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

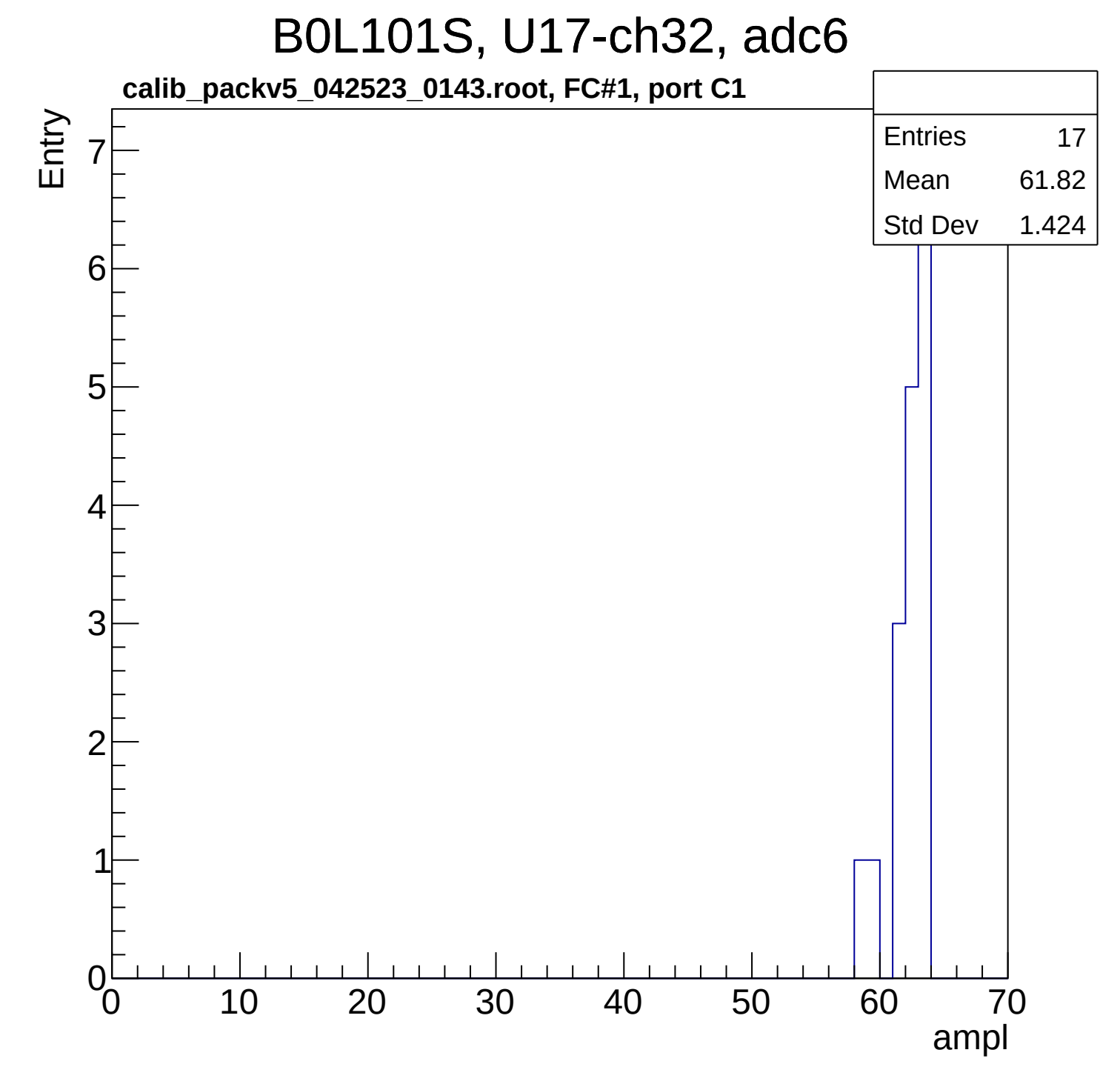

B0L101S, U17-ch32, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 169
Mean 35.6
Std Dev 3.813
Gaus mean : 36.2317
Gaus Width: 3.7327
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

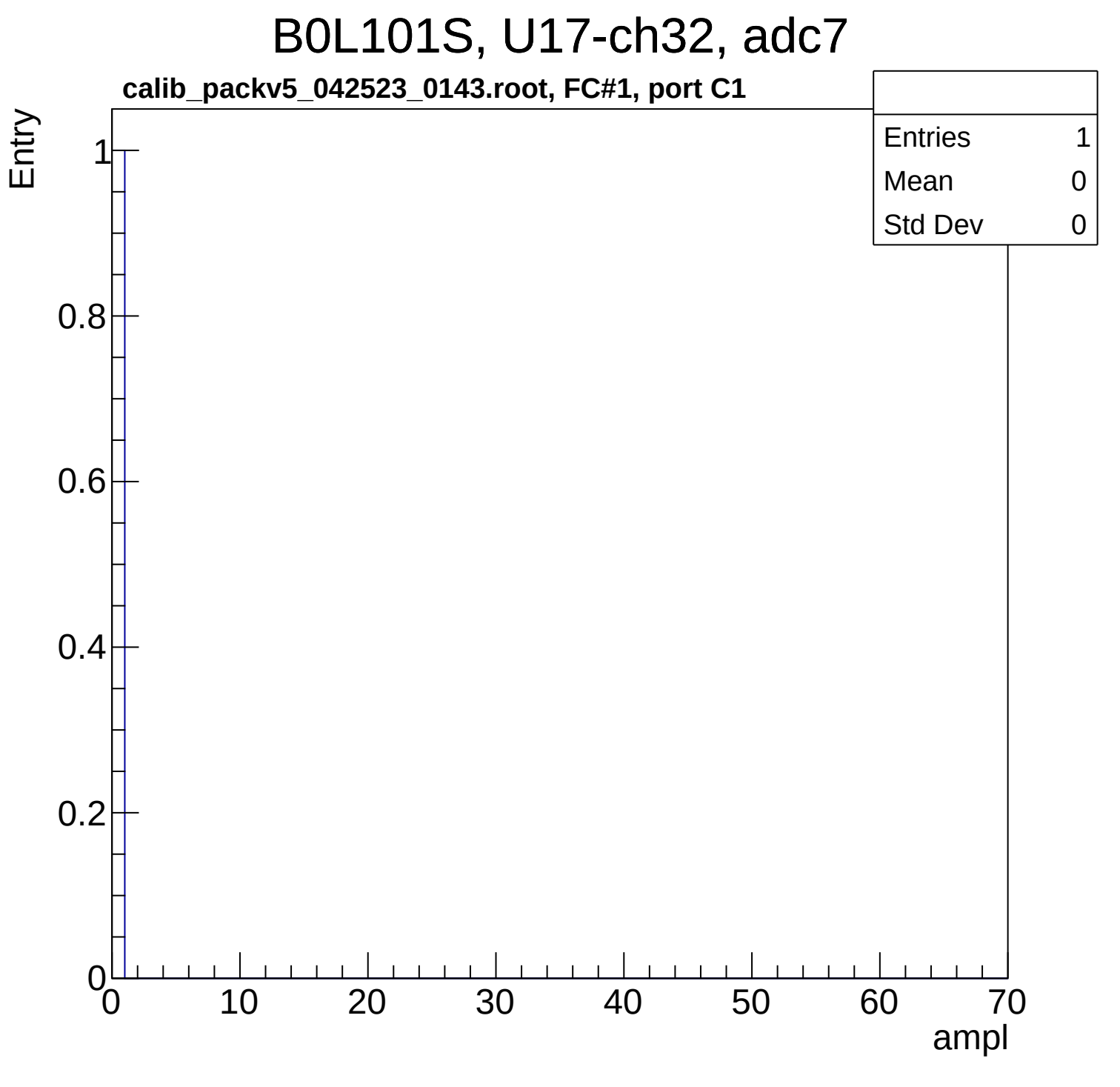

B0L101S, U17-ch32, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 43.03
Std Dev 3.538
Gaus mean : 43.4802
Gaus Width: 3.5470
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

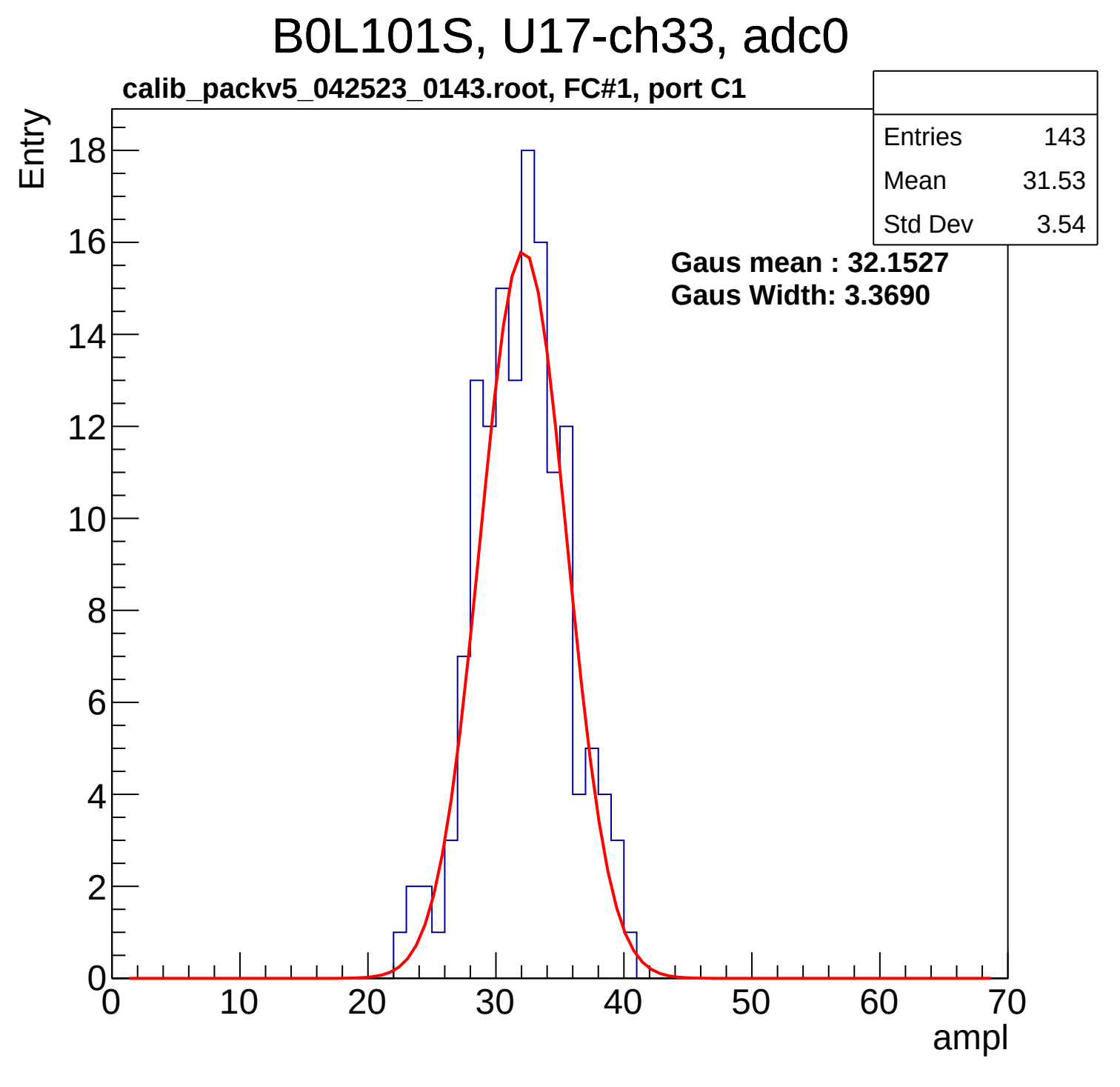

B0L101S, U17-ch32, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 49.09
Std Dev 3.343
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

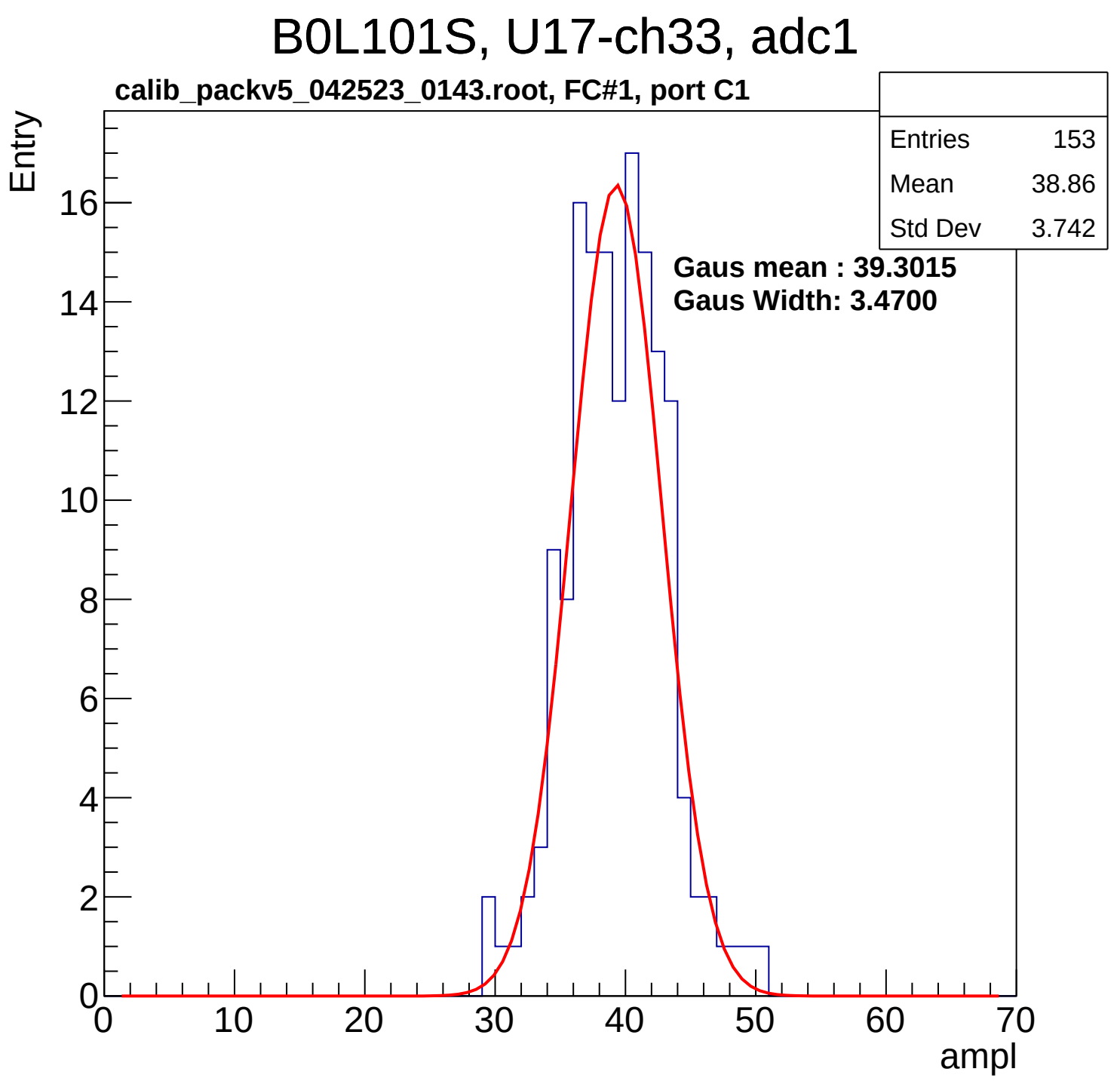

B0L101S, U17-ch32, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 56.33
Std Dev 3.287
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

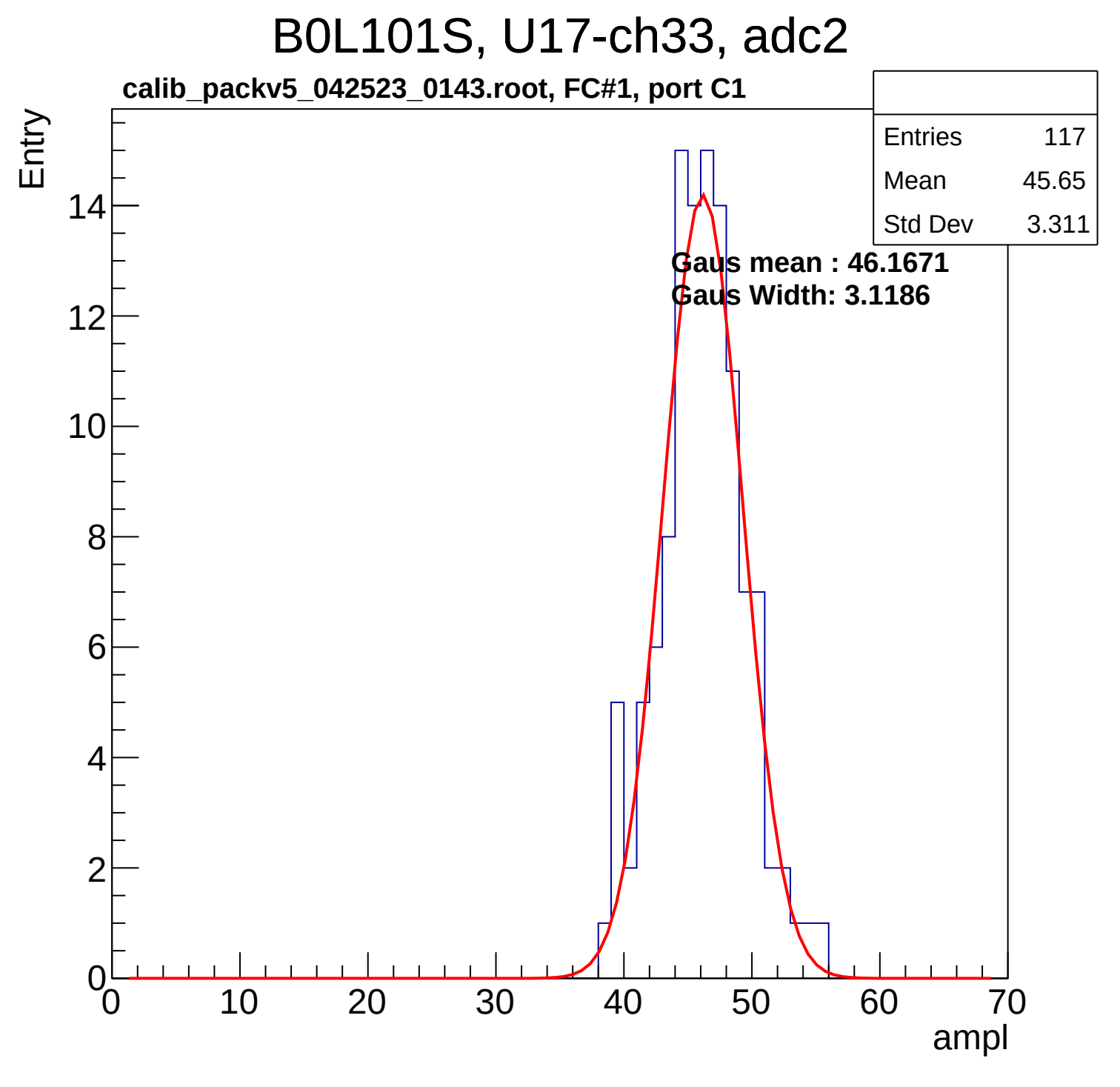

B0L101S, U17-ch32, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 64
Mean 58.16
Std Dev 10.64
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

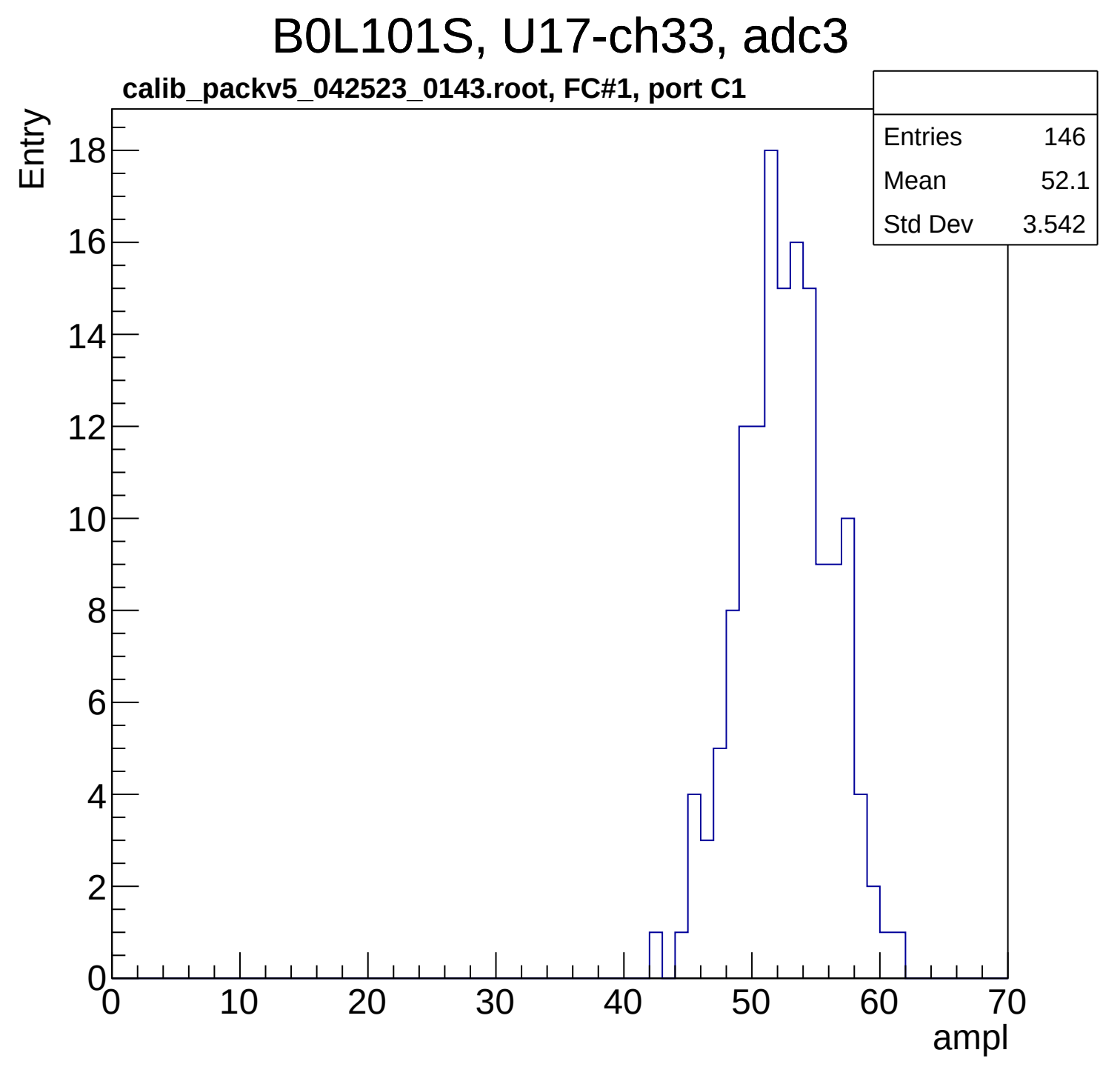

B0L101S, U17-ch32, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 17
Mean 61.82
Std Dev 1.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

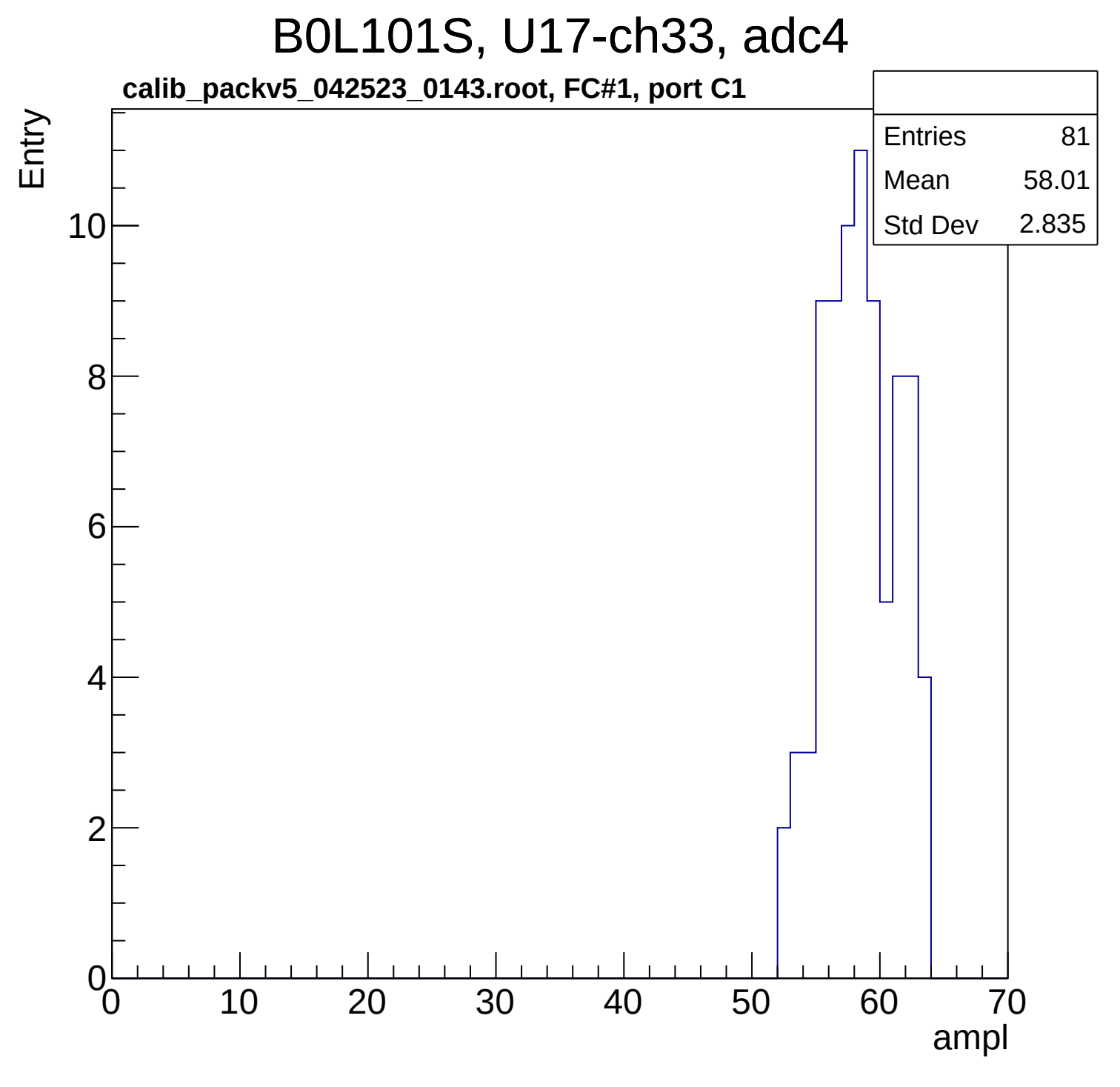

B0L101S, U17-ch32, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

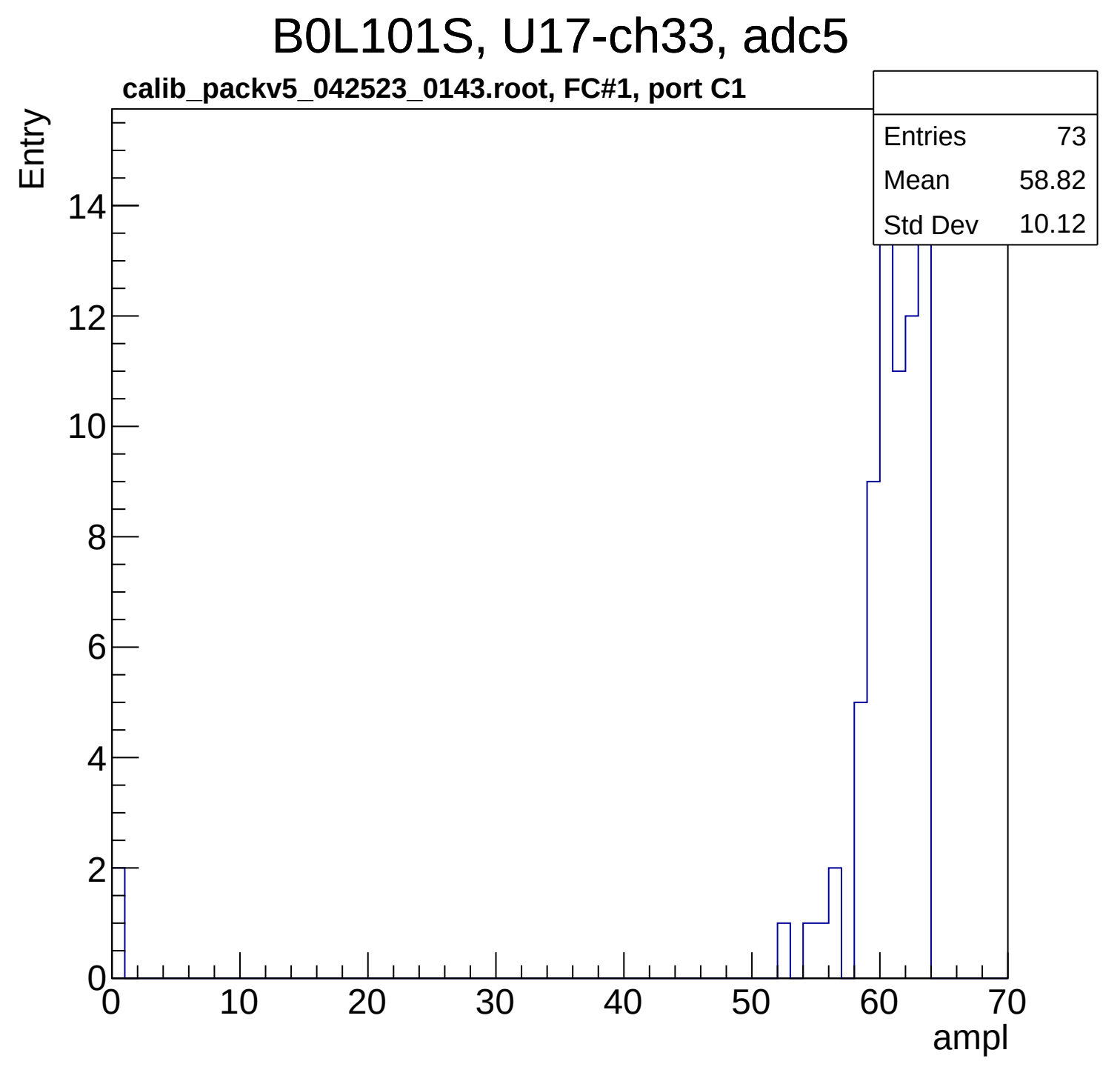

B0L101S, U17-ch33, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 31.53
Std Dev 3.54
Gaus mean : 32.1527
Gaus Width: 3.3690
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

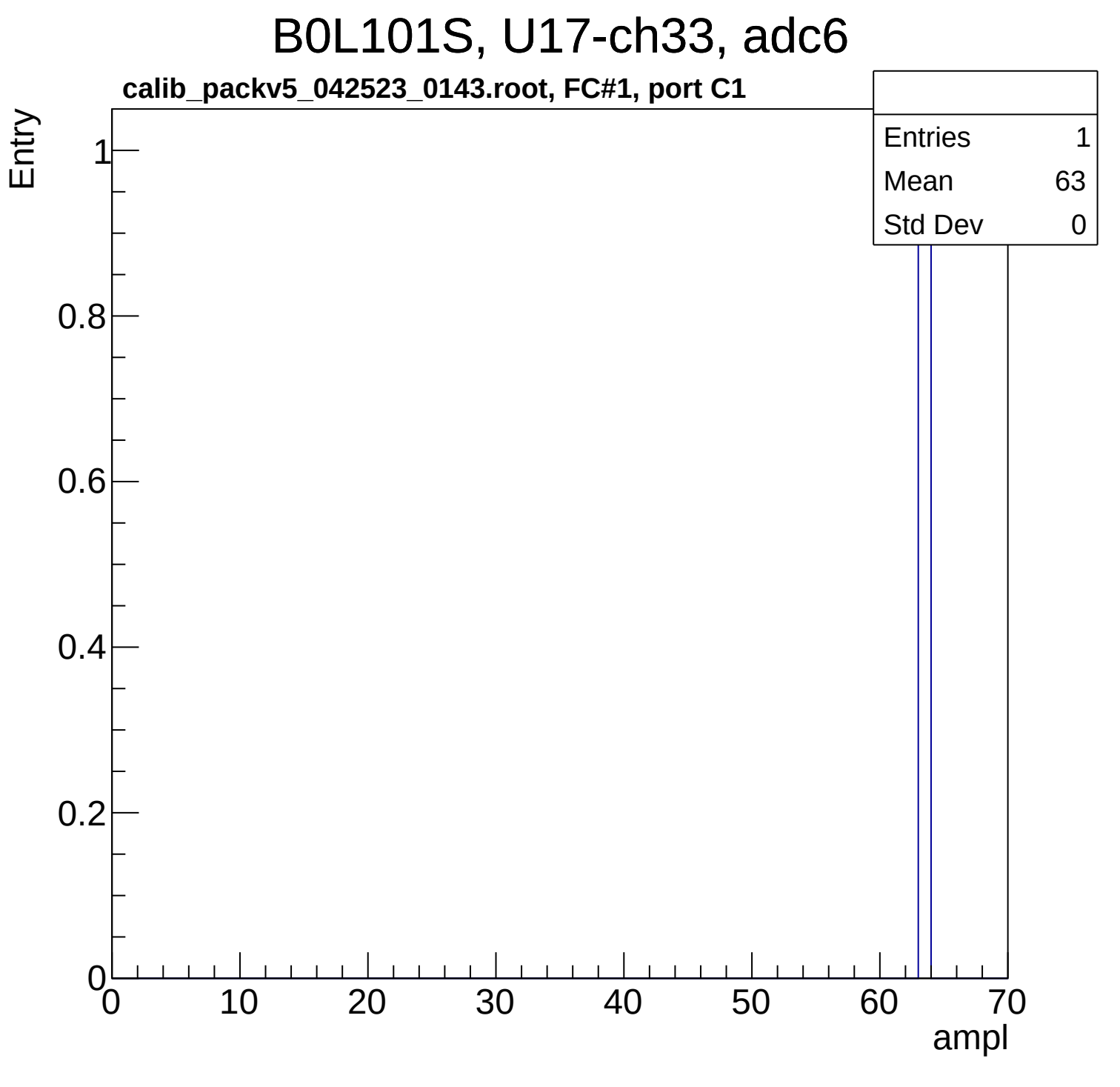

B0L101S, U17-ch33, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 38.86
Std Dev 3.742
Gaus mean : 39.3015
Gaus Width: 3.4700
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

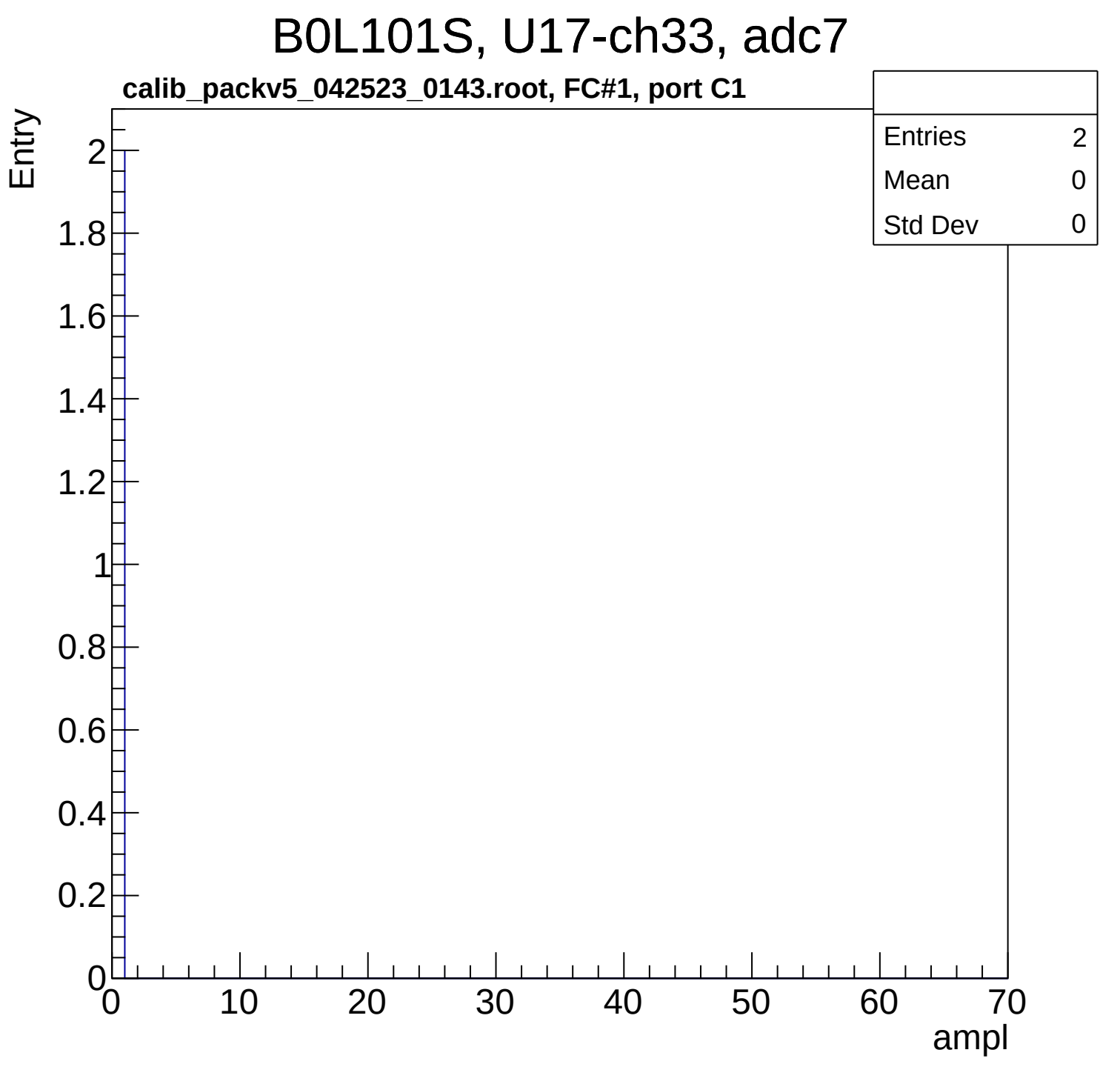

B0L101S, U17-ch33, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 45.65
Std Dev 3.311
Gaus mean : 46.1671
Gaus Width: 3.1186
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

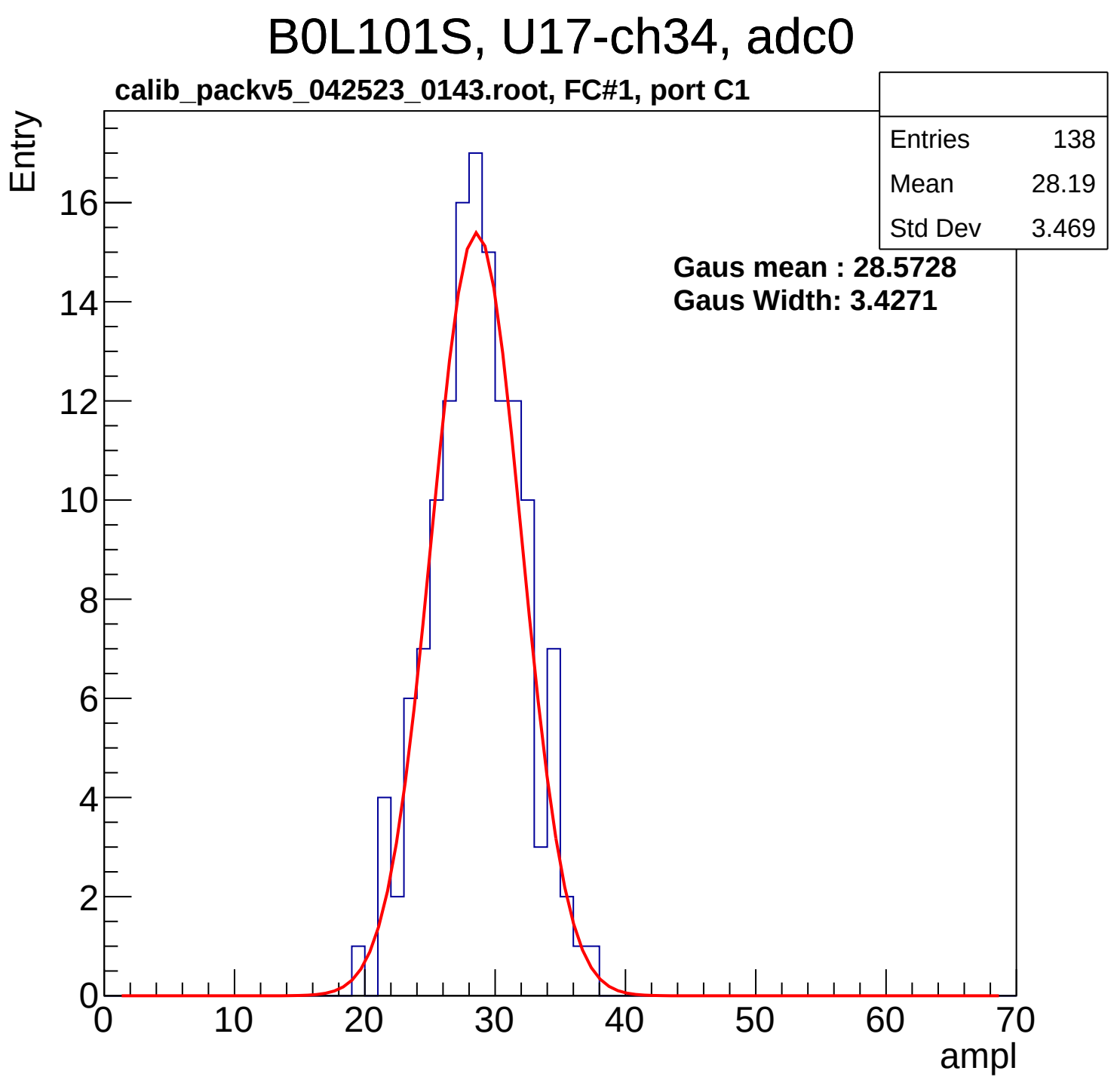

B0L101S, U17-ch33, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 52.1
Std Dev 3.542
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

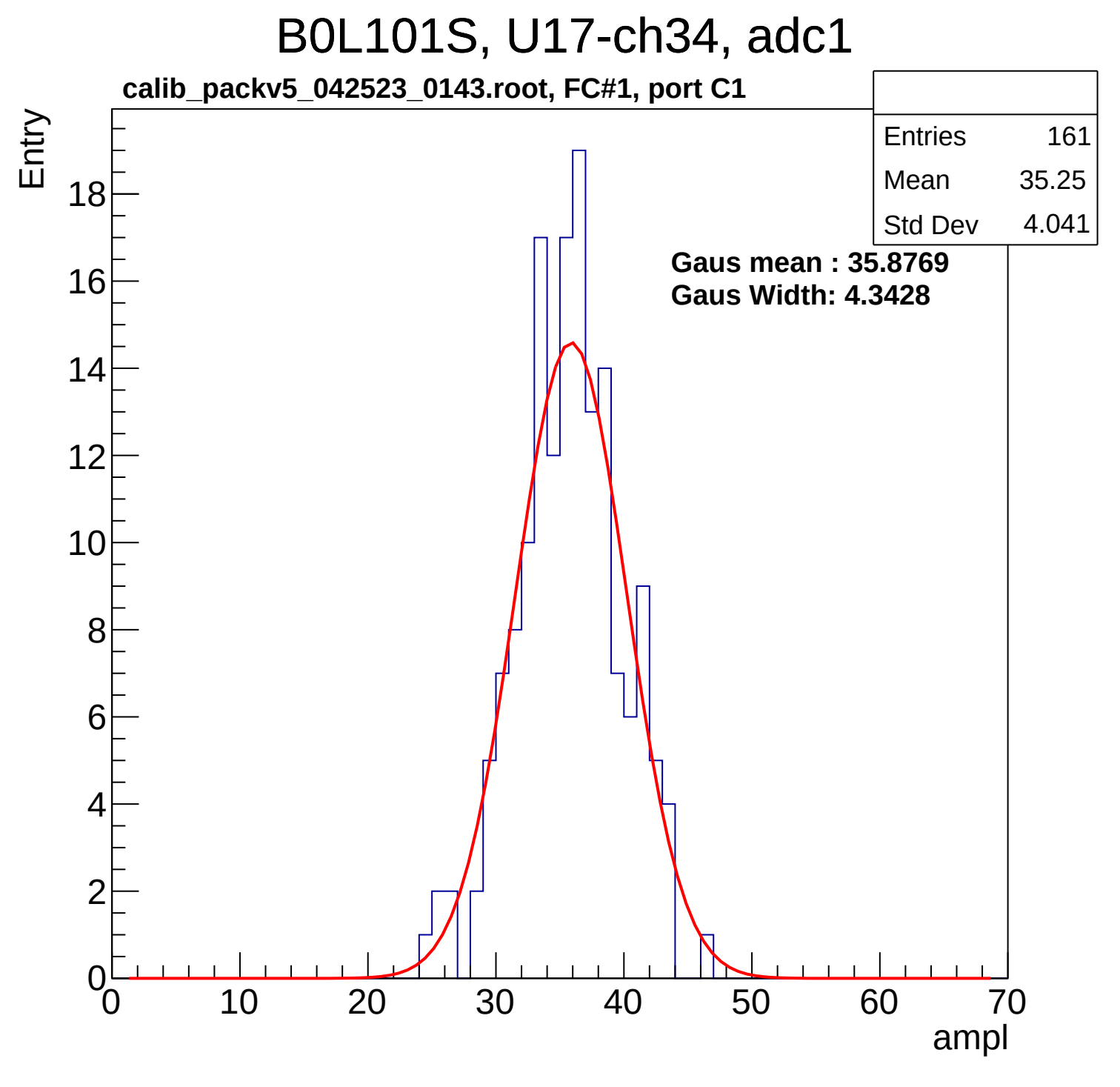

B0L101S, U17-ch33, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 58.01
Std Dev 2.835
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

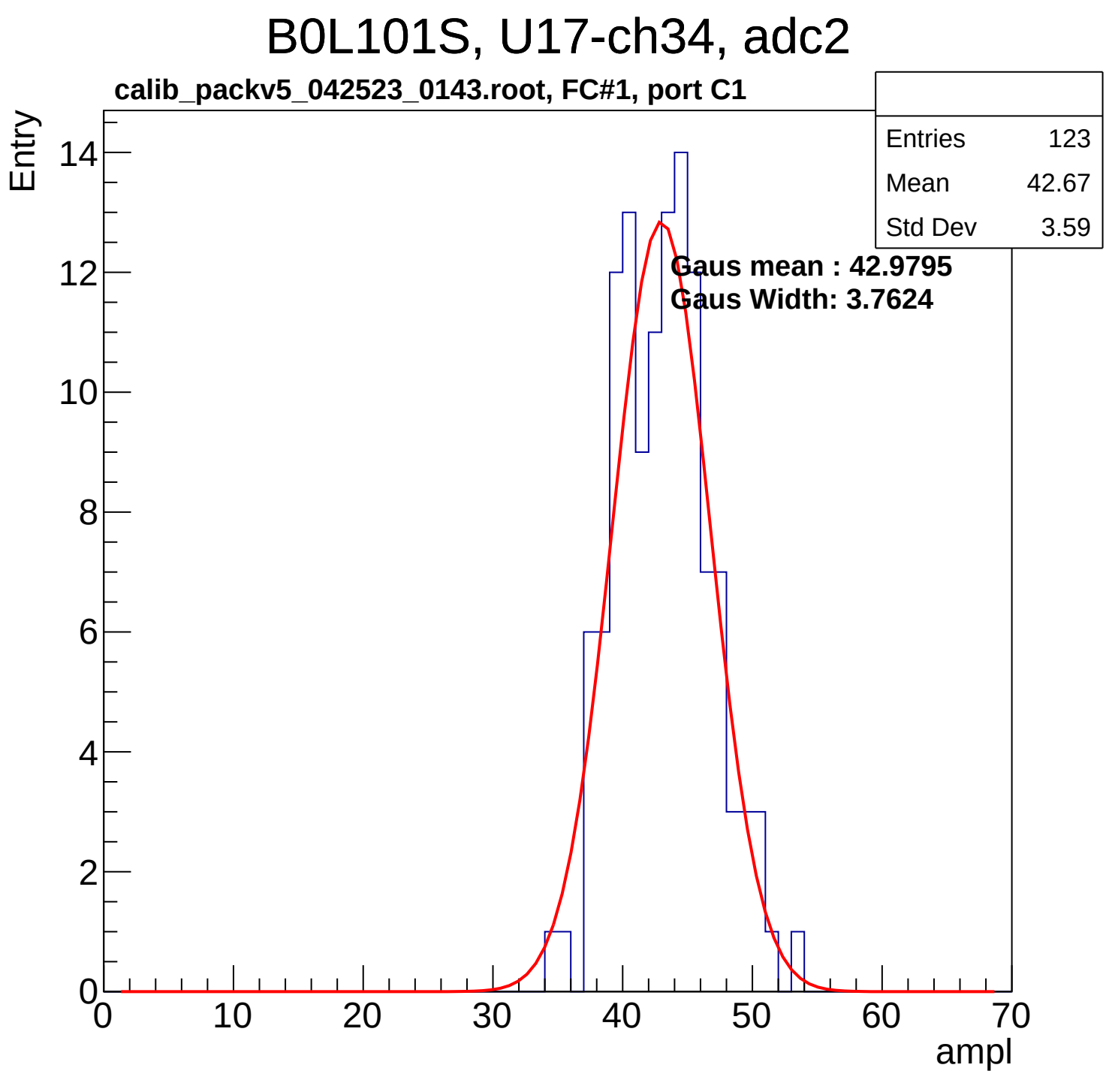

B0L101S, U17-ch33, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 73
Mean 58.82
Std Dev 10.12
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

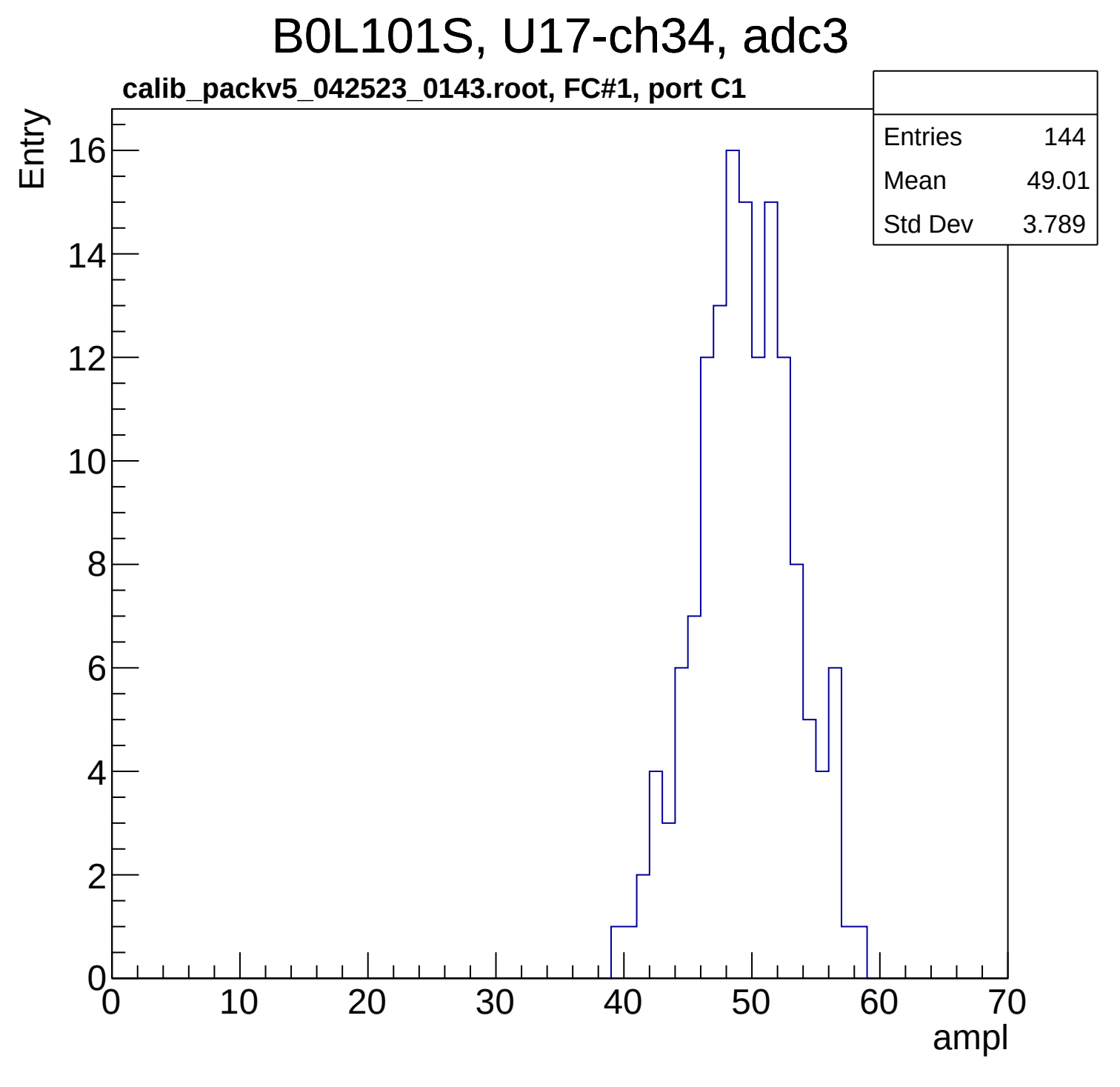

B0L101S, U17-ch33, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

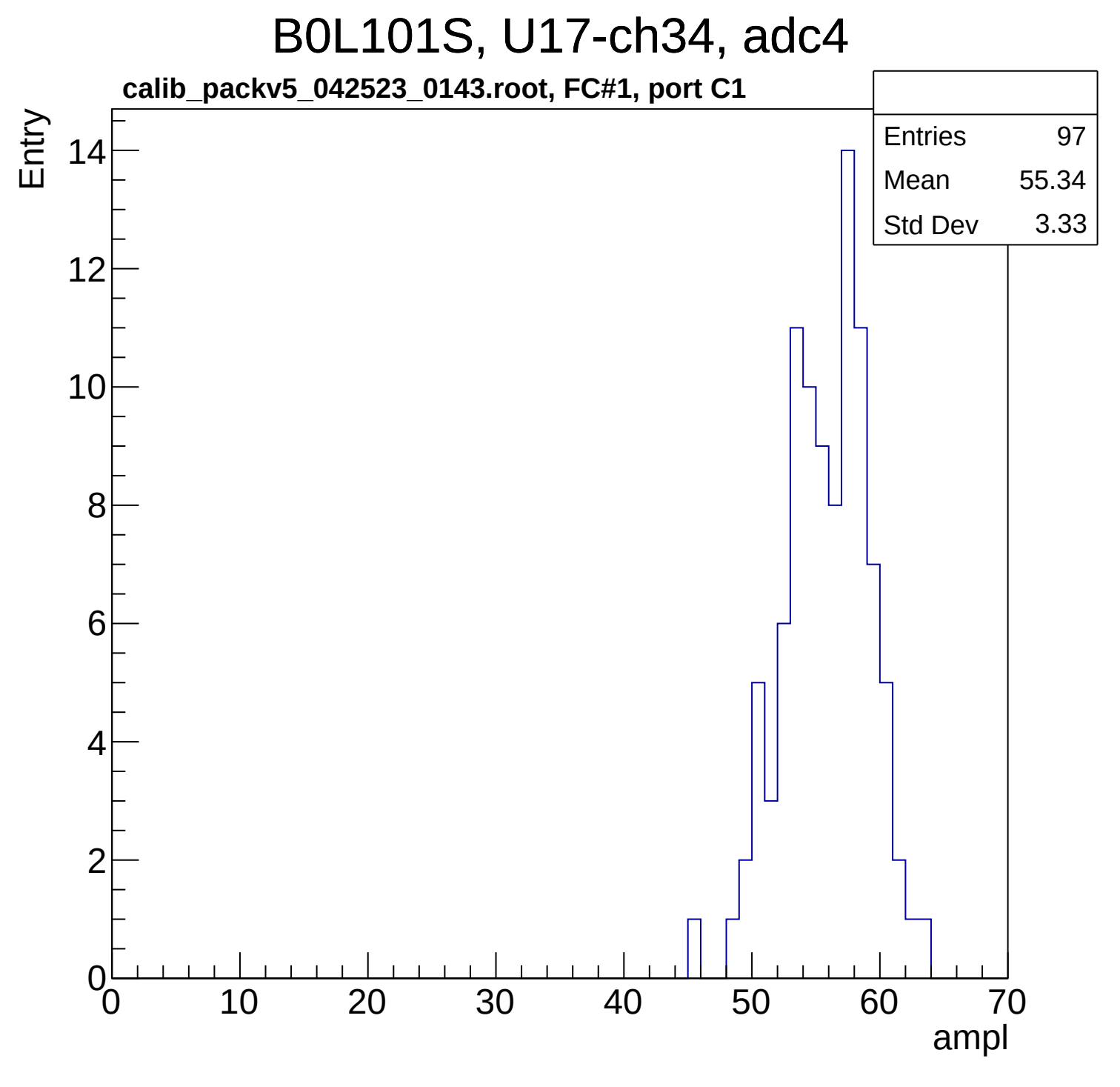

B0L101S, U17-ch33, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

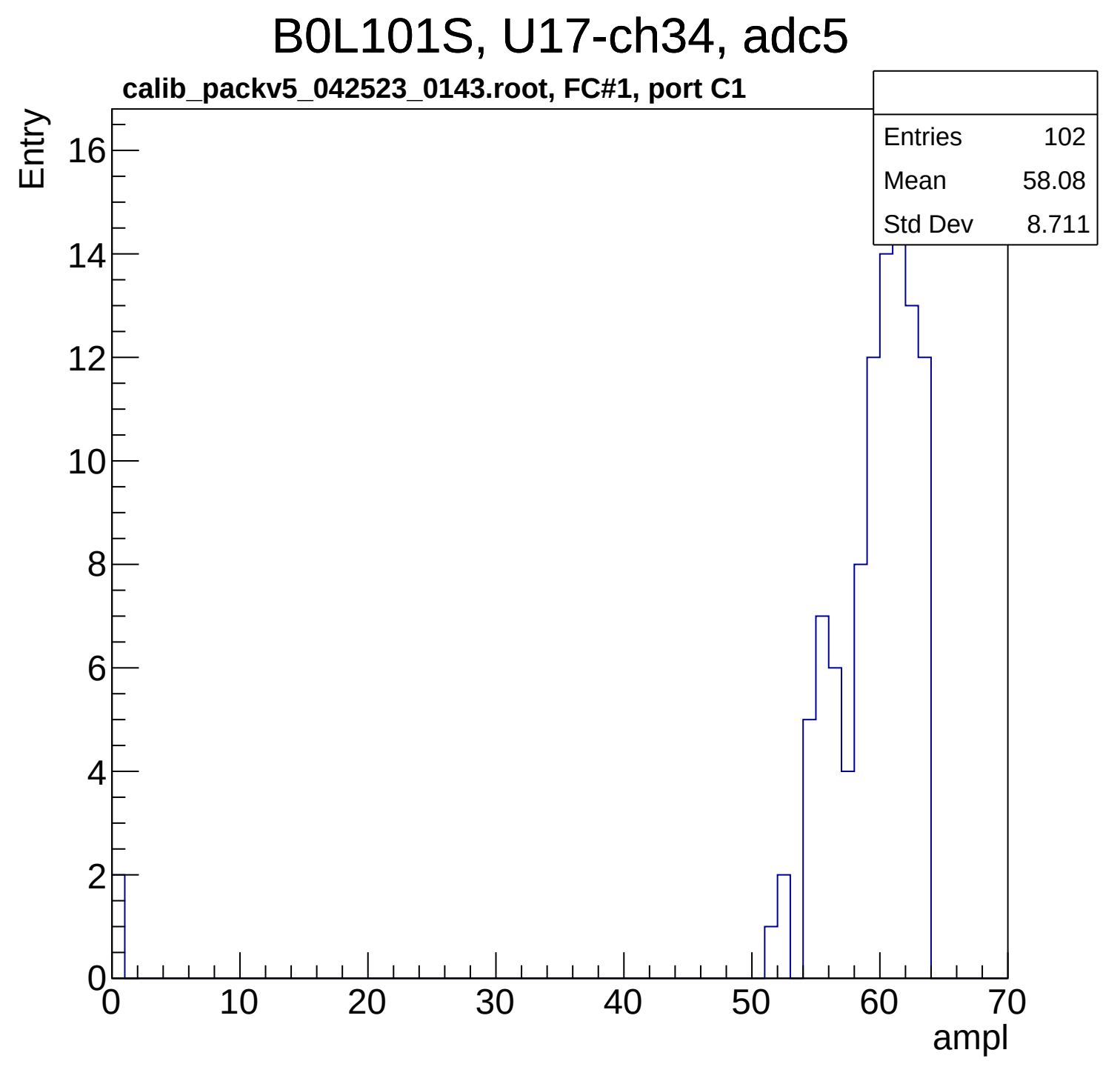

B0L101S, U17-ch34, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 28.19
Std Dev 3.469
Gaus mean : 28.5728
Gaus Width: 3.4271
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

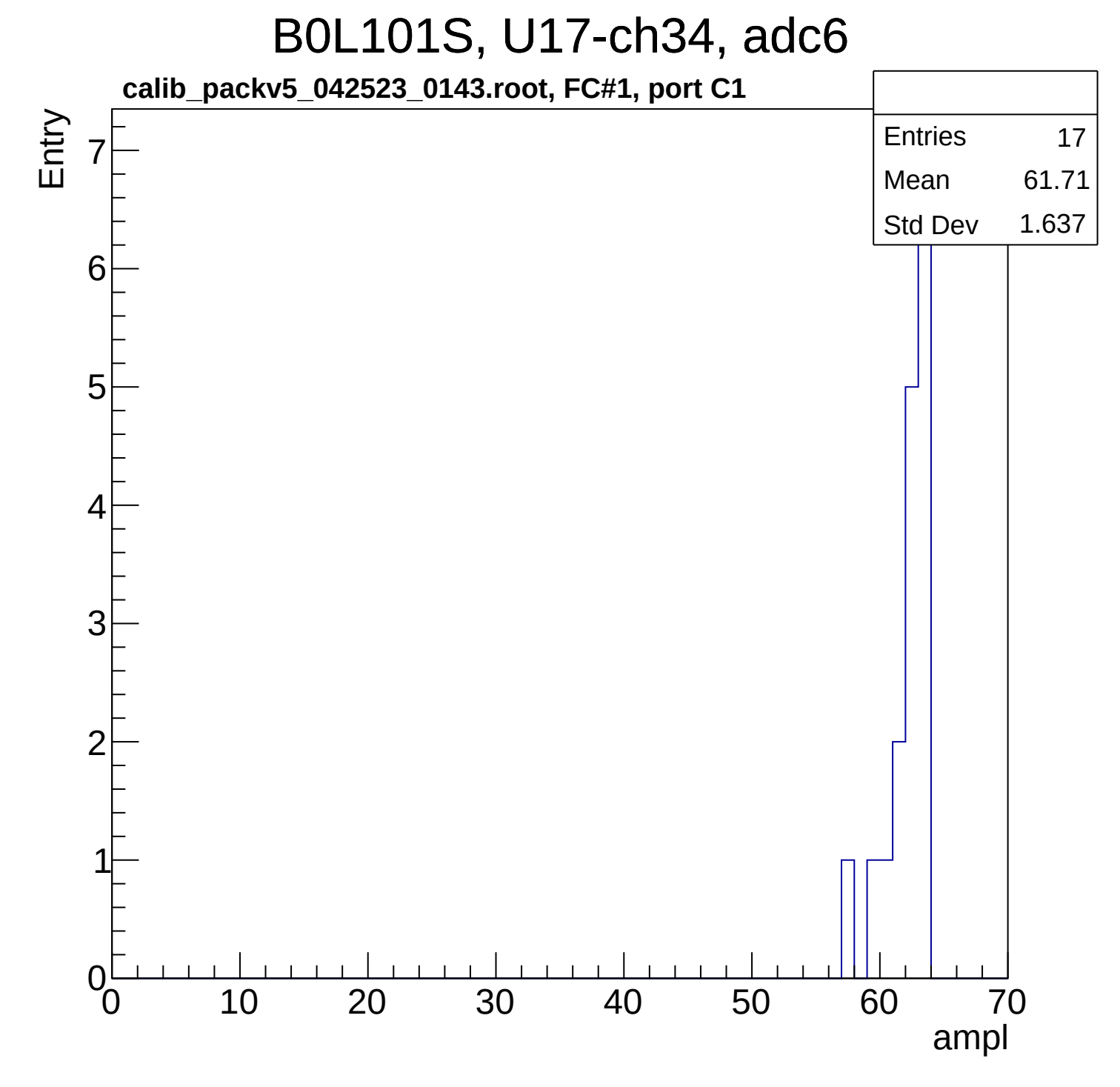

B0L101S, U17-ch34, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 161
Mean 35.25
Std Dev 4.041
Gaus mean : 35.8769
Gaus Width: 4.3428
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

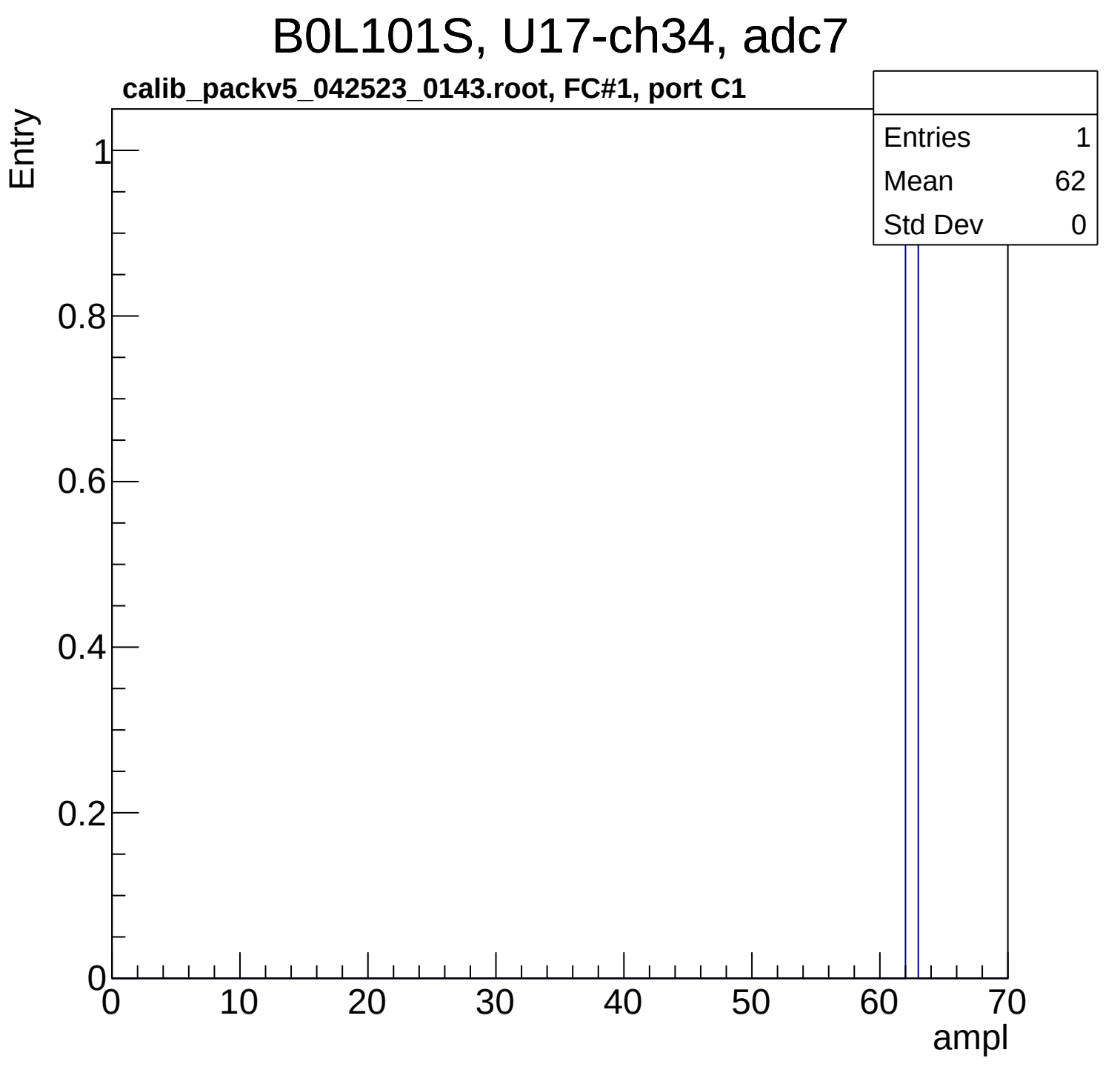

B0L101S, U17-ch34, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 42.67
Std Dev 3.59
Gaus mean : 42.9795
Gaus Width: 3.7624
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

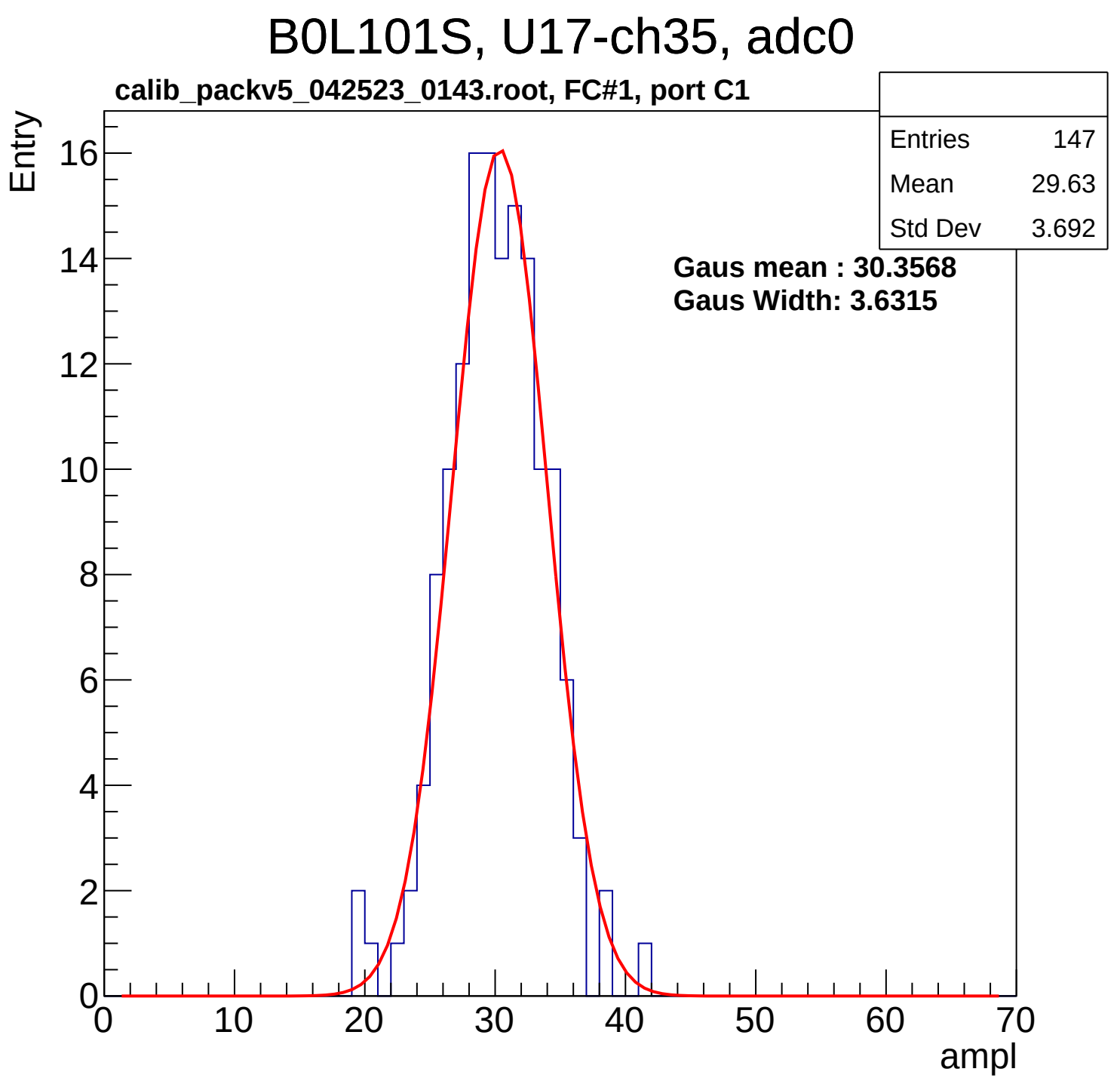

B0L101S, U17-ch34, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 49.01
Std Dev 3.789
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

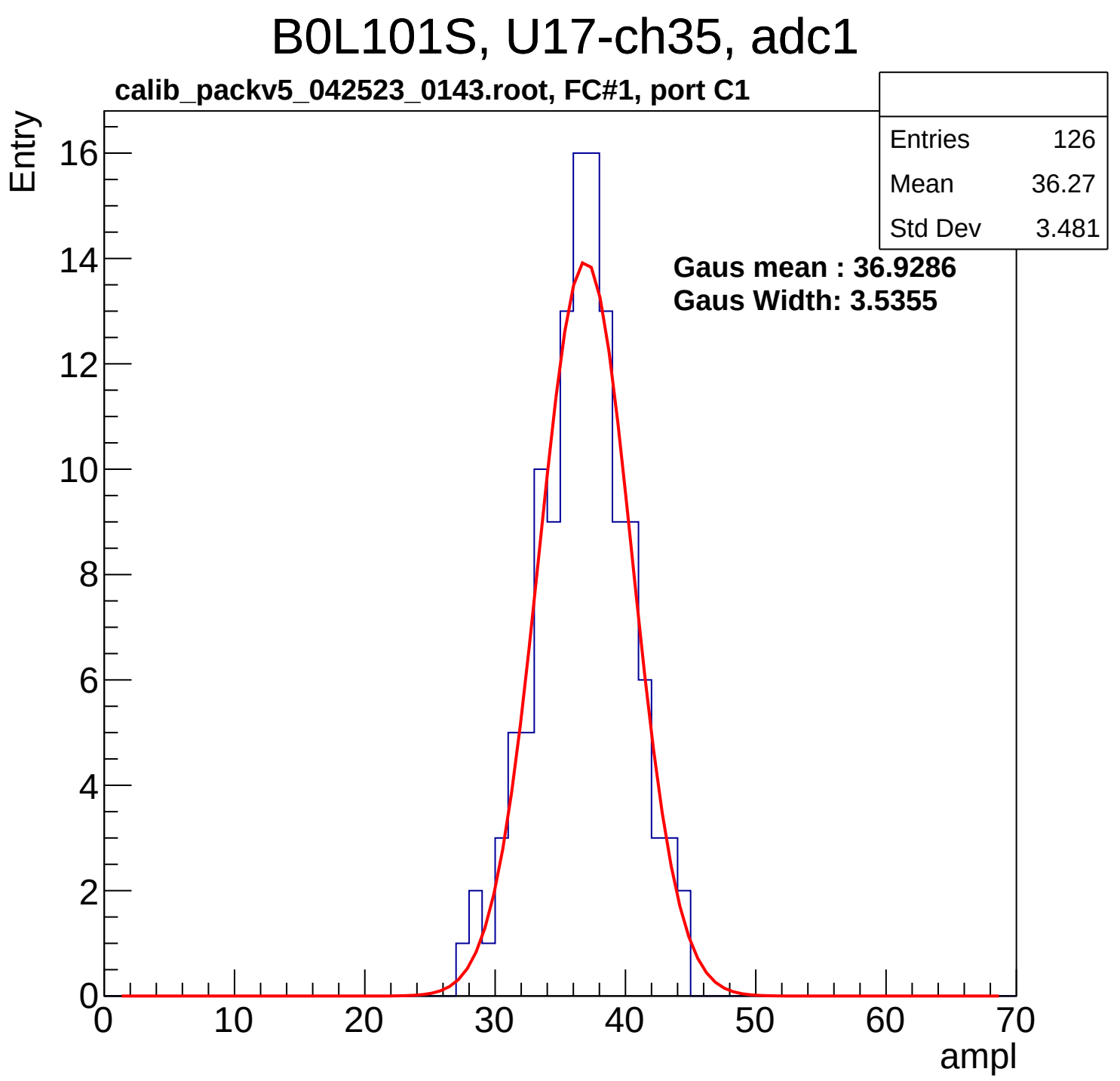

B0L101S, U17-ch34, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 97
Mean 55.34
Std Dev 3.33
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

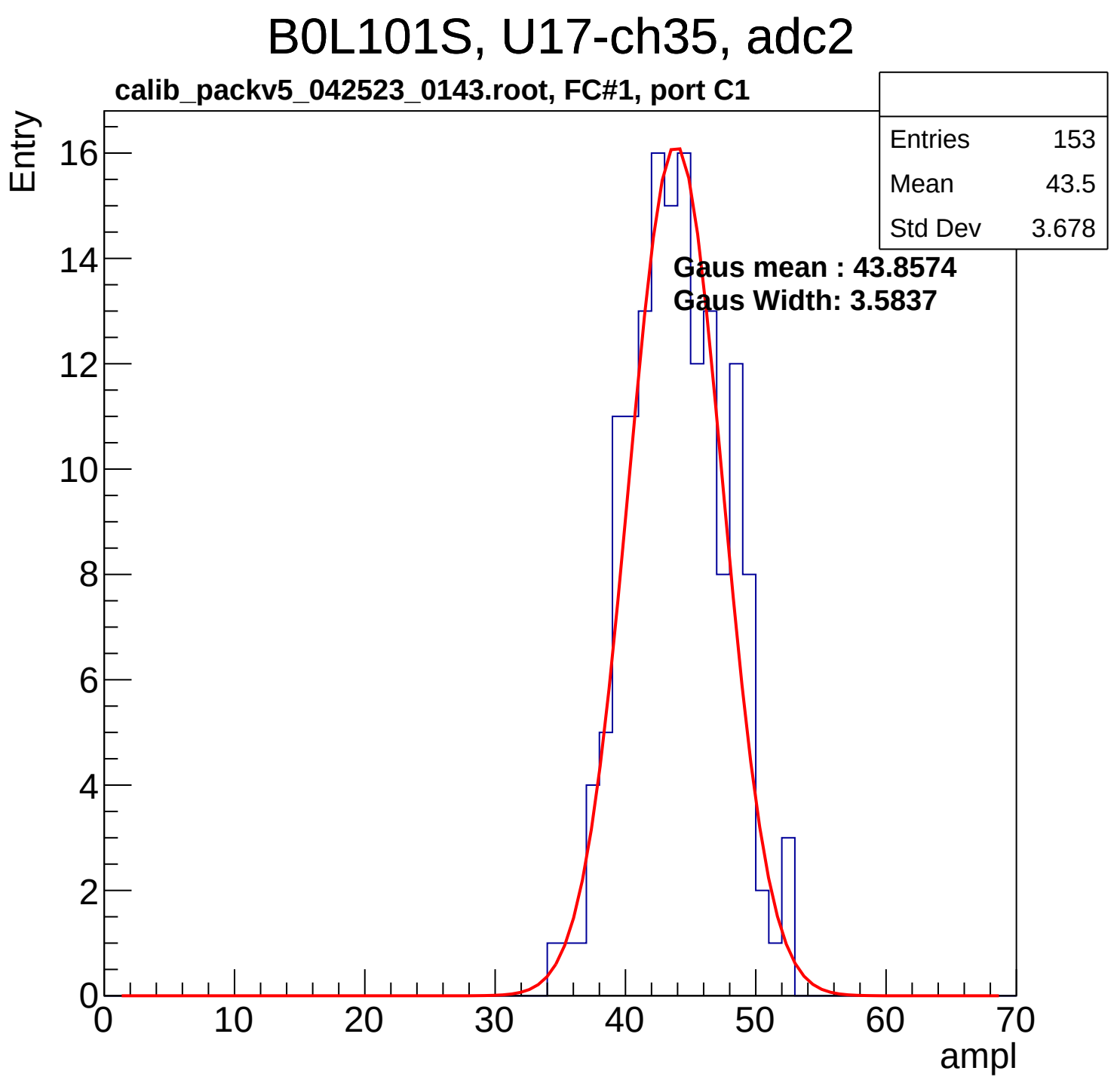

B0L101S, U17-ch34, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 58.08
Std Dev 8.711
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

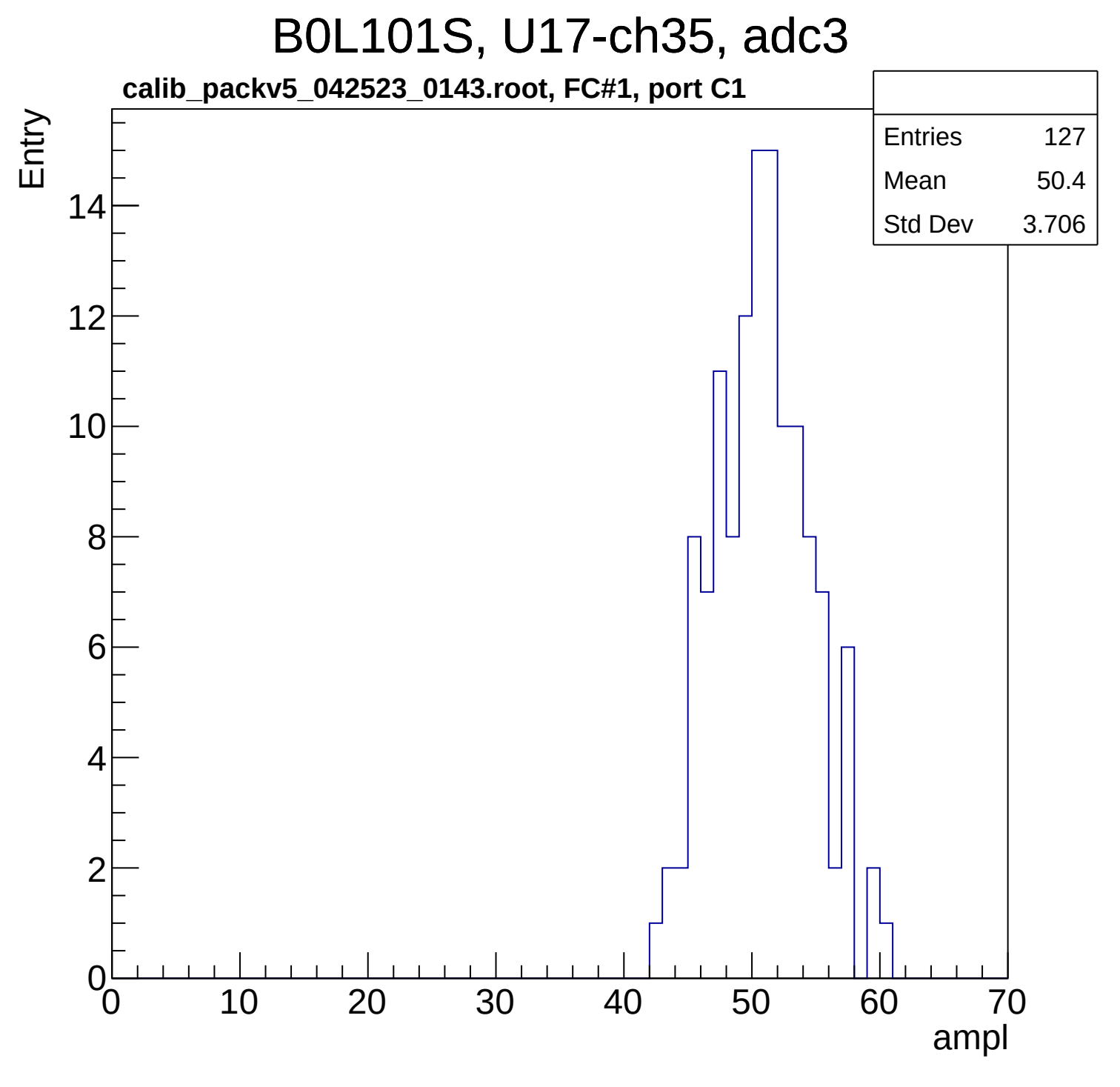

B0L101S, U17-ch34, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 17
Mean 61.71
Std Dev 1.637
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

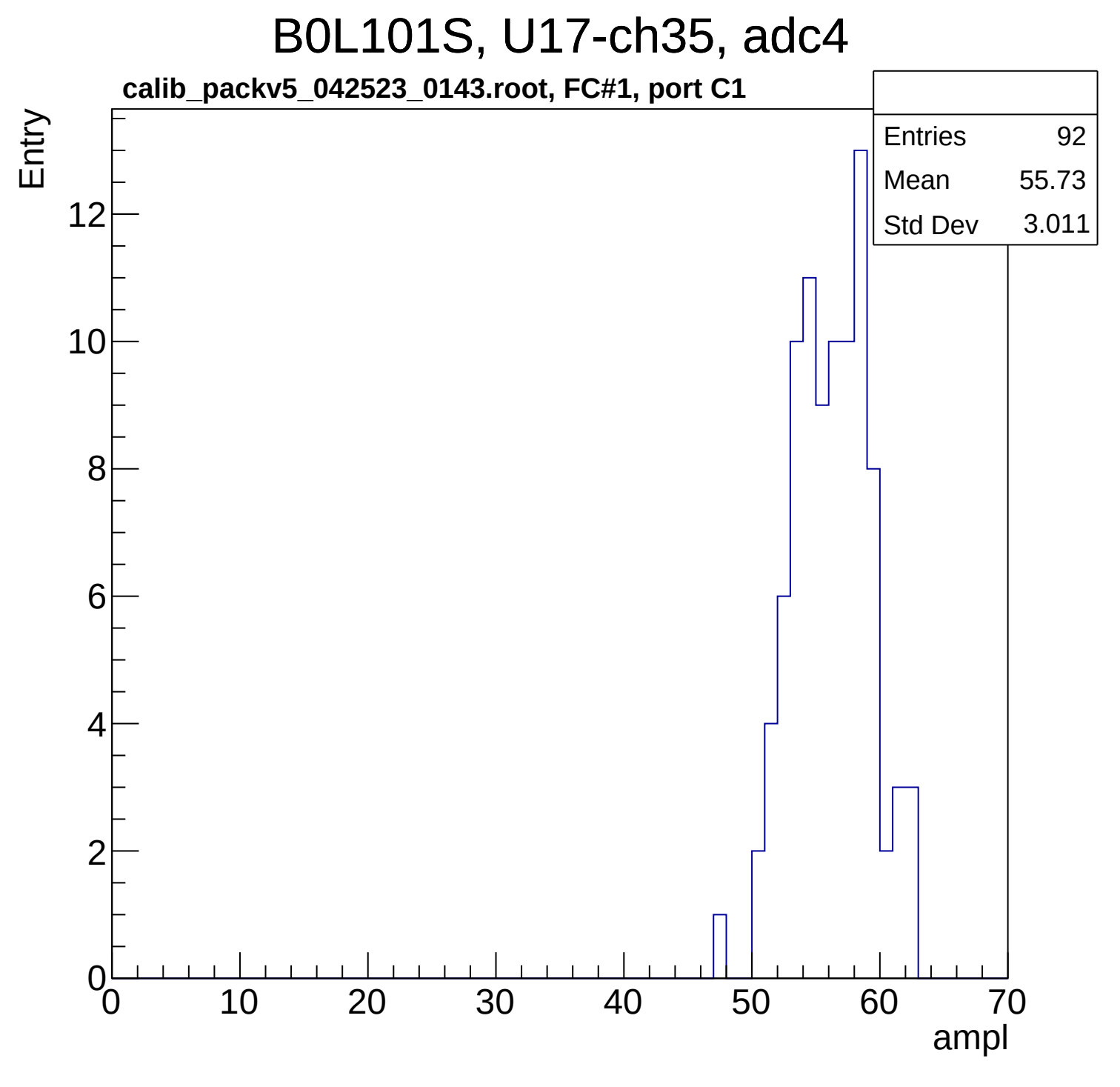

B0L101S, U17-ch34, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

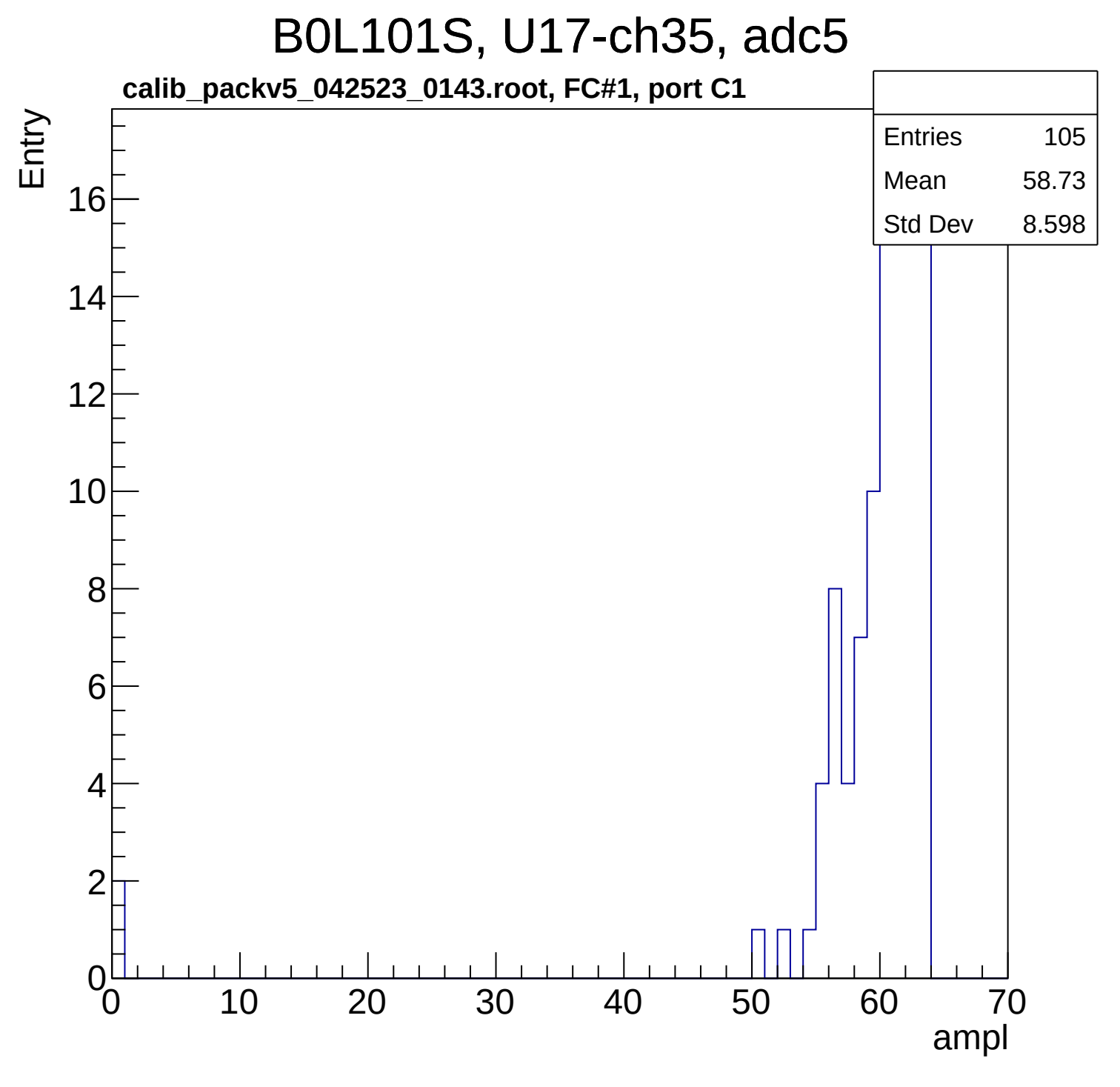

B0L101S, U17-ch35, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 29.63
Std Dev 3.692
Gaus mean : 30.3568
Gaus Width: 3.6315
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

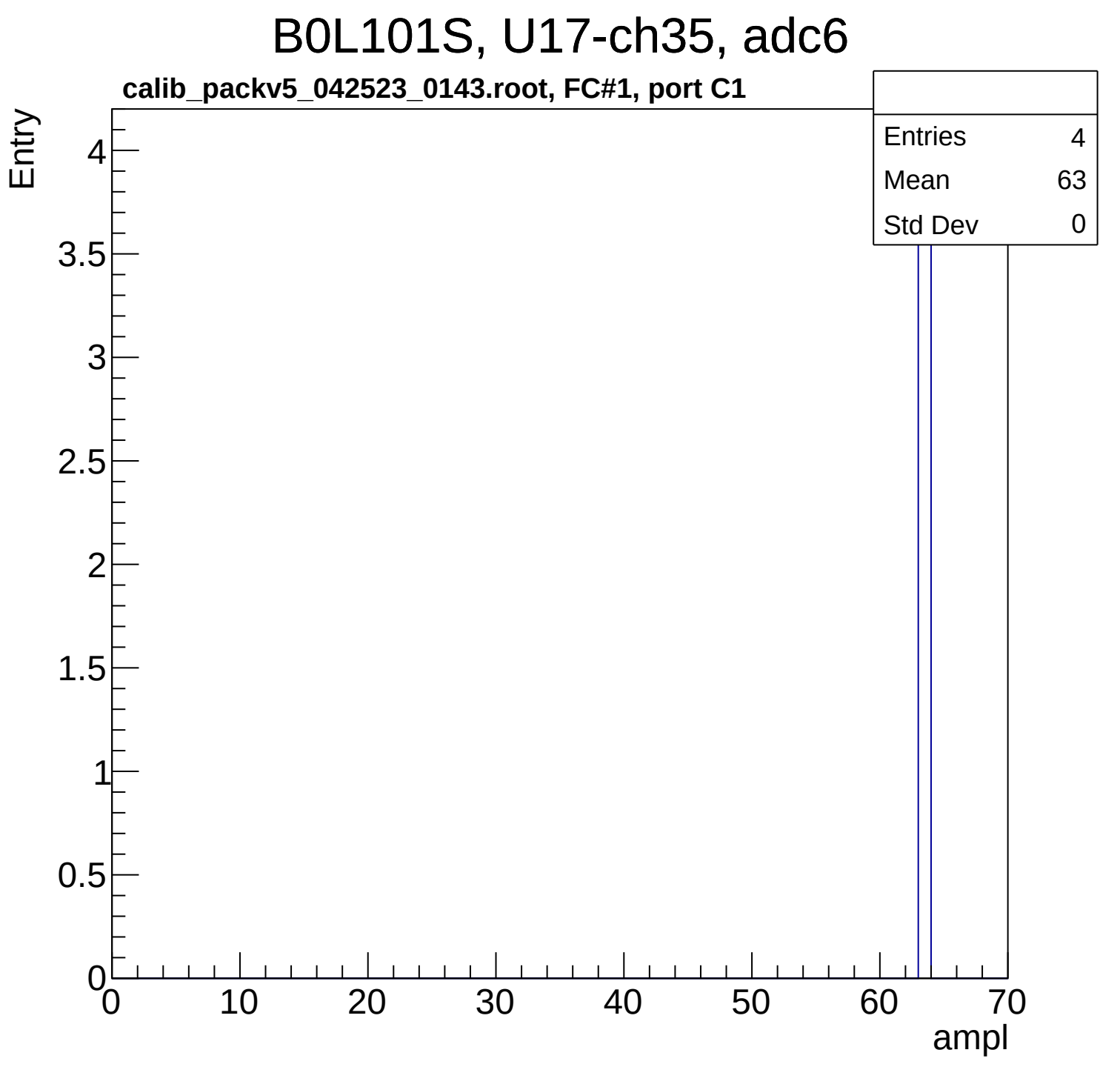

B0L101S, U17-ch35, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 36.27
Std Dev 3.481
Gaus mean : 36.9286
Gaus Width: 3.5355
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

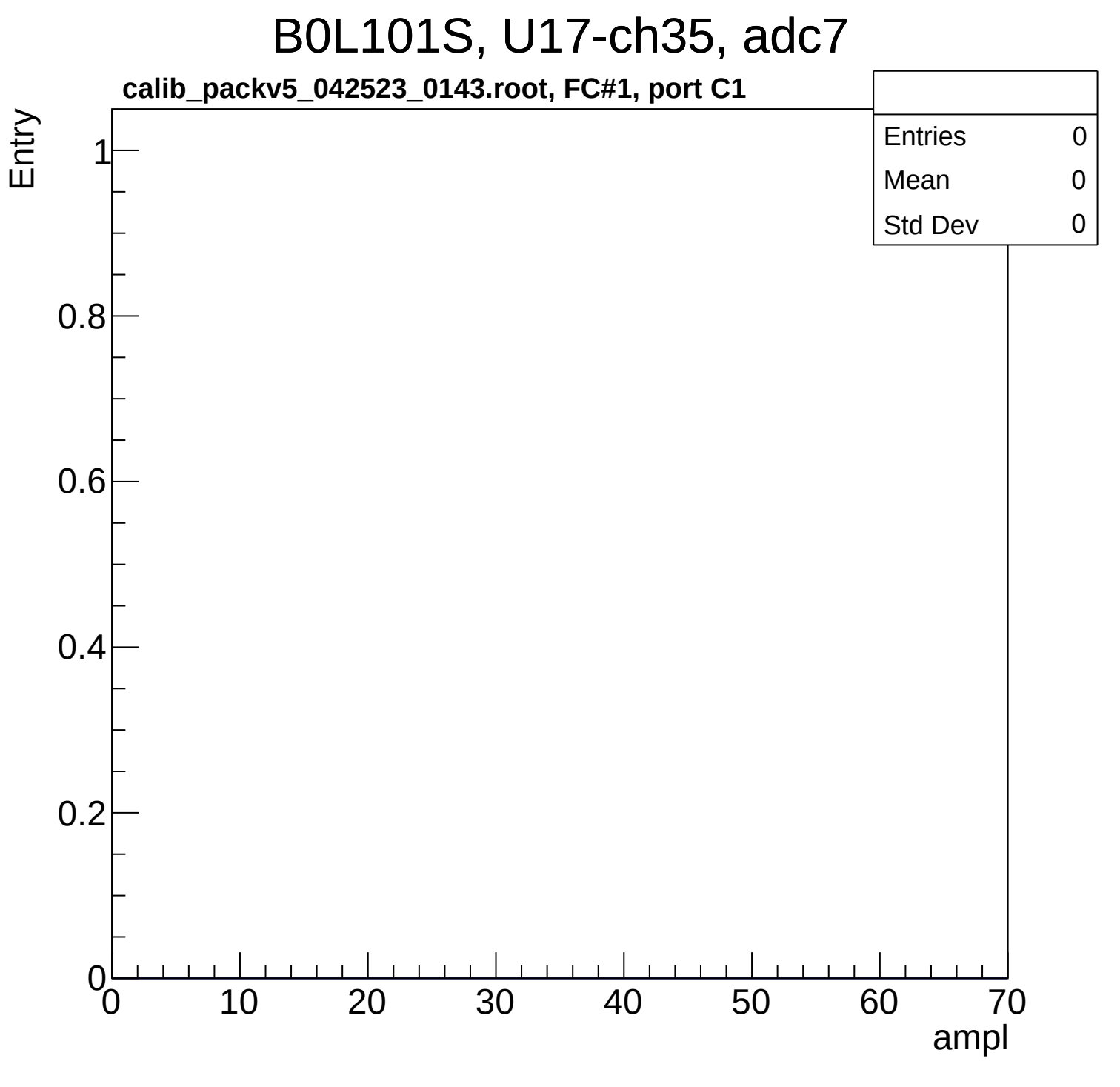

B0L101S, U17-ch35, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 43.5
Std Dev 3.678
Gaus mean : 43.8574
Gaus Width: 3.5837
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

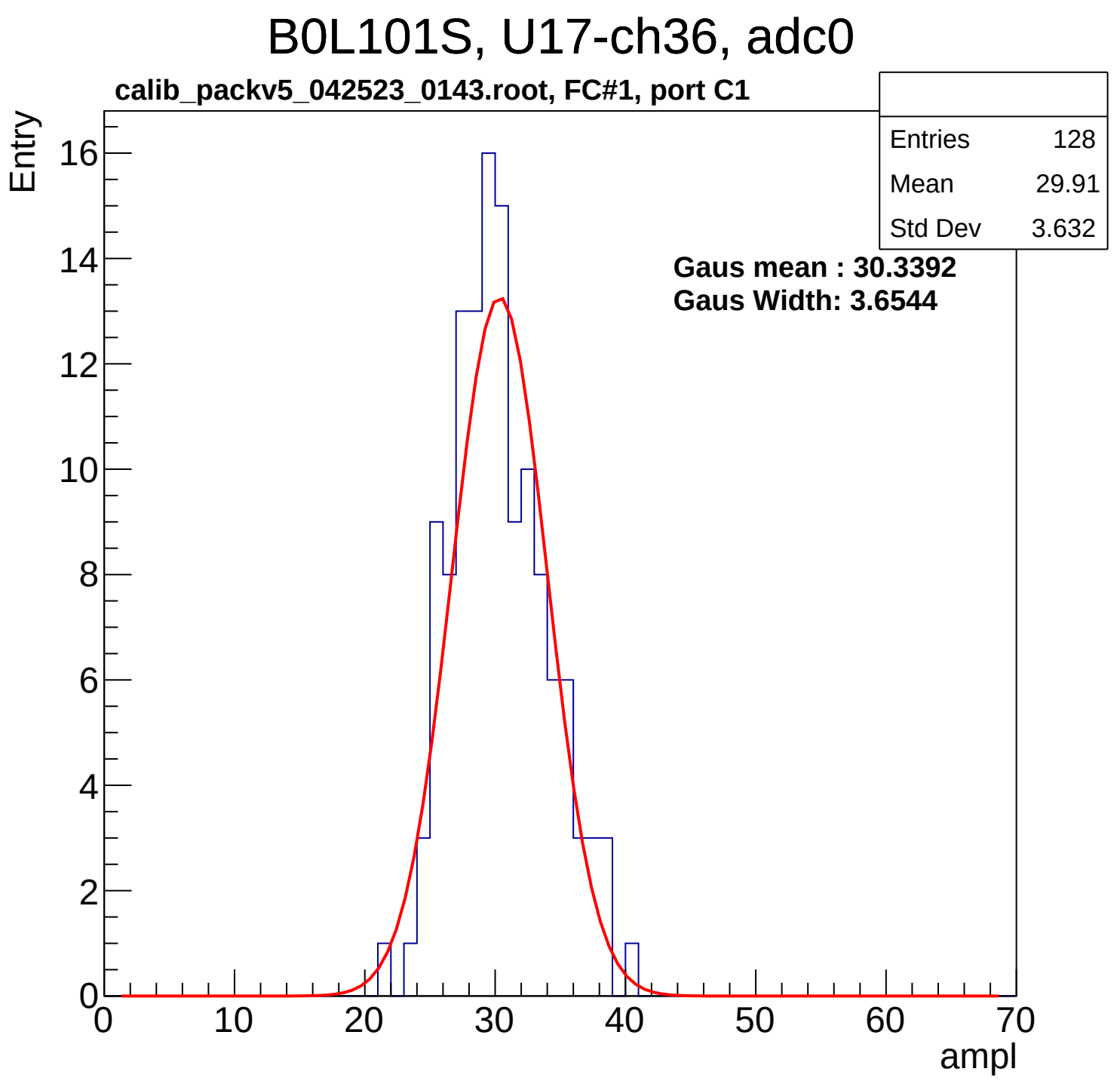

B0L101S, U17-ch35, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 50.4
Std Dev 3.706
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

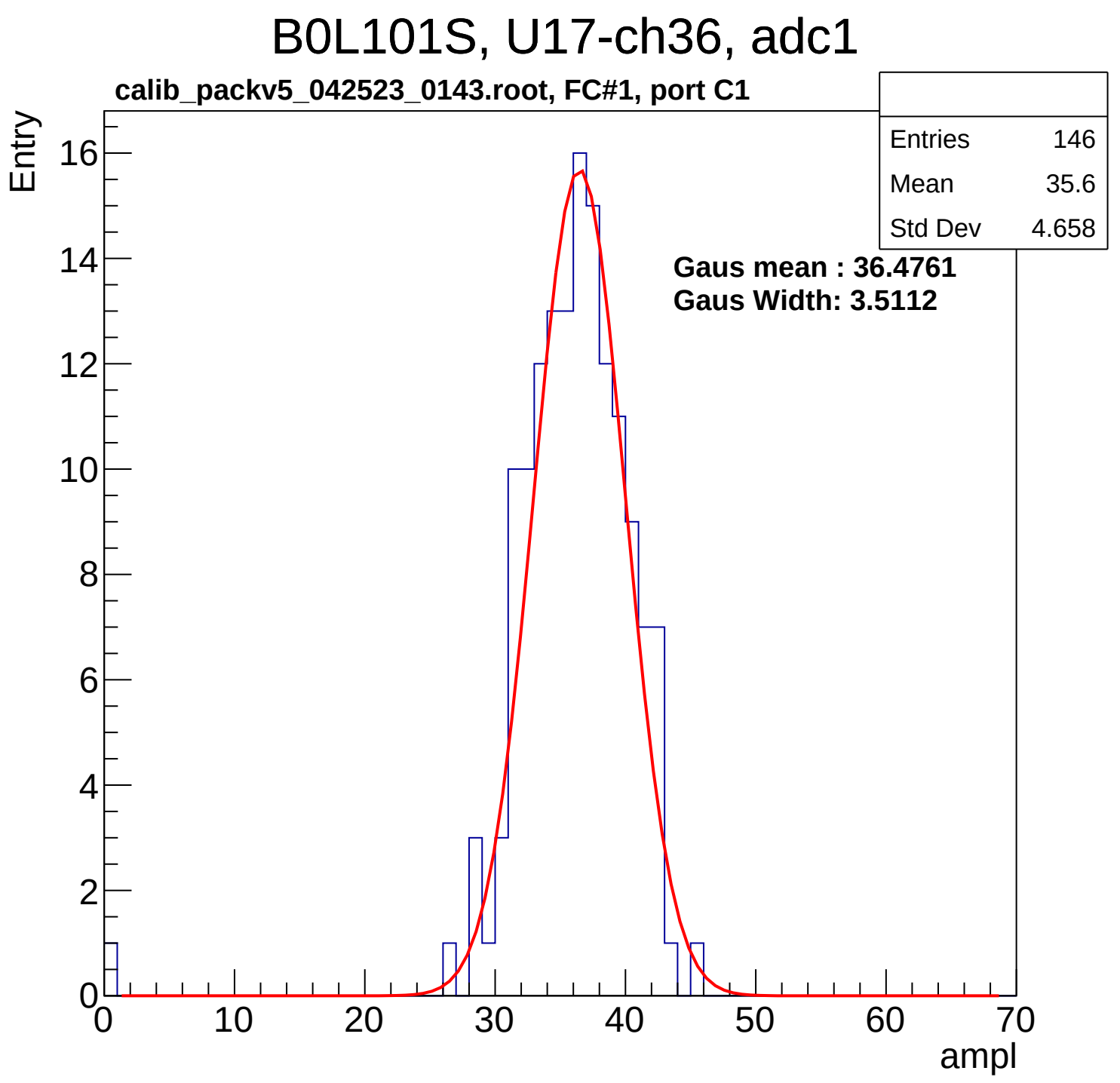

B0L101S, U17-ch35, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 92
Mean 55.73
Std Dev 3.011
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

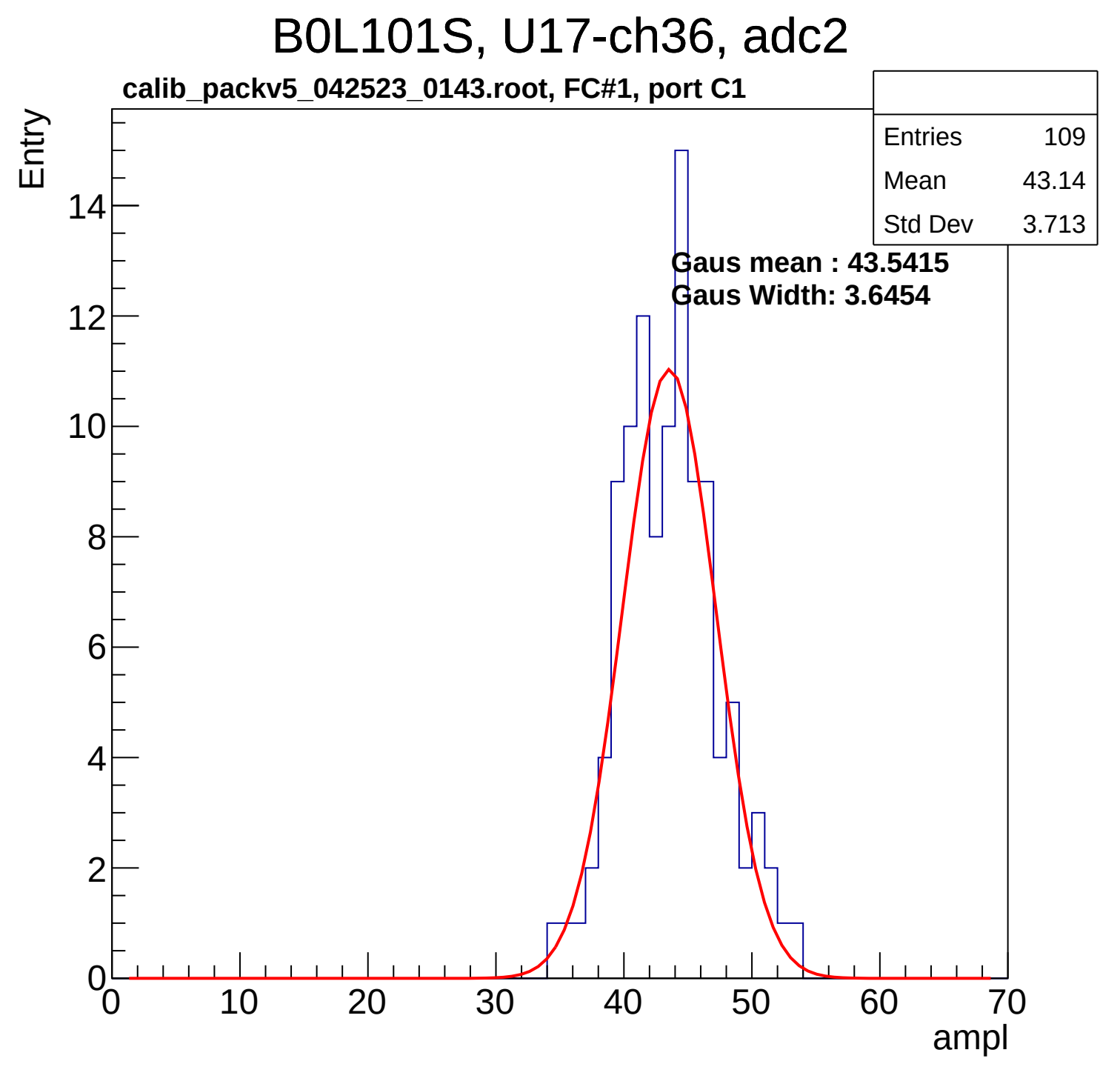

B0L101S, U17-ch35, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 58.73
Std Dev 8.598
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

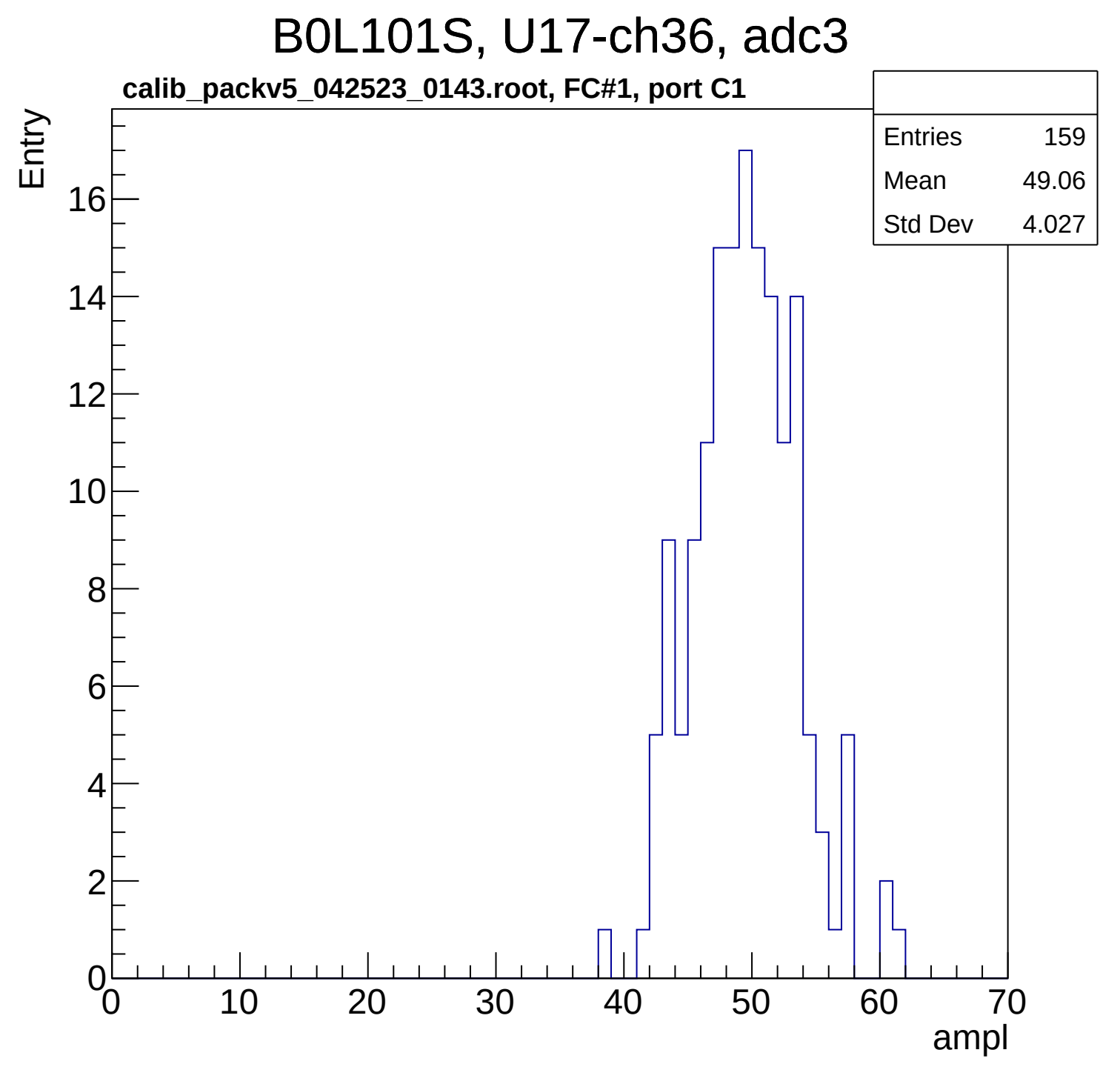

B0L101S, U17-ch35, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

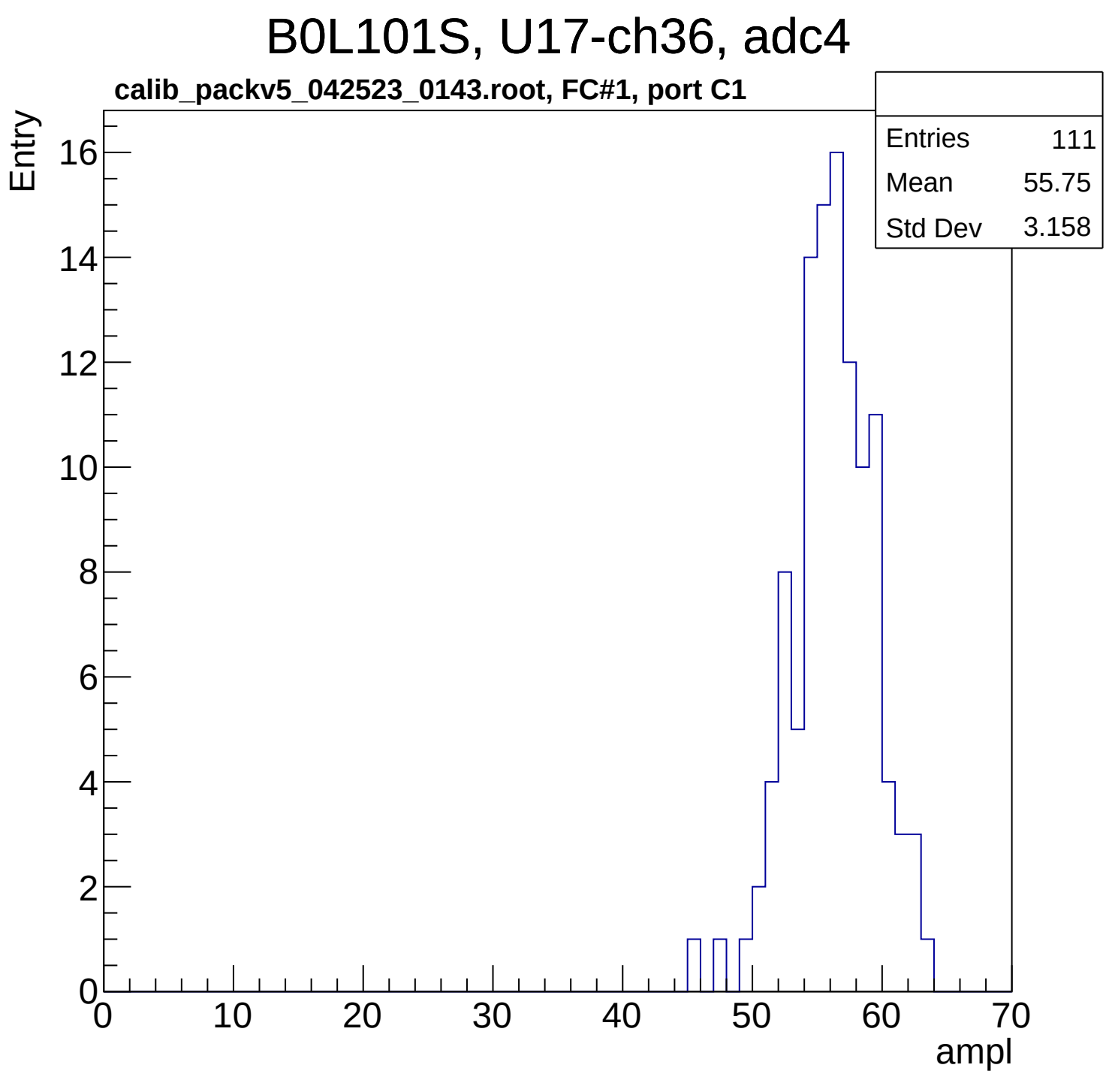

B0L101S, U17-ch35, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

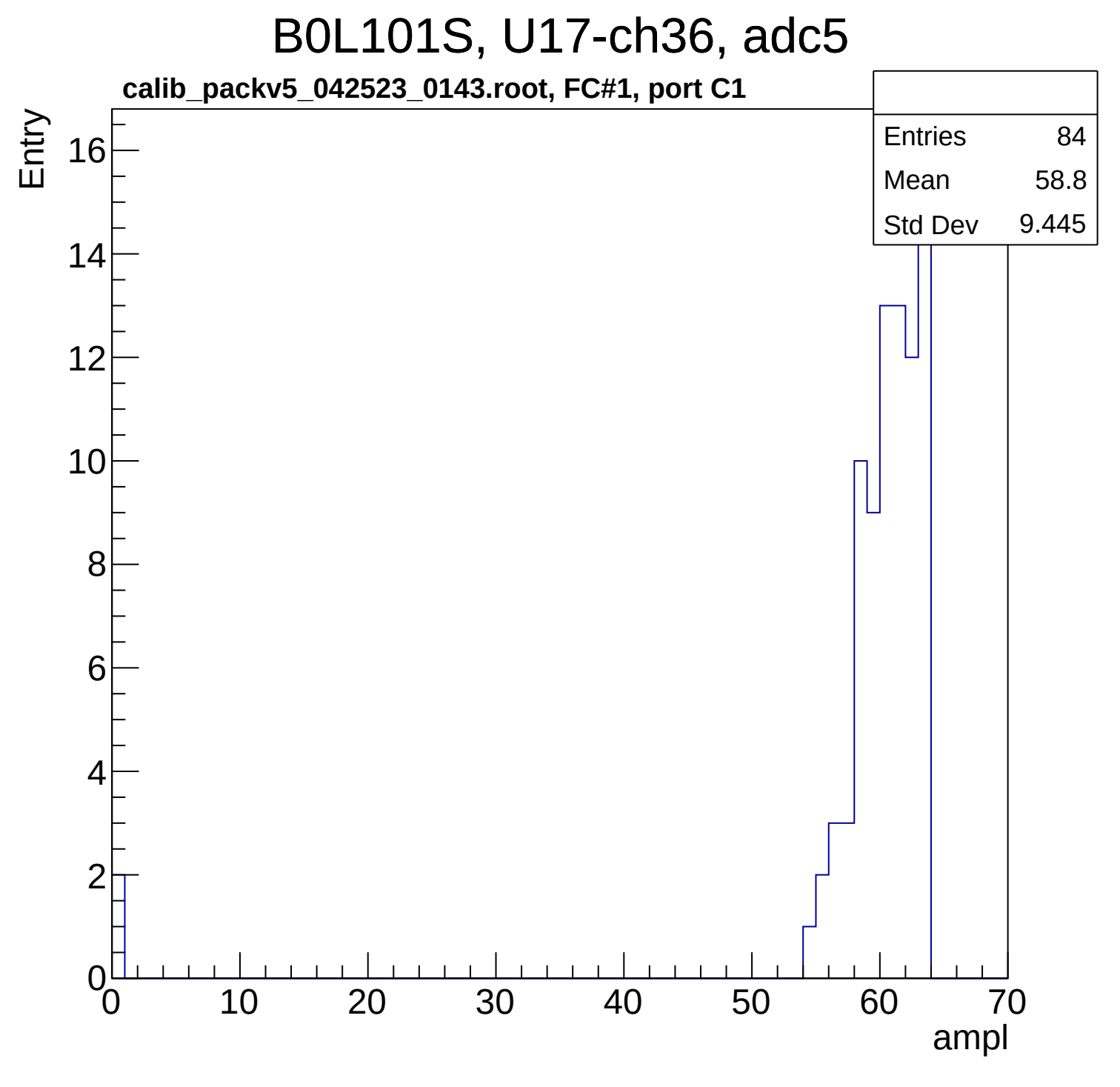

B0L101S, U17-ch36, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 29.91
Std Dev 3.632
Gaus mean : 30.3392
Gaus Width: 3.6544
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

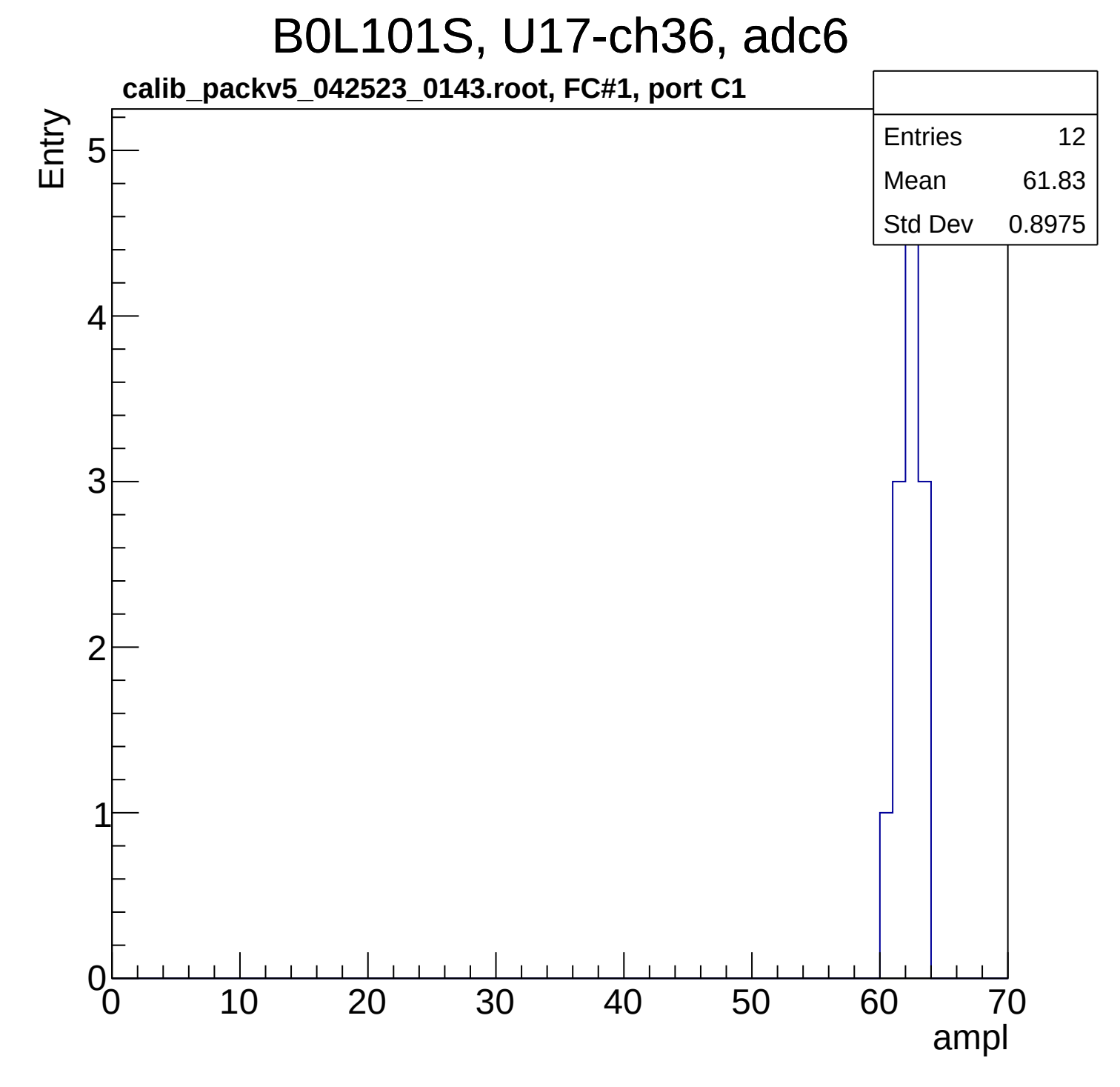

B0L101S, U17-ch36, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 35.6
Std Dev 4.658
Gaus mean : 36.4761
Gaus Width: 3.5112
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

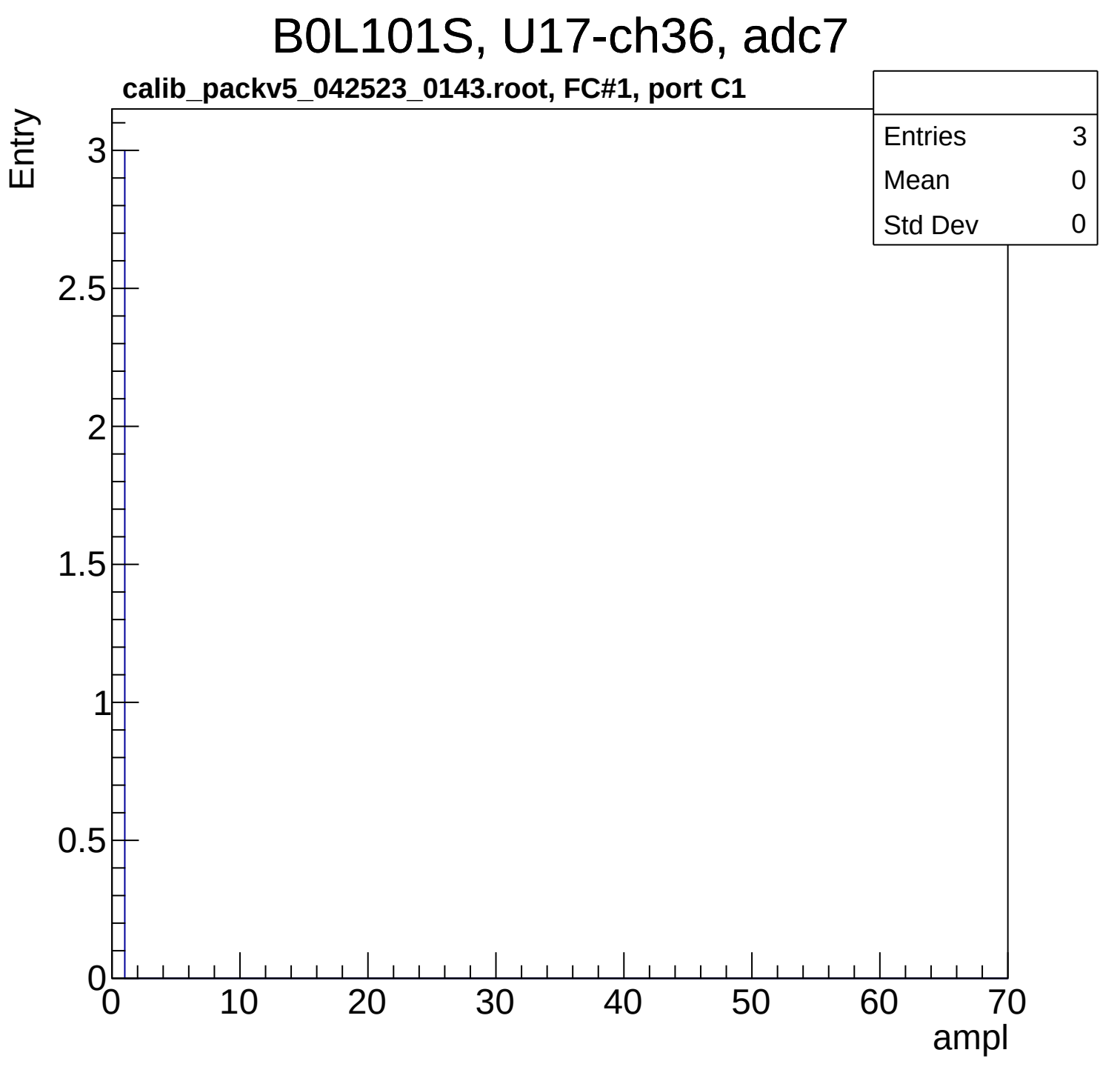

B0L101S, U17-ch36, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 43.14
Std Dev 3.713
Gaus mean : 43.5415
Gaus Width: 3.6454
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

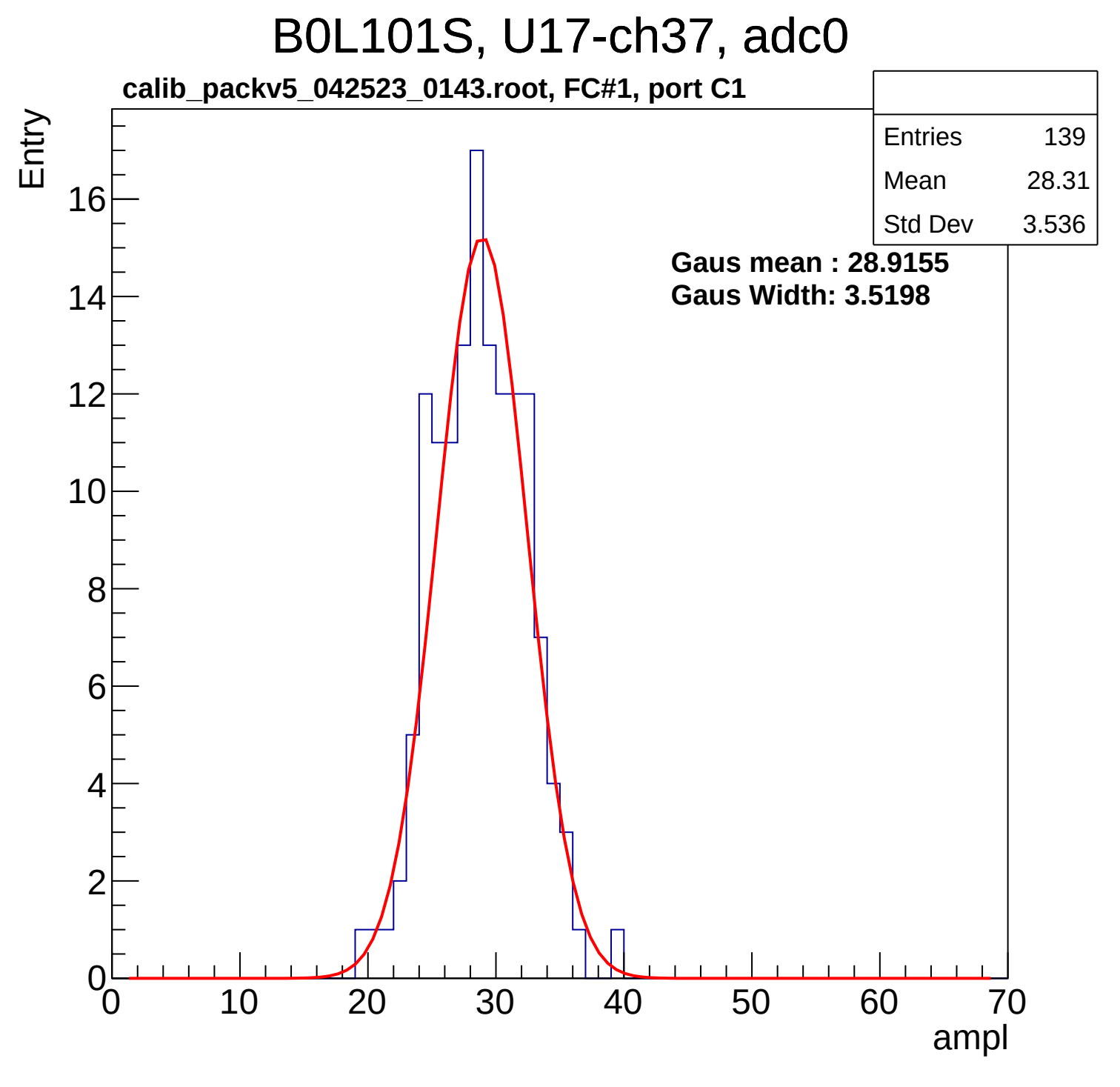

B0L101S, U17-ch36, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 49.06
Std Dev 4.027
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

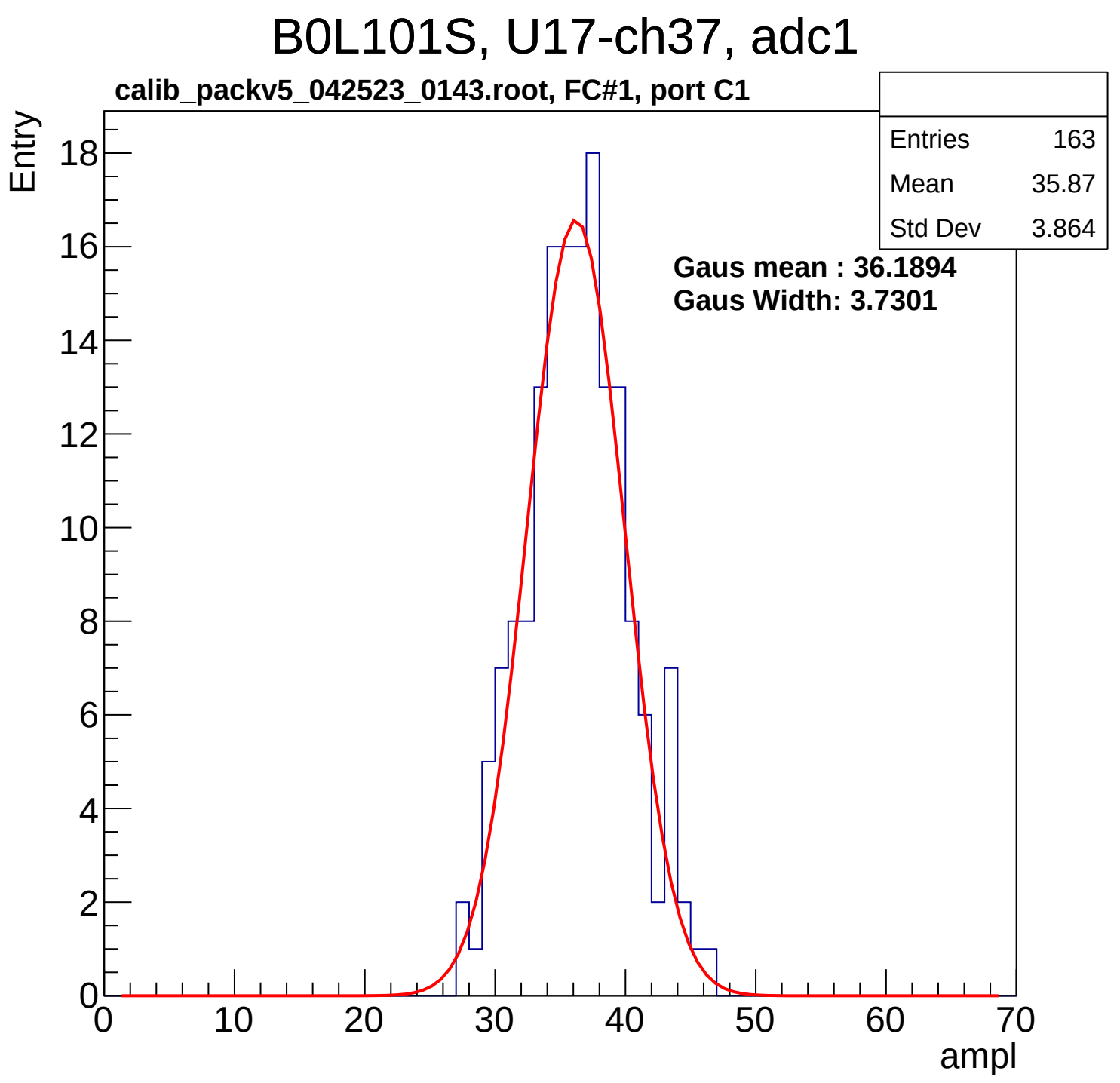

B0L101S, U17-ch36, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 55.75
Std Dev 3.158
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

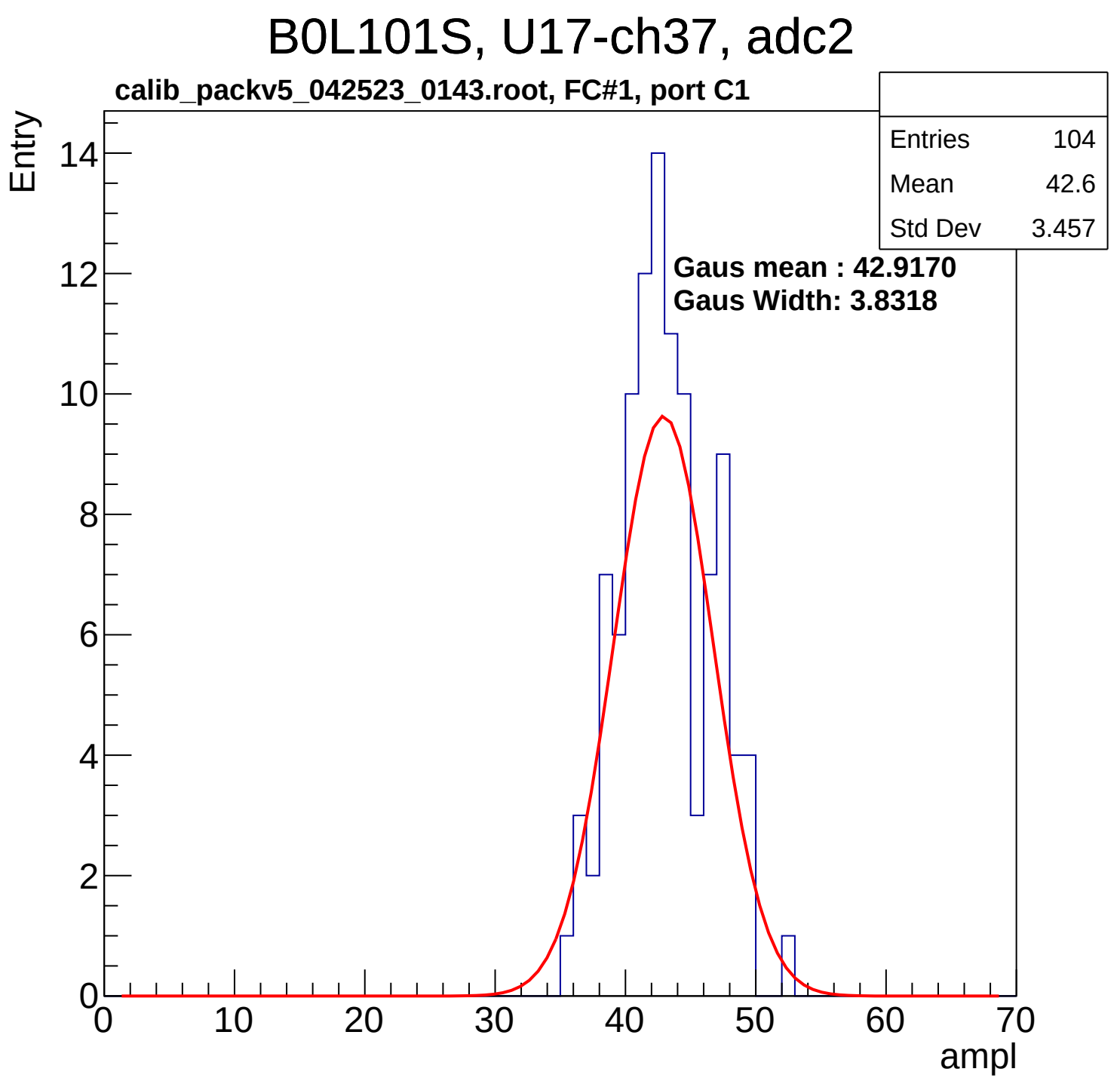

B0L101S, U17-ch36, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 84
Mean 58.8
Std Dev 9.445
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

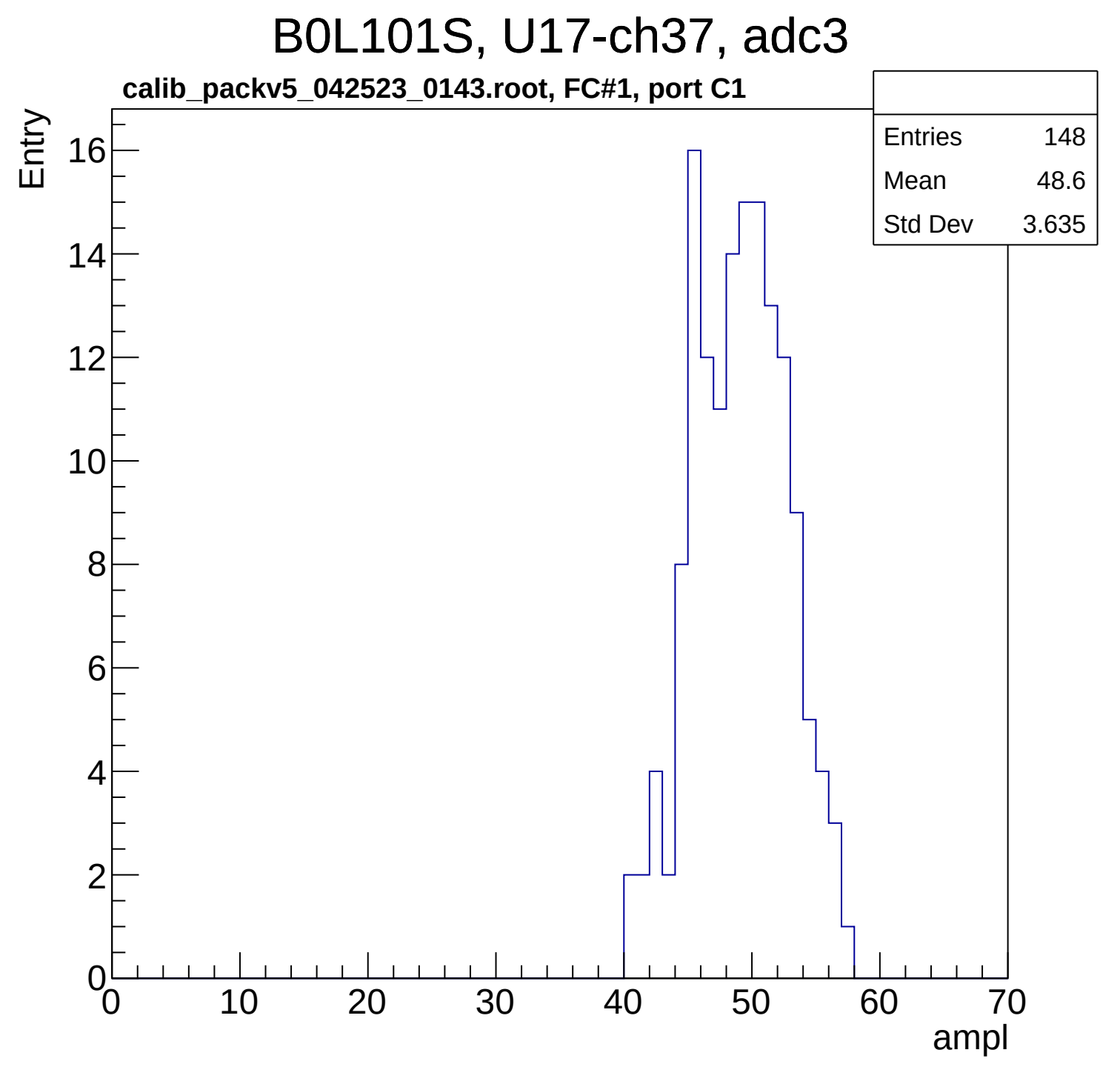

B0L101S, U17-ch36, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.83
Std Dev 0.8975
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

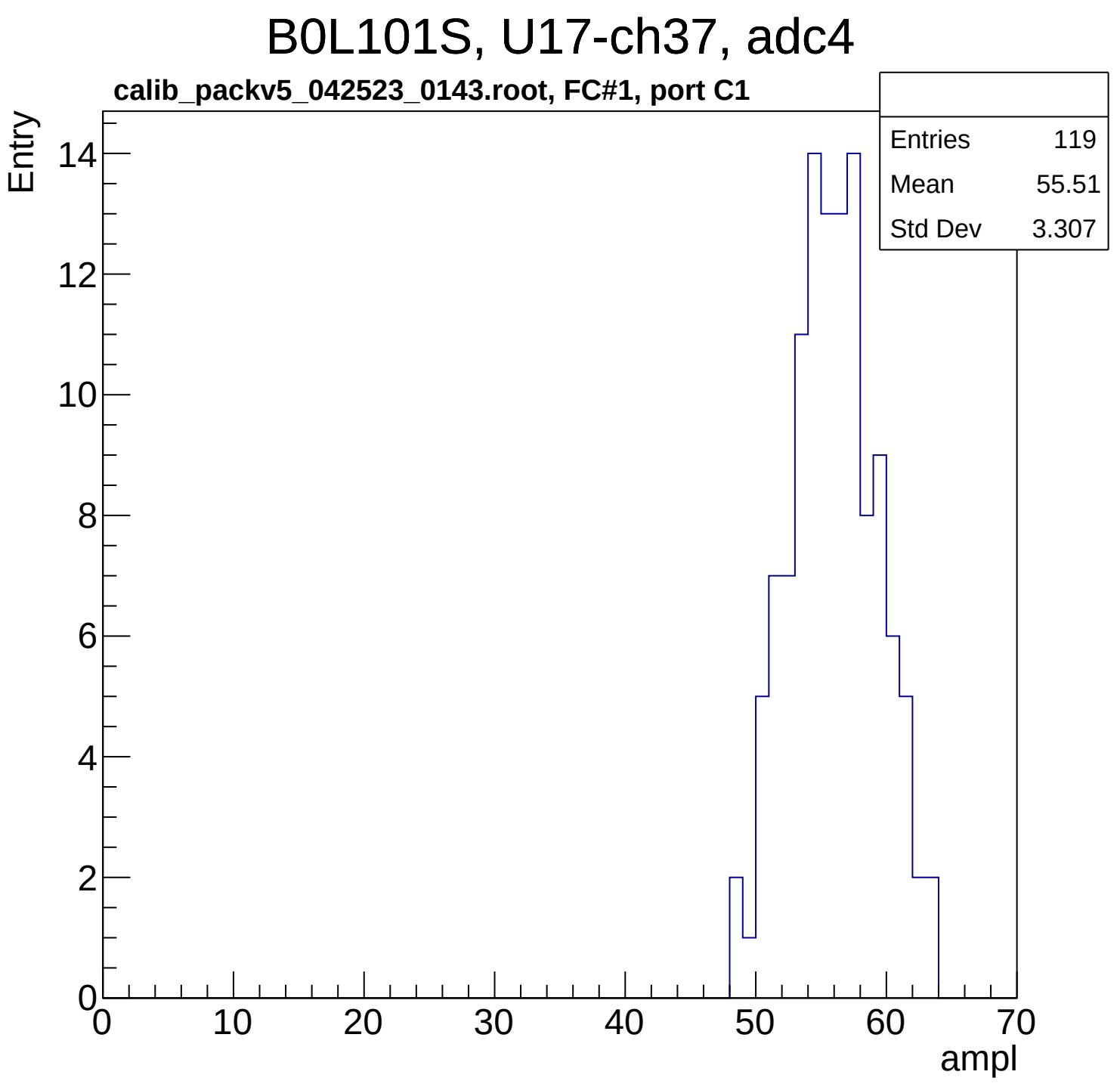

B0L101S, U17-ch36, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

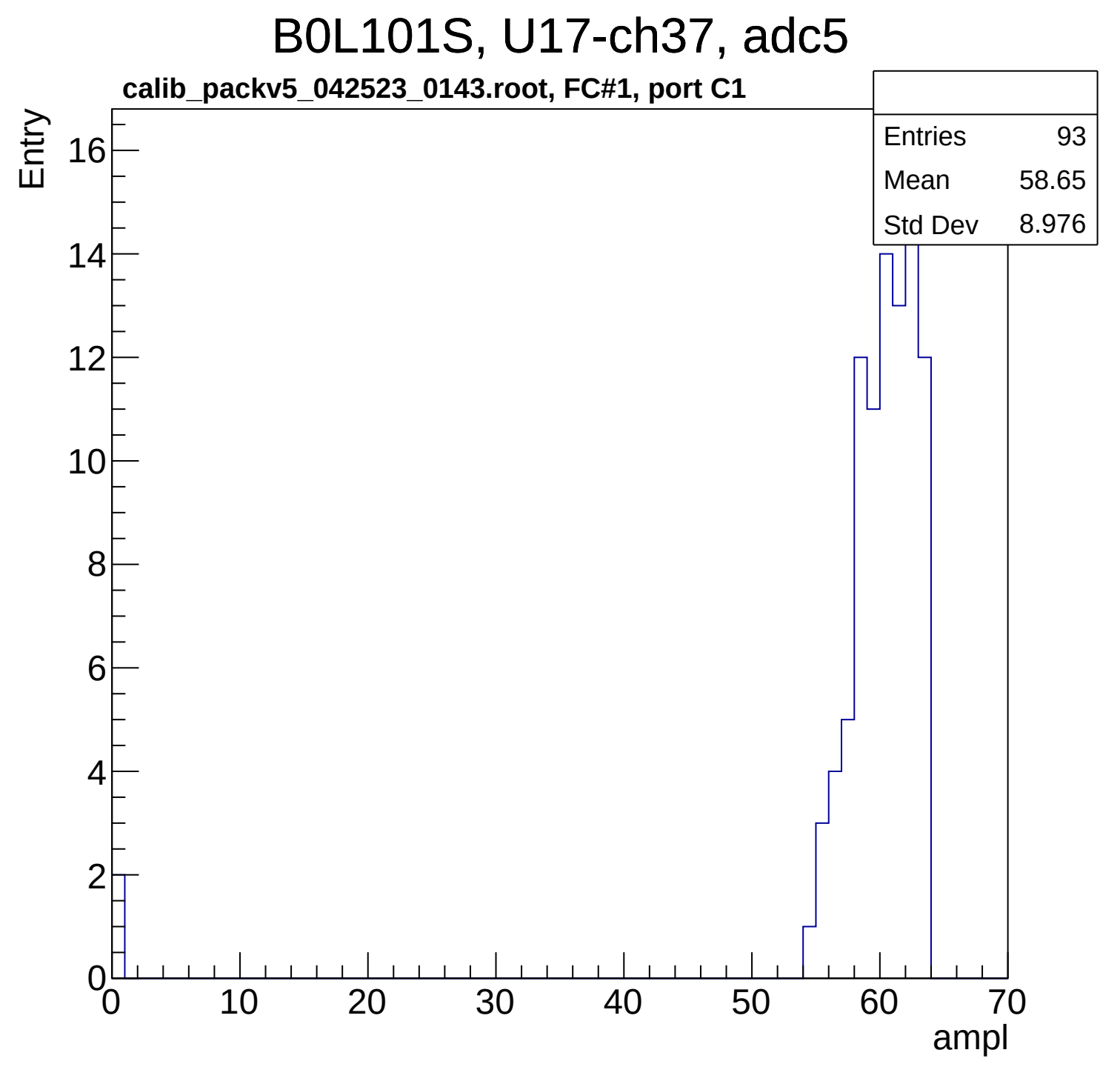

B0L101S, U17-ch37, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 28.31
Std Dev 3.536
Gaus mean : 28.9155
Gaus Width: 3.5198
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

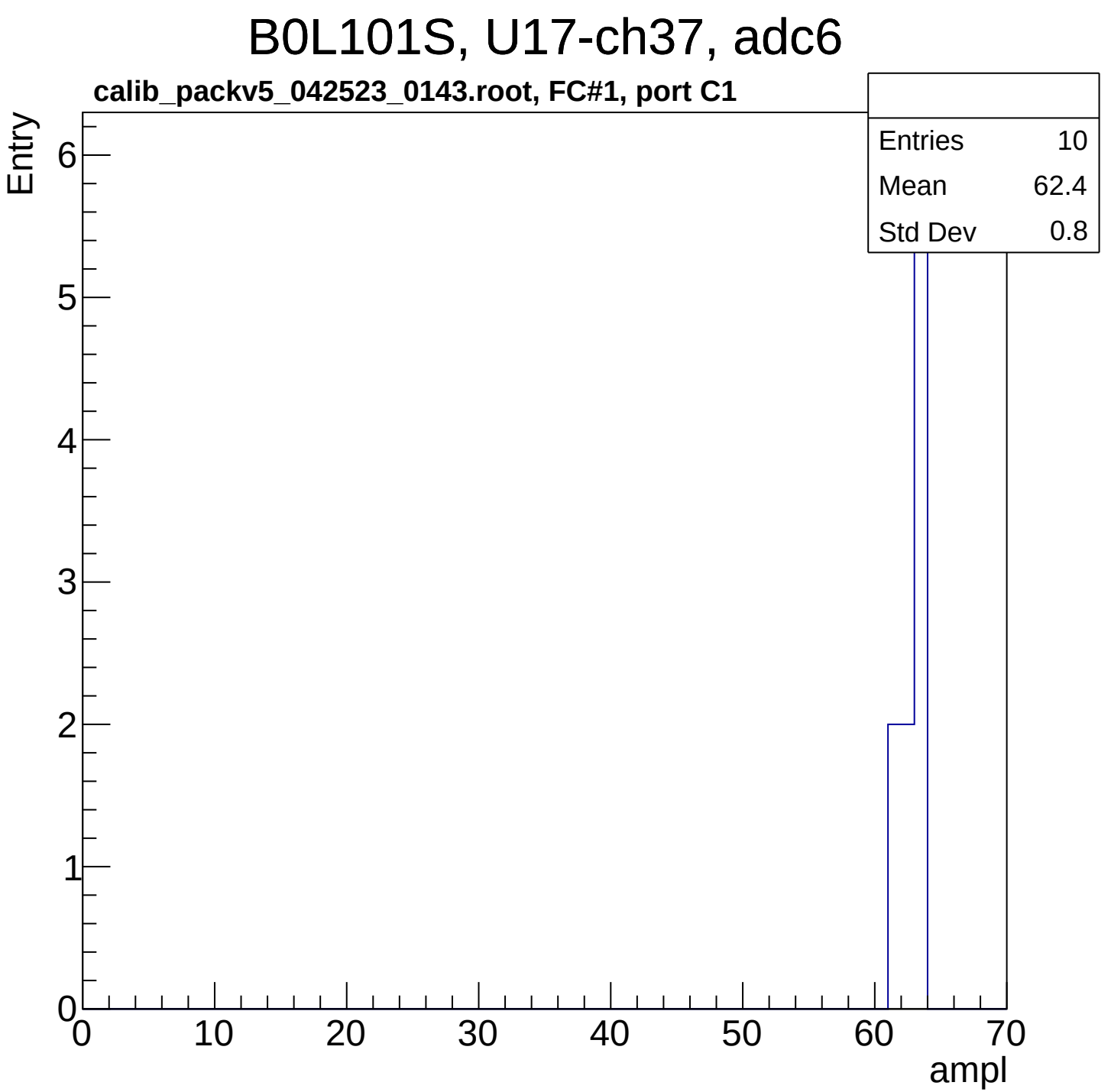

B0L101S, U17-ch37, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 35.87
Std Dev 3.864
Gaus mean : 36.1894
Gaus Width: 3.7301
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

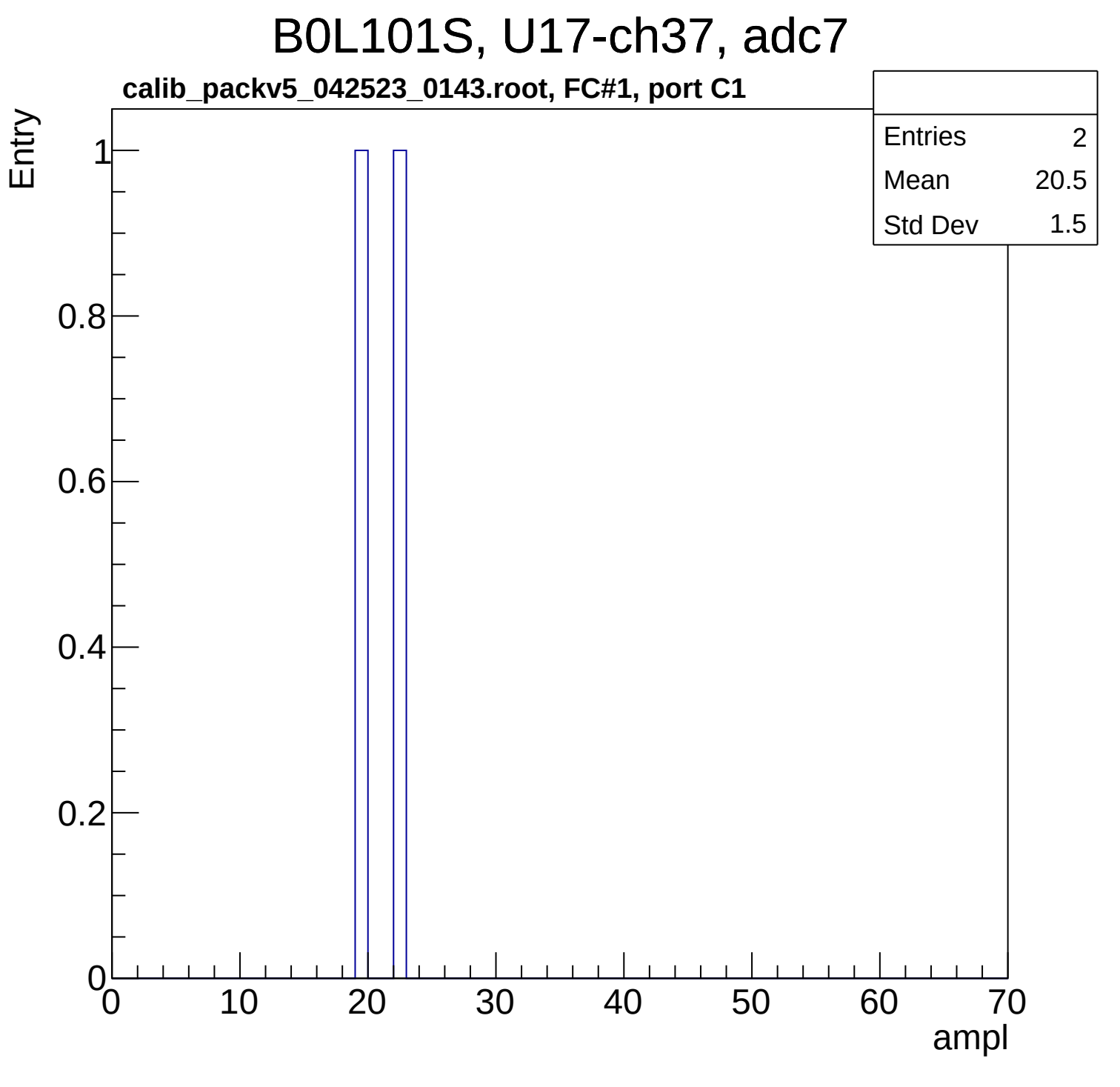

B0L101S, U17-ch37, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 42.6
Std Dev 3.457
Gaus mean : 42.9170
Gaus Width: 3.8318
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

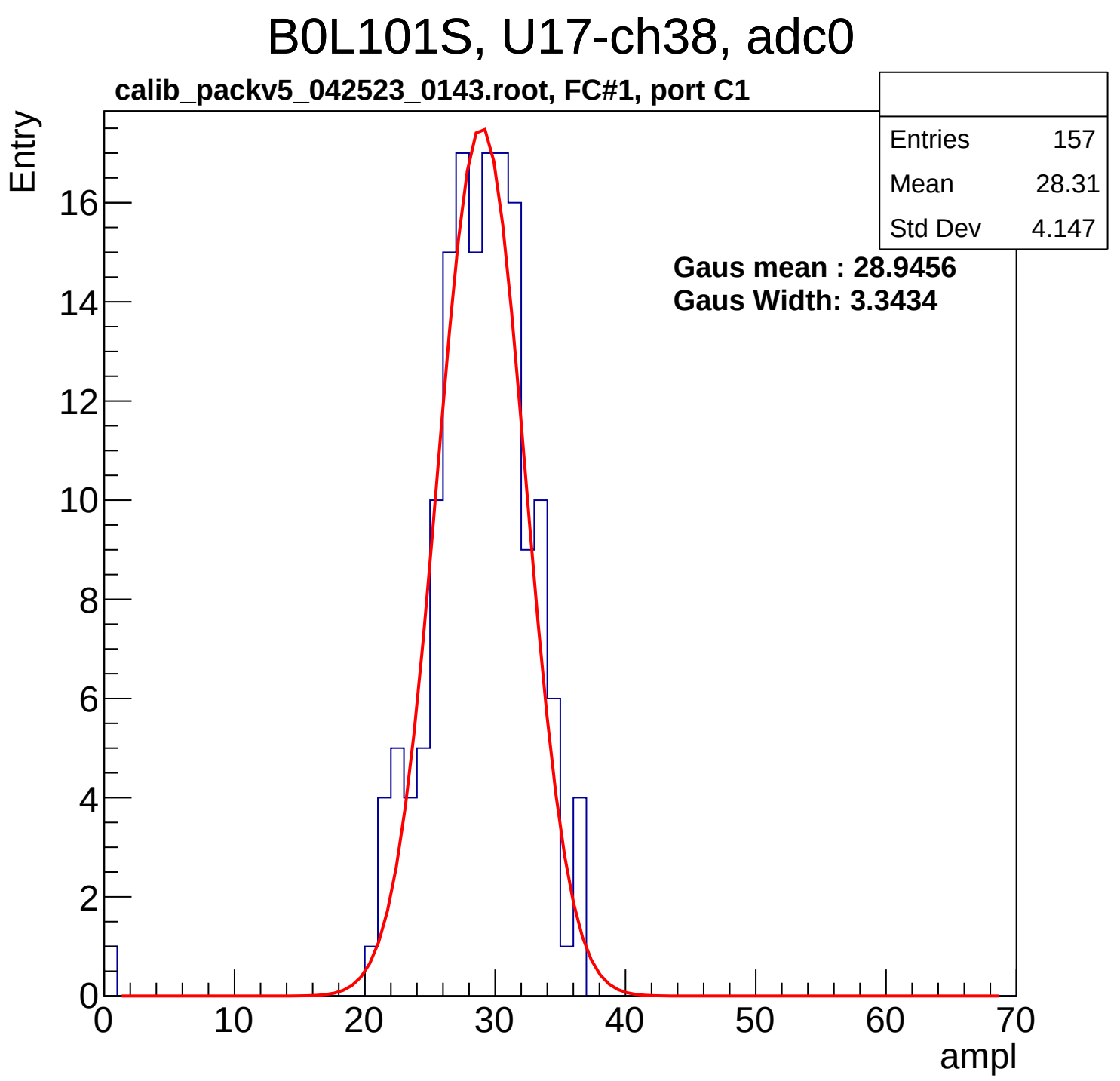

B0L101S, U17-ch37, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 48.6
Std Dev 3.635
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

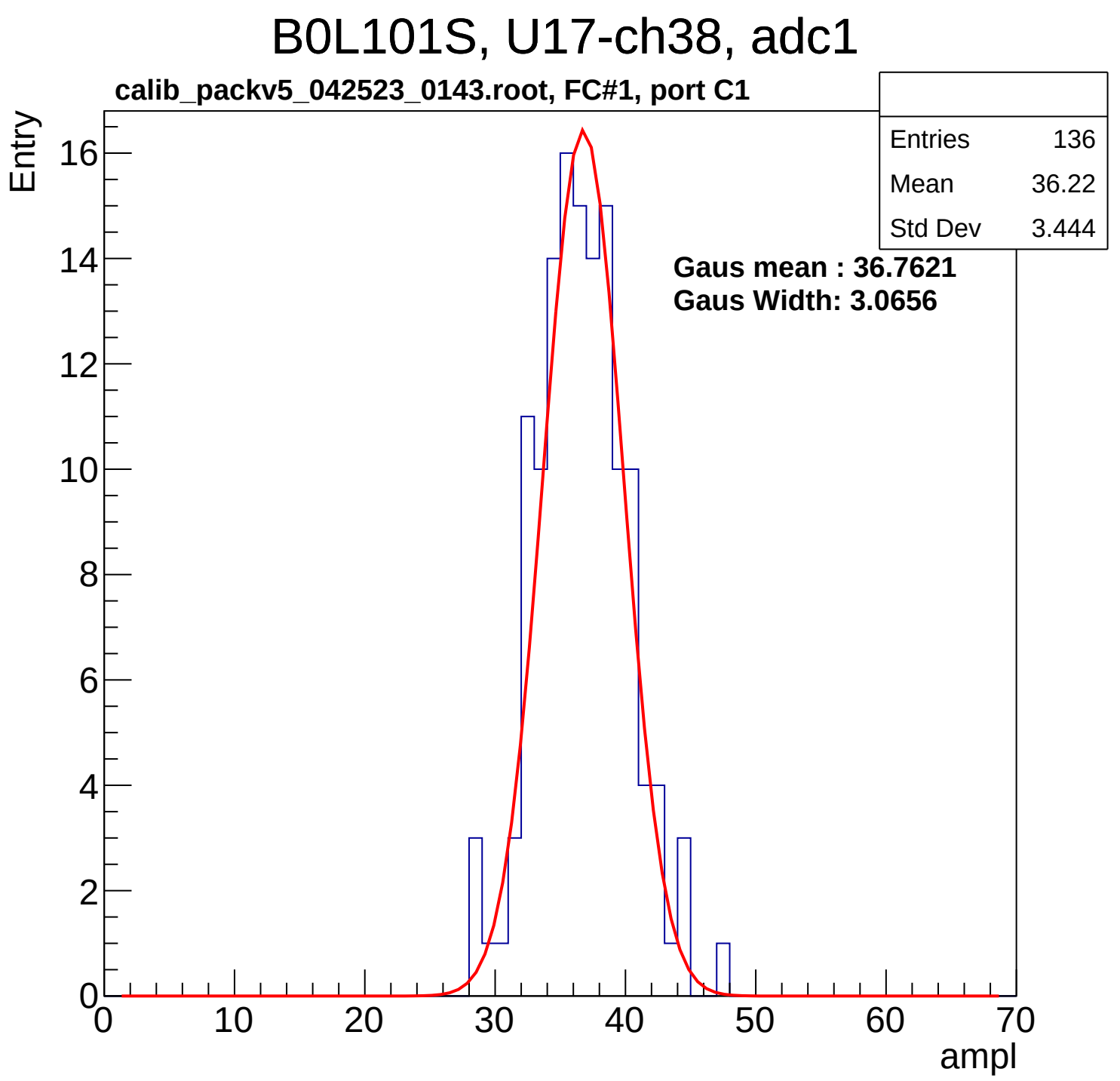

B0L101S, U17-ch37, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 55.51
Std Dev 3.307
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

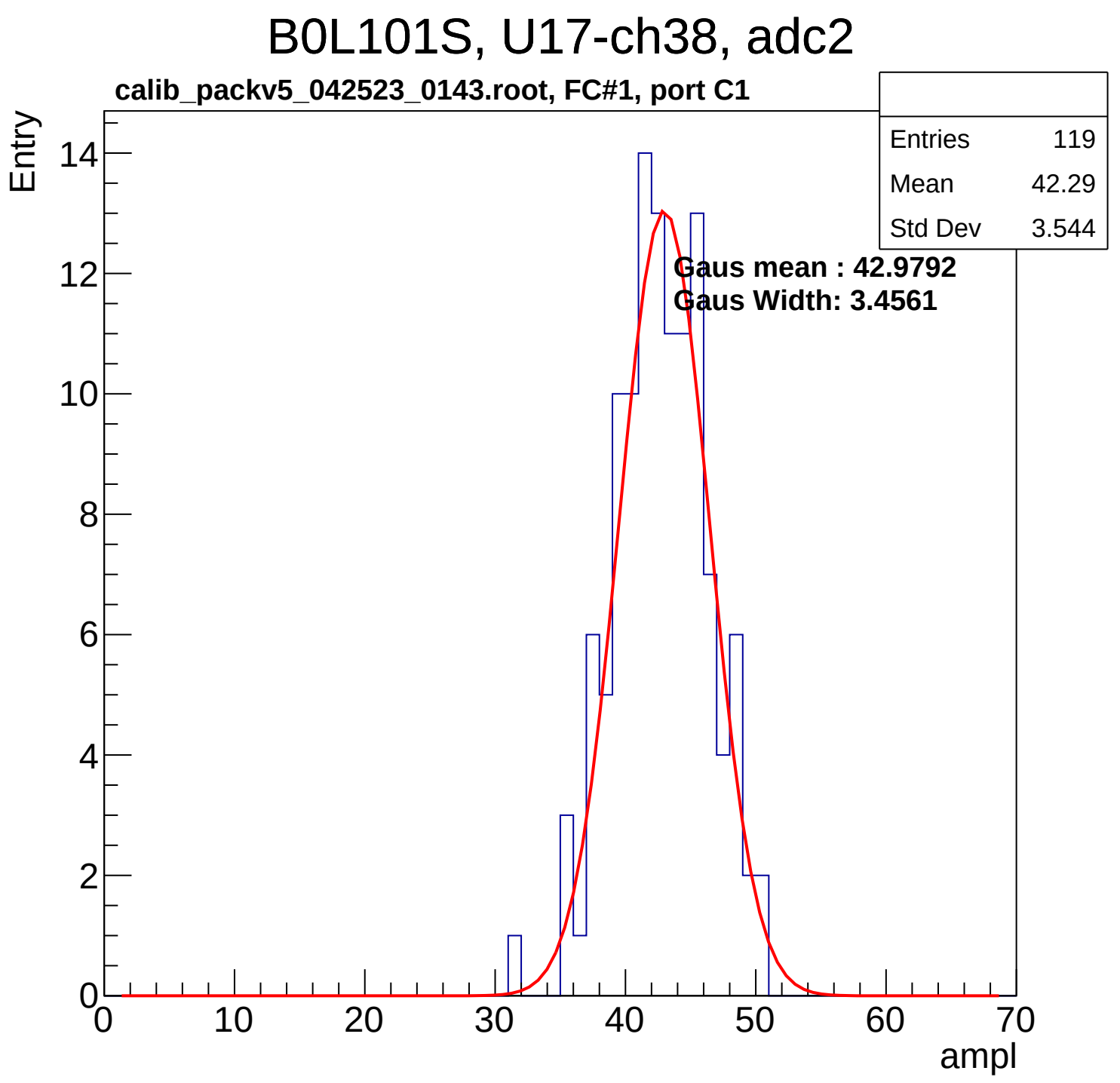

B0L101S, U17-ch37, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 93
Mean 58.65
Std Dev 8.976
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

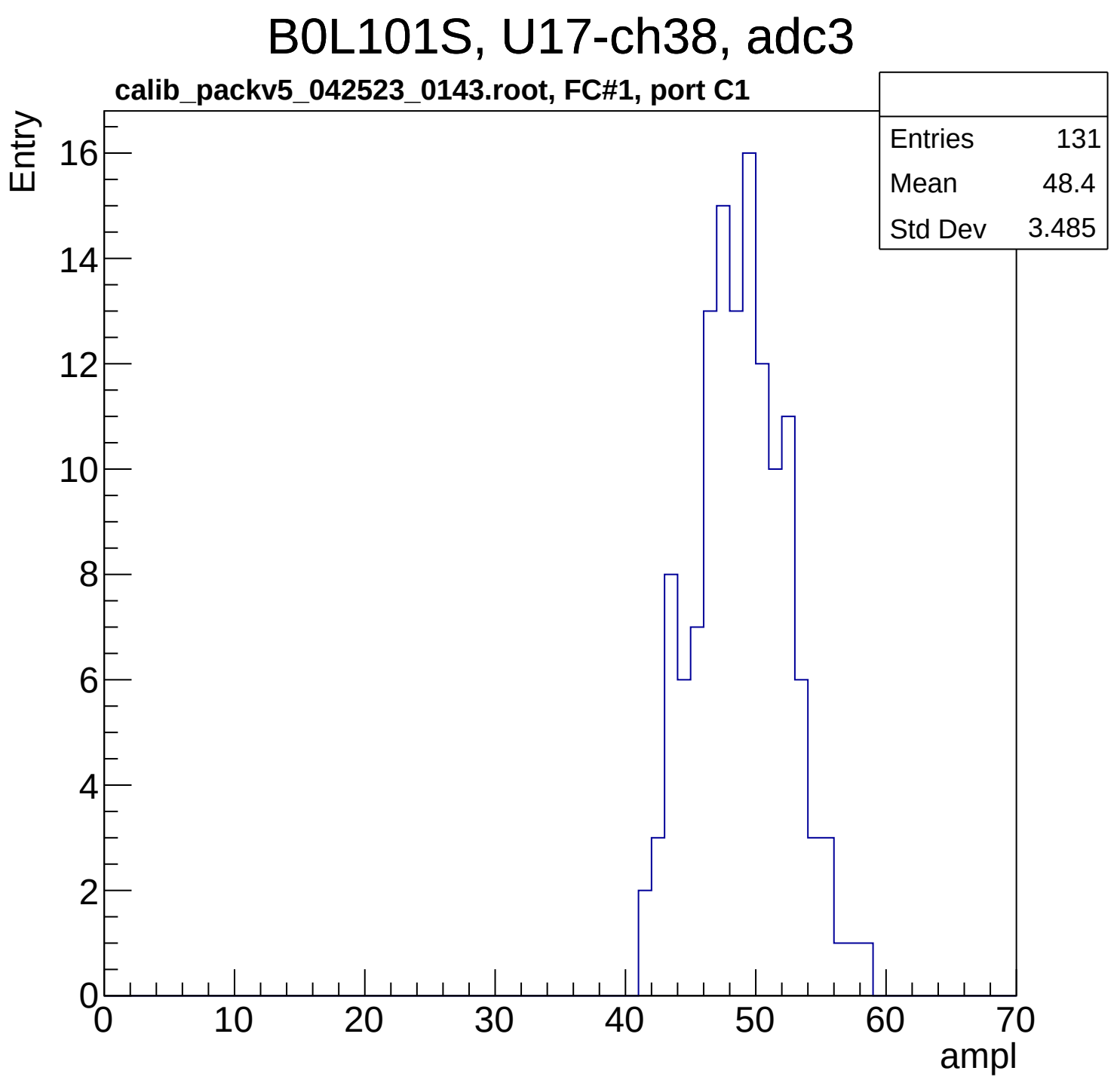

B0L101S, U17-ch37, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 62.4
Std Dev 0.8
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

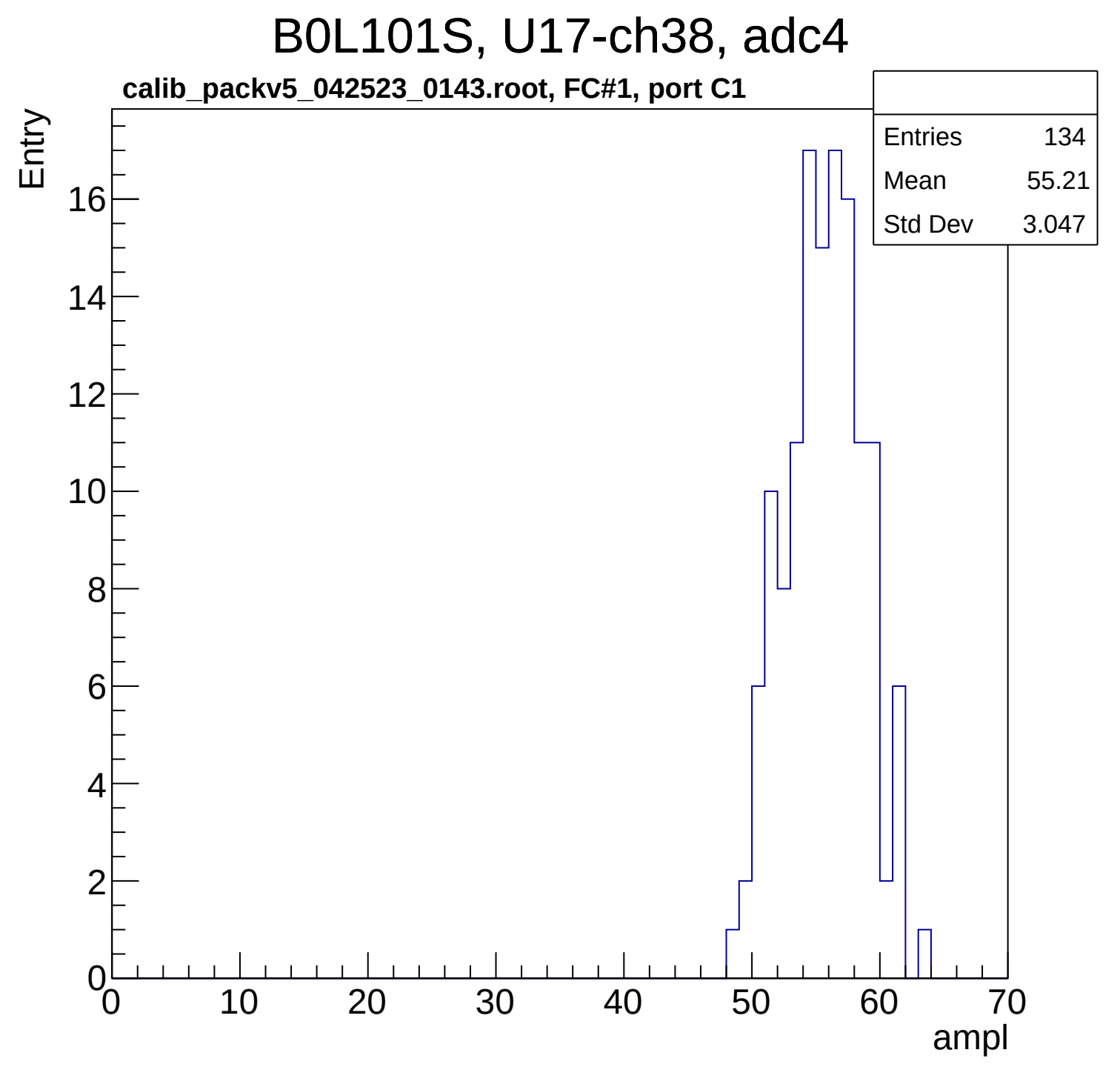

B0L101S, U17-ch37, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 20.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

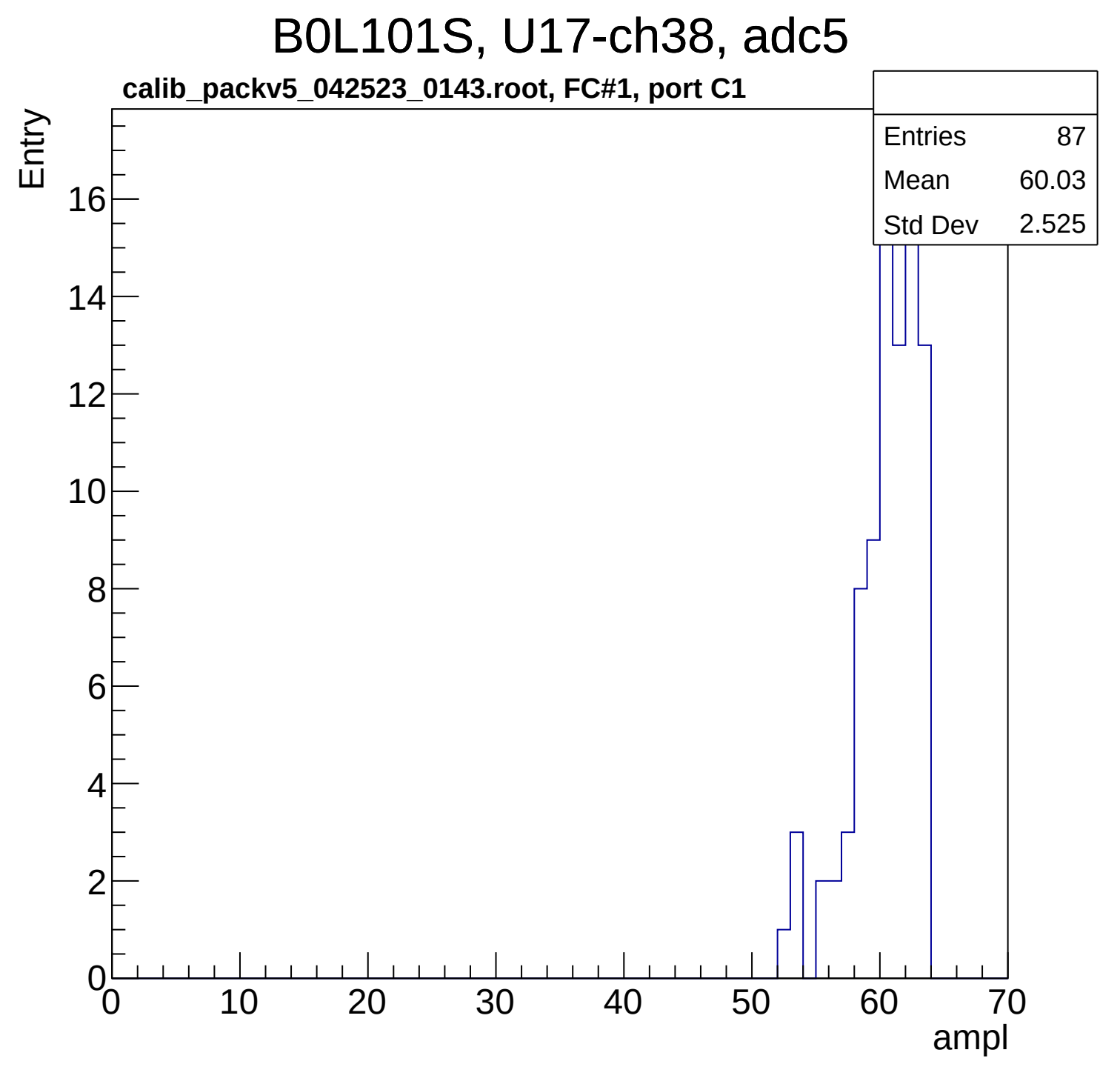

B0L101S, U17-ch38, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 28.31
Std Dev 4.147
Gaus mean : 28.9456
Gaus Width: 3.3434
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

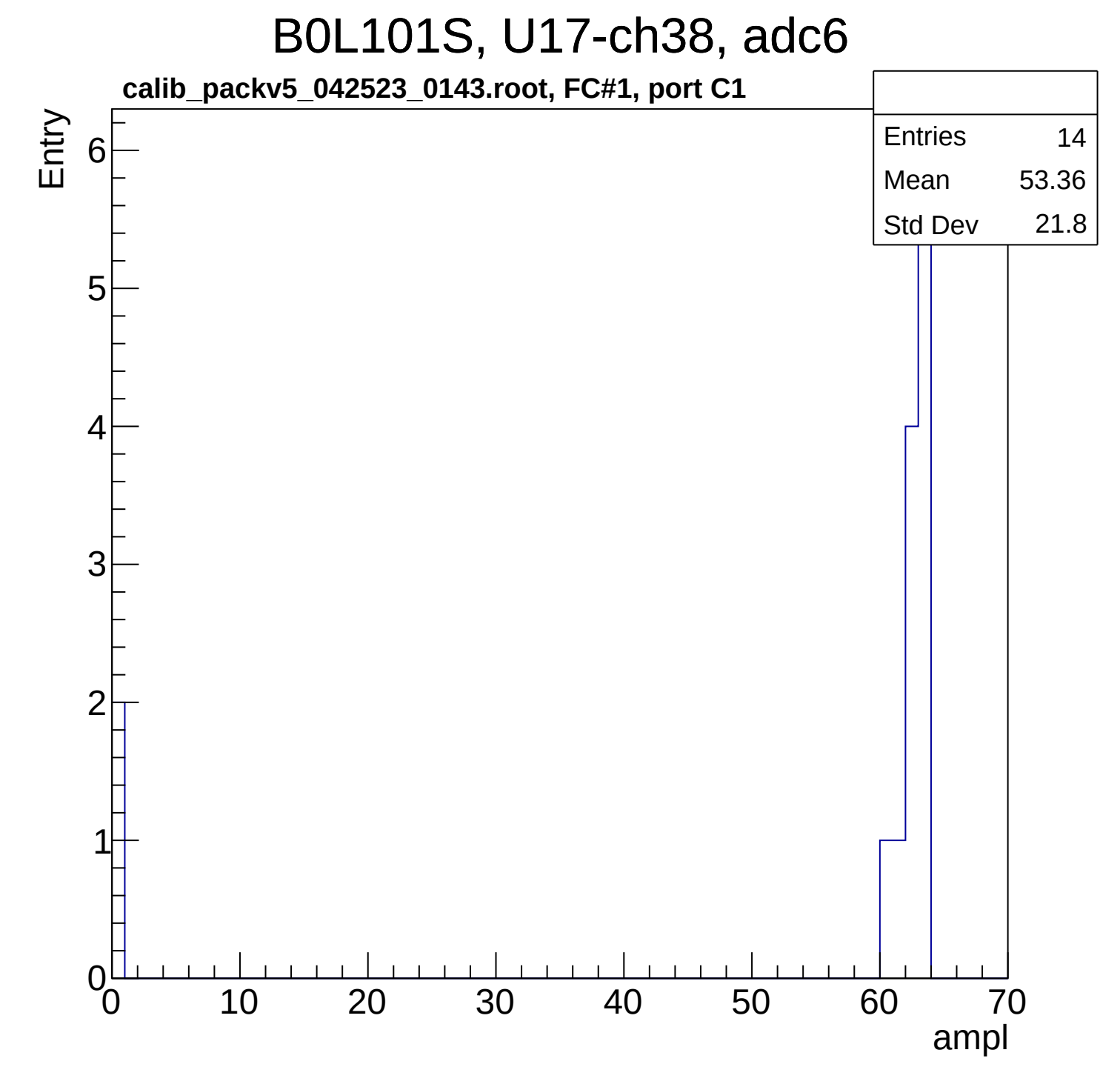

B0L101S, U17-ch38, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 36.22
Std Dev 3.444
Gaus mean : 36.7621
Gaus Width: 3.0656
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

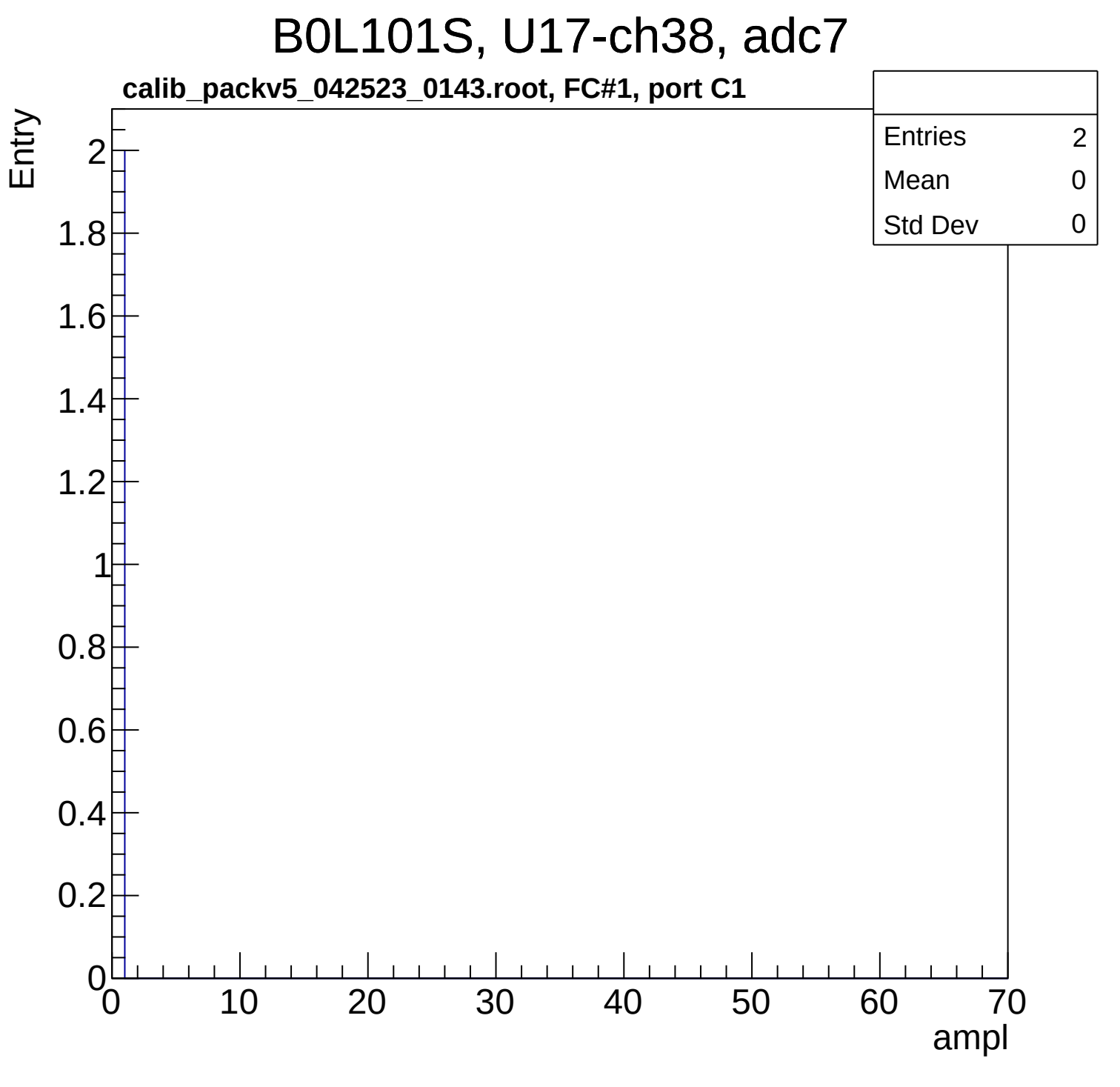

B0L101S, U17-ch38, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 42.29
Std Dev 3.544
Gaus mean : 42.9792
Gaus Width: 3.4561
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

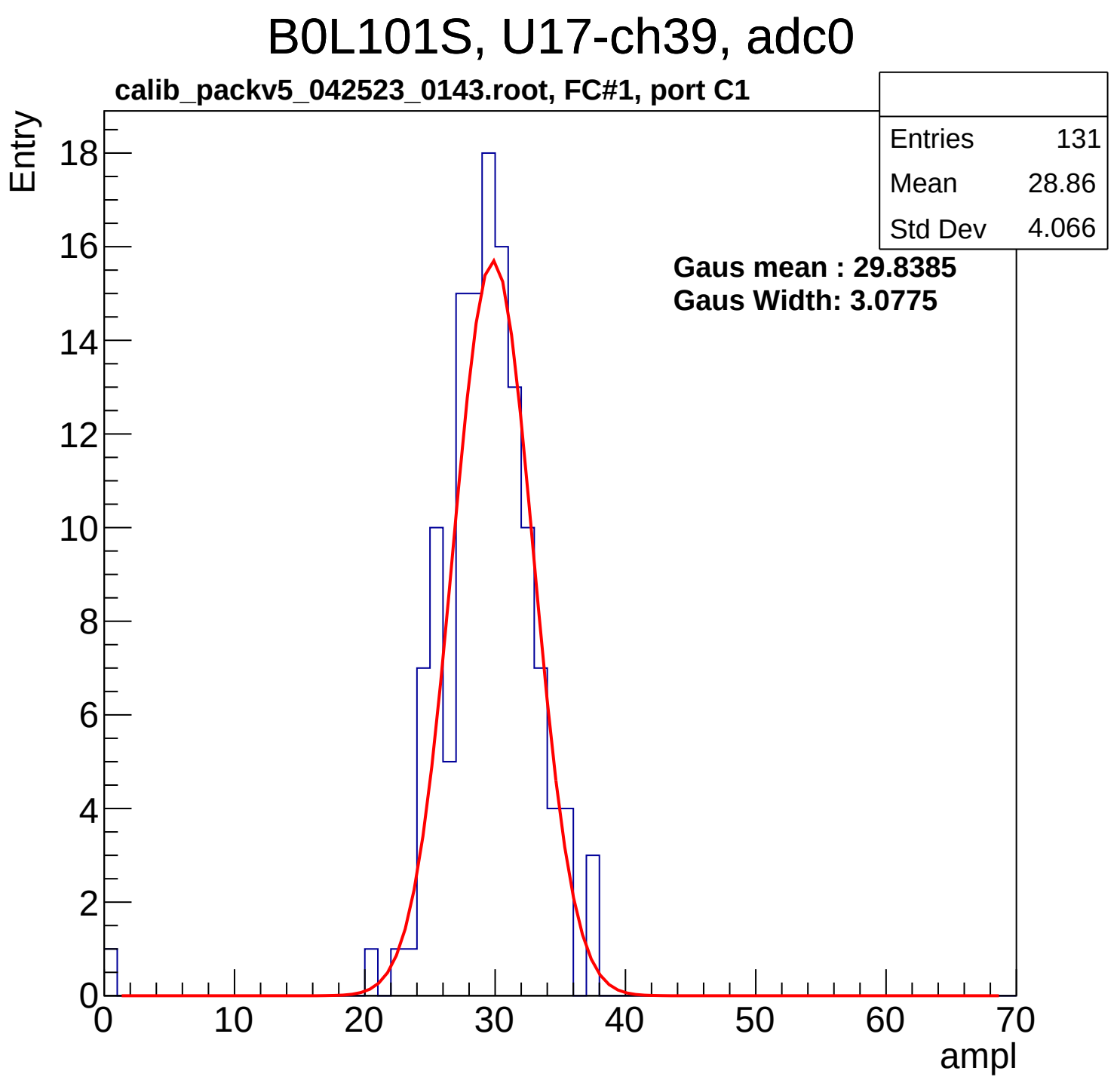

B0L101S, U17-ch38, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 48.4
Std Dev 3.485
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

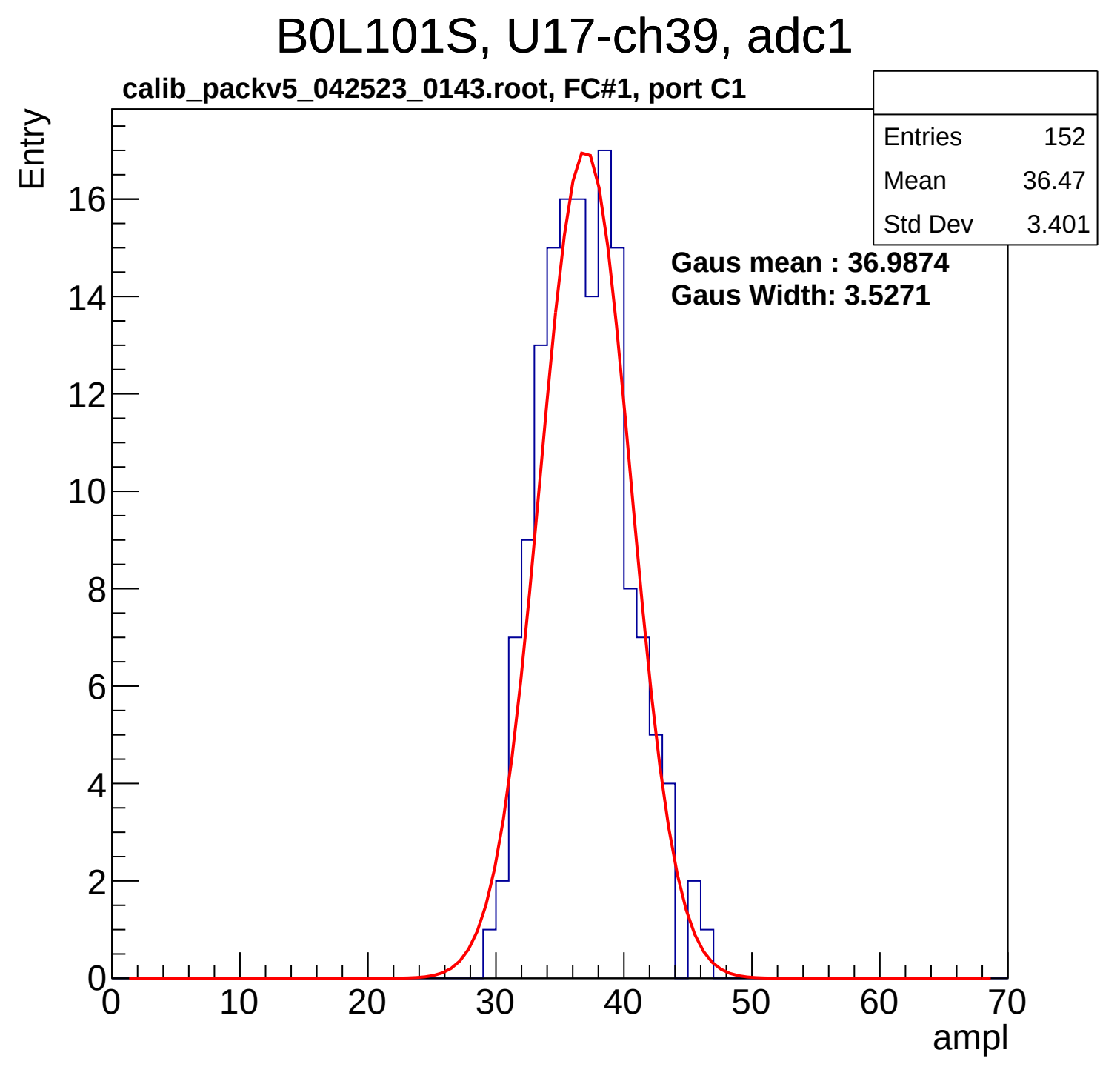

B0L101S, U17-ch38, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 55.21
Std Dev 3.047
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

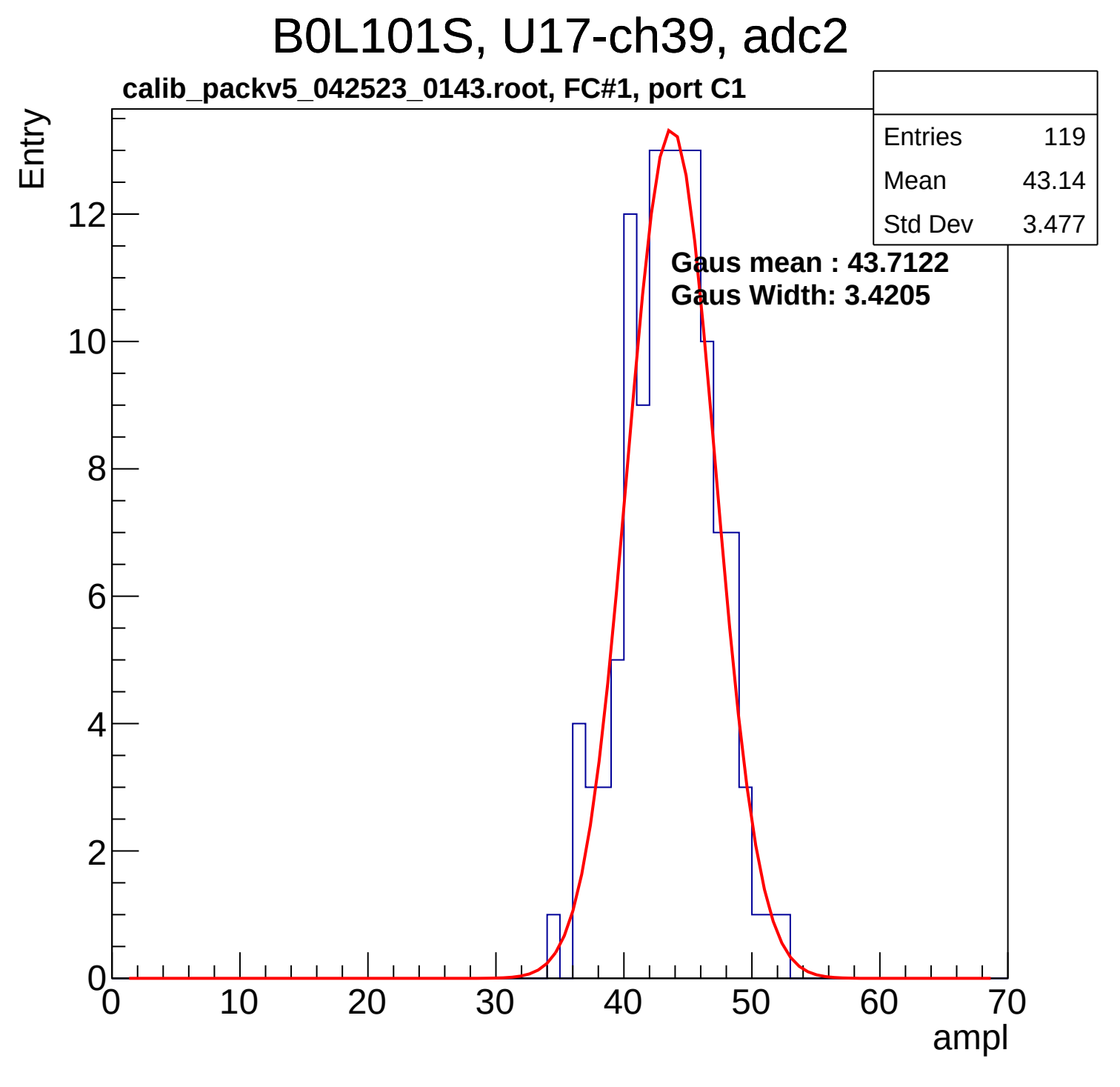

B0L101S, U17-ch38, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 60.03
Std Dev 2.525
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

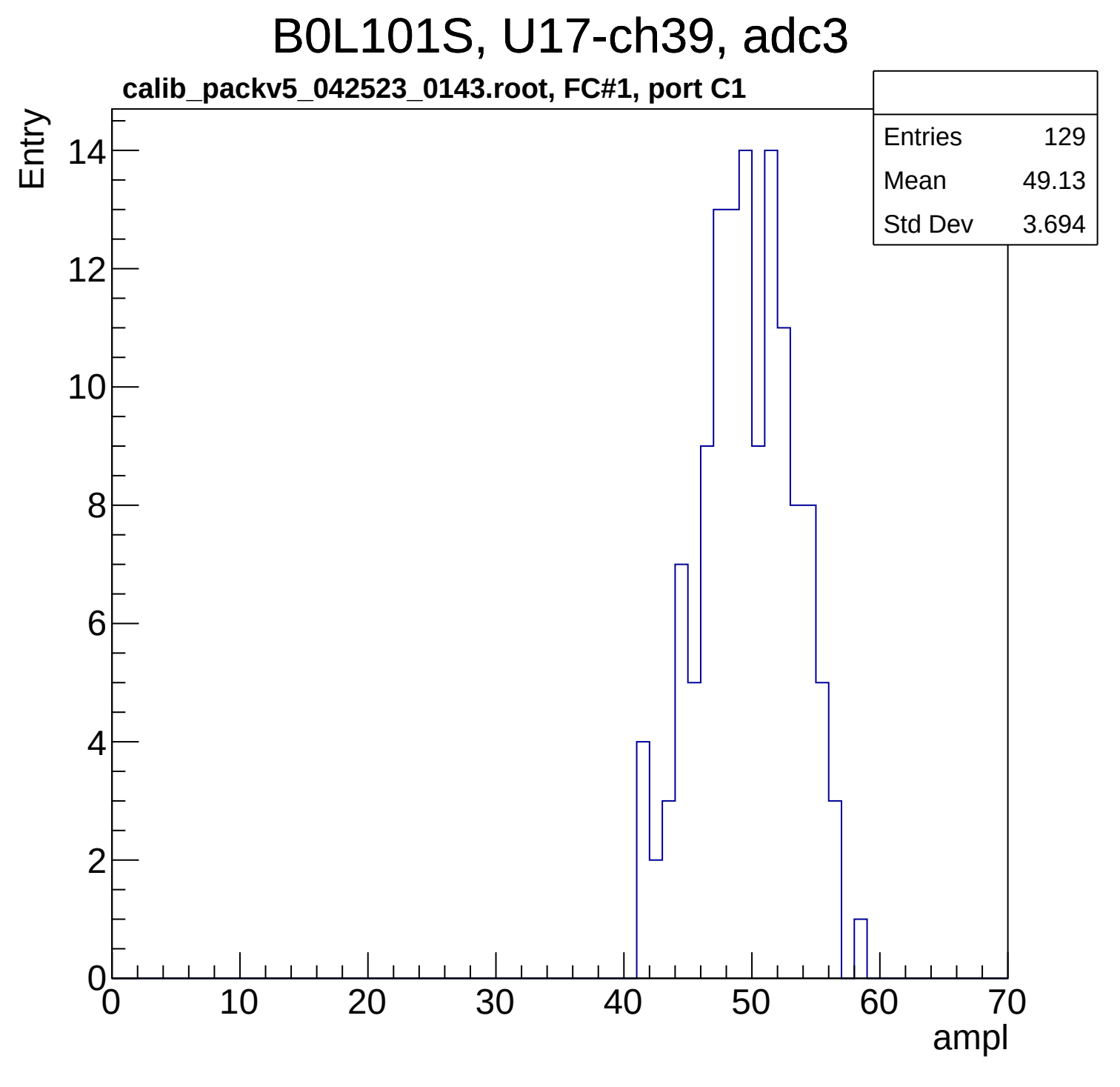

B0L101S, U17-ch38, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 53.36
Std Dev 21.8
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

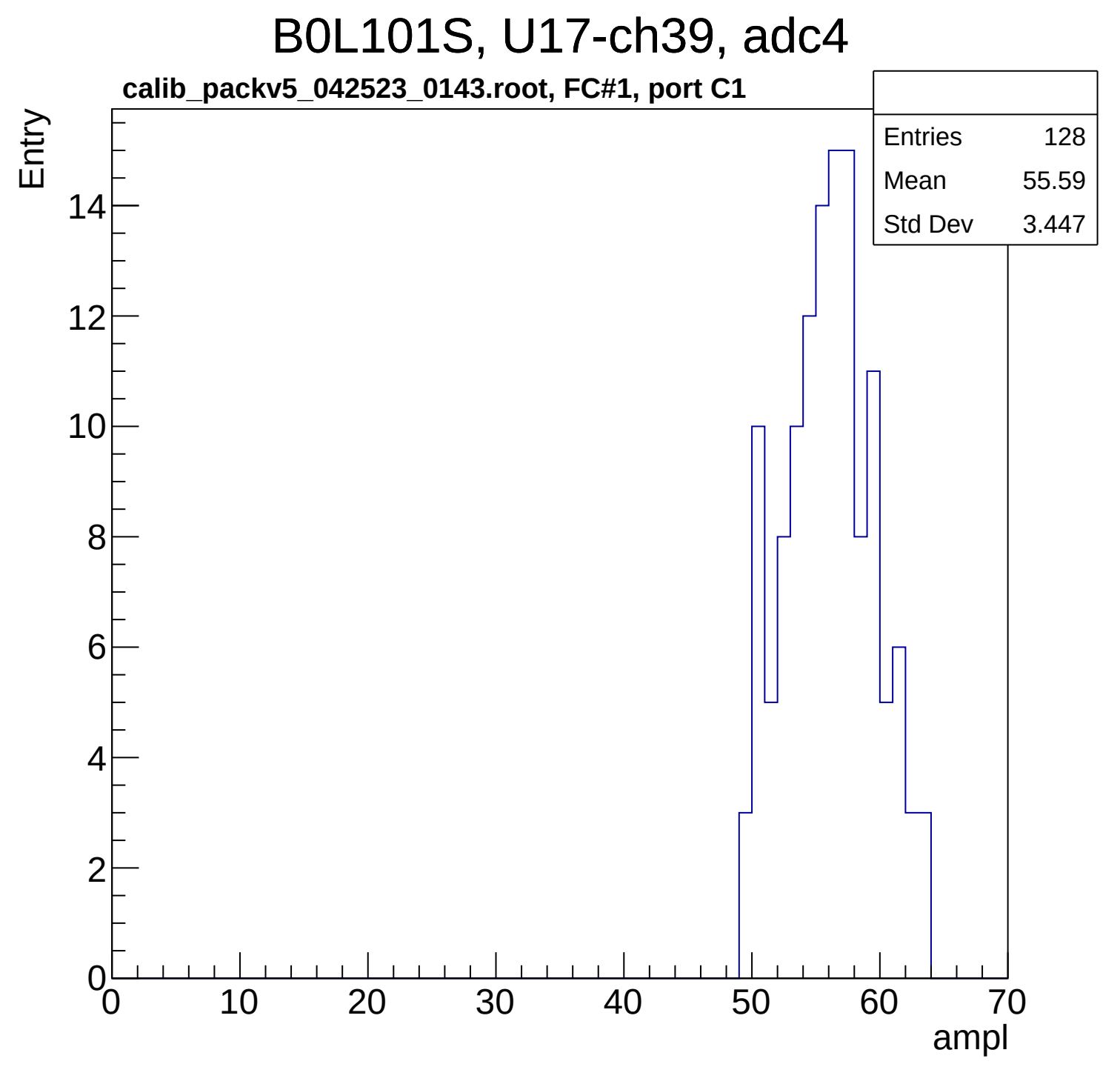

B0L101S, U17-ch38, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

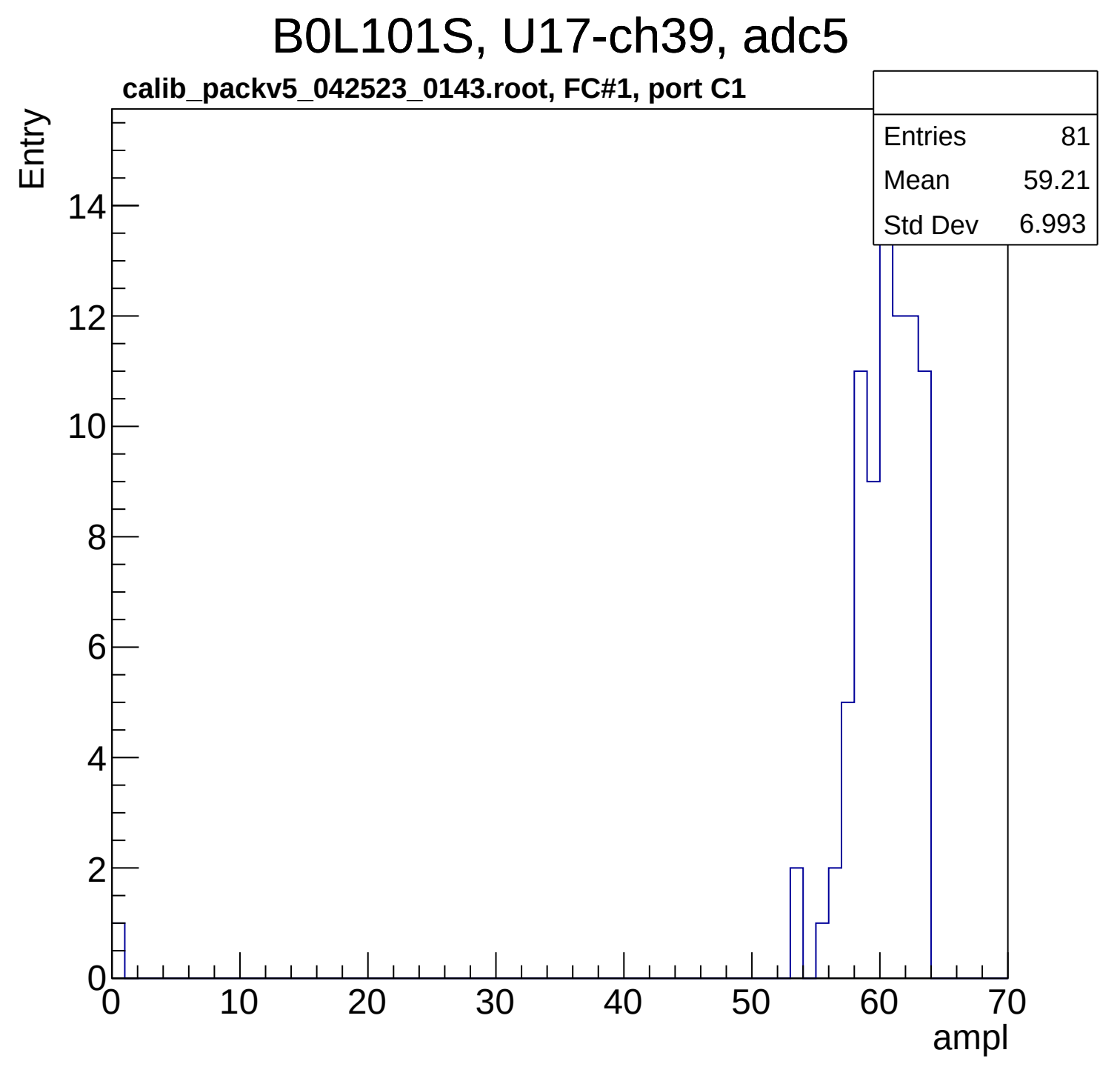

B0L101S, U17-ch39, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 28.86
Std Dev 4.066
Gaus mean : 29.8385
Gaus Width: 3.0775
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

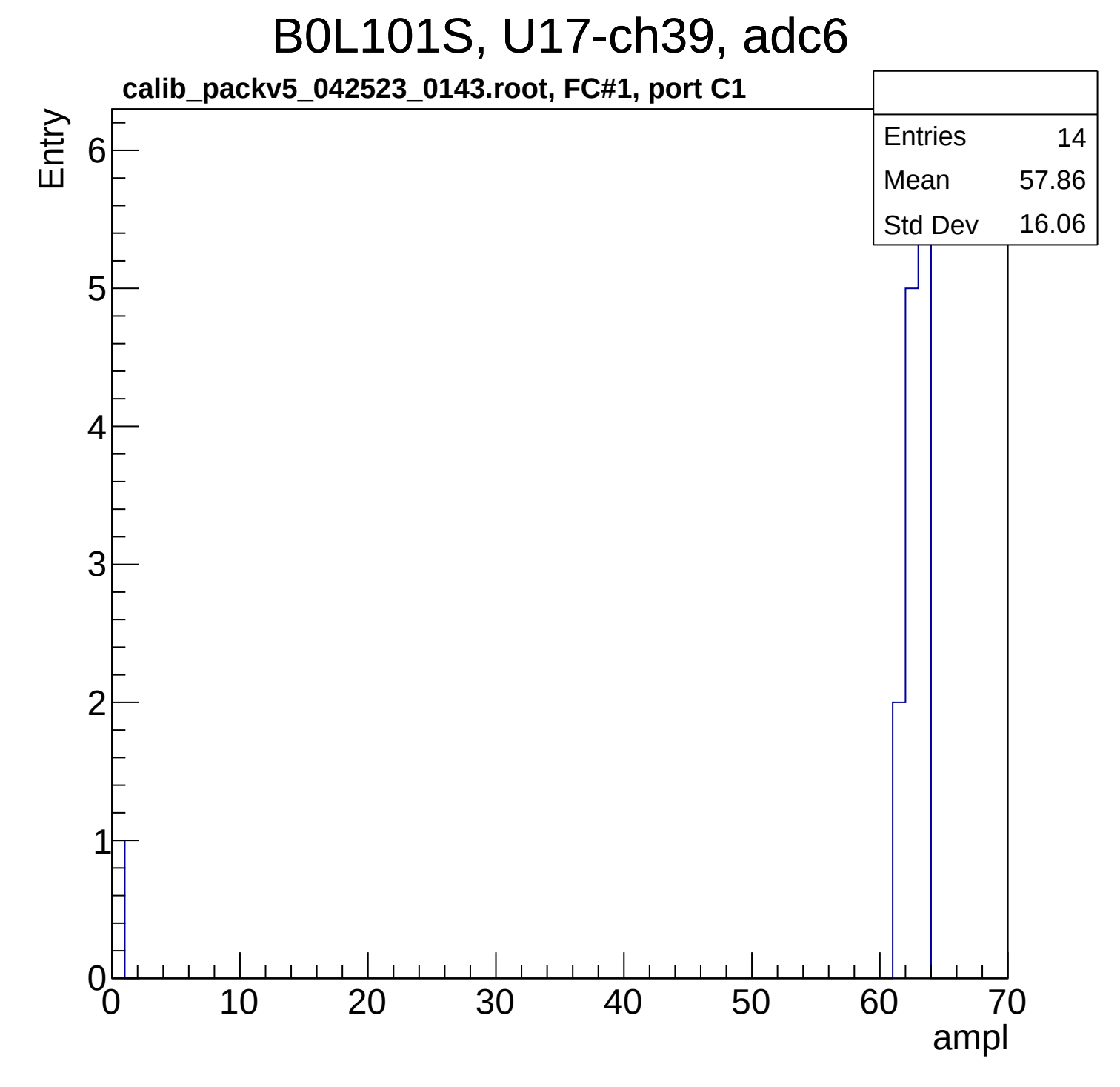

B0L101S, U17-ch39, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 152
Mean 36.47
Std Dev 3.401
Gaus mean : 36.9874
Gaus Width: 3.5271
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

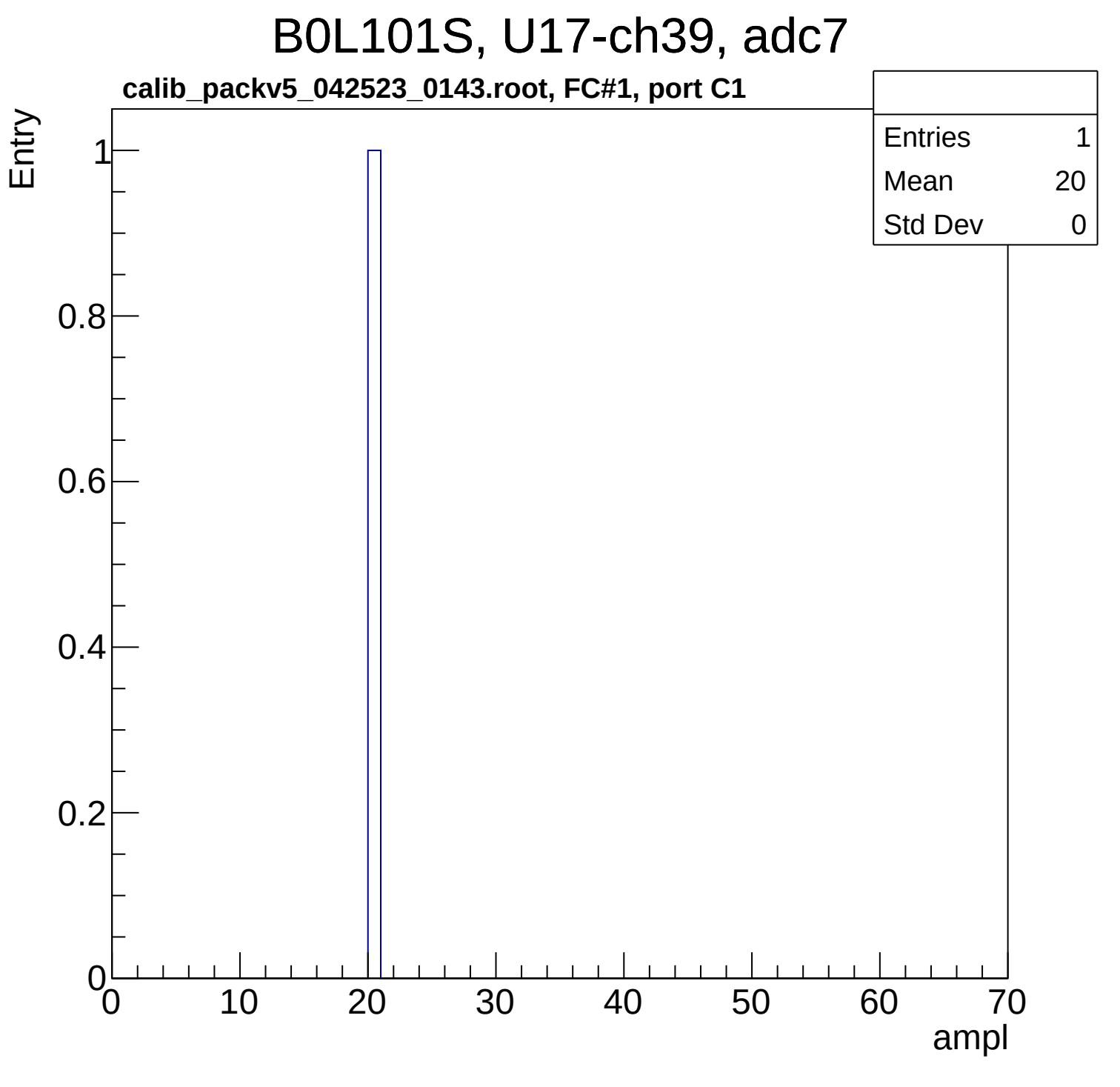

B0L101S, U17-ch39, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 43.14
Std Dev 3.477
Gaus mean : 43.7122
Gaus Width: 3.4205
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

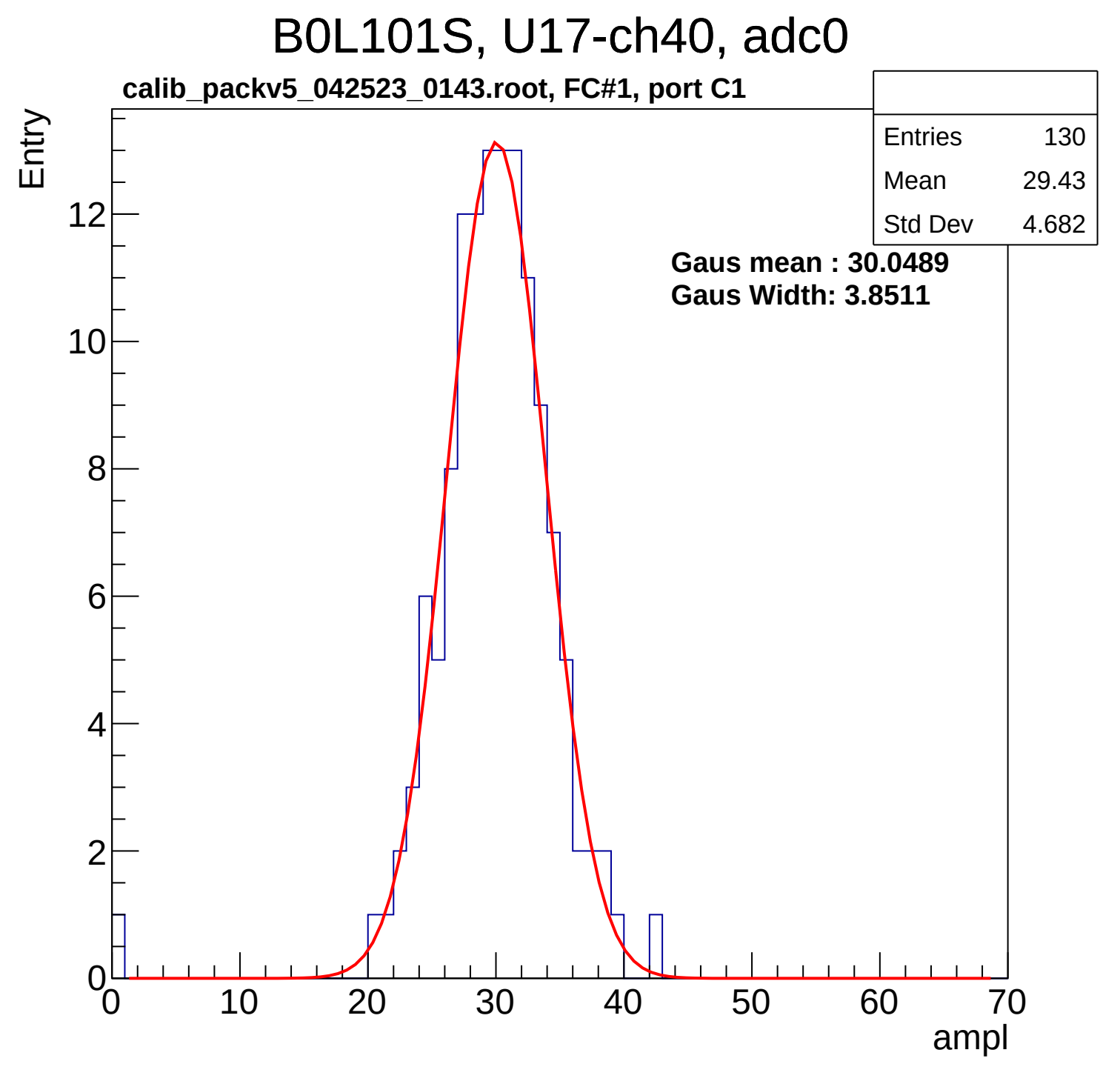

B0L101S, U17-ch39, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 49.13
Std Dev 3.694
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

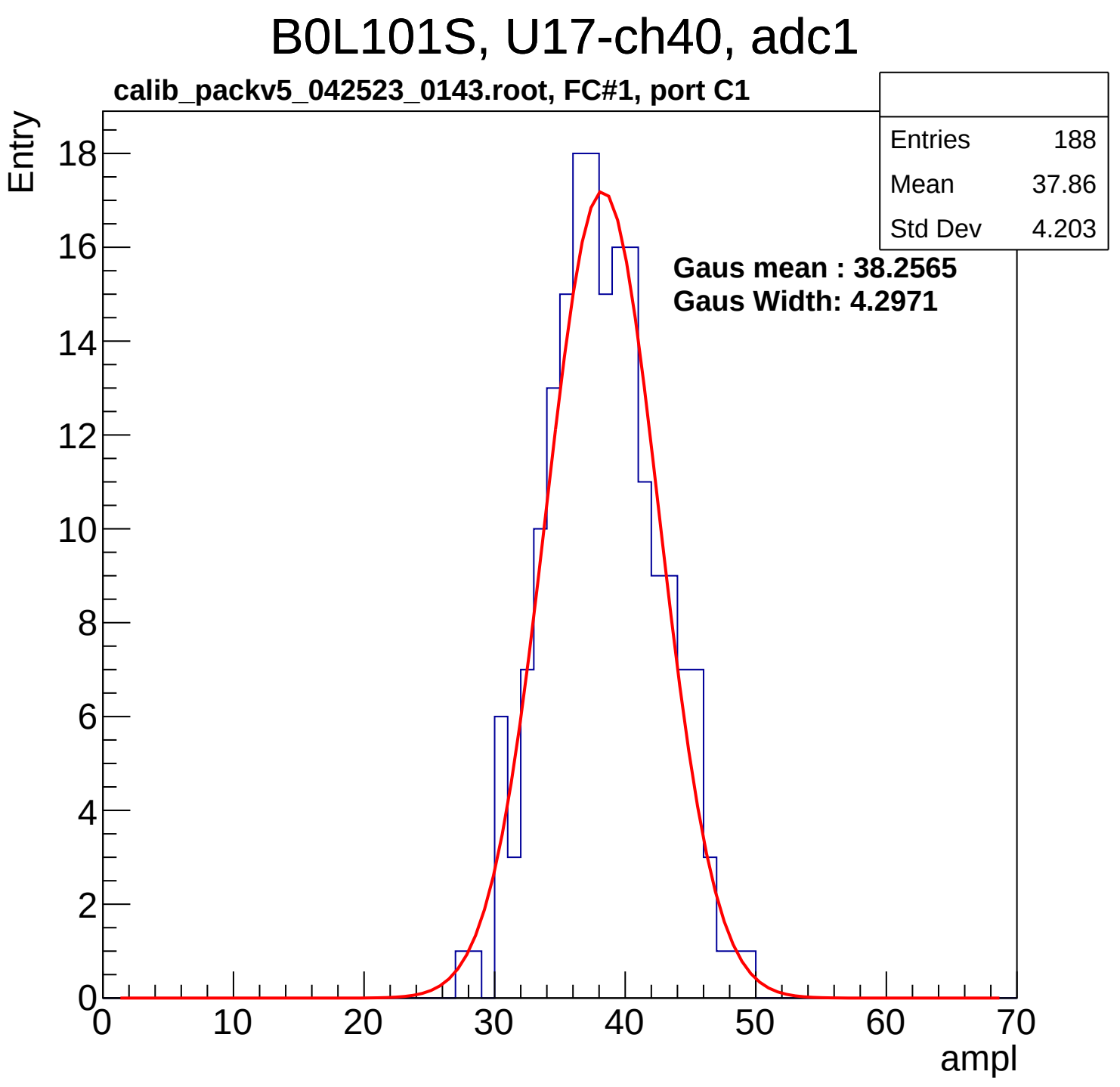

B0L101S, U17-ch39, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 55.59
Std Dev 3.447
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

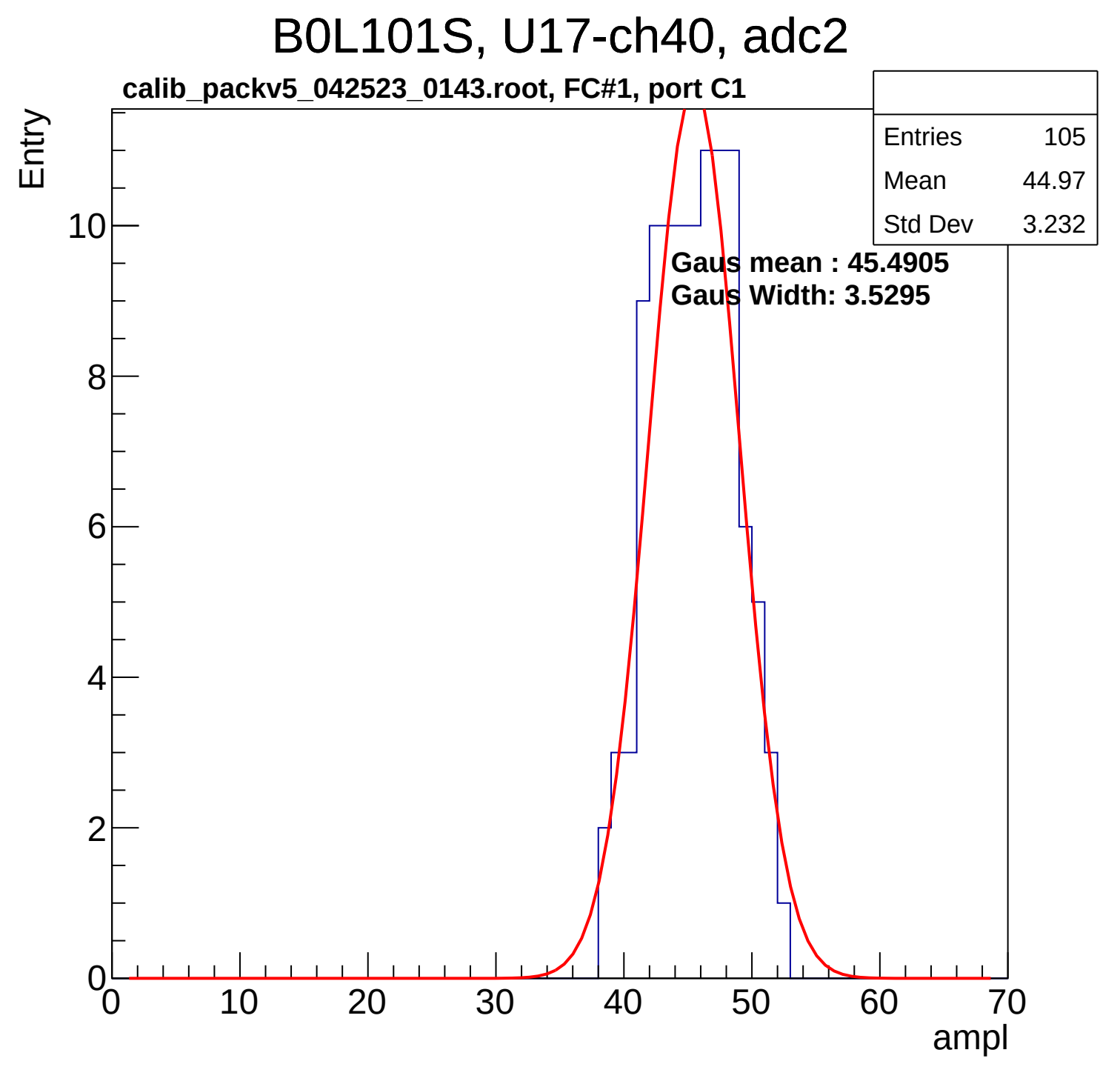

B0L101S, U17-ch39, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.21
Std Dev 6.993
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

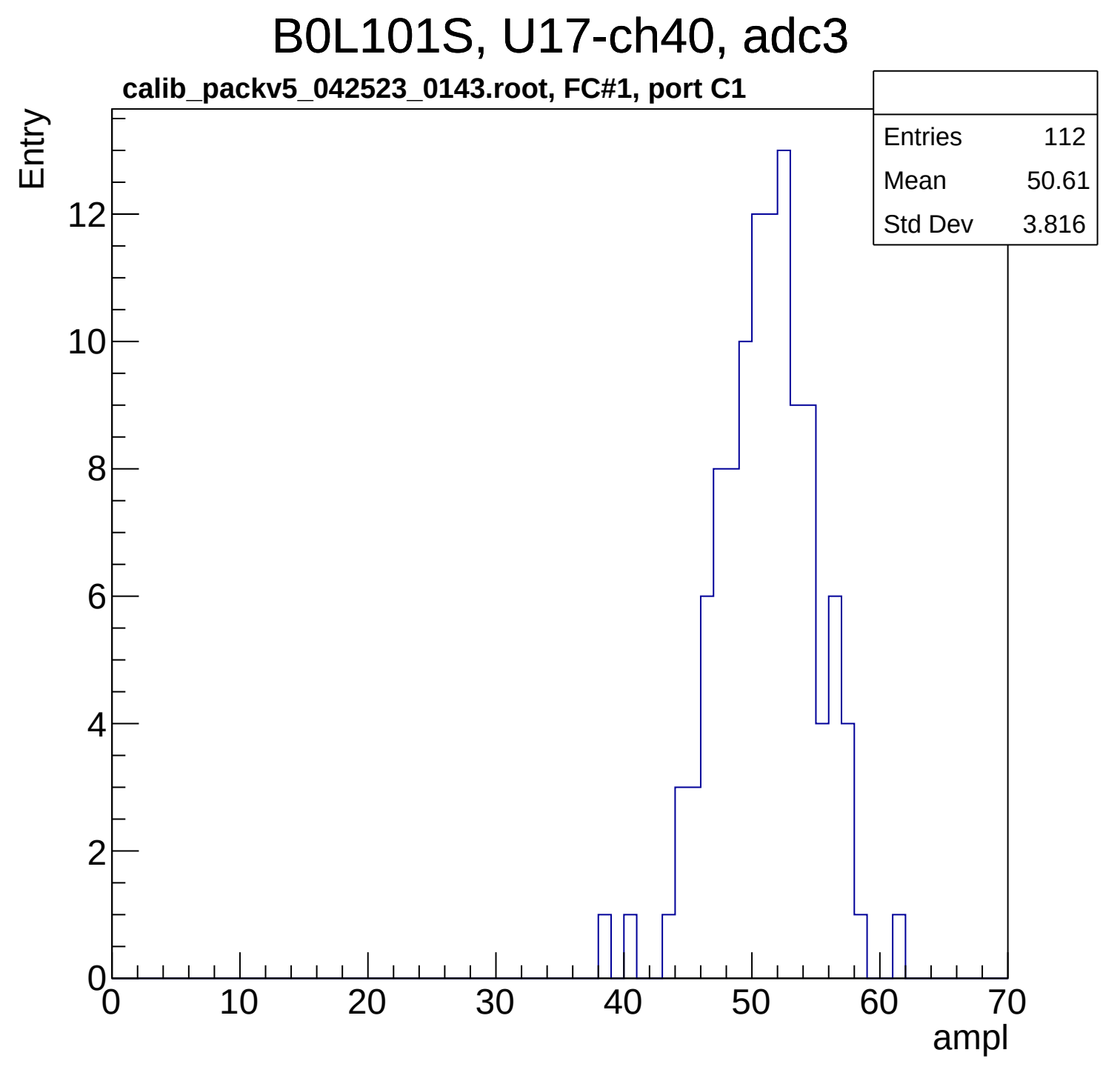

B0L101S, U17-ch39, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 57.86
Std Dev 16.06
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

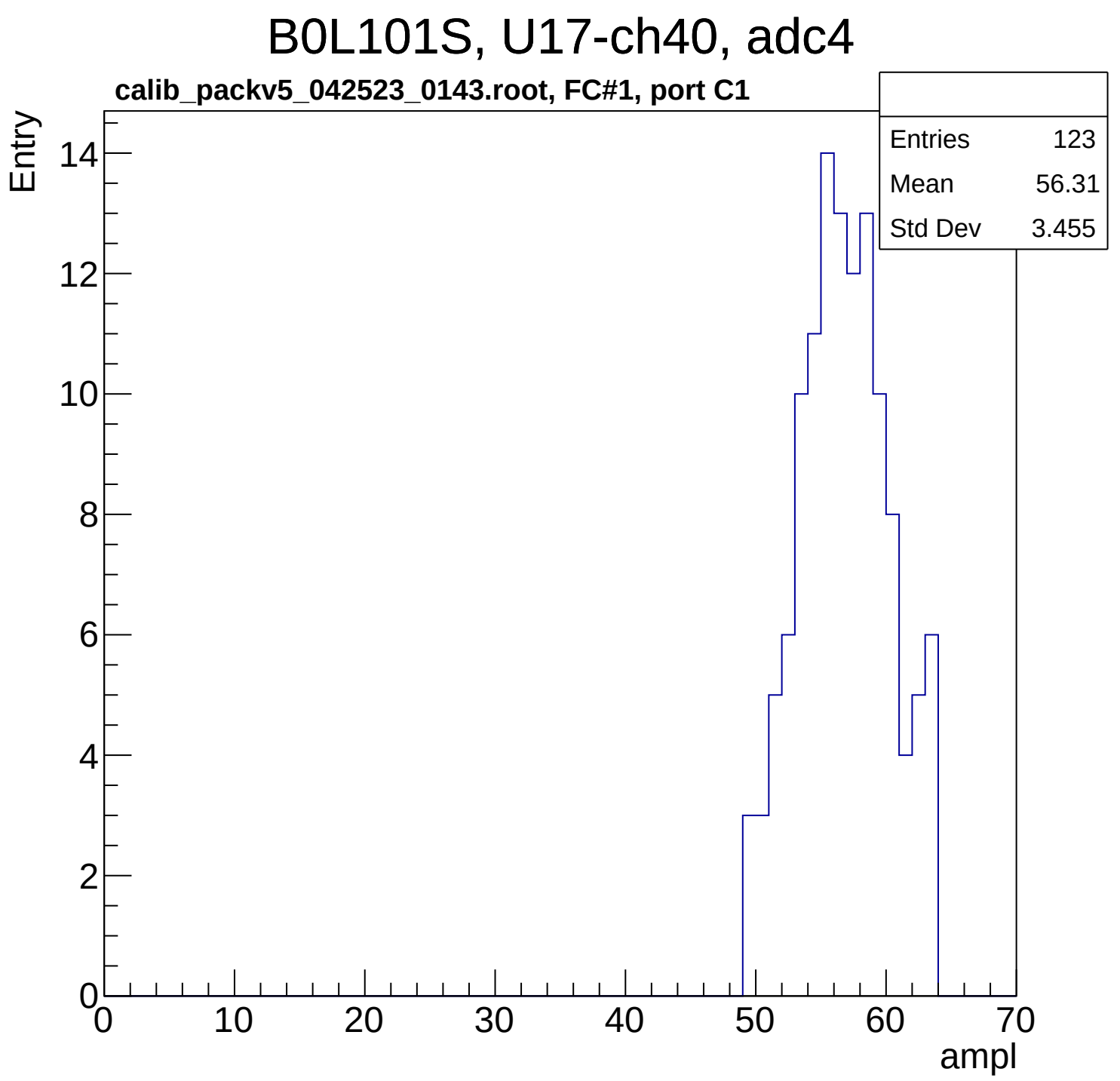

B0L101S, U17-ch39, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

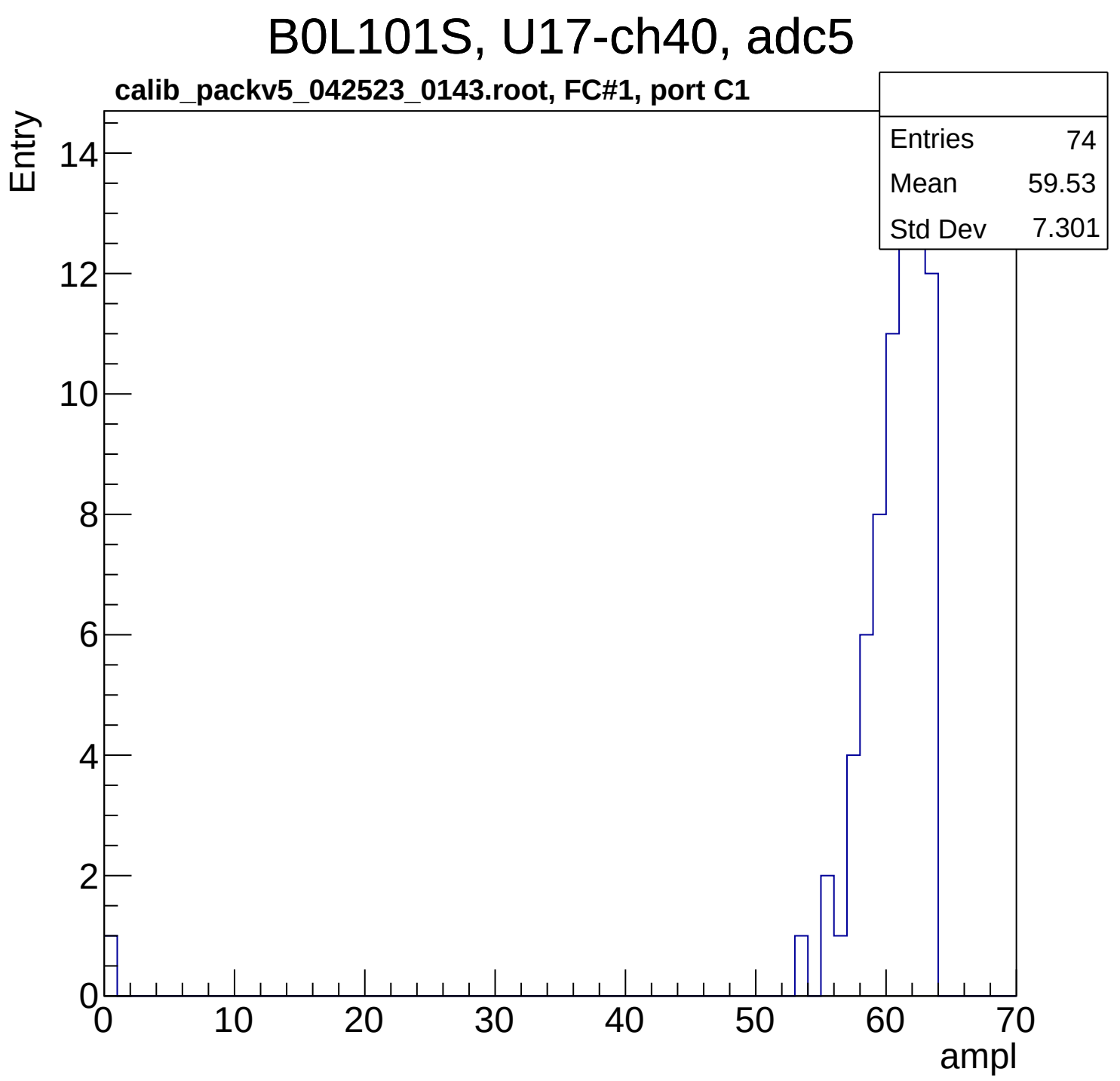

B0L101S, U17-ch40, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 29.43
Std Dev 4.682
Gaus mean : 30.0489
Gaus Width: 3.8511
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

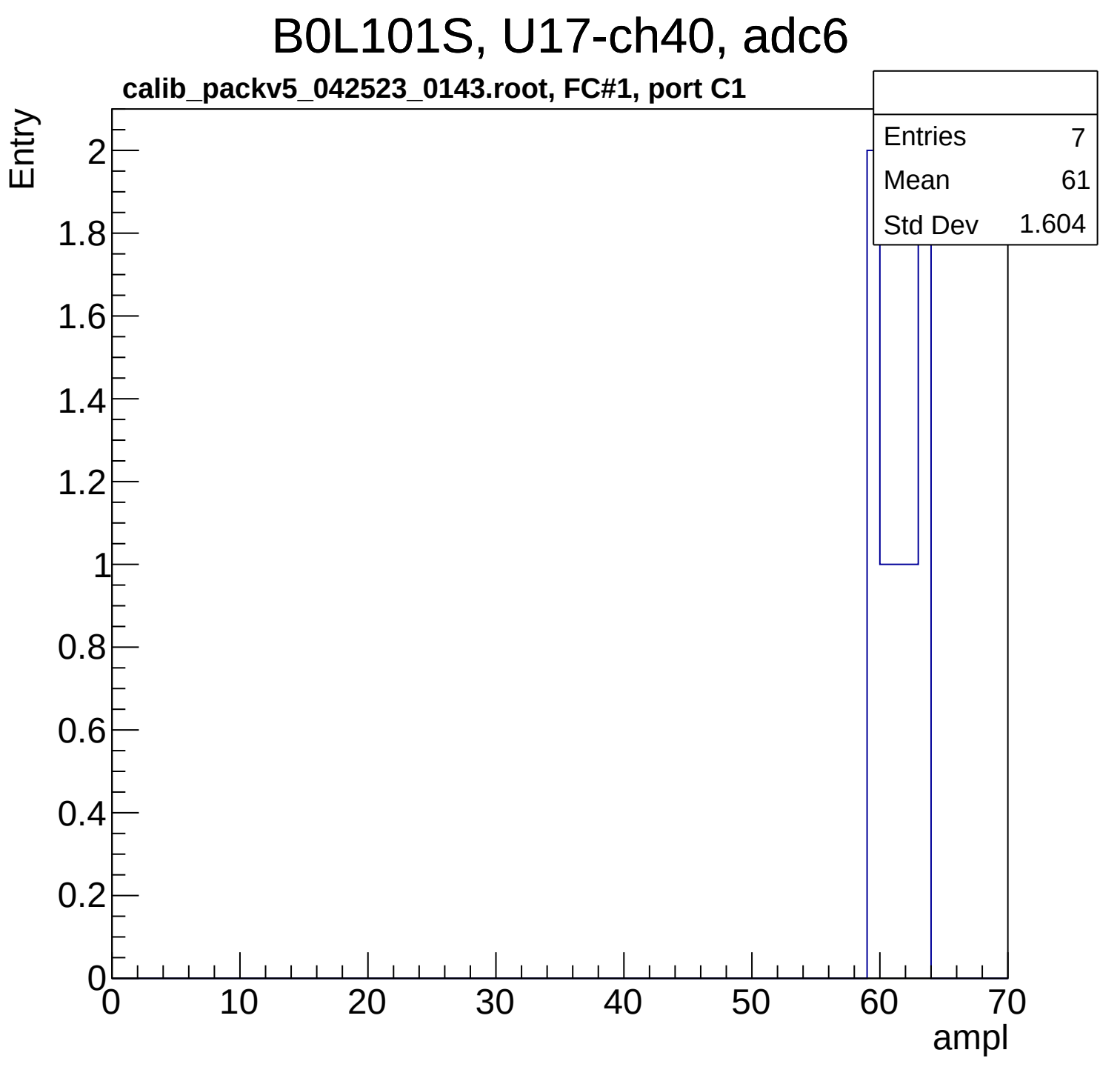

B0L101S, U17-ch40, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 188
Mean 37.86
Std Dev 4.203
Gaus mean : 38.2565
Gaus Width: 4.2971
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

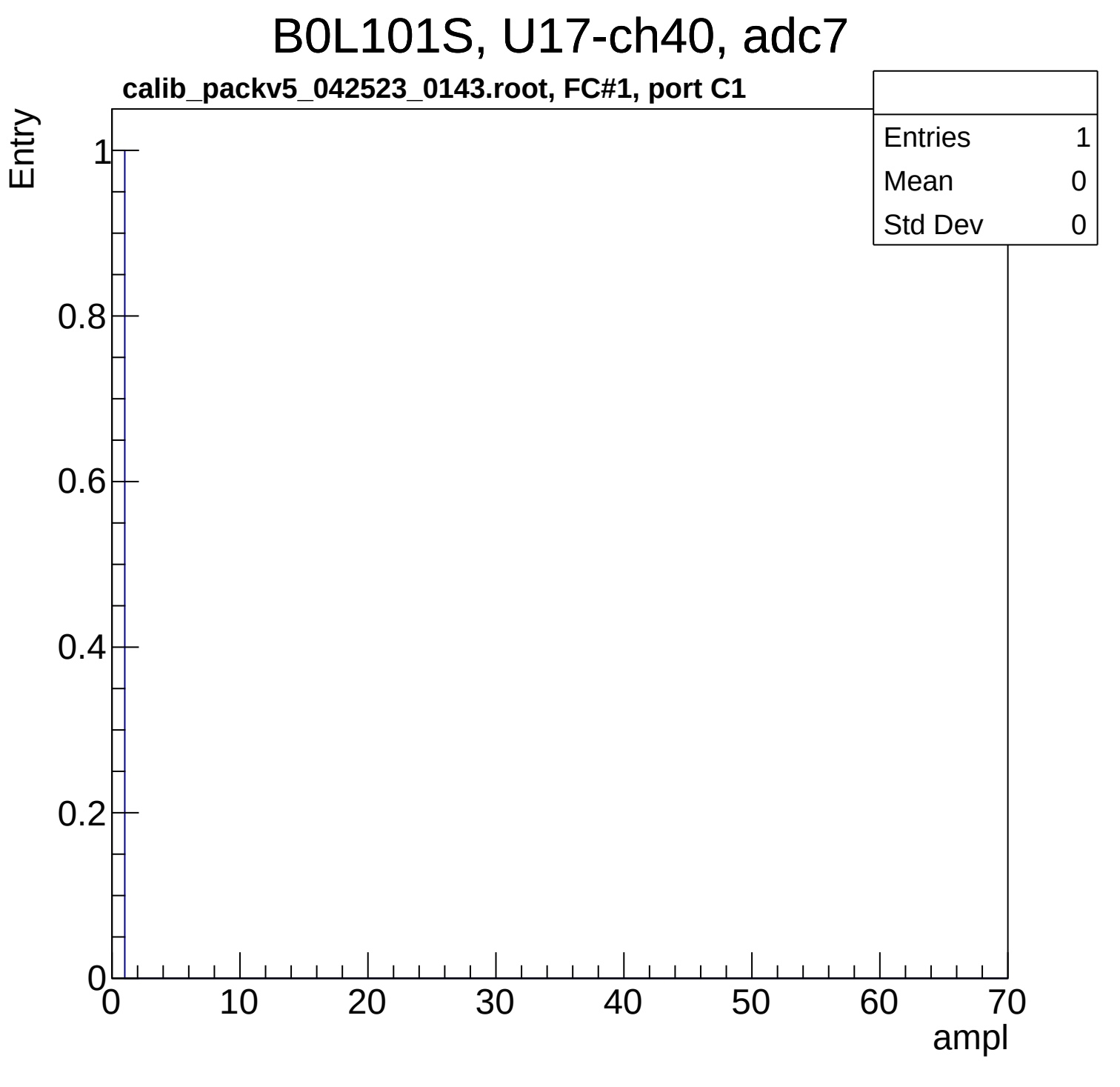

B0L101S, U17-ch40, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 44.97
Std Dev 3.232
Gaus mean : 45.4905
Gaus Width: 3.5295
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

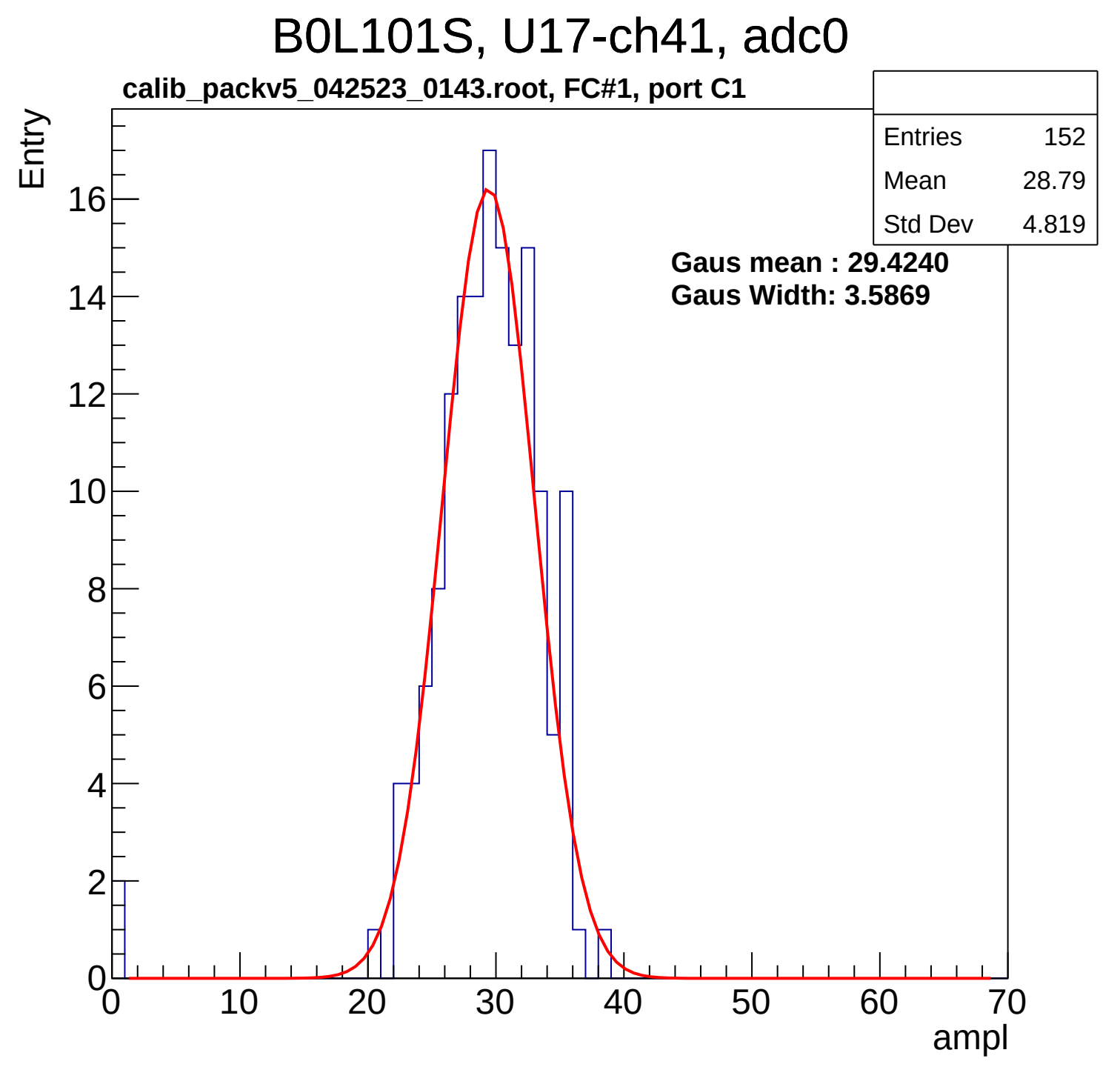

B0L101S, U17-ch40, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 50.61
Std Dev 3.816
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

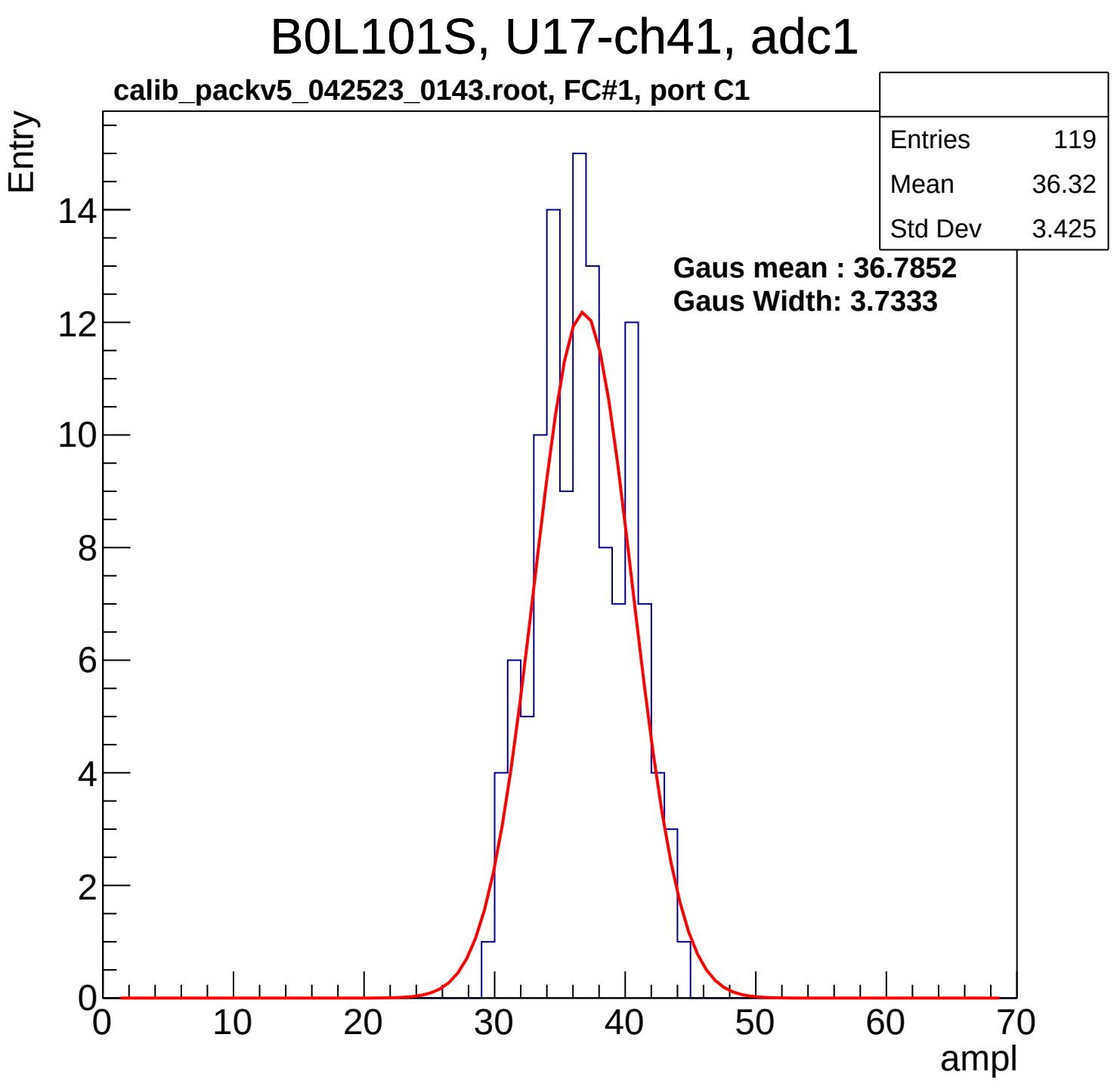

B0L101S, U17-ch40, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 56.31
Std Dev 3.455
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

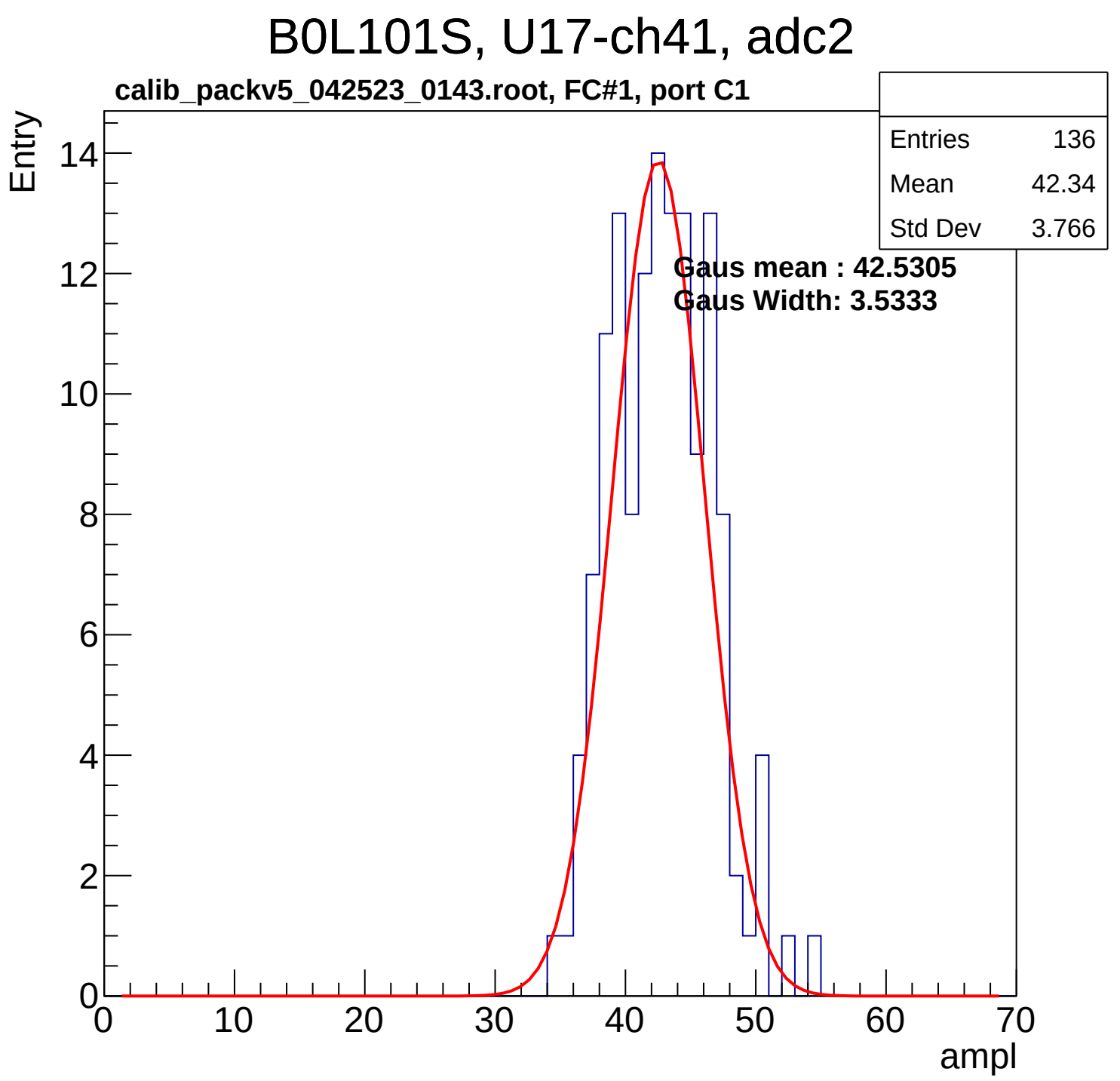

B0L101S, U17-ch40, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 74
Mean 59.53
Std Dev 7.301
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

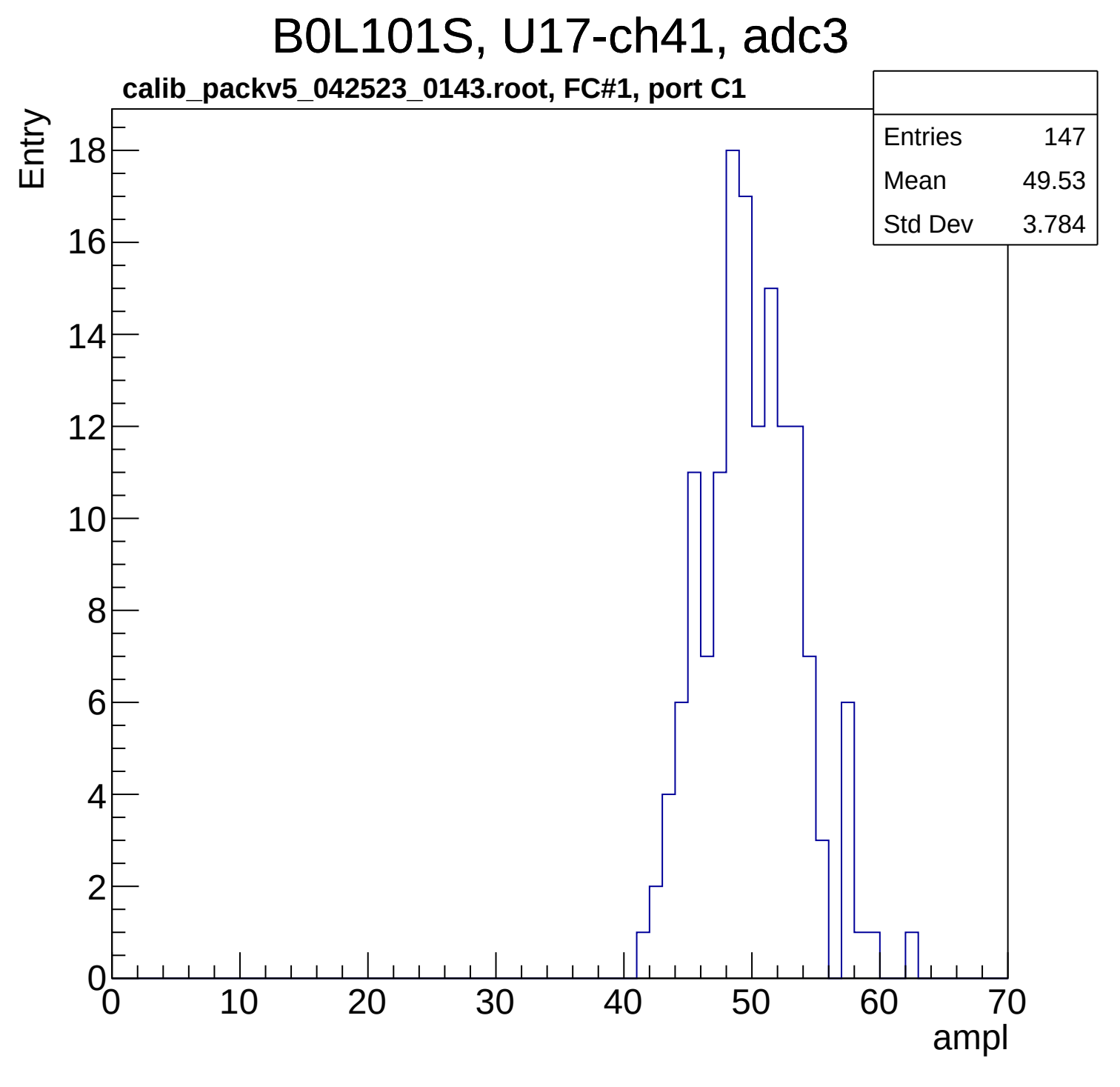

B0L101S, U17-ch40, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 7
Mean 61
Std Dev 1.604
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

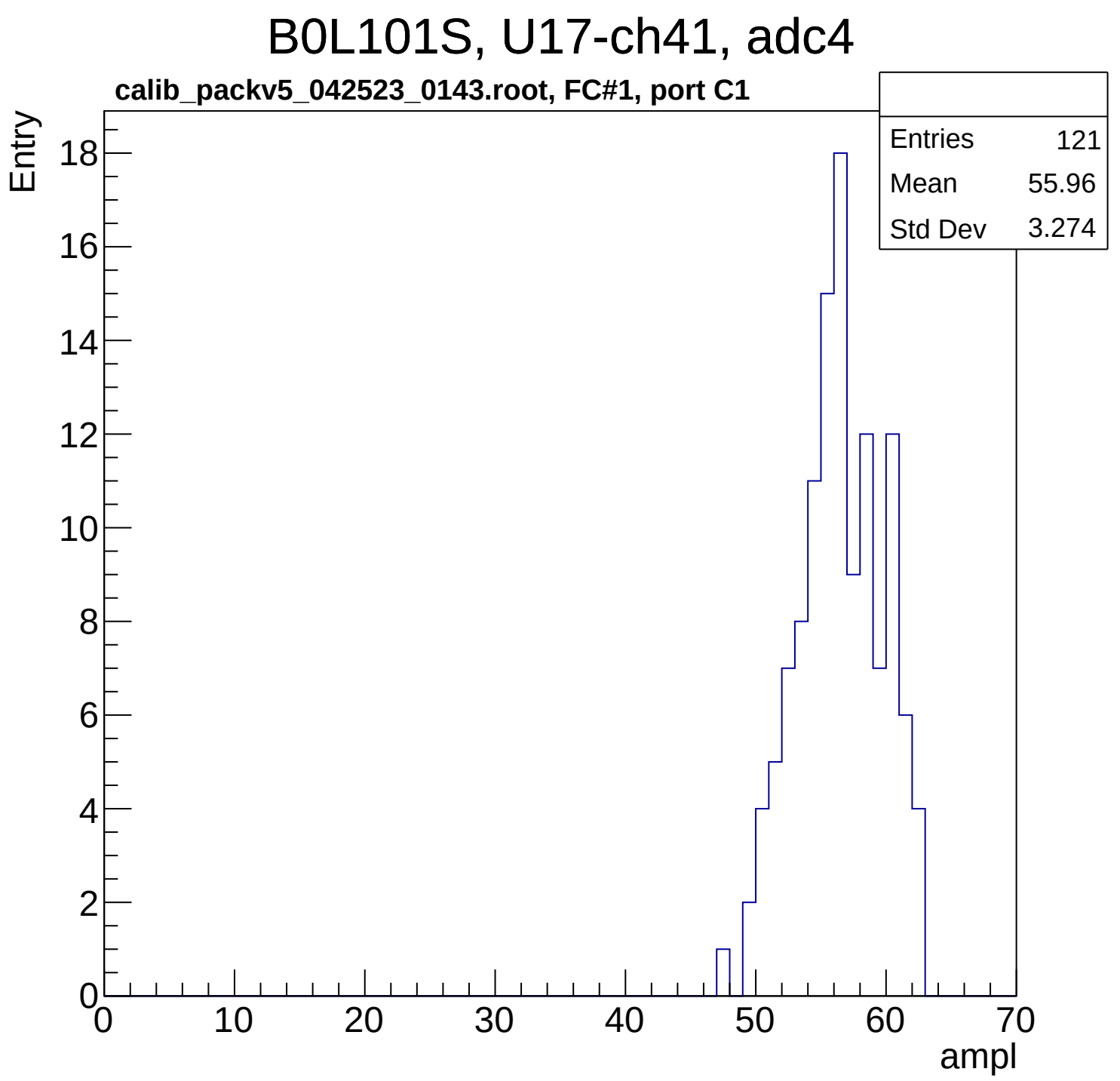

B0L101S, U17-ch40, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

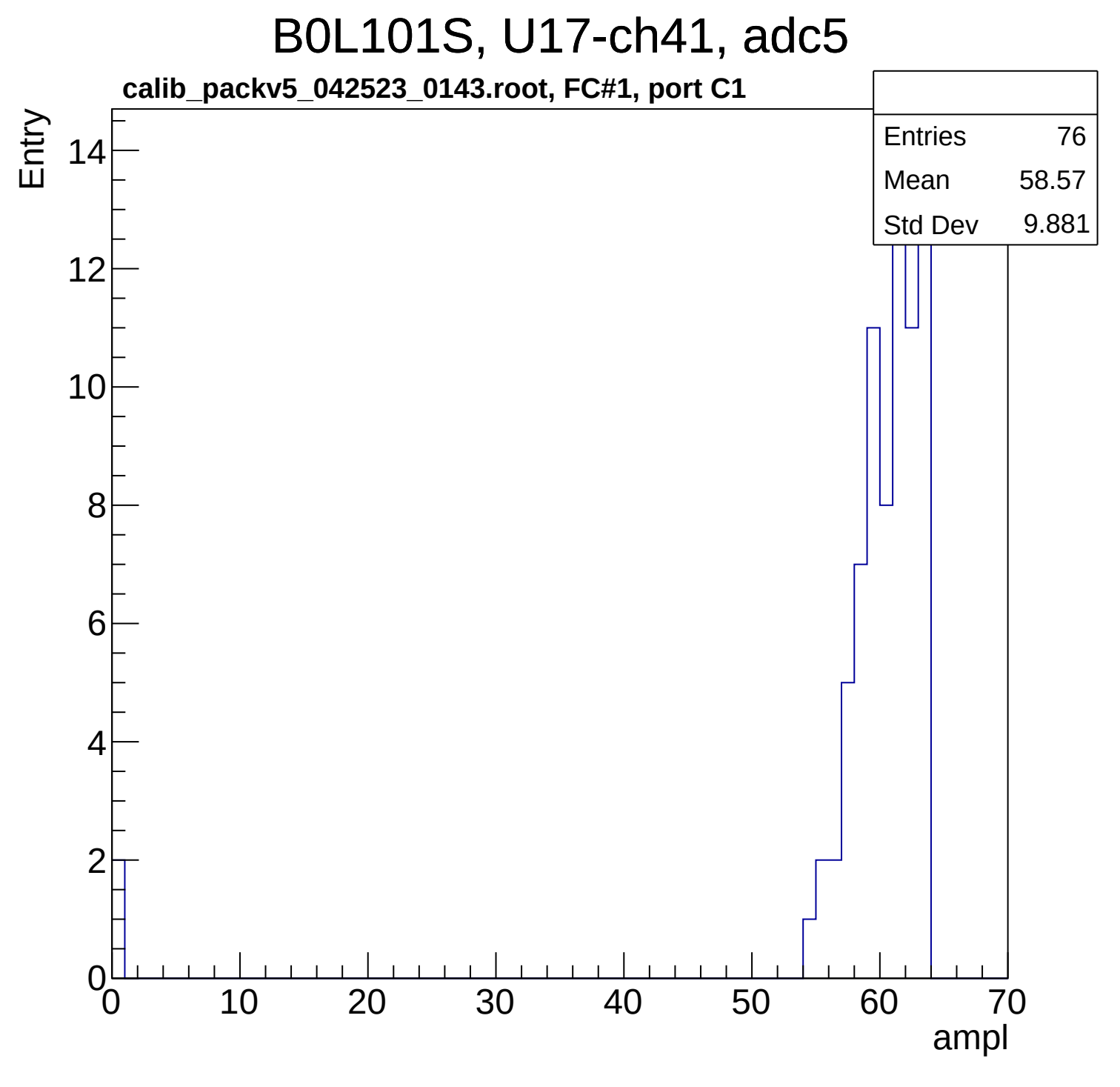

B0L101S, U17-ch41, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 152
Mean 28.79
Std Dev 4.819
Gaus mean : 29.4240
Gaus Width: 3.5869
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

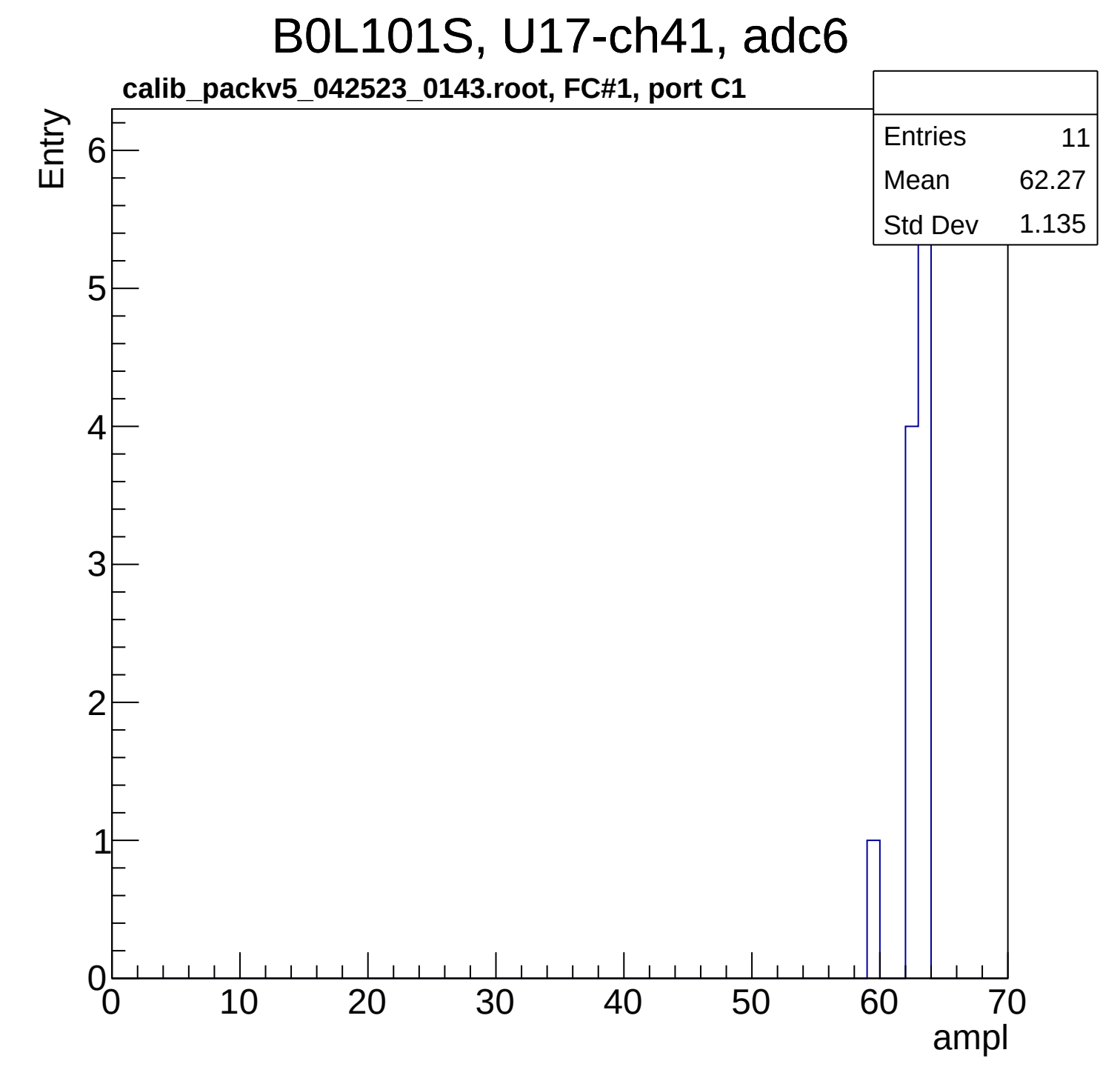

B0L101S, U17-ch41, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 36.32
Std Dev 3.425
Gaus mean : 36.7852
Gaus Width: 3.7333
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

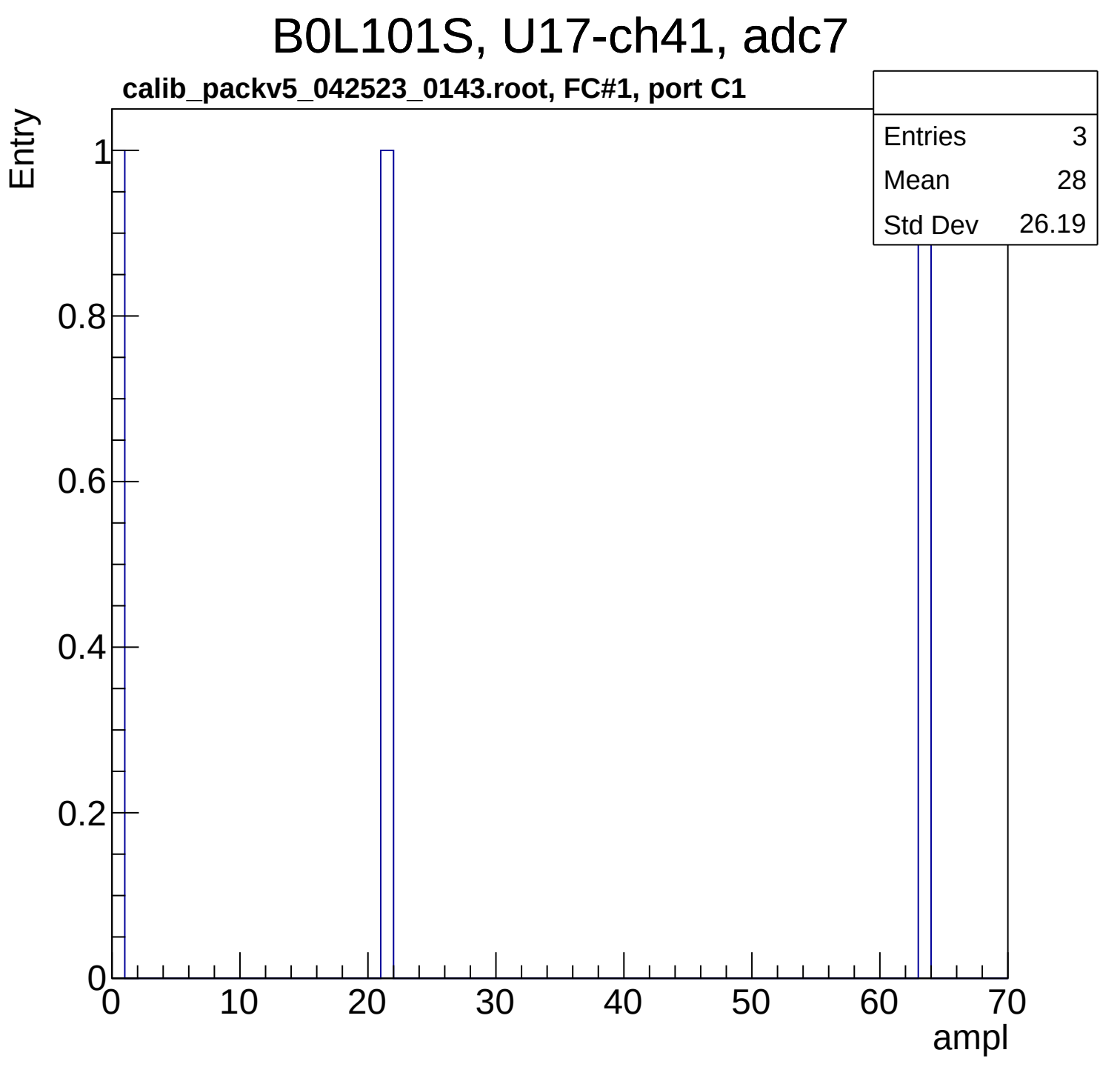

B0L101S, U17-ch41, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 42.34
Std Dev 3.766
Gaus mean : 42.5305
Gaus Width: 3.5333
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

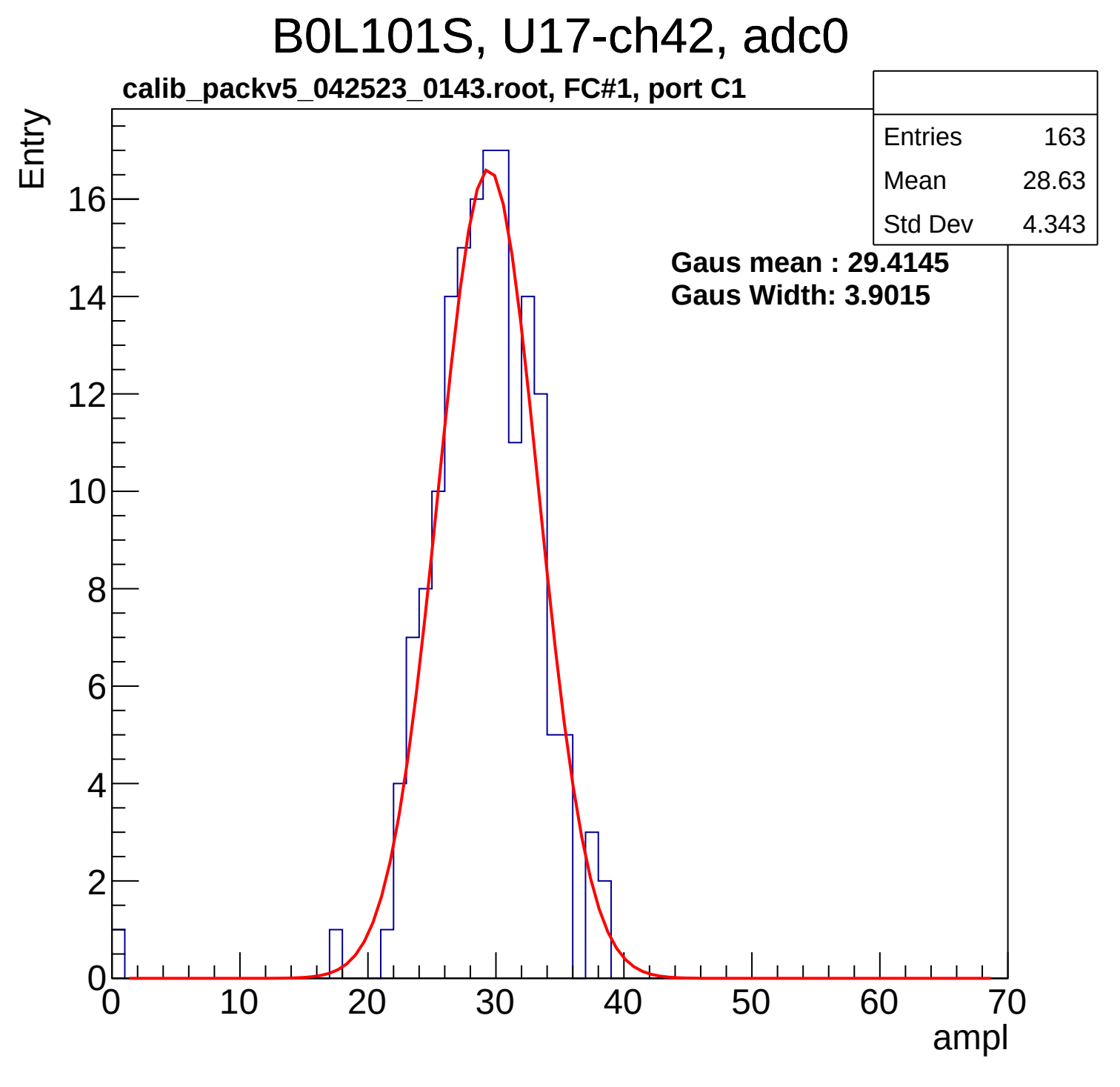

B0L101S, U17-ch41, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 49.53
Std Dev 3.784
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

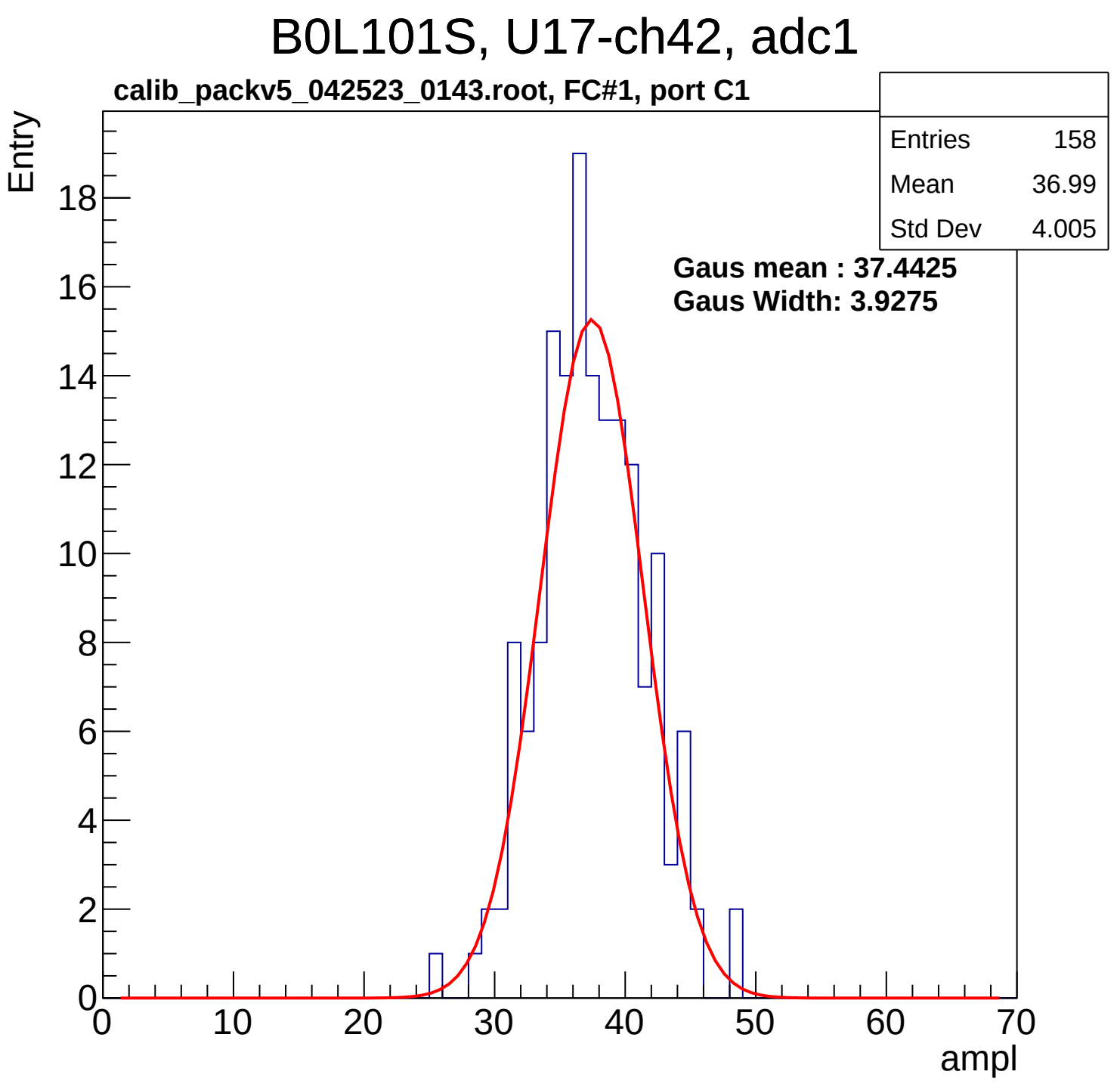

B0L101S, U17-ch41, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 55.96
Std Dev 3.274
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

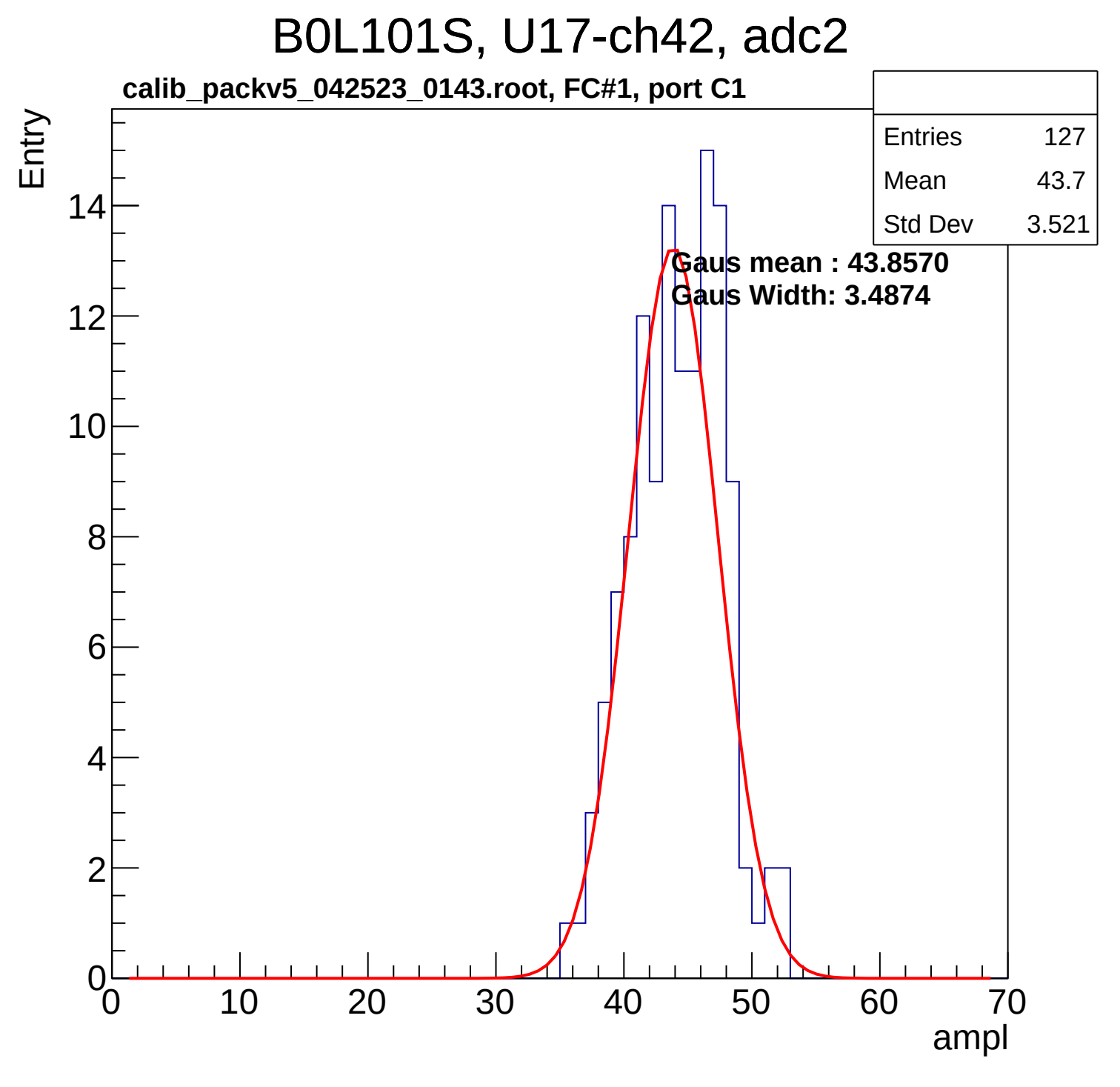

B0L101S, U17-ch41, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 76
Mean 58.57
Std Dev 9.881
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

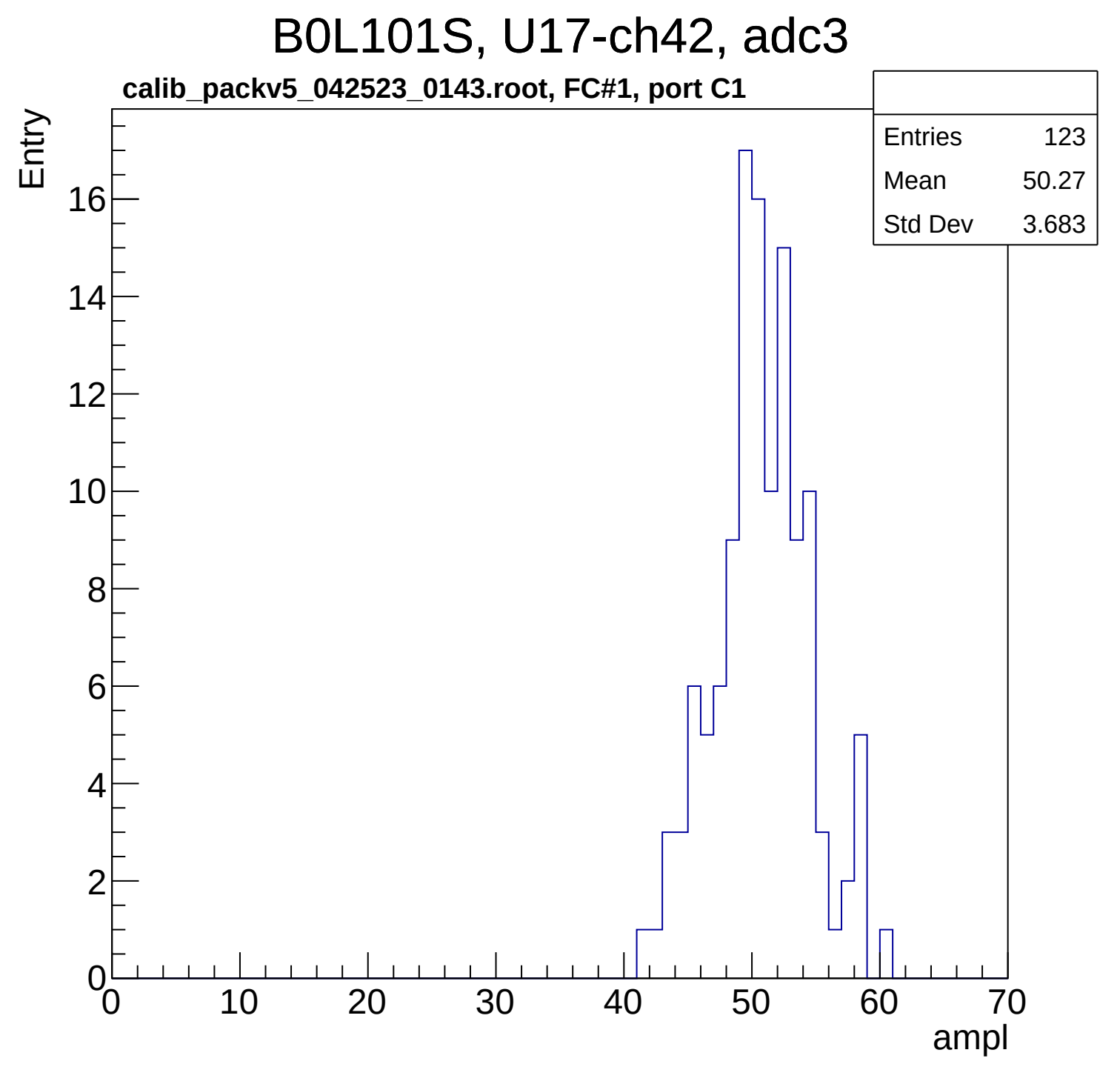

B0L101S, U17-ch41, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 62.27
Std Dev 1.135
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

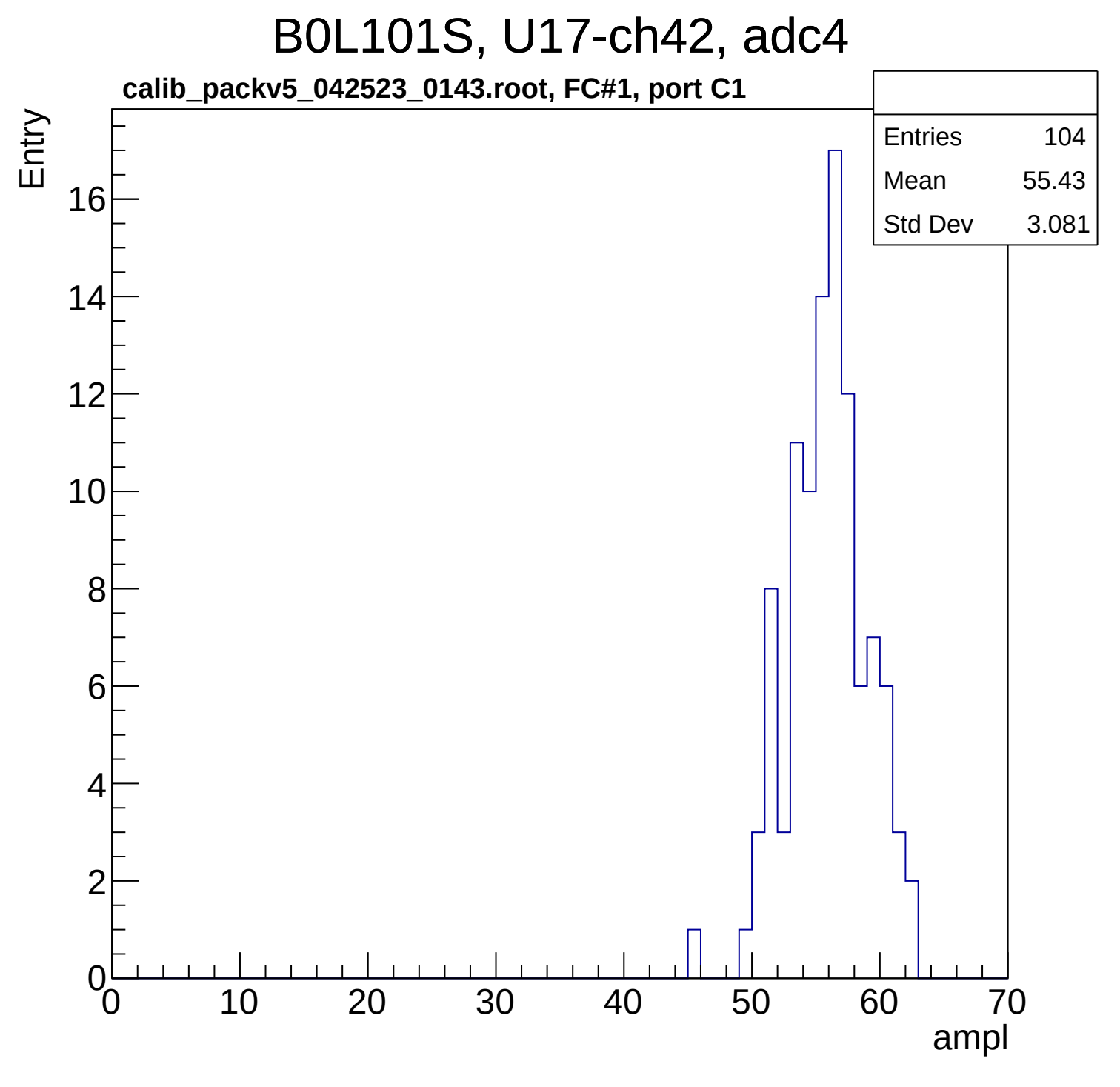

B0L101S, U17-ch41, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 28
Std Dev 26.19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

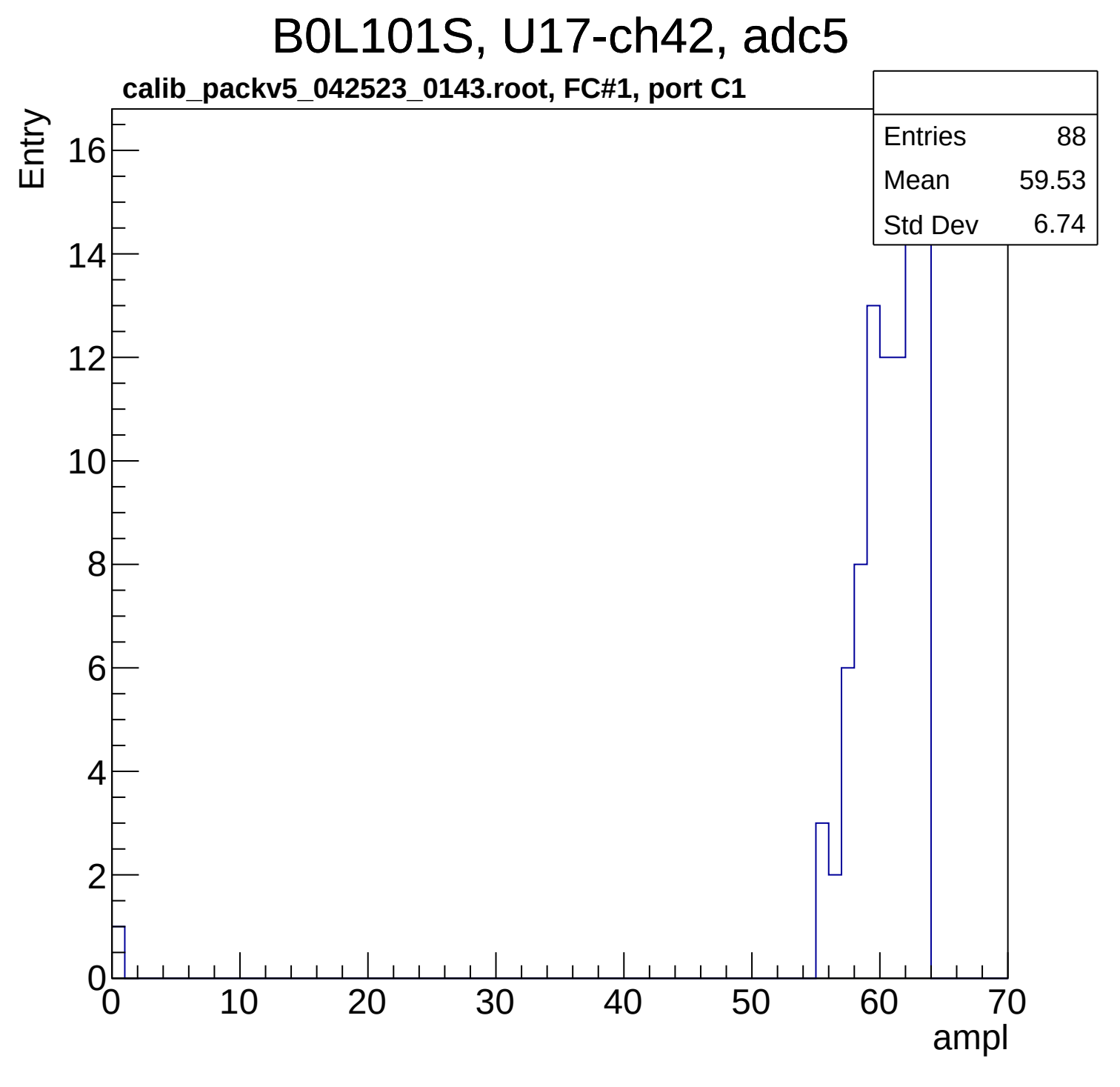

B0L101S, U17-ch42, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 28.63
Std Dev 4.343
Gaus mean : 29.4145
Gaus Width: 3.9015
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

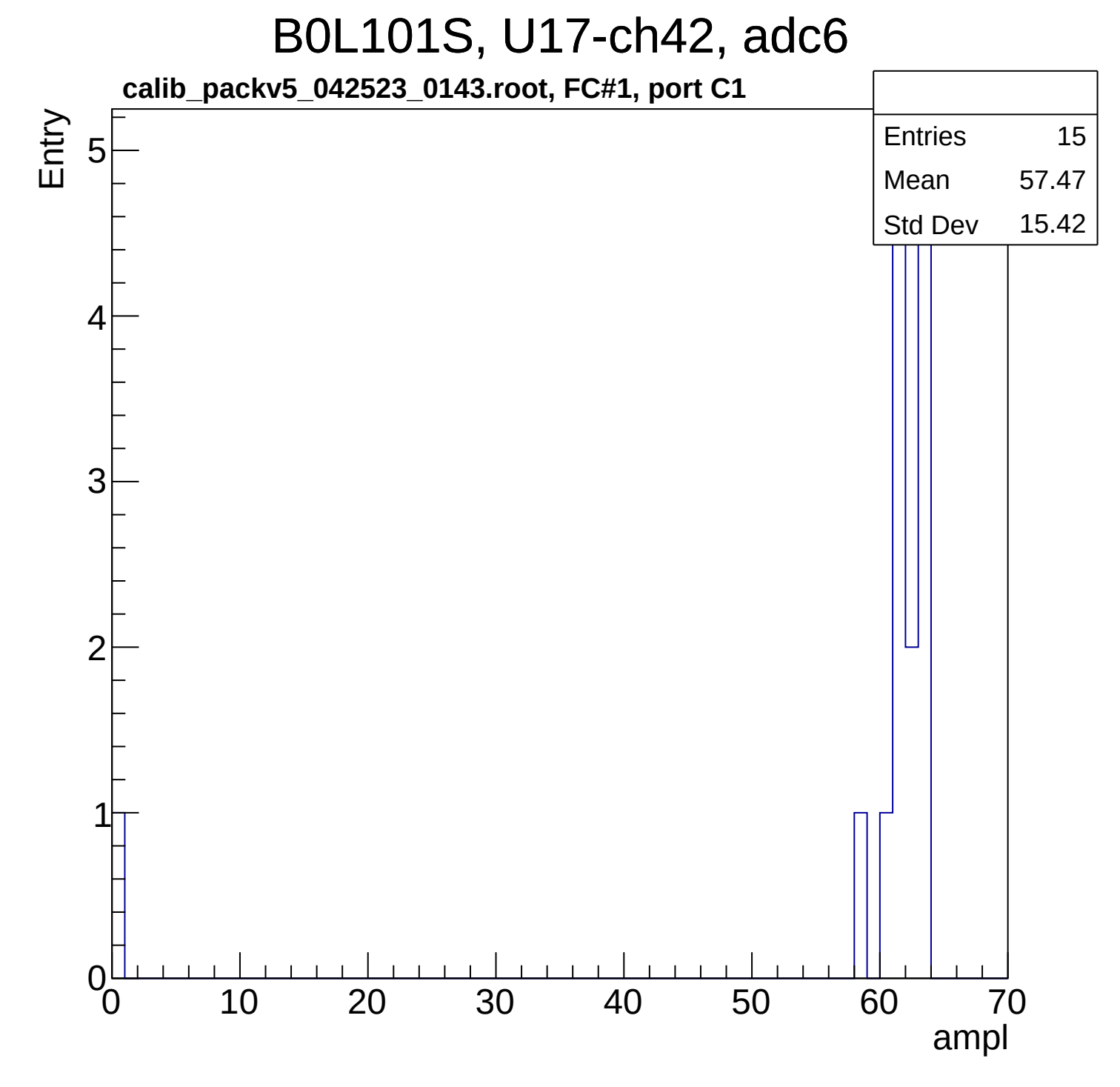

B0L101S, U17-ch42, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 158
Mean 36.99
Std Dev 4.005
Gaus mean : 37.4425
Gaus Width: 3.9275
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

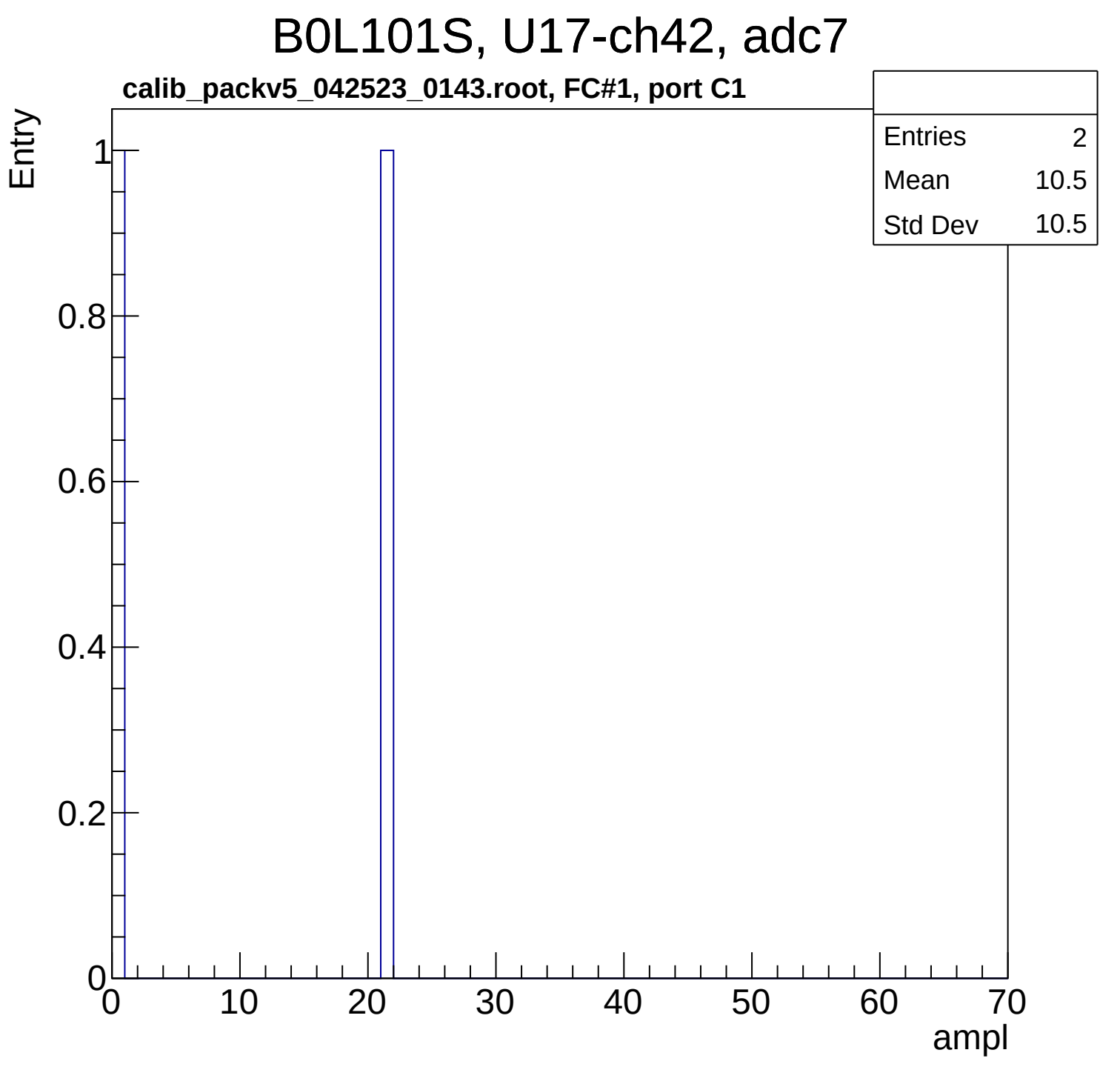

B0L101S, U17-ch42, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 43.7
Std Dev 3.521
Gaus mean : 43.8570
Gaus Width: 3.4874
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

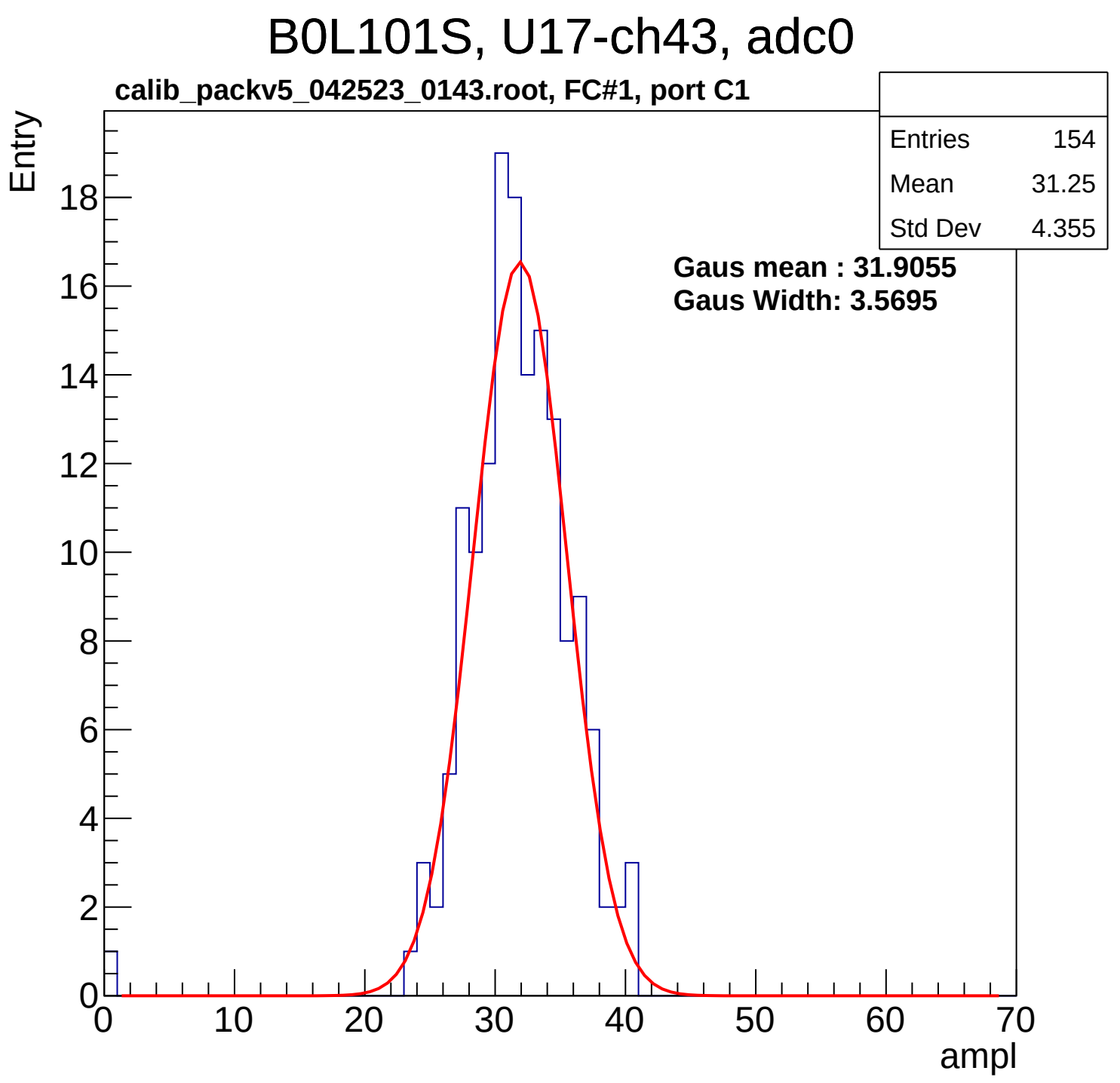

B0L101S, U17-ch42, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 50.27
Std Dev 3.683
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

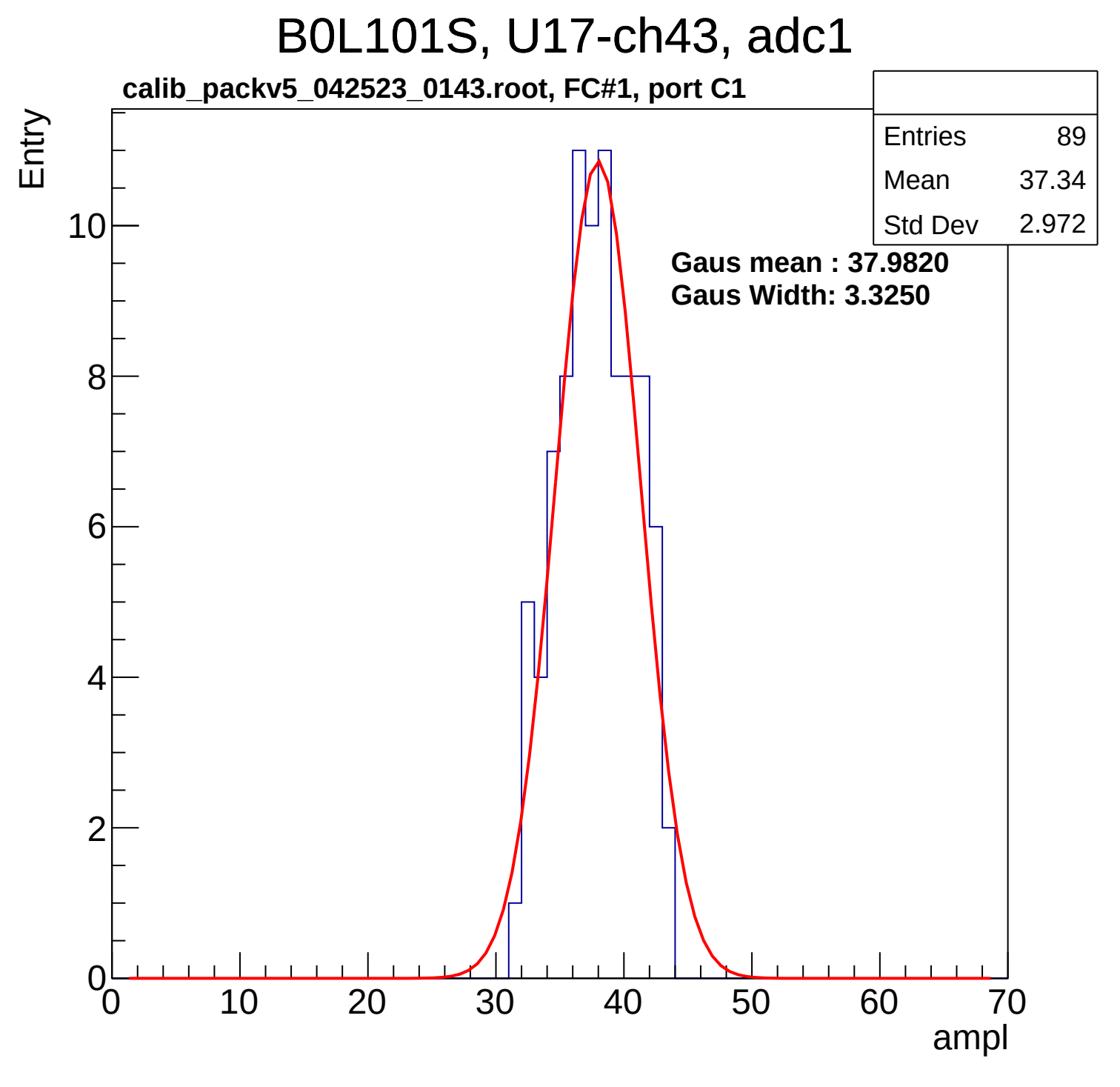

B0L101S, U17-ch42, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 55.43
Std Dev 3.081
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

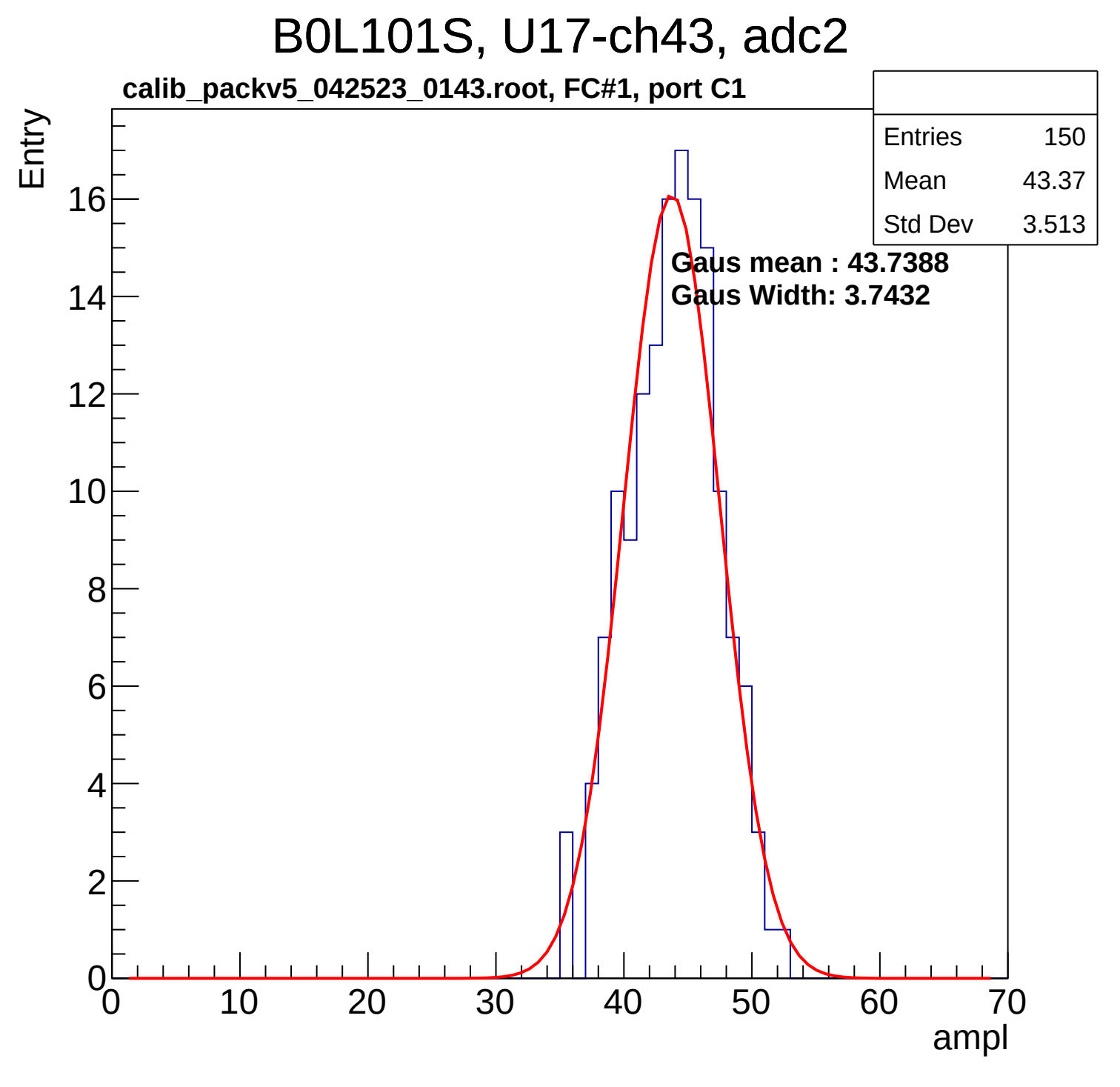

B0L101S, U17-ch42, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 88
Mean 59.53
Std Dev 6.74
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

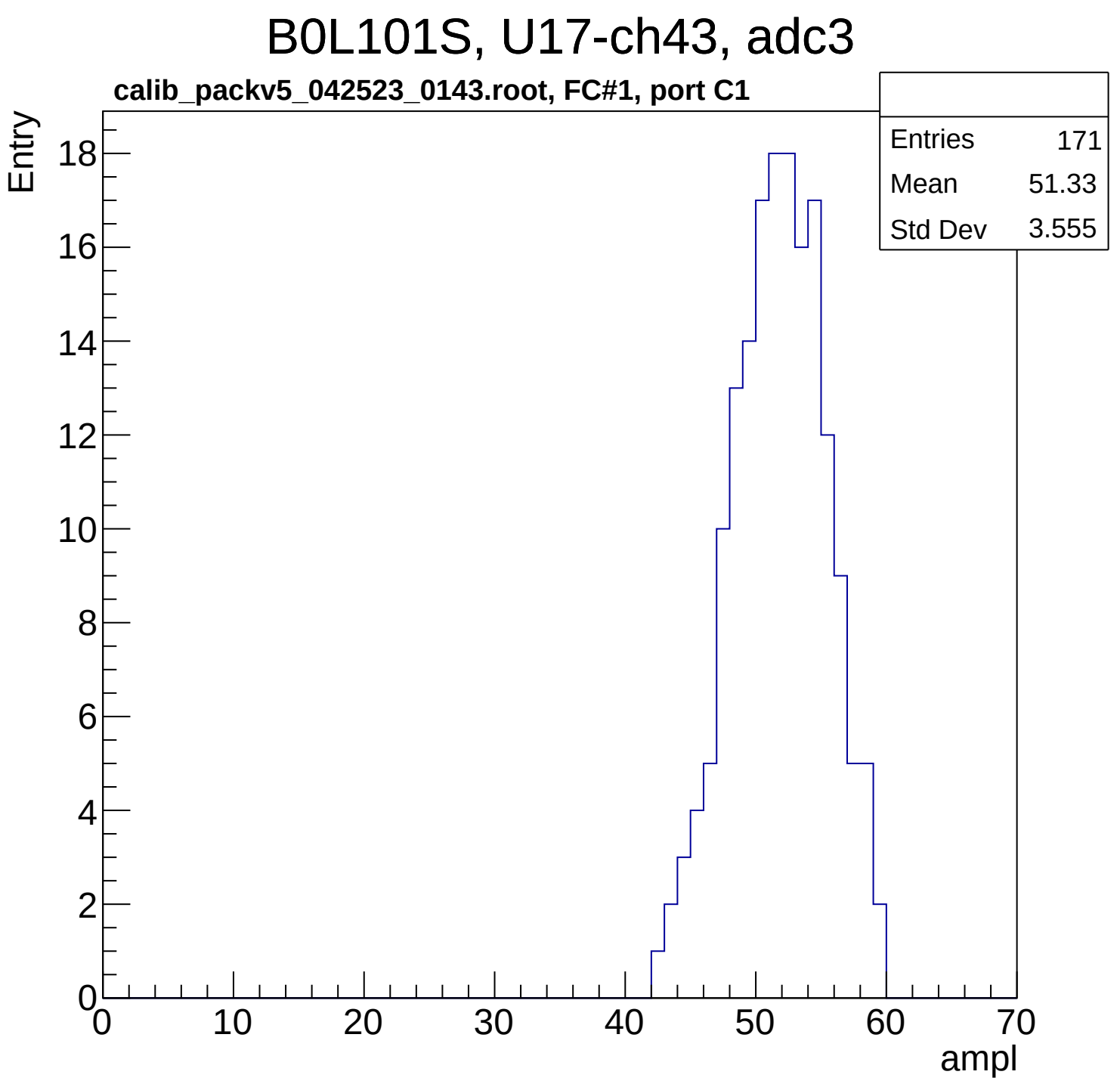

B0L101S, U17-ch42, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 15
Mean 57.47
Std Dev 15.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

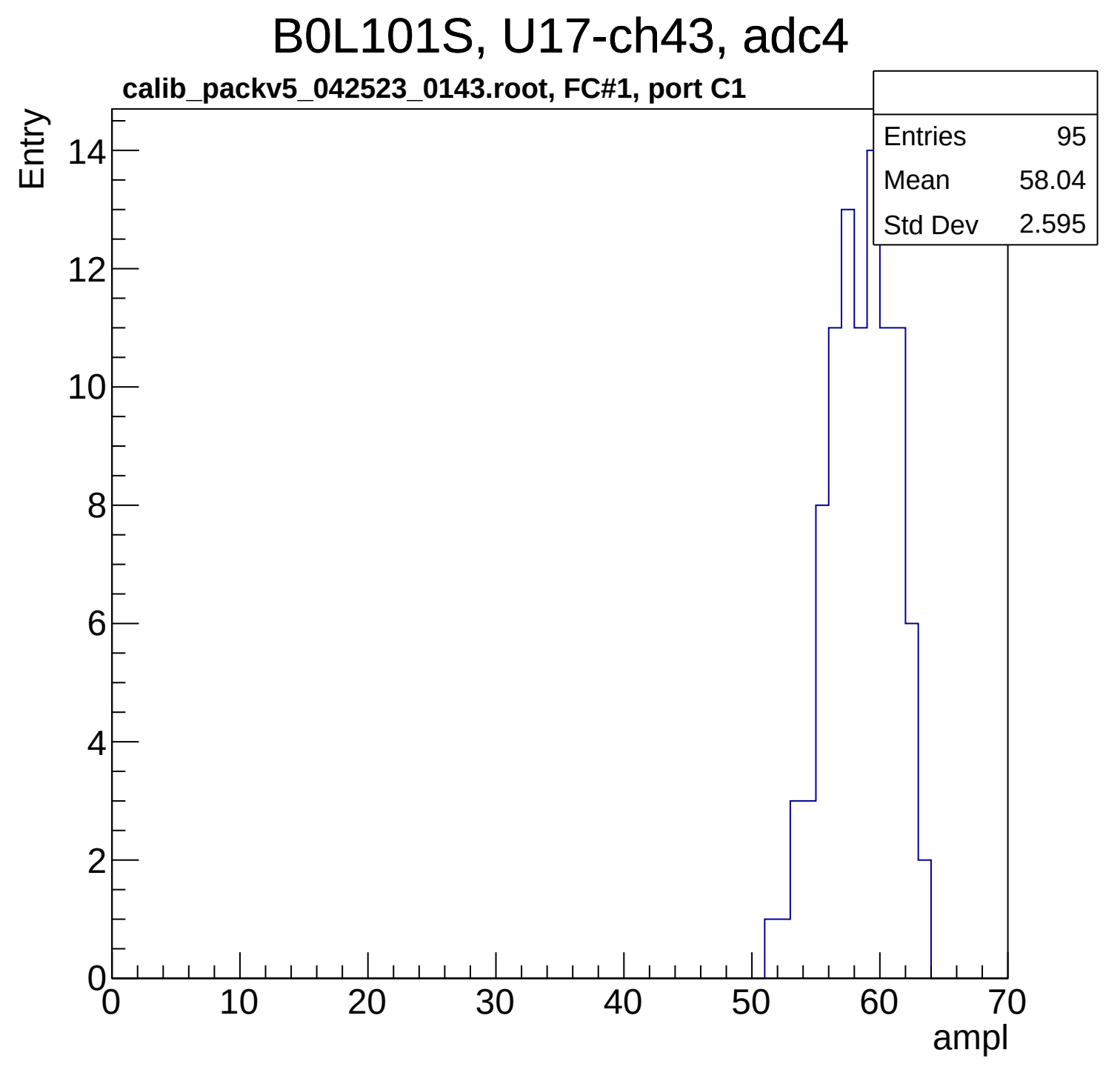

B0L101S, U17-ch42, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

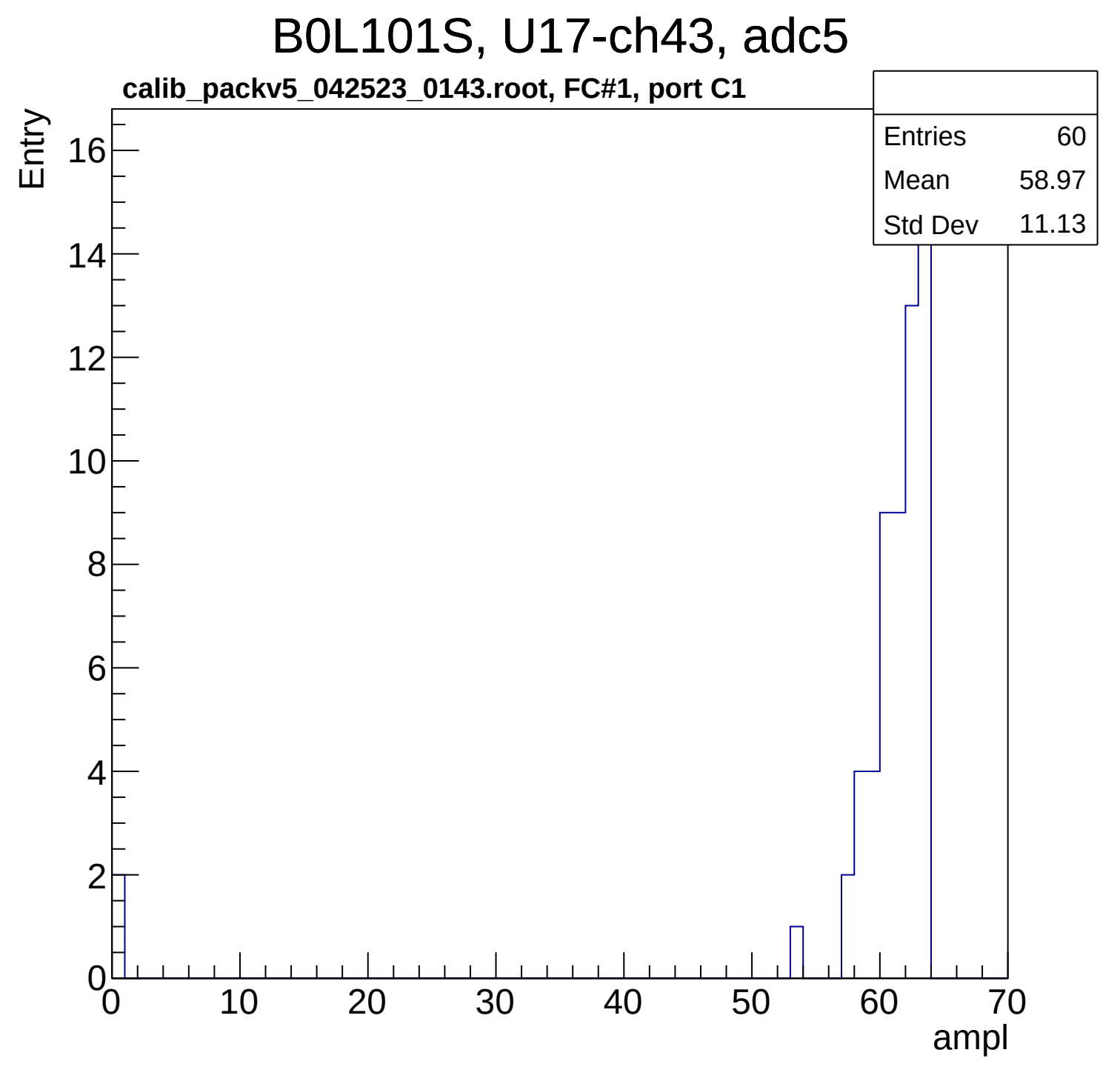

B0L101S, U17-ch43, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 31.25
Std Dev 4.355
Gaus mean : 31.9055
Gaus Width: 3.5695
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

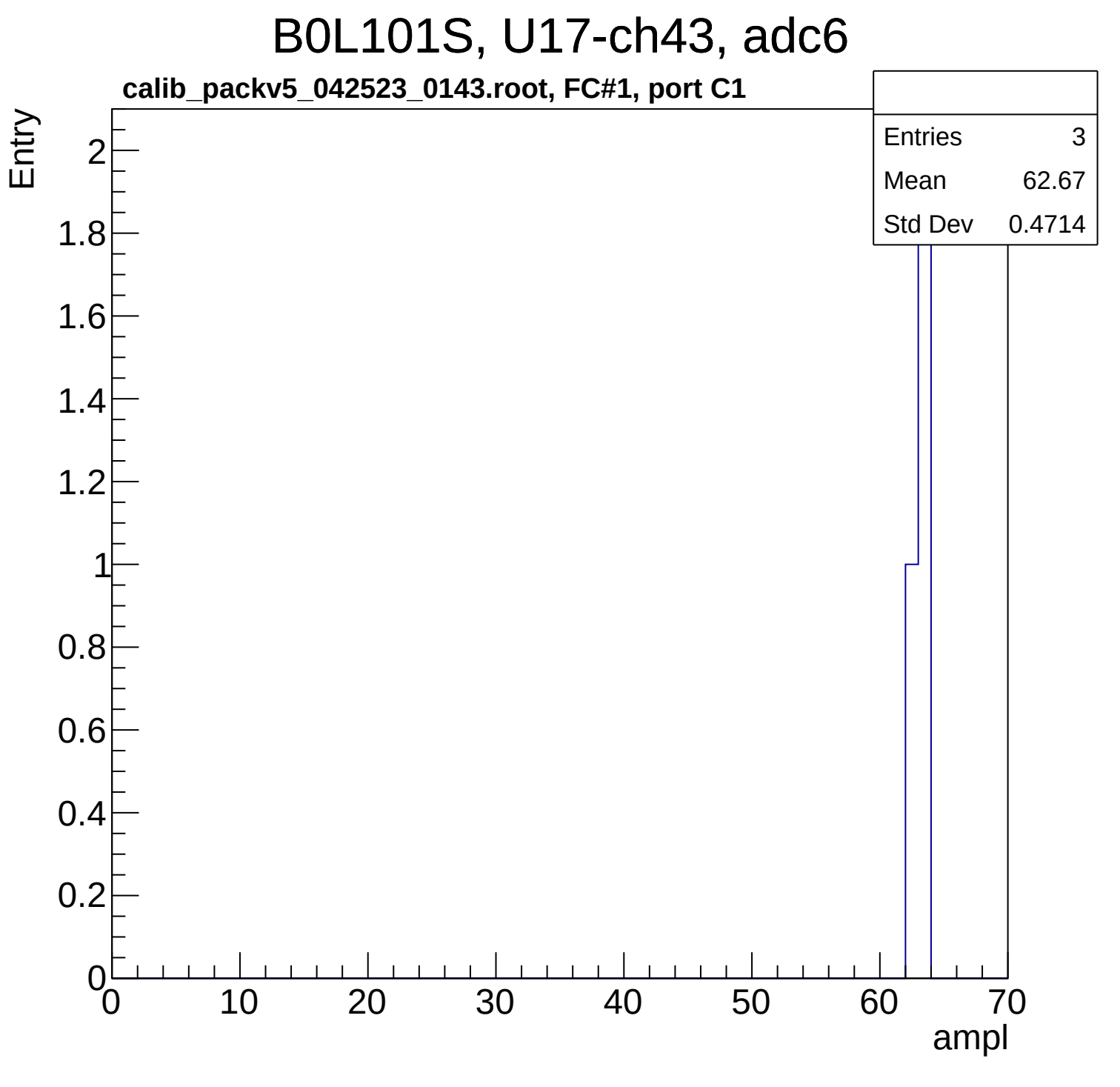

B0L101S, U17-ch43, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 89
Mean 37.34
Std Dev 2.972
Gaus mean : 37.9820
Gaus Width: 3.3250
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

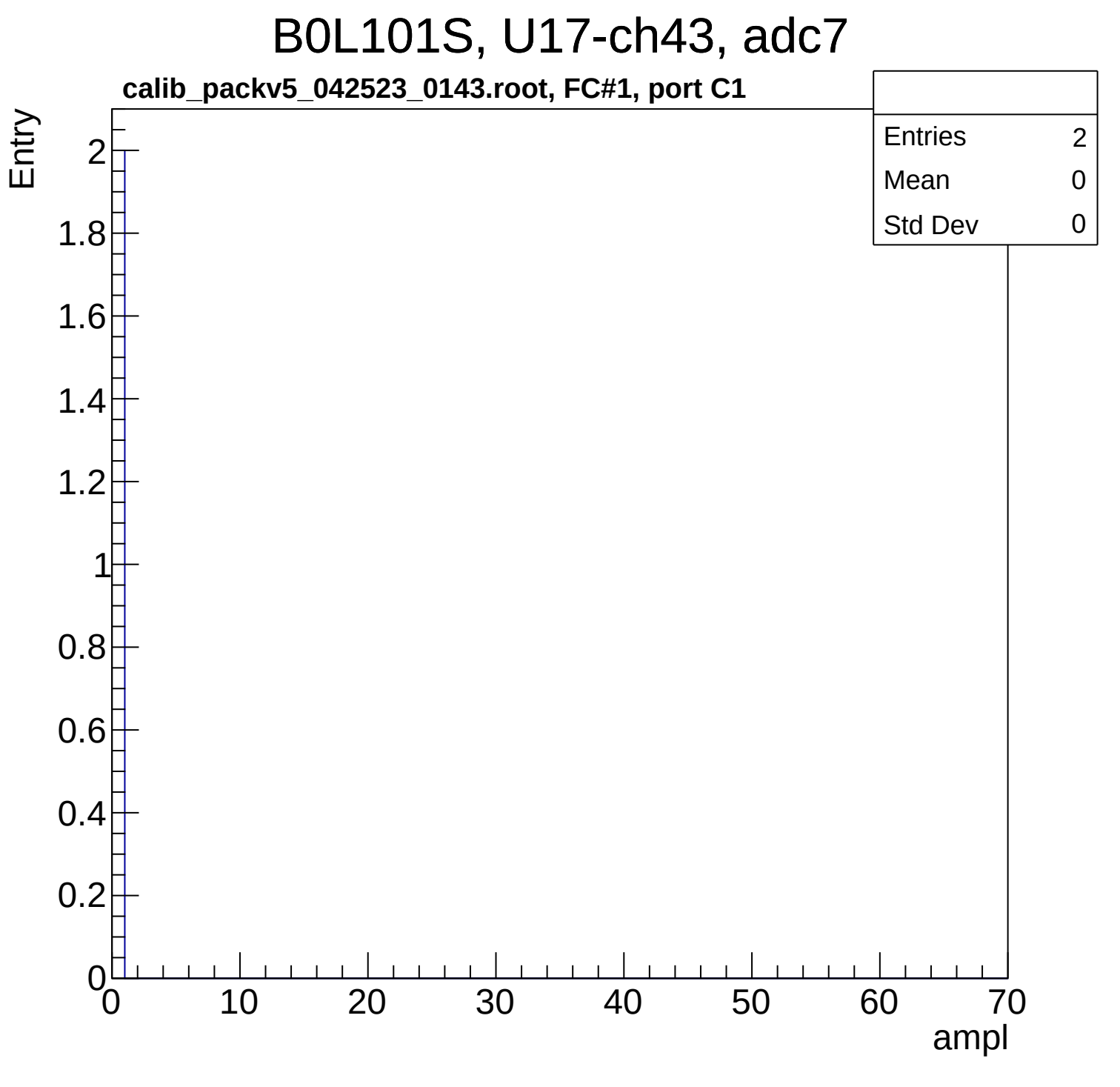

B0L101S, U17-ch43, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 43.37
Std Dev 3.513
Gaus mean : 43.7388
Gaus Width: 3.7432
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

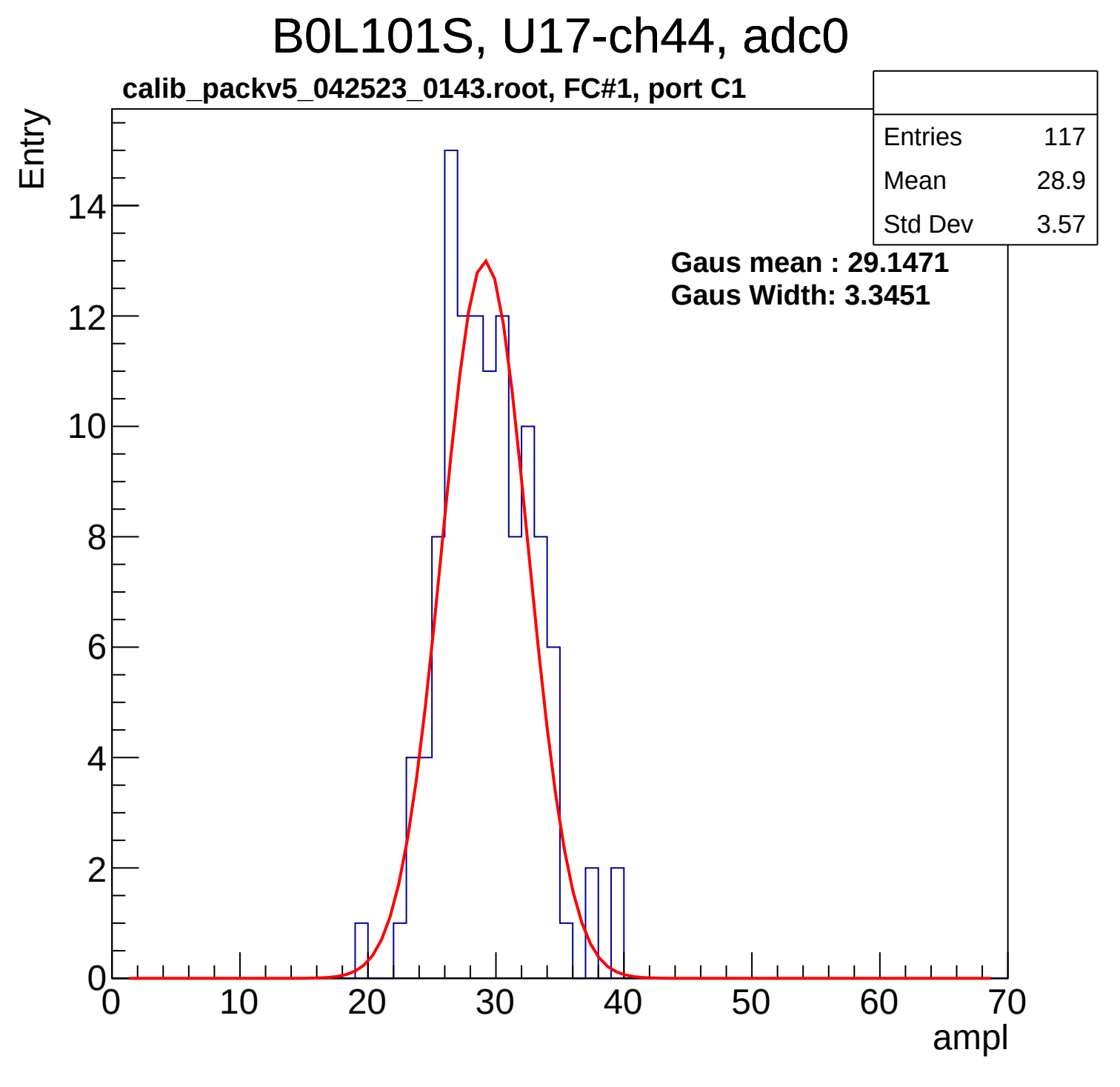

B0L101S, U17-ch43, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 171
Mean 51.33
Std Dev 3.555
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

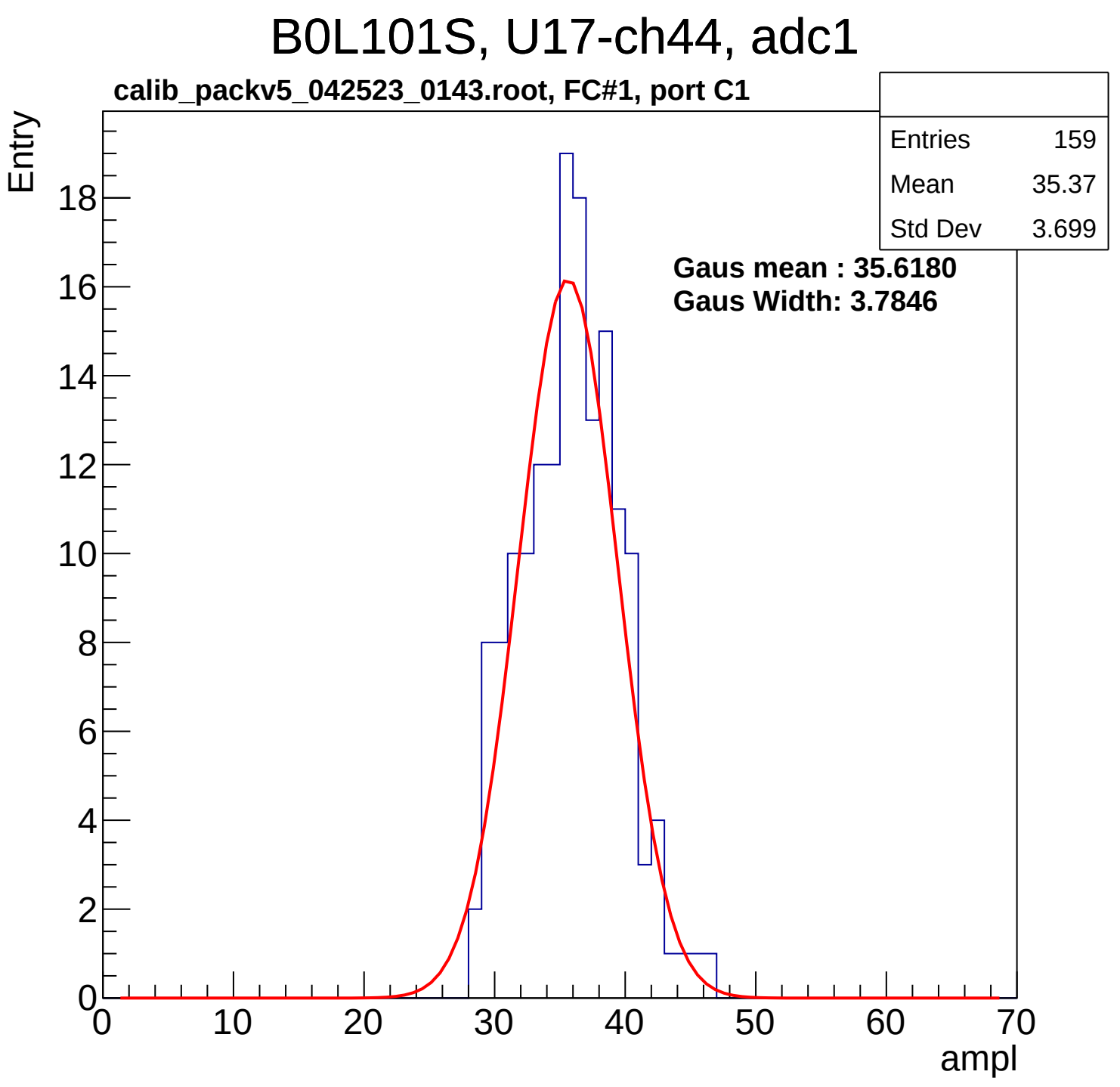

B0L101S, U17-ch43, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 95
Mean 58.04
Std Dev 2.595
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

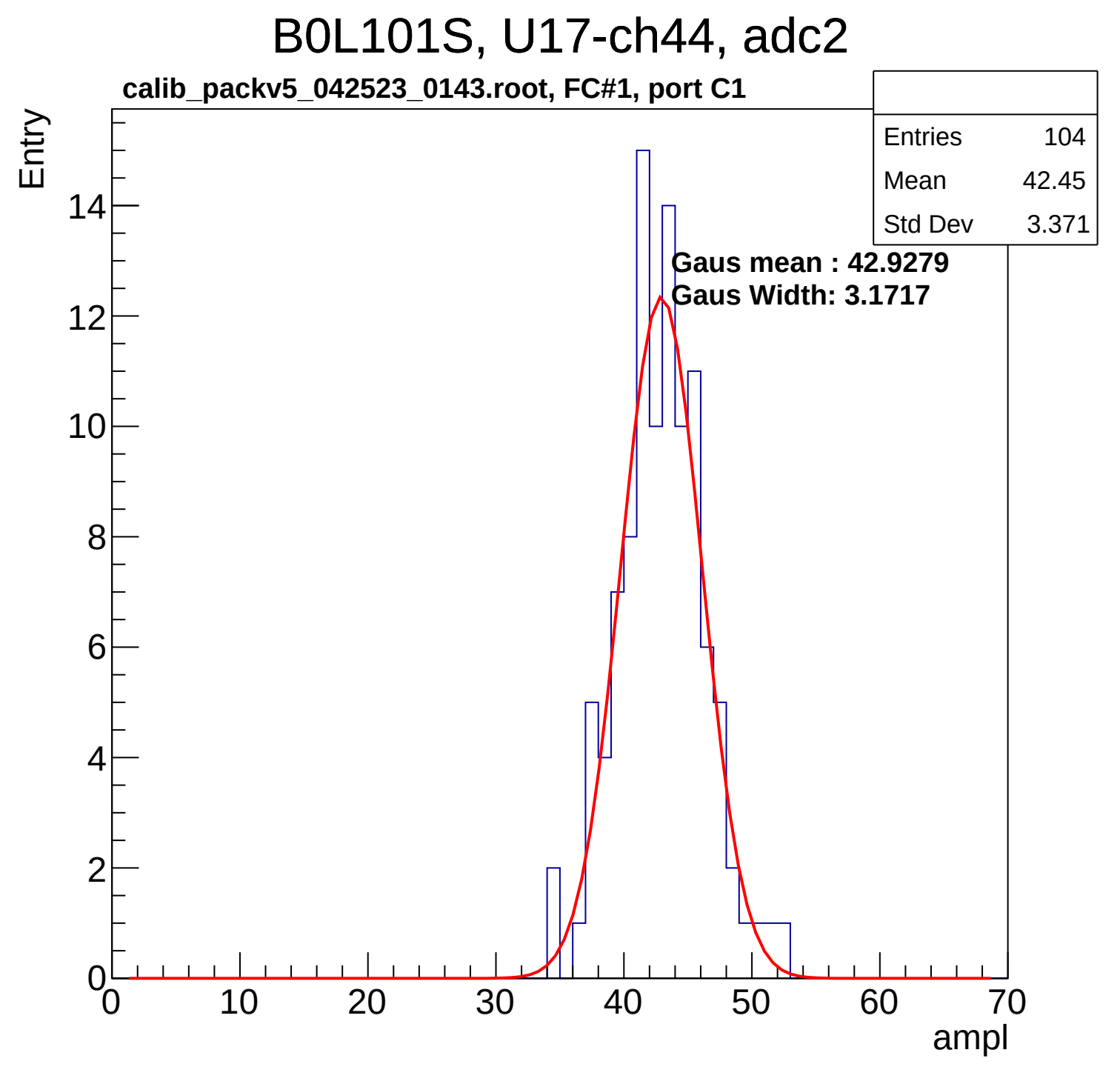

B0L101S, U17-ch43, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 60
Mean 58.97
Std Dev 11.13
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

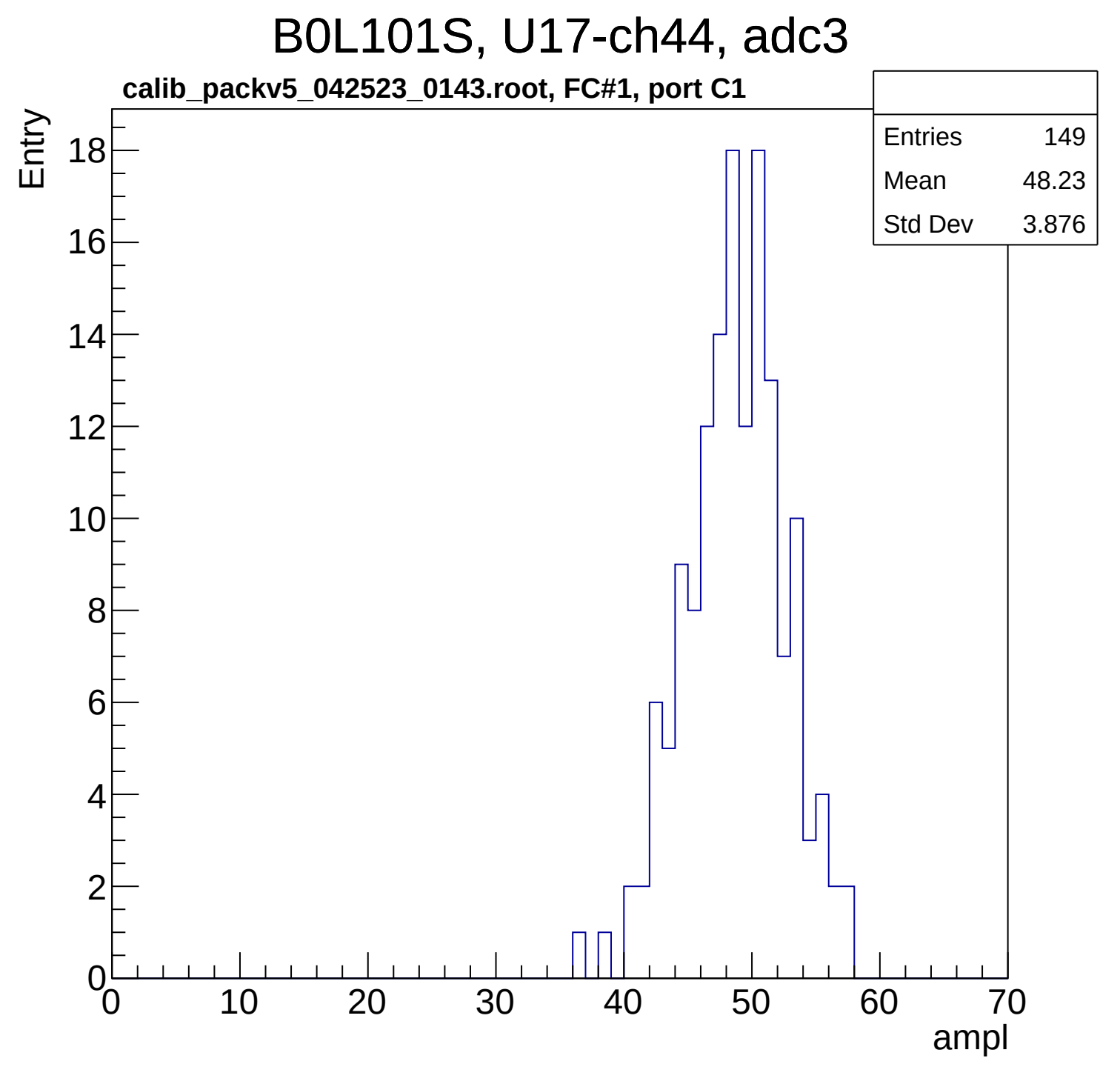

B0L101S, U17-ch43, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

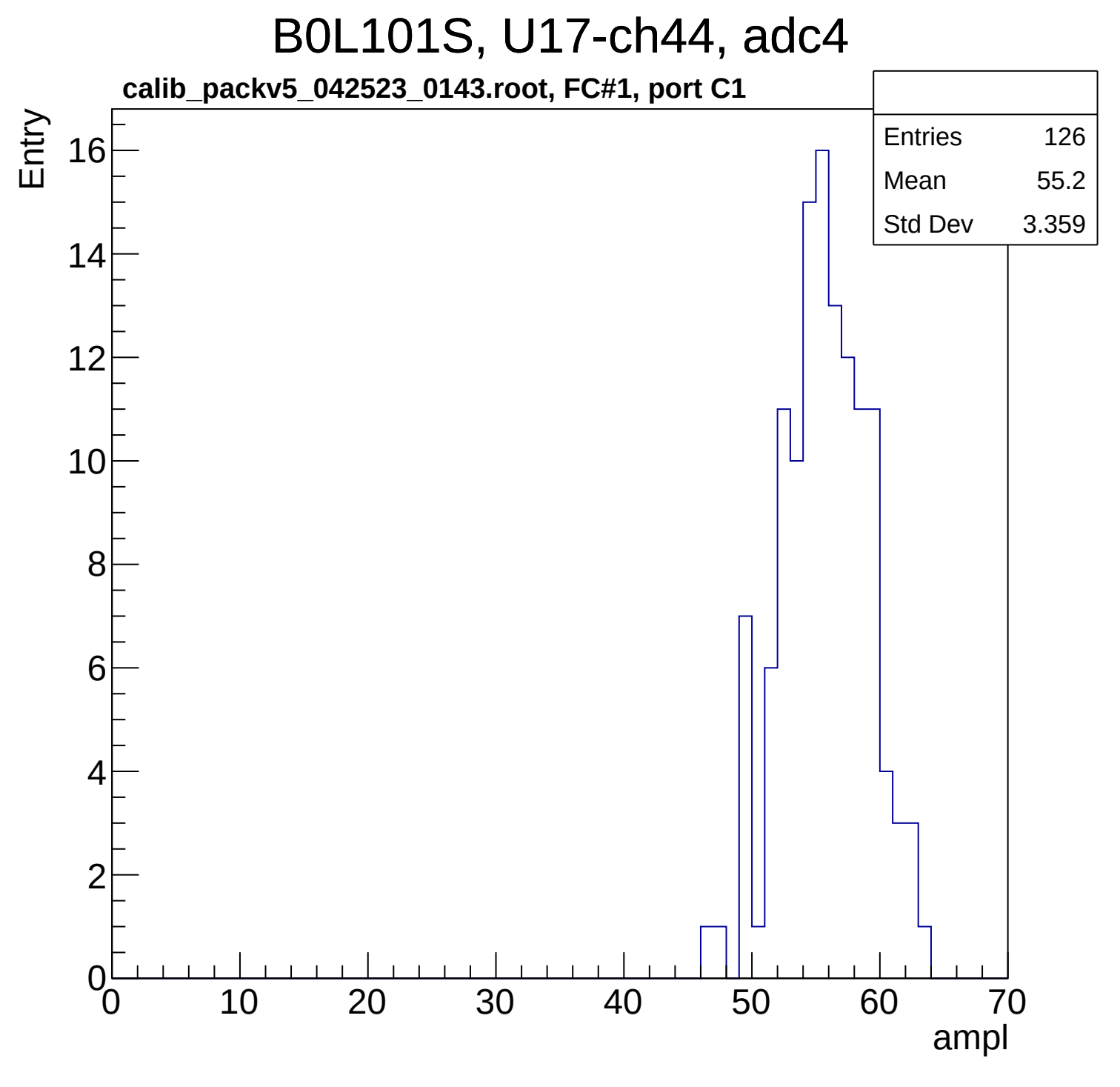

B0L101S, U17-ch43, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

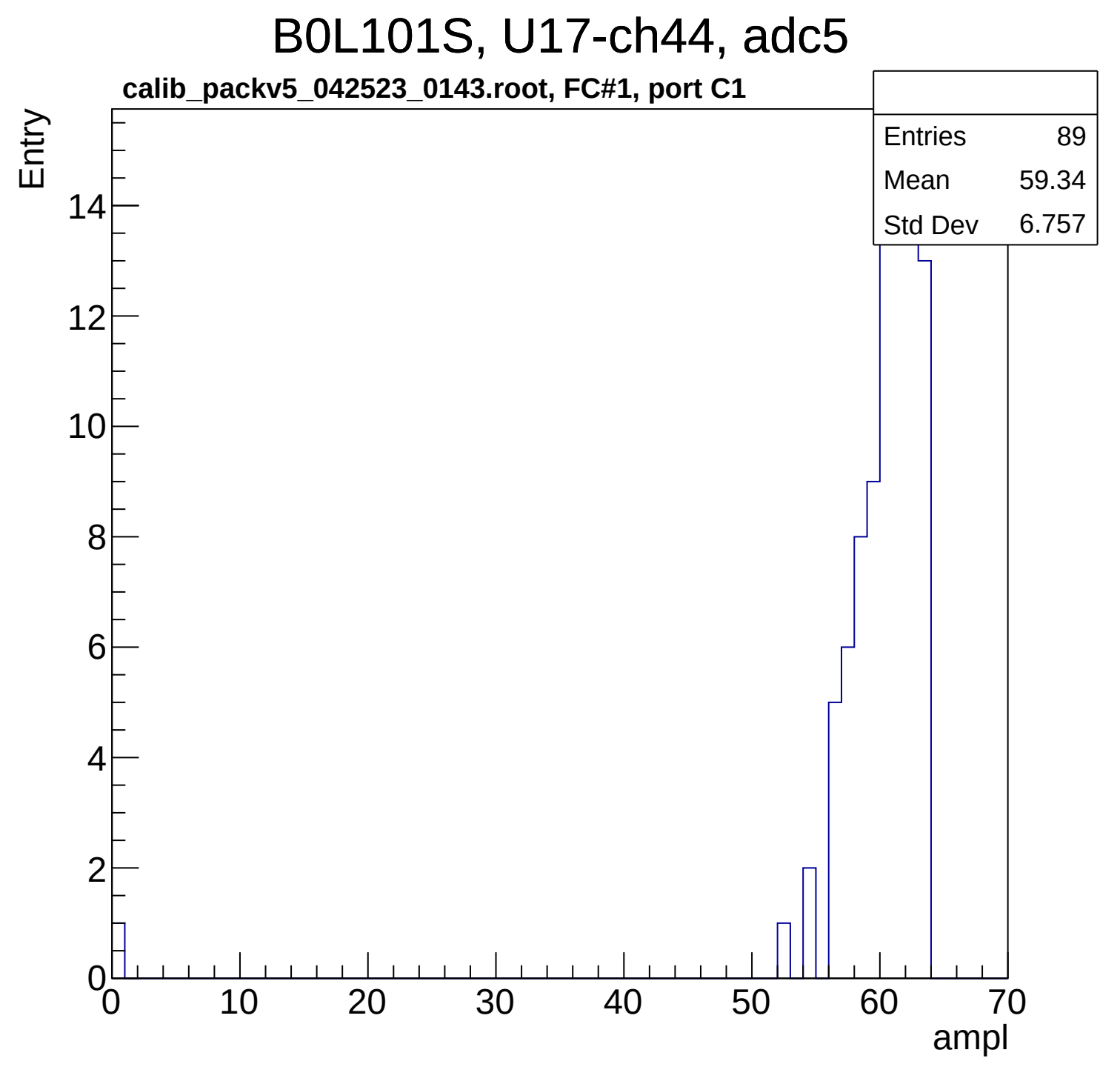

B0L101S, U17-ch44, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 28.9
Std Dev 3.57
Gaus mean : 29.1471
Gaus Width: 3.3451
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

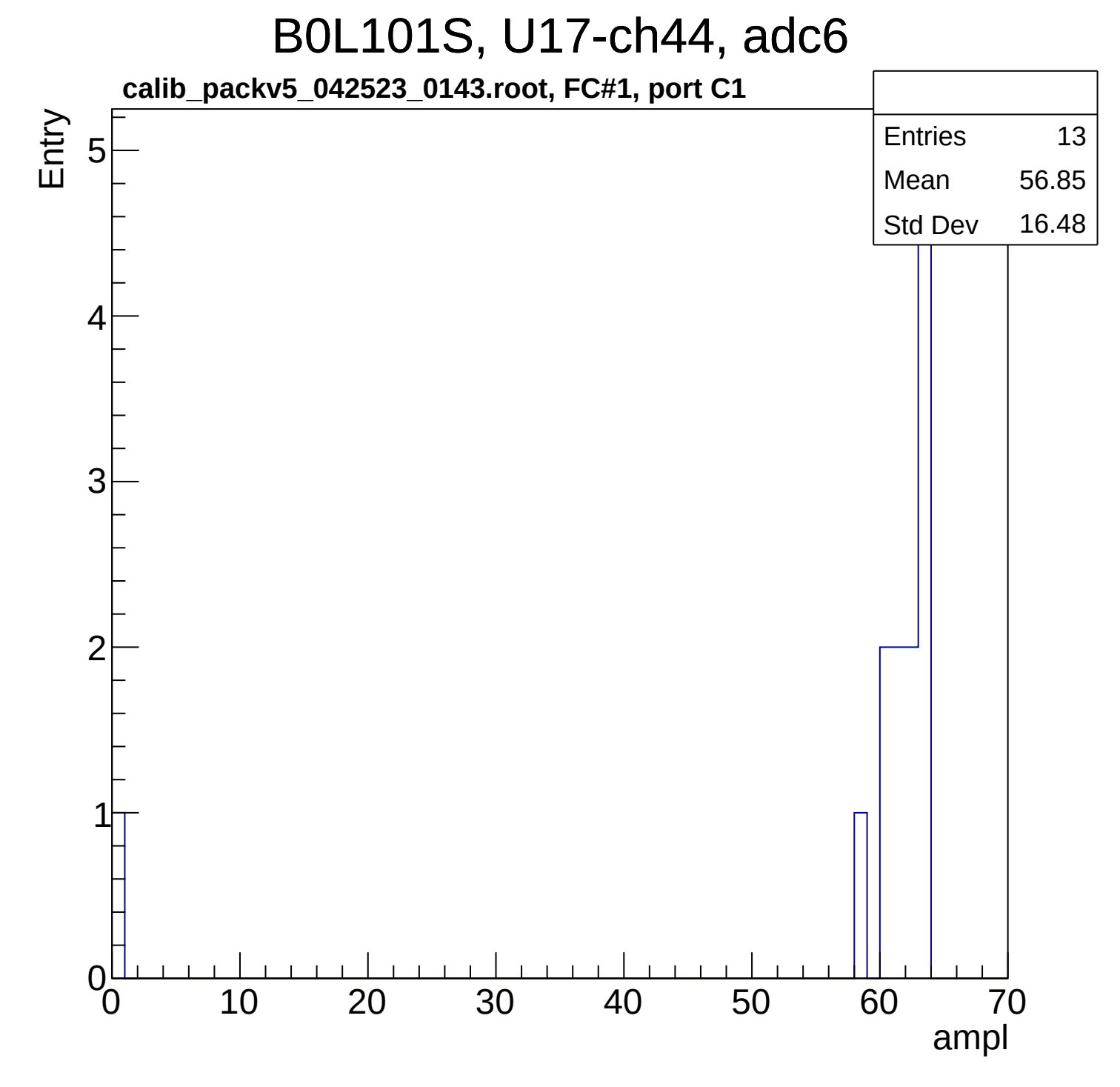

B0L101S, U17-ch44, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 35.37
Std Dev 3.699
Gaus mean : 35.6180
Gaus Width: 3.7846
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

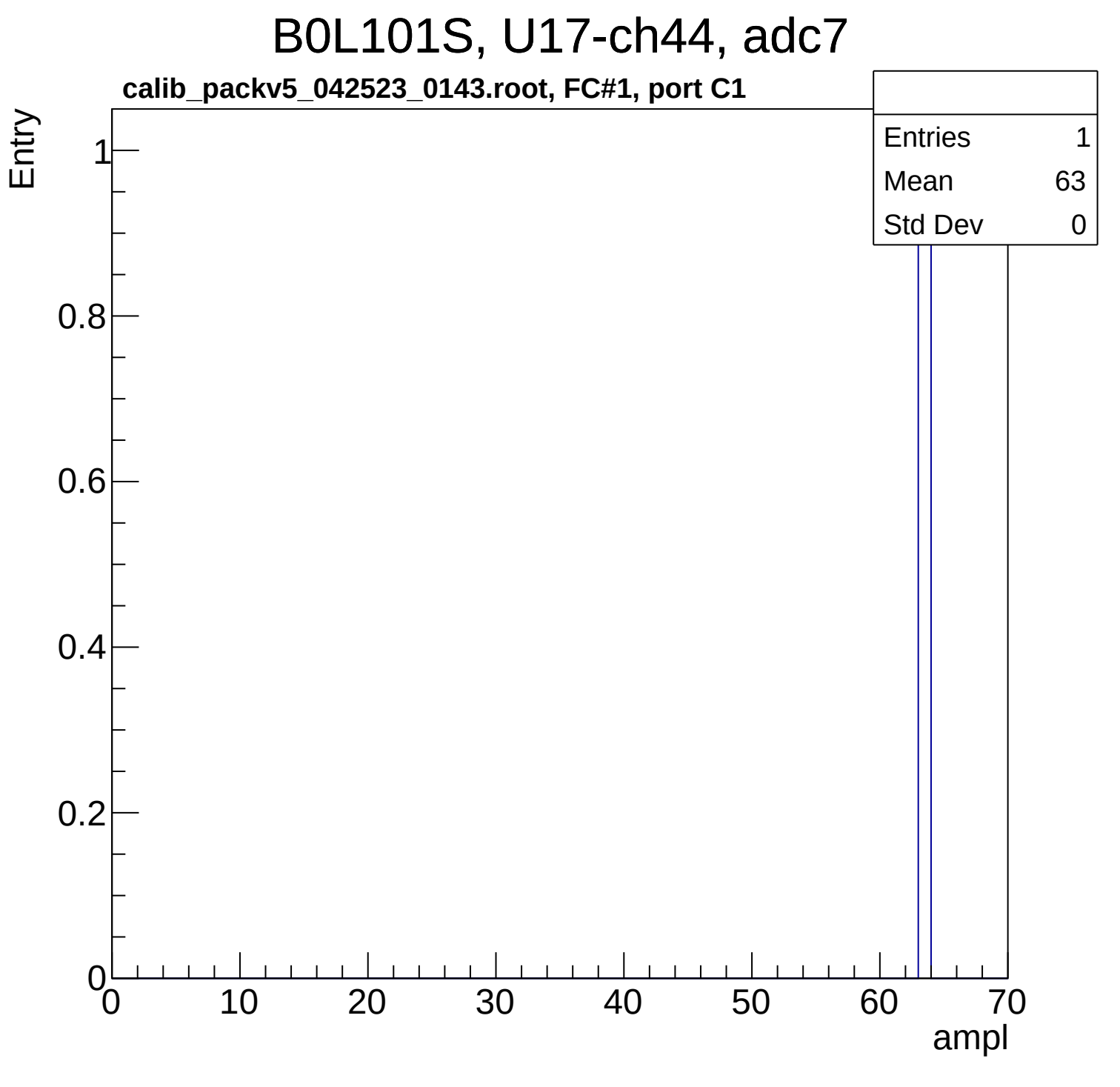

B0L101S, U17-ch44, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 42.45
Std Dev 3.371
Gaus mean : 42.9279
Gaus Width: 3.1717
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

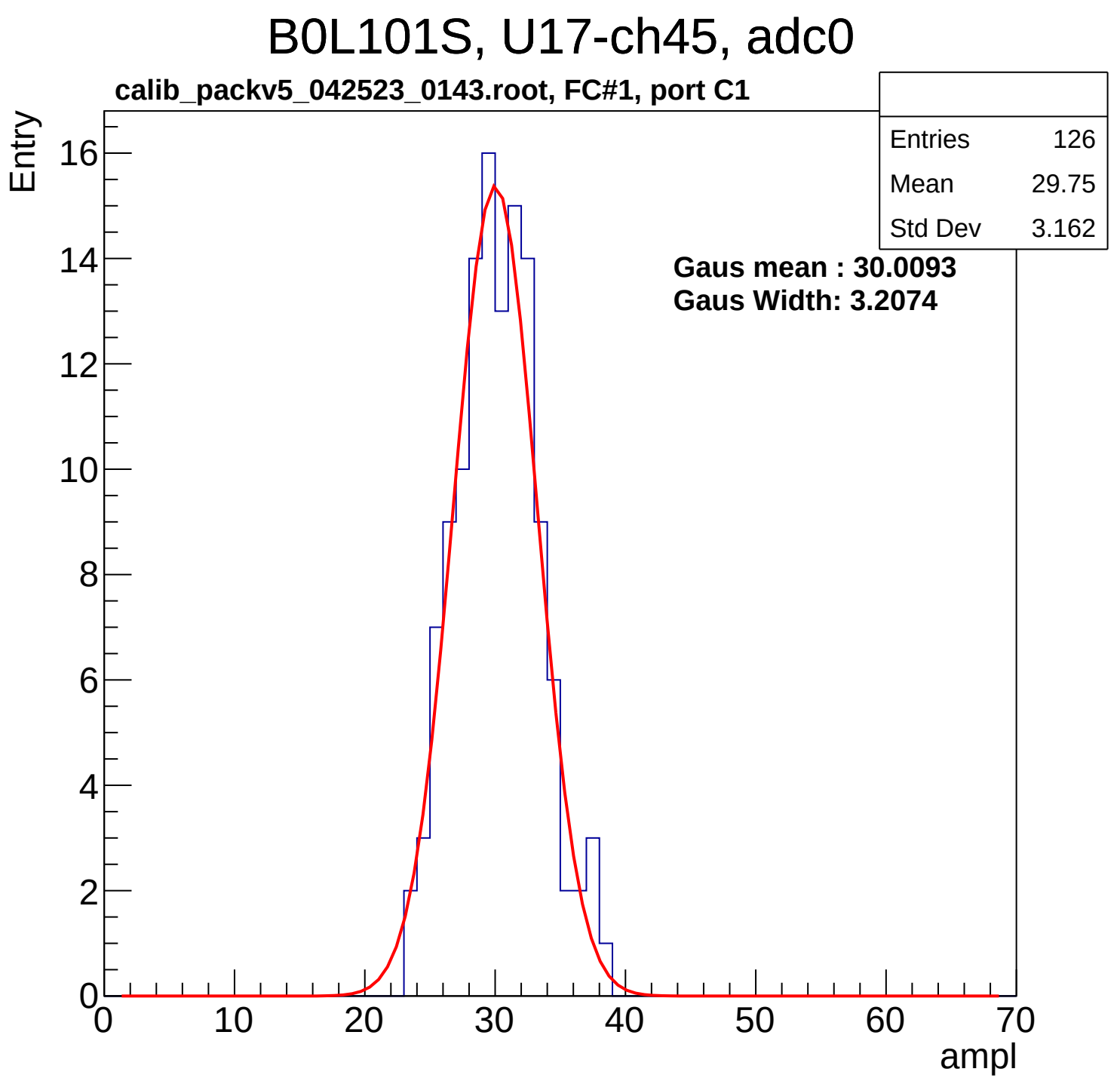

B0L101S, U17-ch44, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 48.23
Std Dev 3.876
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

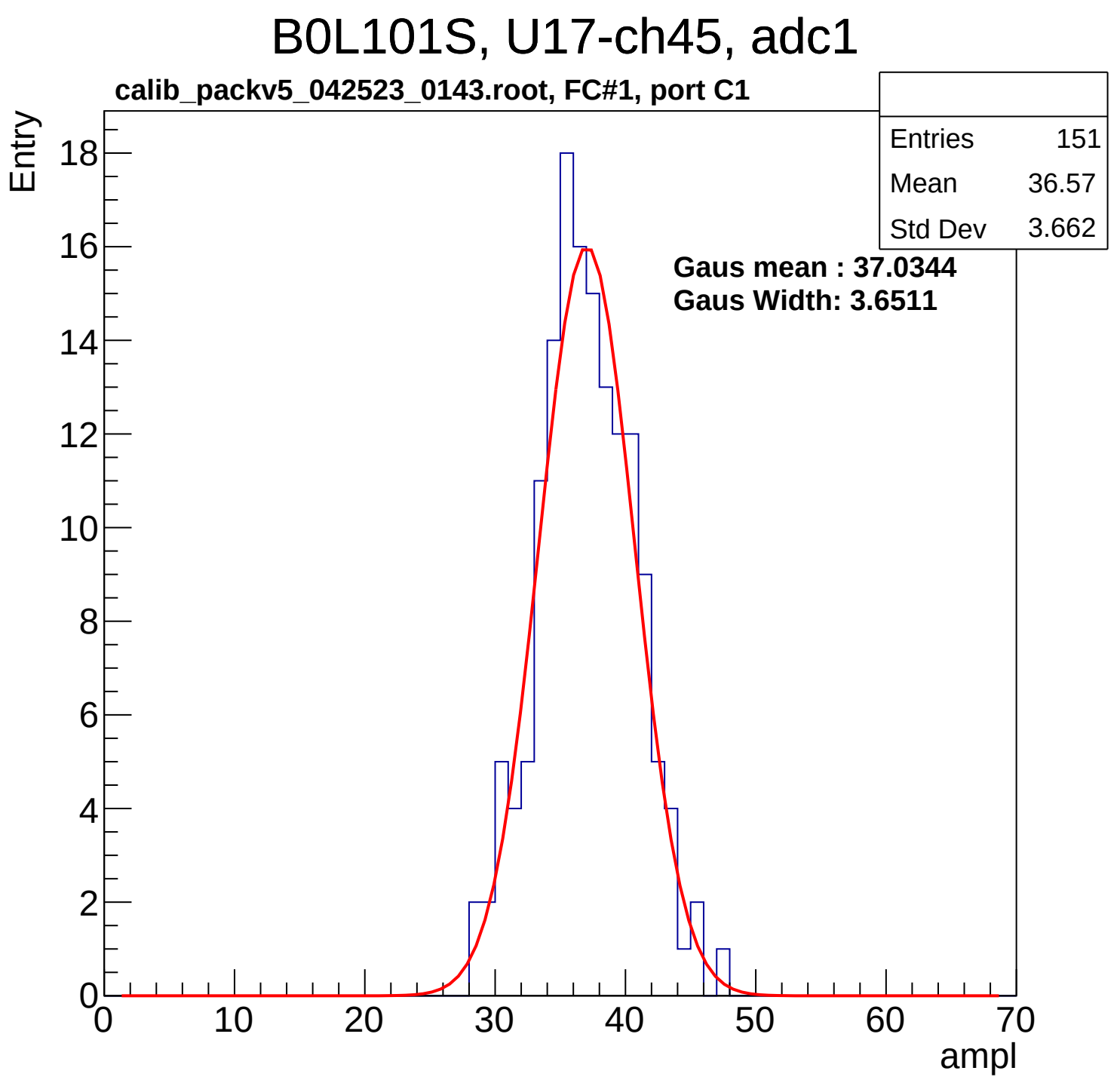

B0L101S, U17-ch44, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 55.2
Std Dev 3.359
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

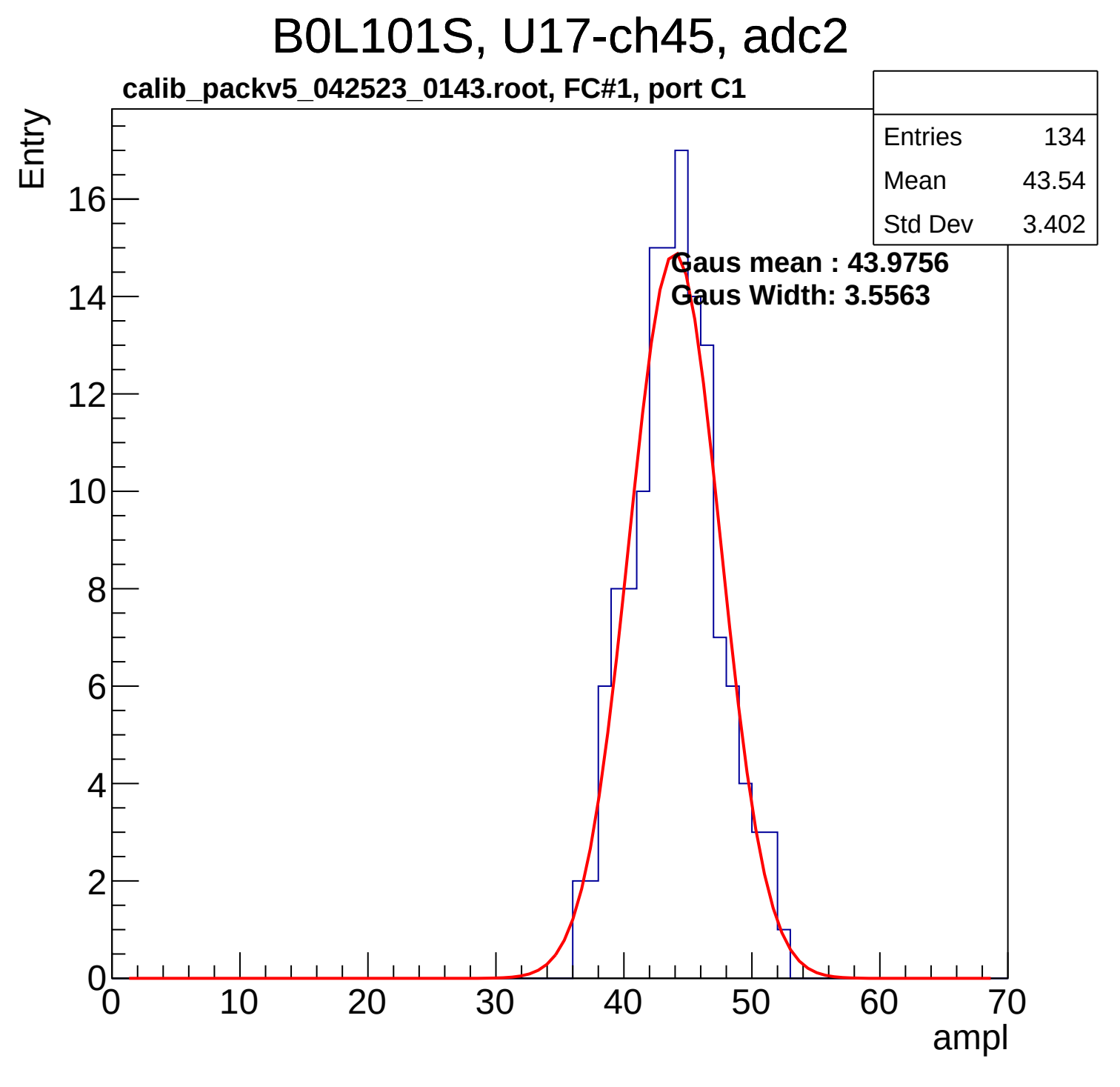

B0L101S, U17-ch44, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 89
Mean 59.34
Std Dev 6.757
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

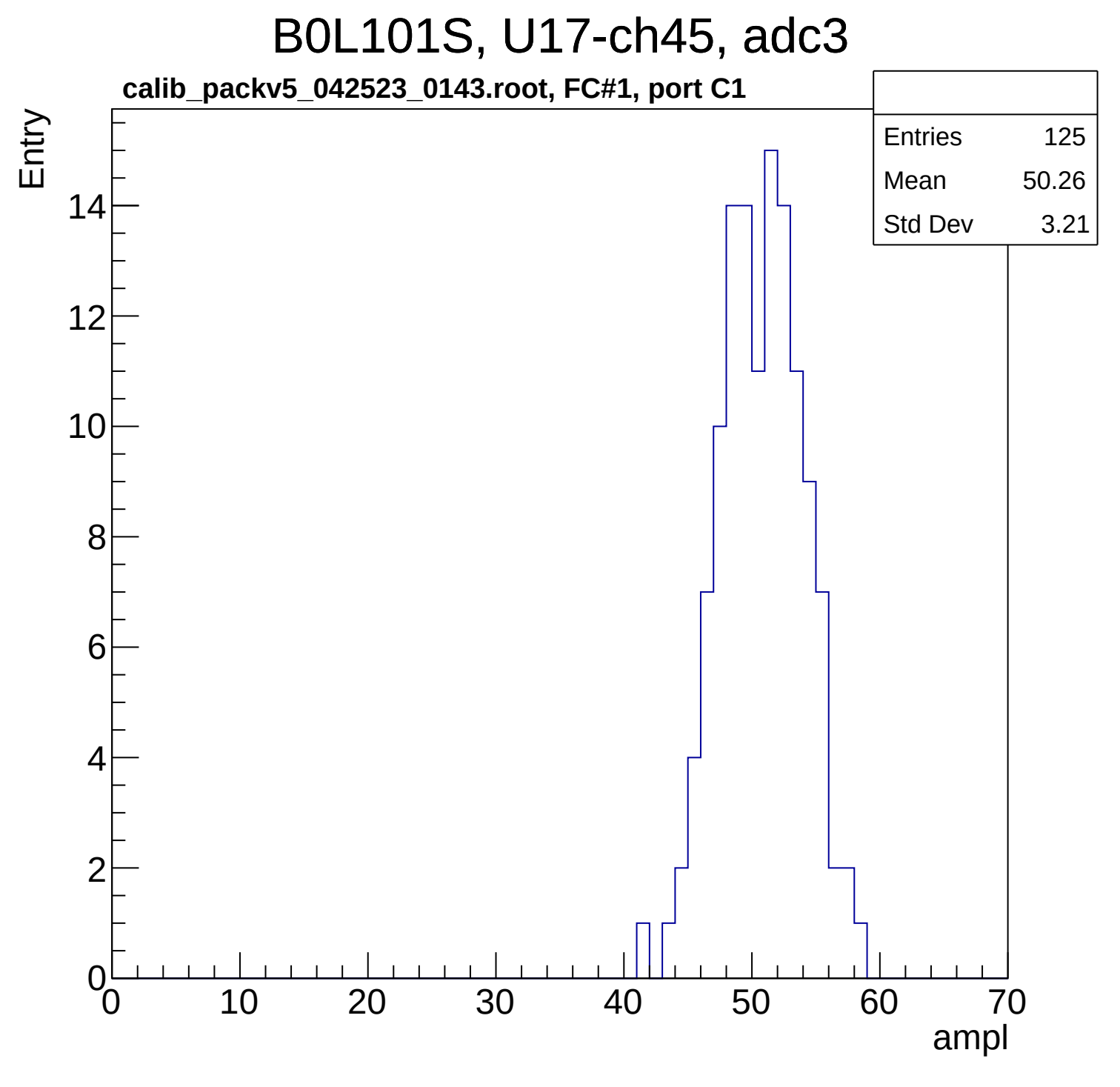

B0L101S, U17-ch44, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 13
Mean 56.85
Std Dev 16.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

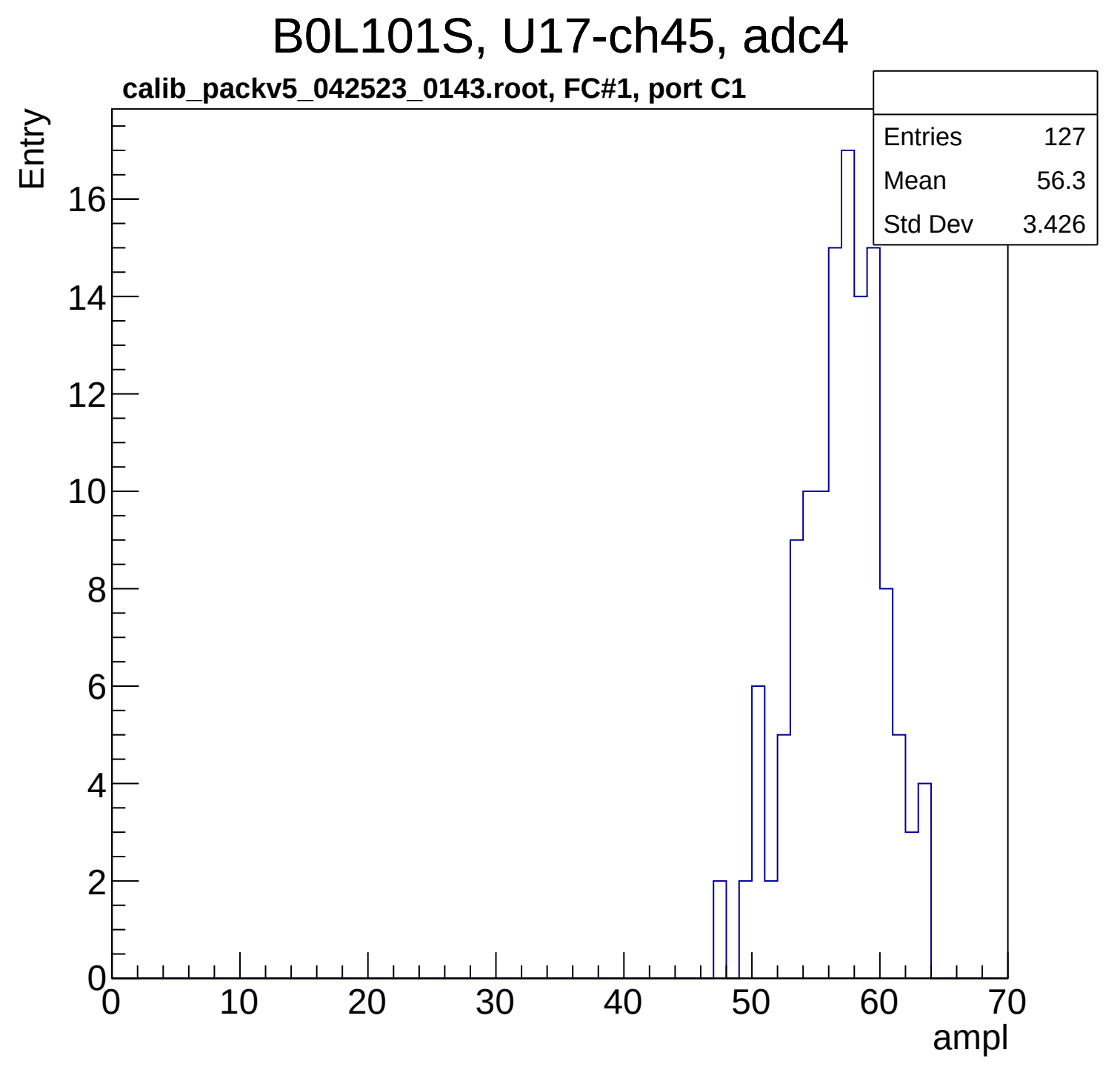

B0L101S, U17-ch44, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

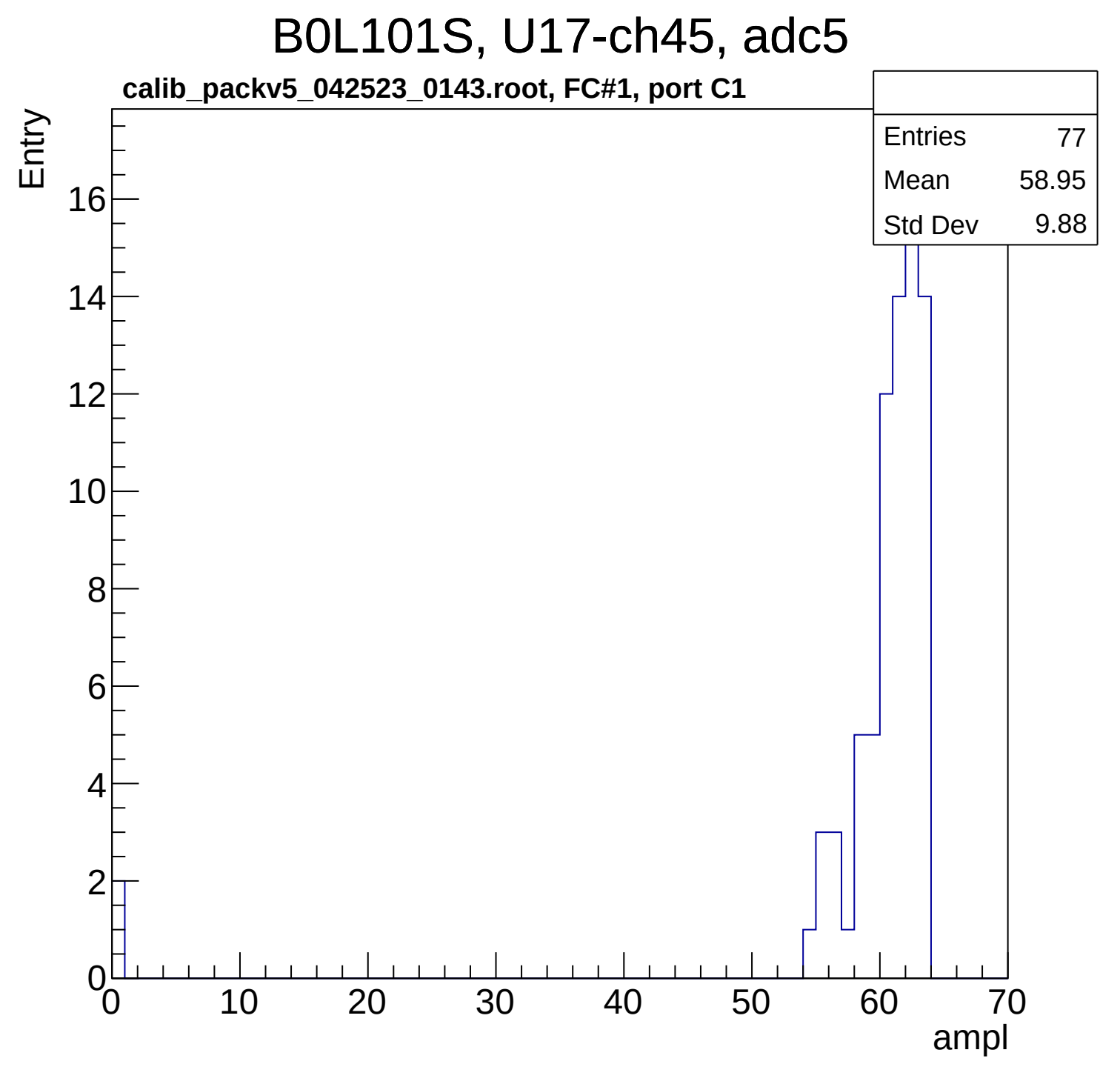

B0L101S, U17-ch45, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 29.75
Std Dev 3.162
Gaus mean : 30.0093
Gaus Width: 3.2074
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

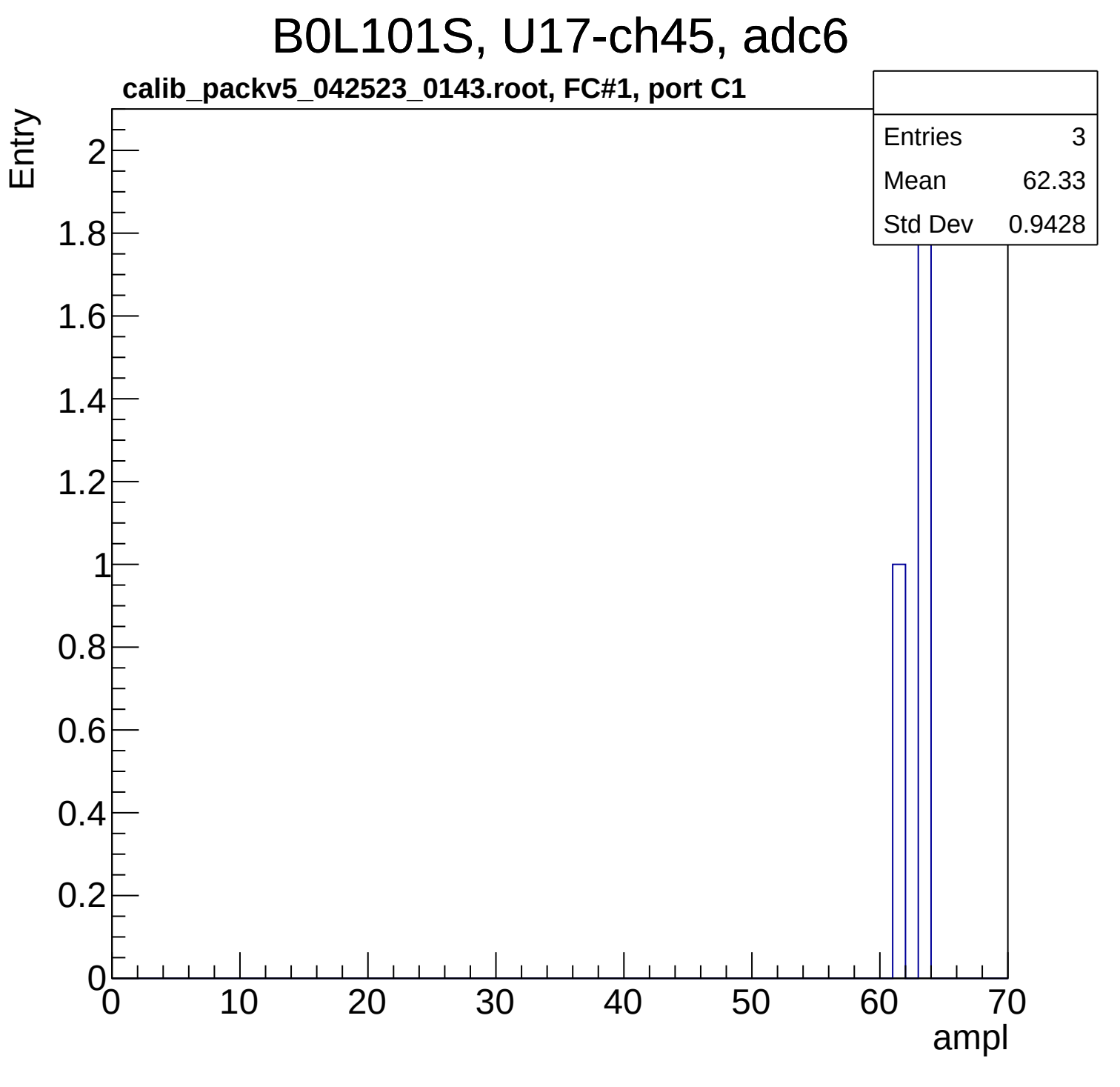

B0L101S, U17-ch45, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 36.57
Std Dev 3.662
Gaus mean : 37.0344
Gaus Width: 3.6511
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

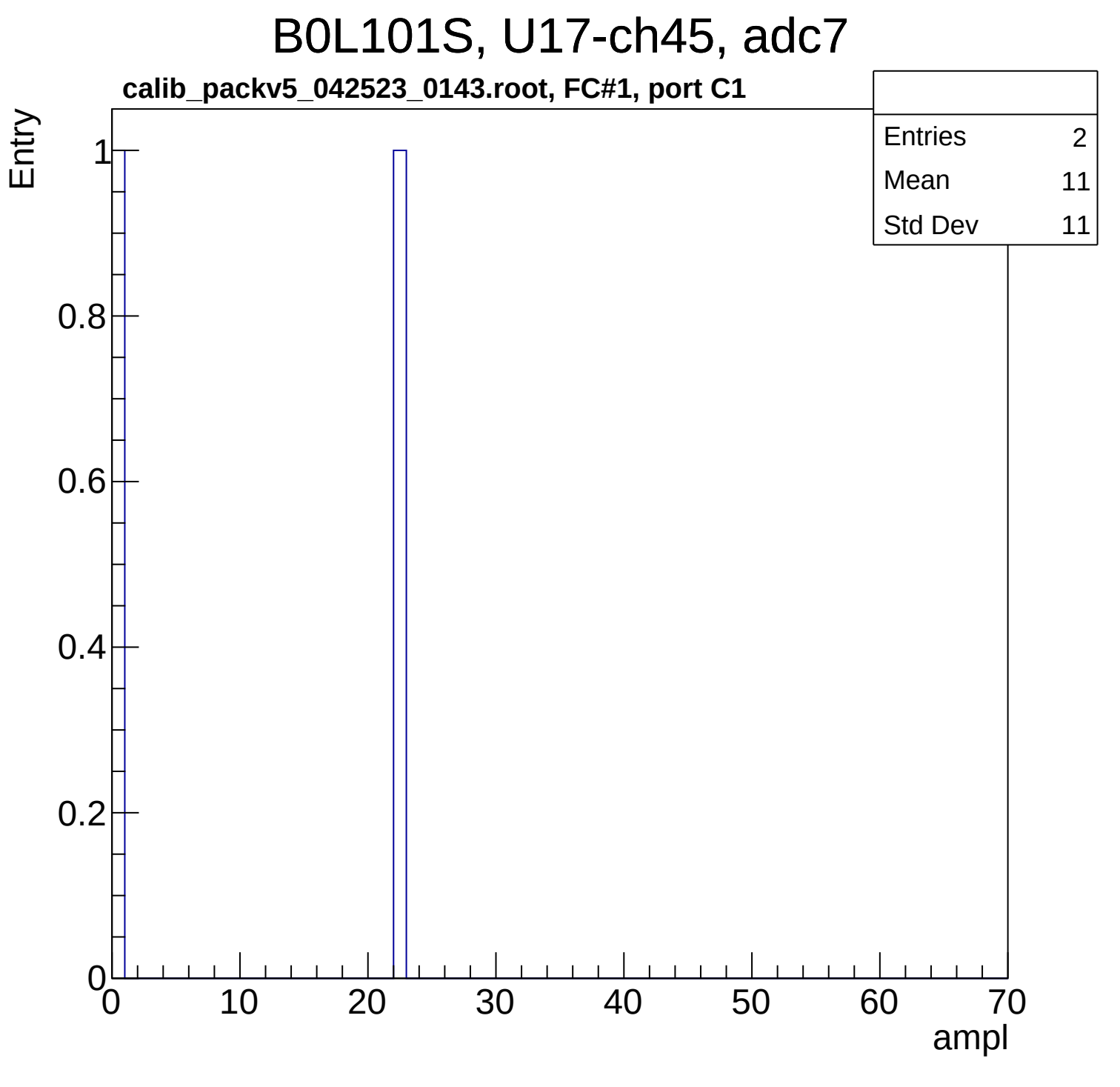

B0L101S, U17-ch45, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 43.54
Std Dev 3.402
Gaus mean : 43.9756
Gaus Width: 3.5563
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

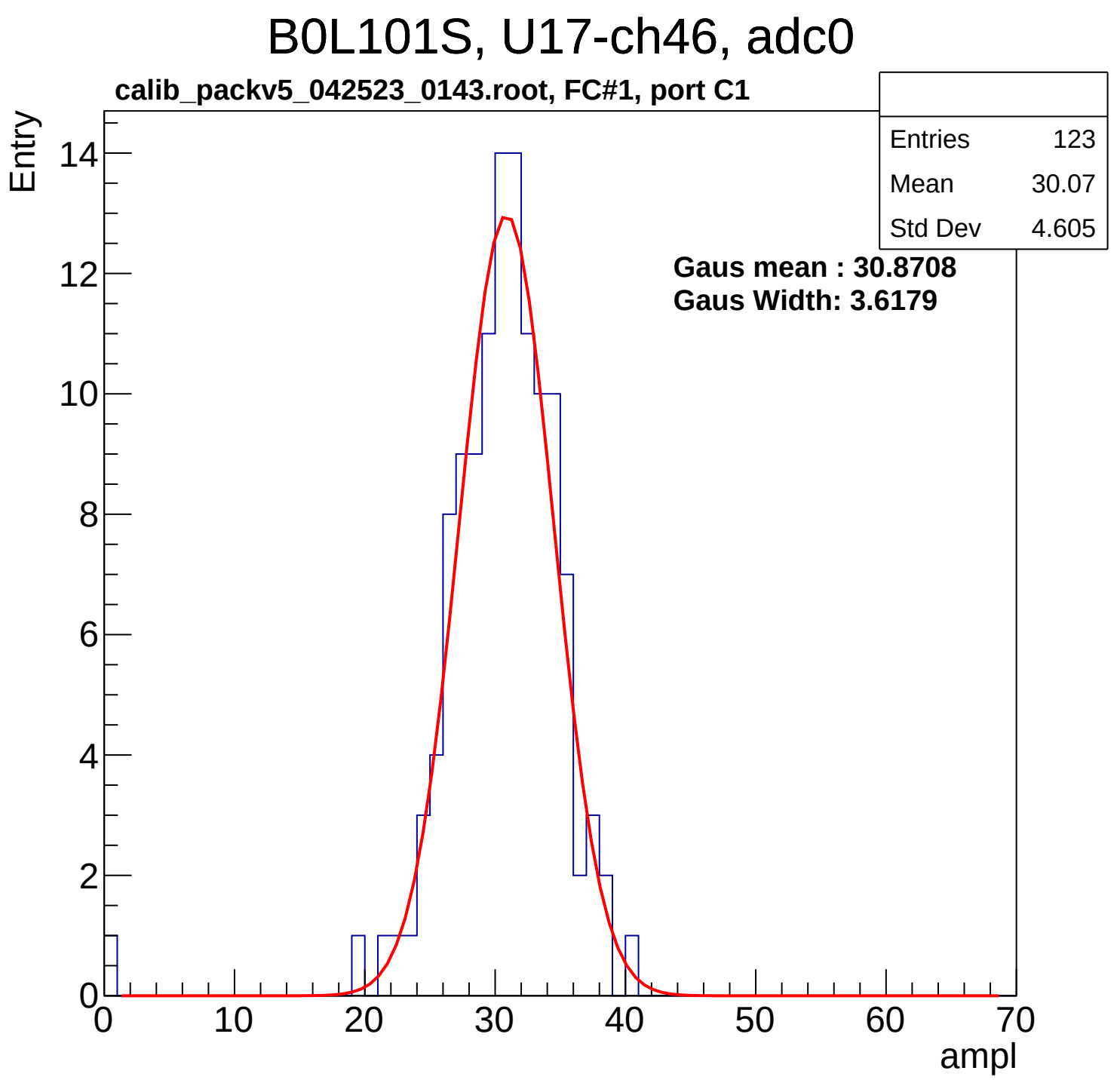

B0L101S, U17-ch45, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 50.26
Std Dev 3.21
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

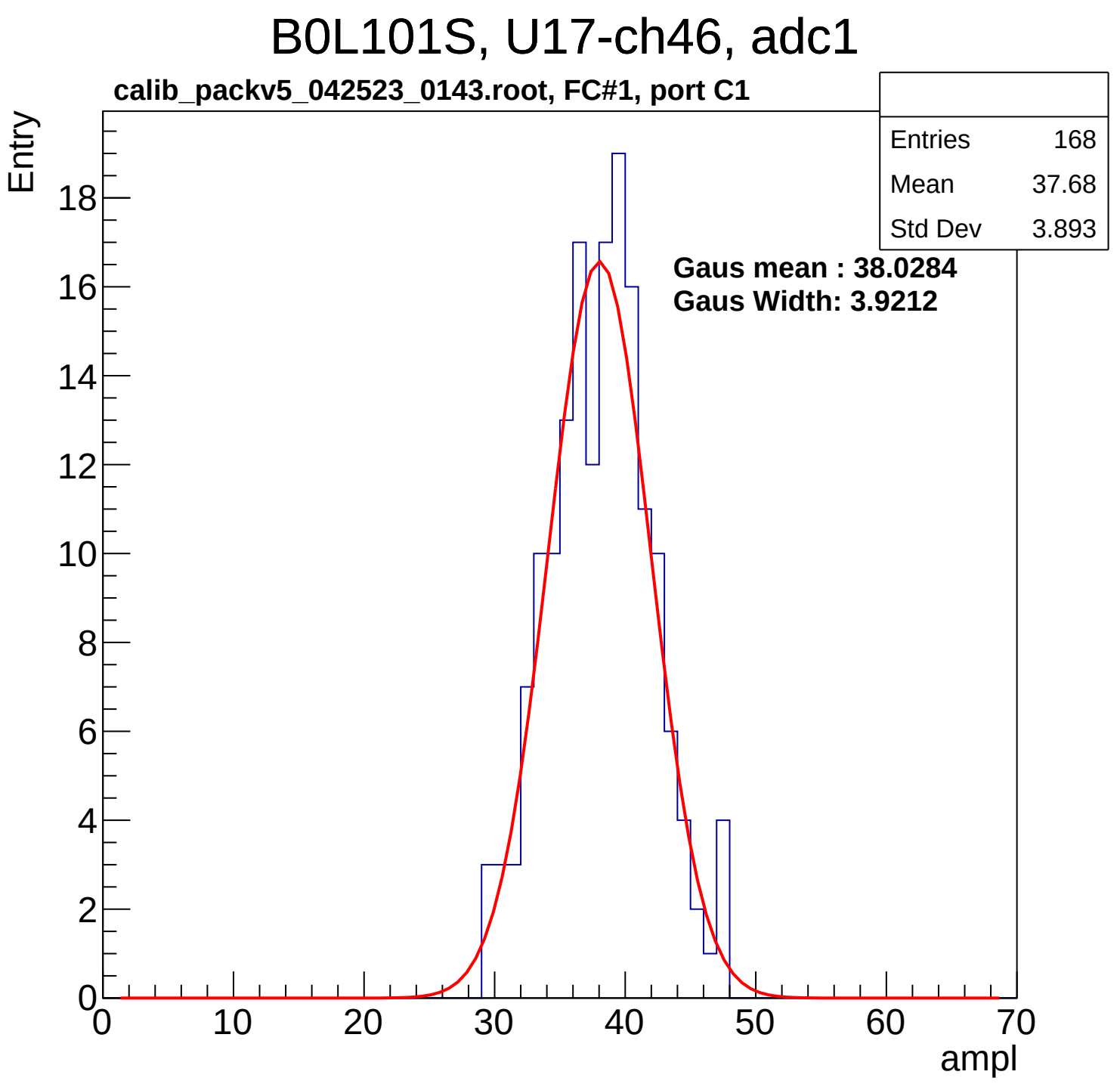

B0L101S, U17-ch45, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 56.3
Std Dev 3.426
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

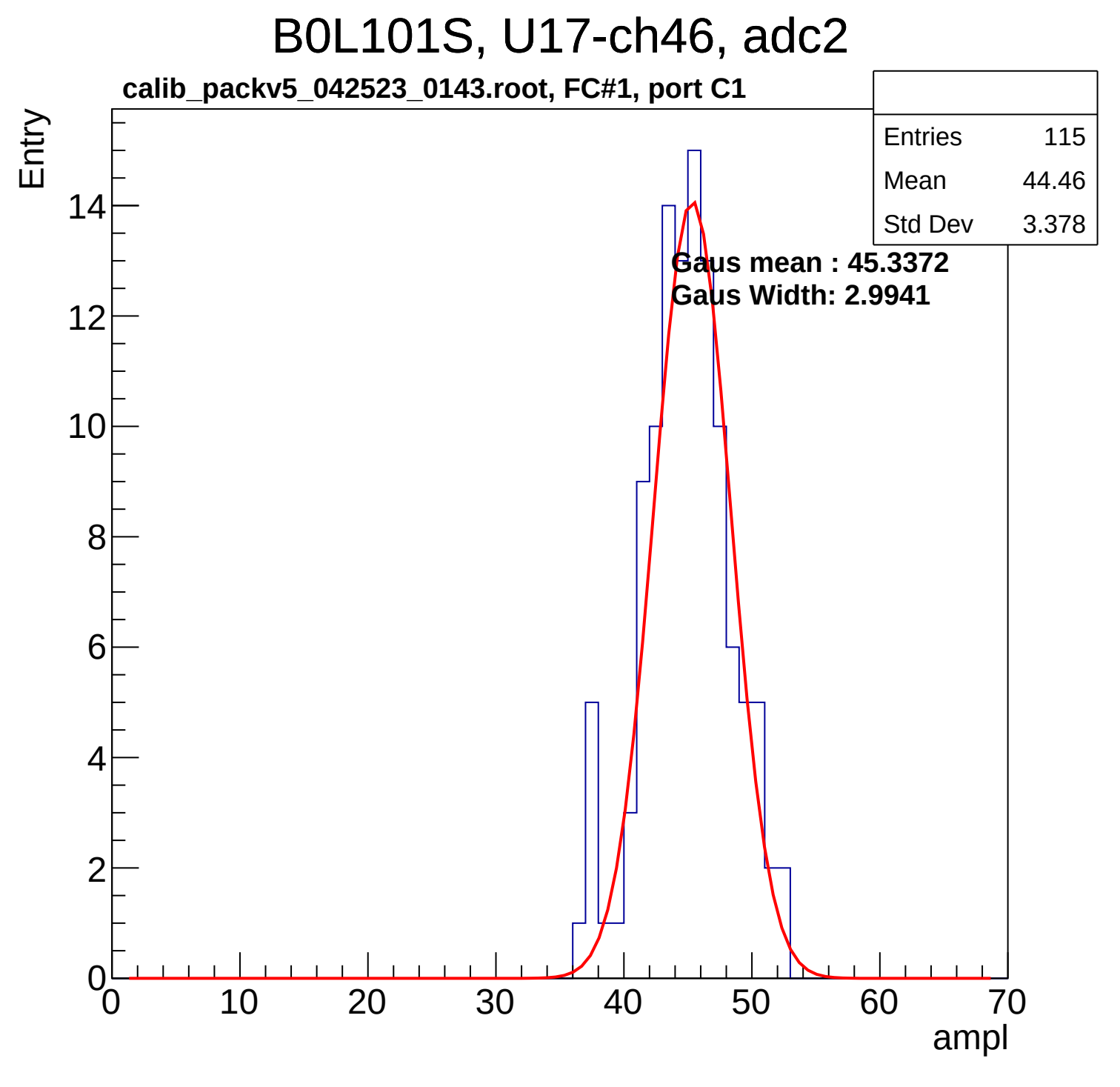

B0L101S, U17-ch45, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 77
Mean 58.95
Std Dev 9.88
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

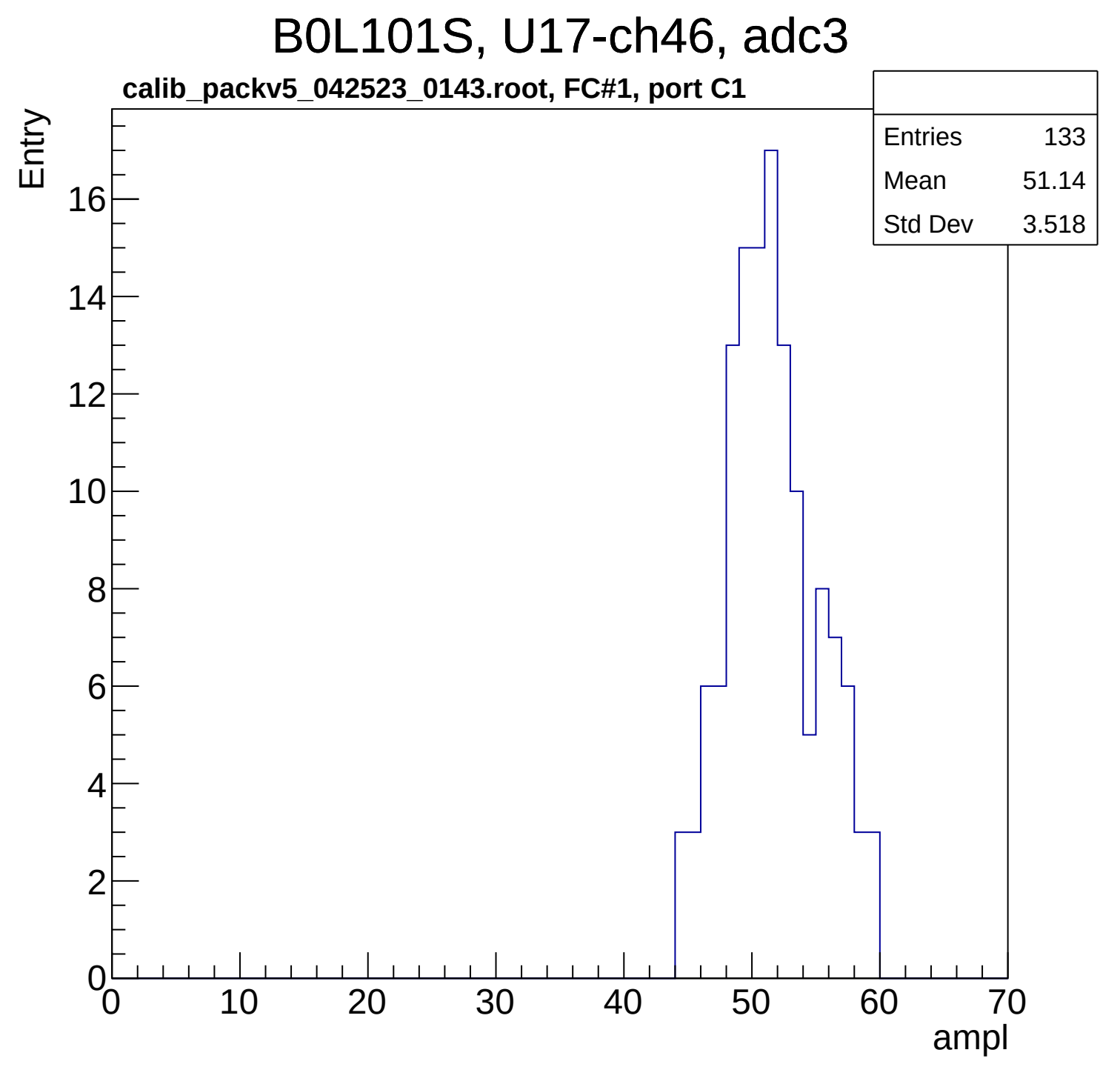

B0L101S, U17-ch45, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

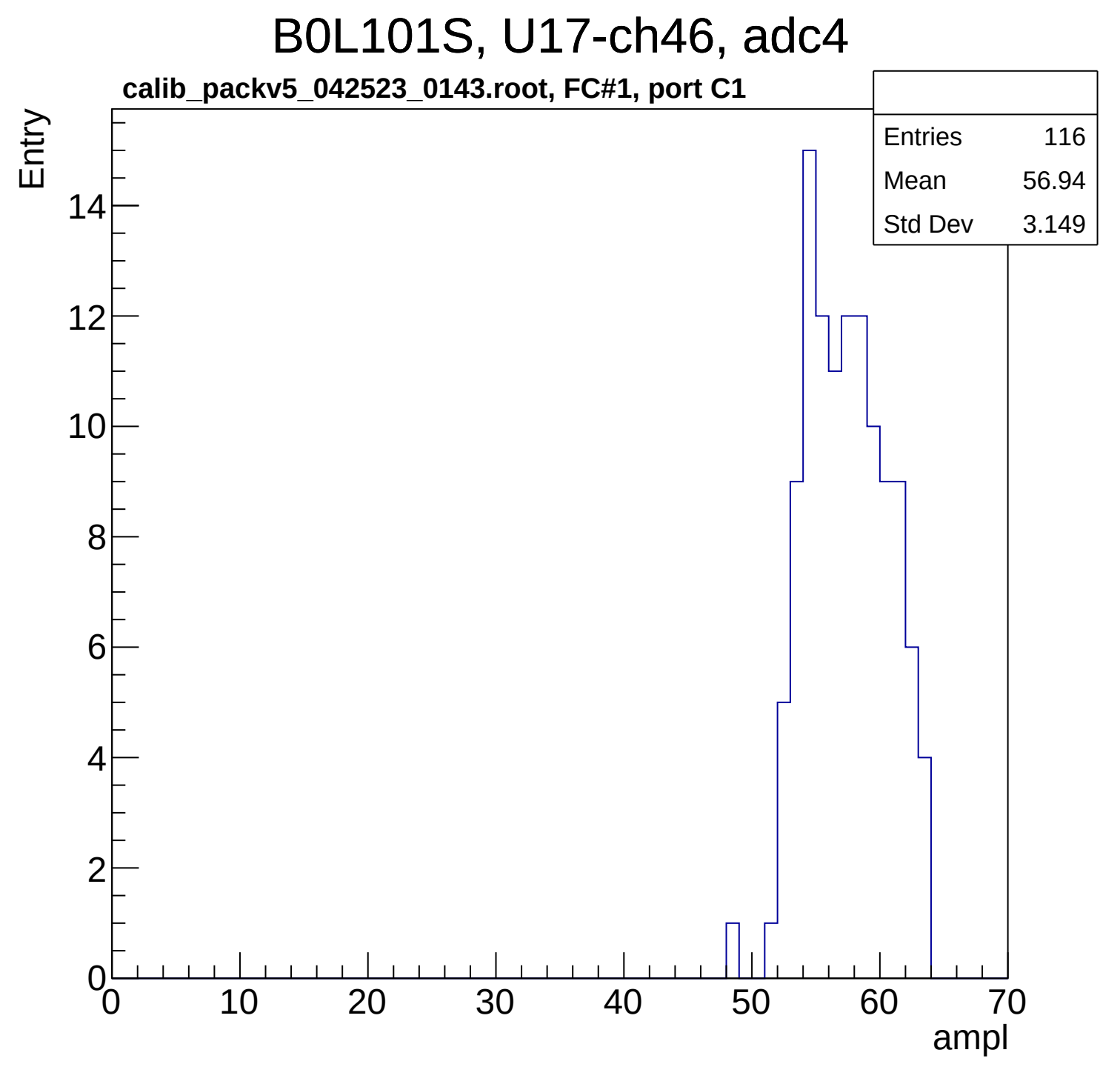

B0L101S, U17-ch45, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

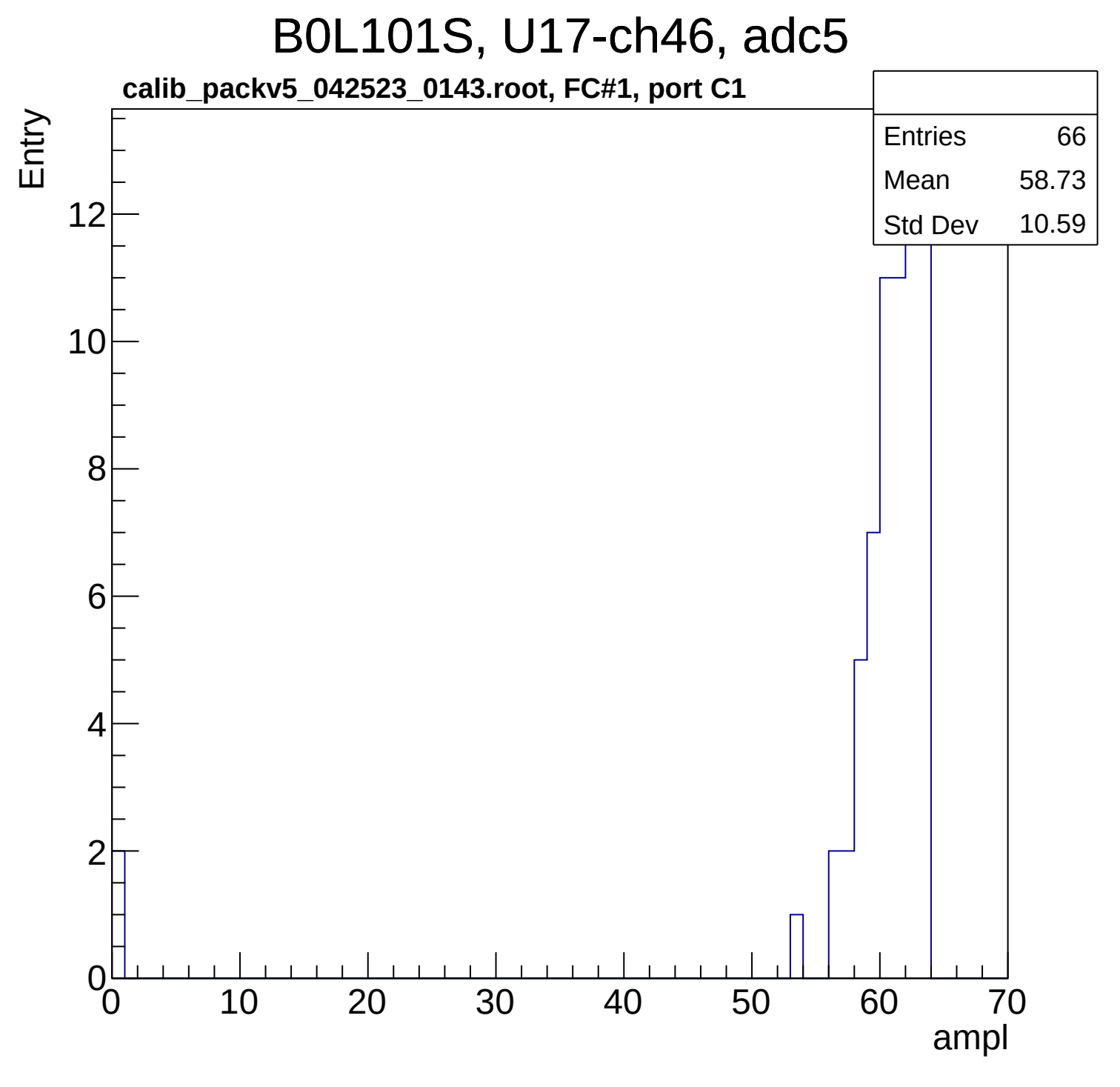

B0L101S, U17-ch46, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 30.07
Std Dev 4.605
Gaus mean : 30.8708
Gaus Width: 3.6179
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

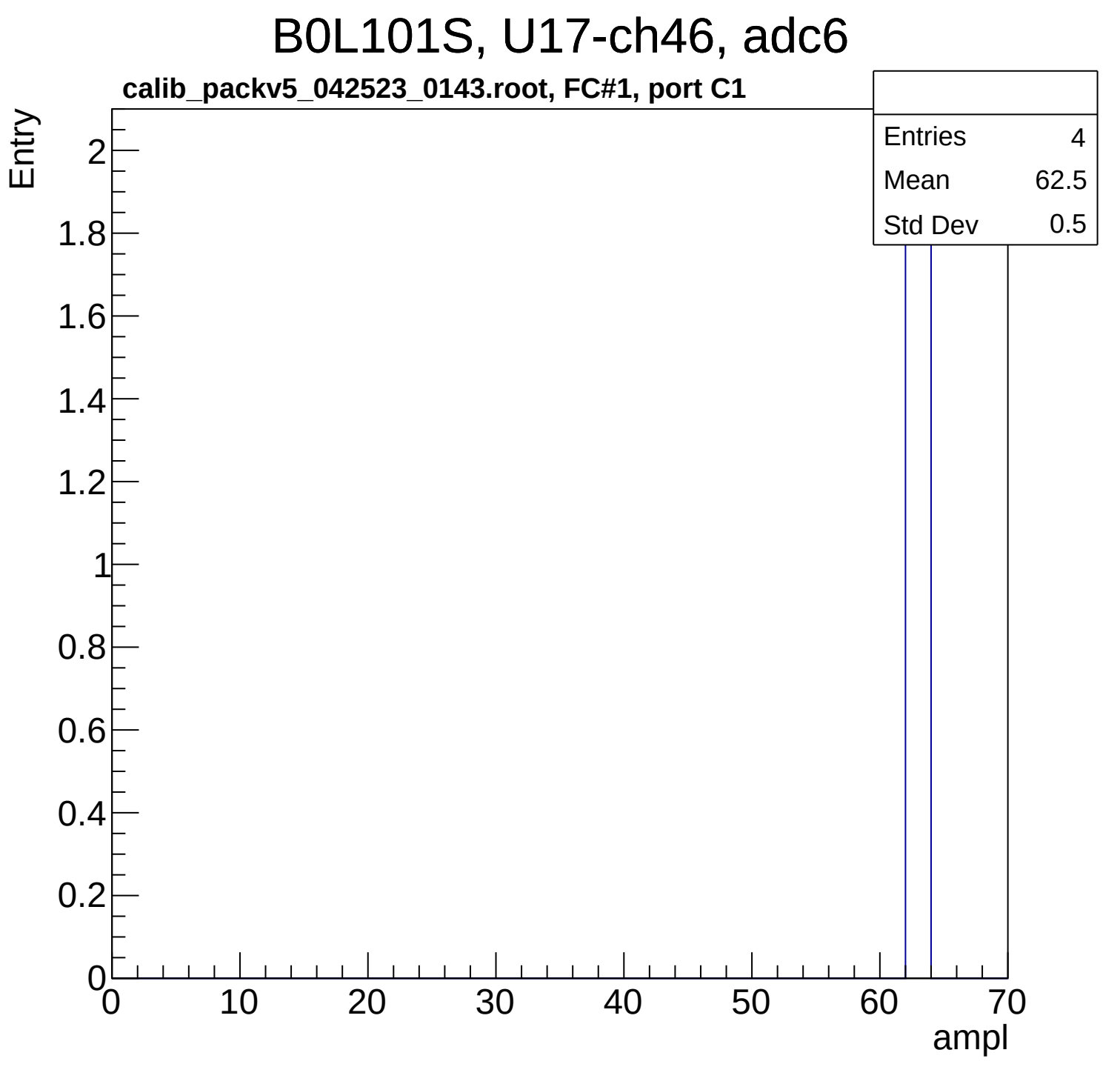

B0L101S, U17-ch46, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 168
Mean 37.68
Std Dev 3.893
Gaus mean : 38.0284
Gaus Width: 3.9212
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

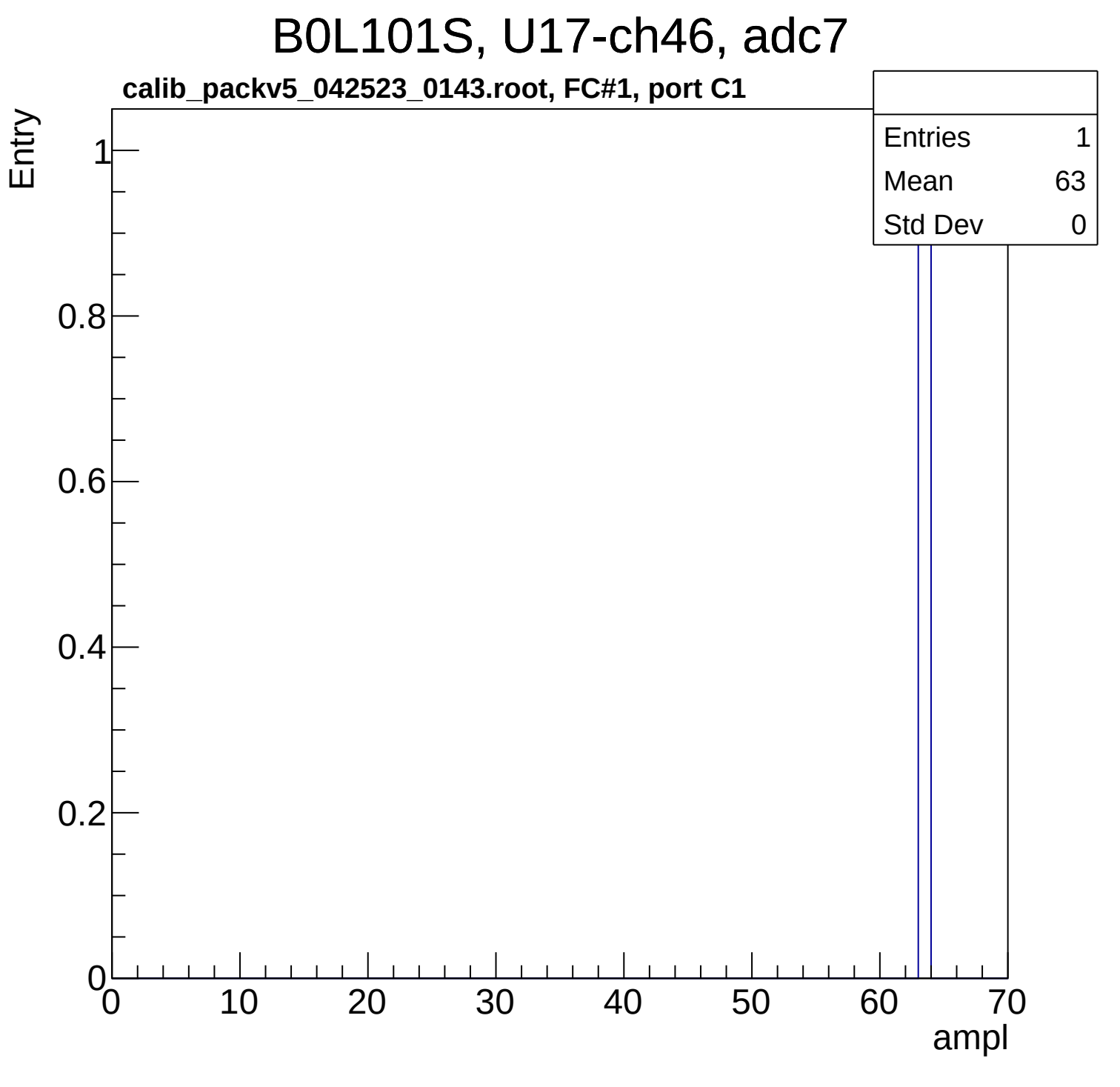

B0L101S, U17-ch46, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 44.46
Std Dev 3.378
Gaus mean : 45.3372
Gaus Width: 2.9941
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

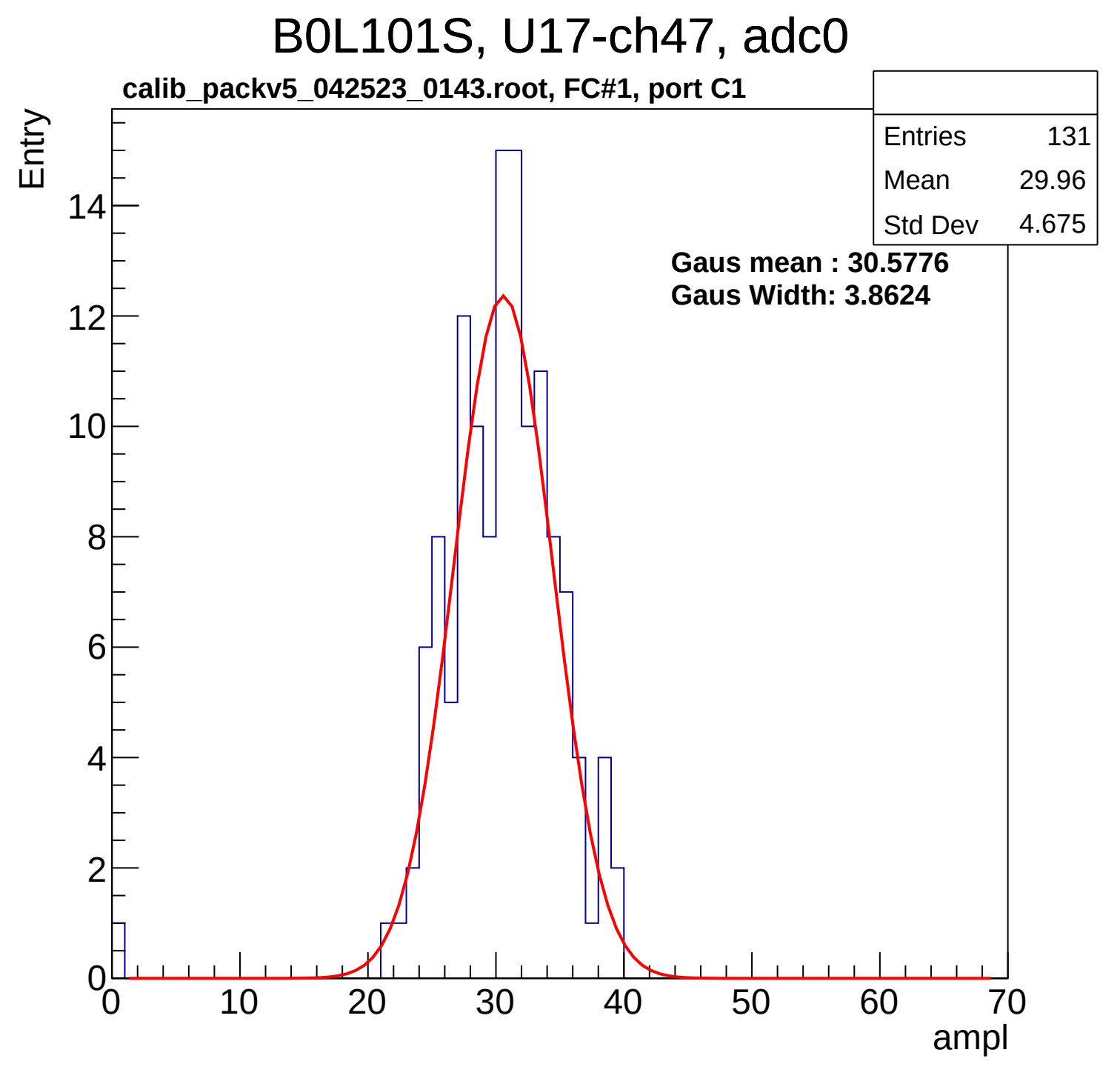

B0L101S, U17-ch46, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 51.14
Std Dev 3.518
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

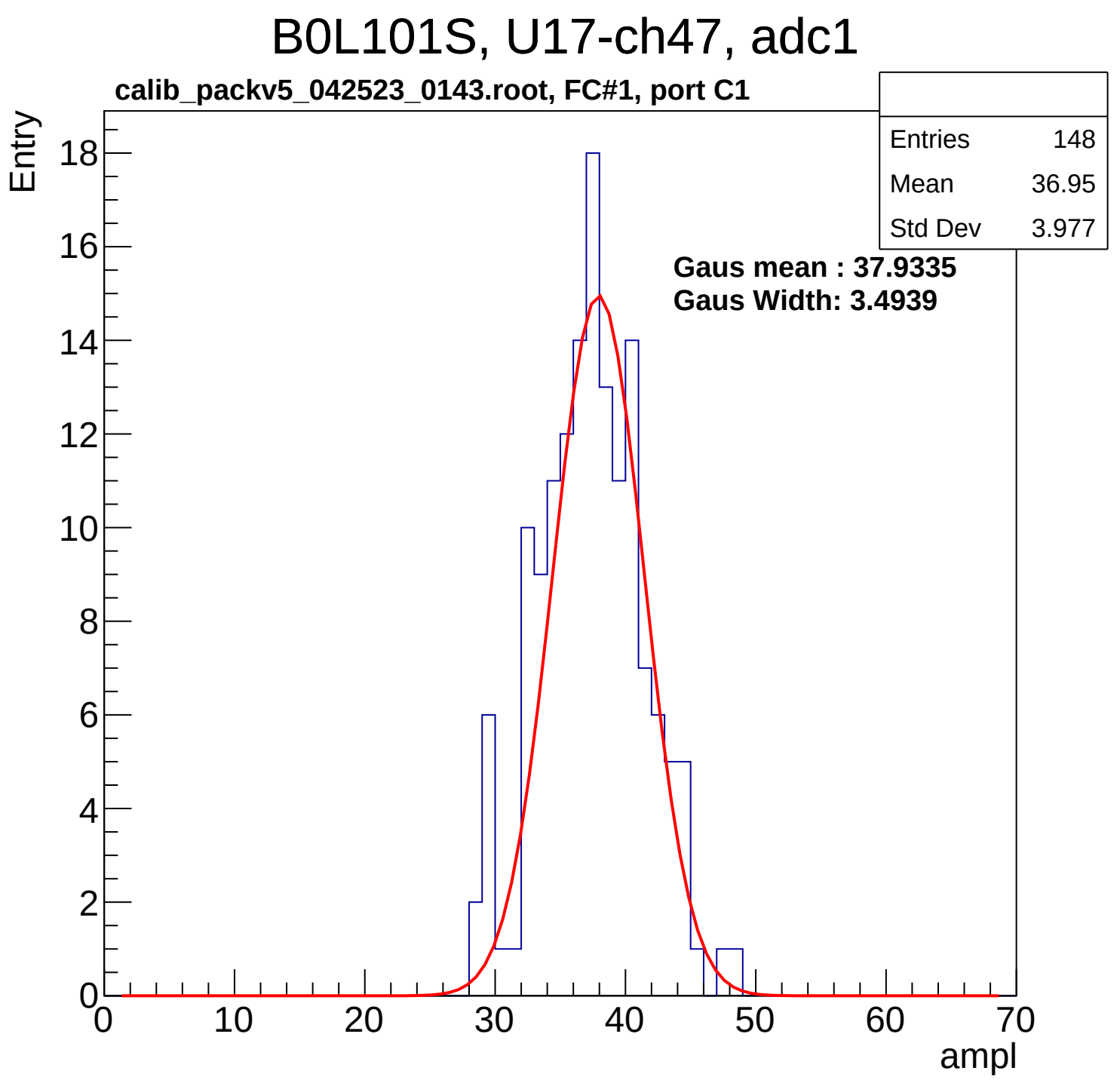

B0L101S, U17-ch46, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 56.94
Std Dev 3.149
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

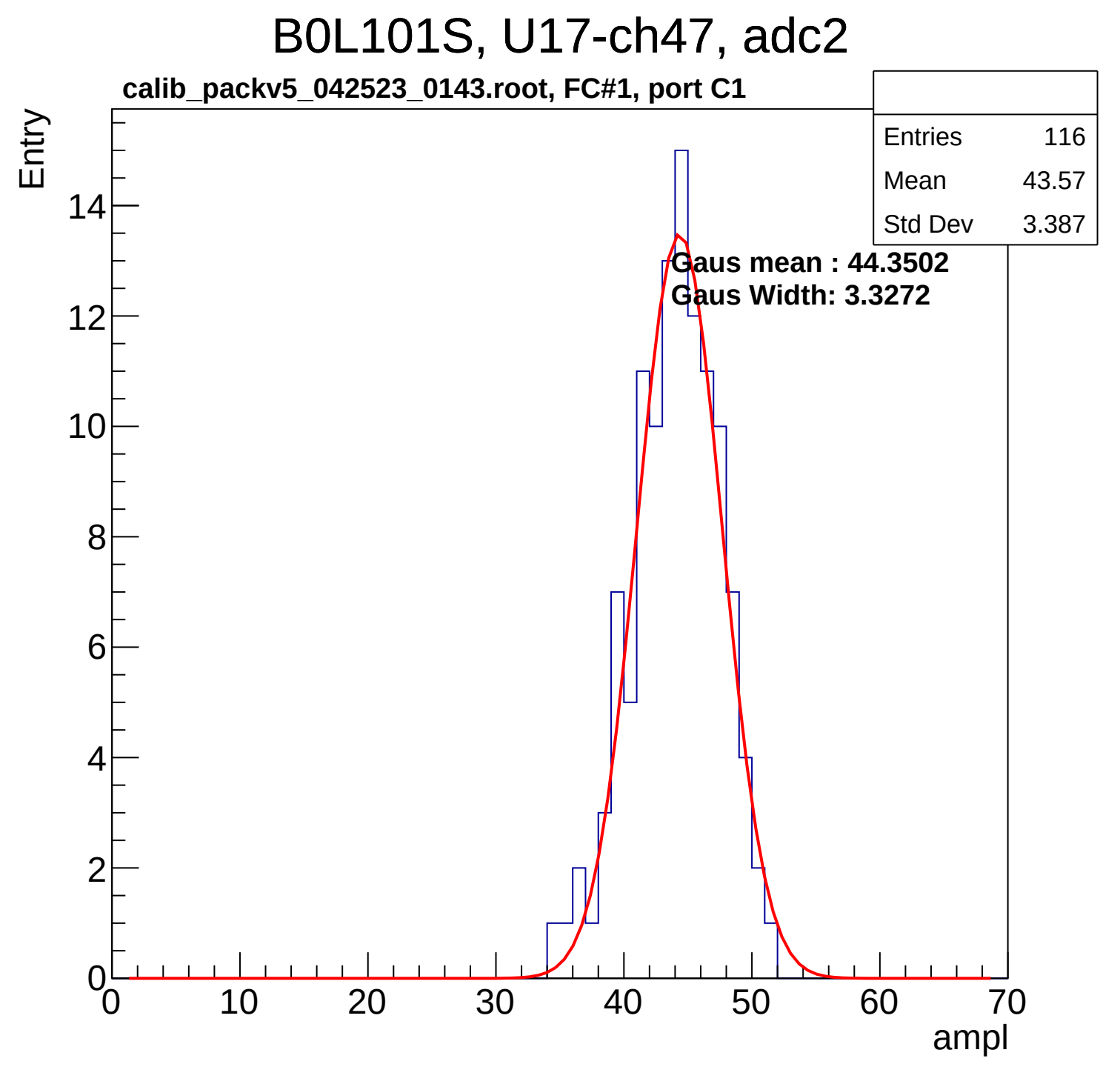

B0L101S, U17-ch46, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 66
Mean 58.73
Std Dev 10.59
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

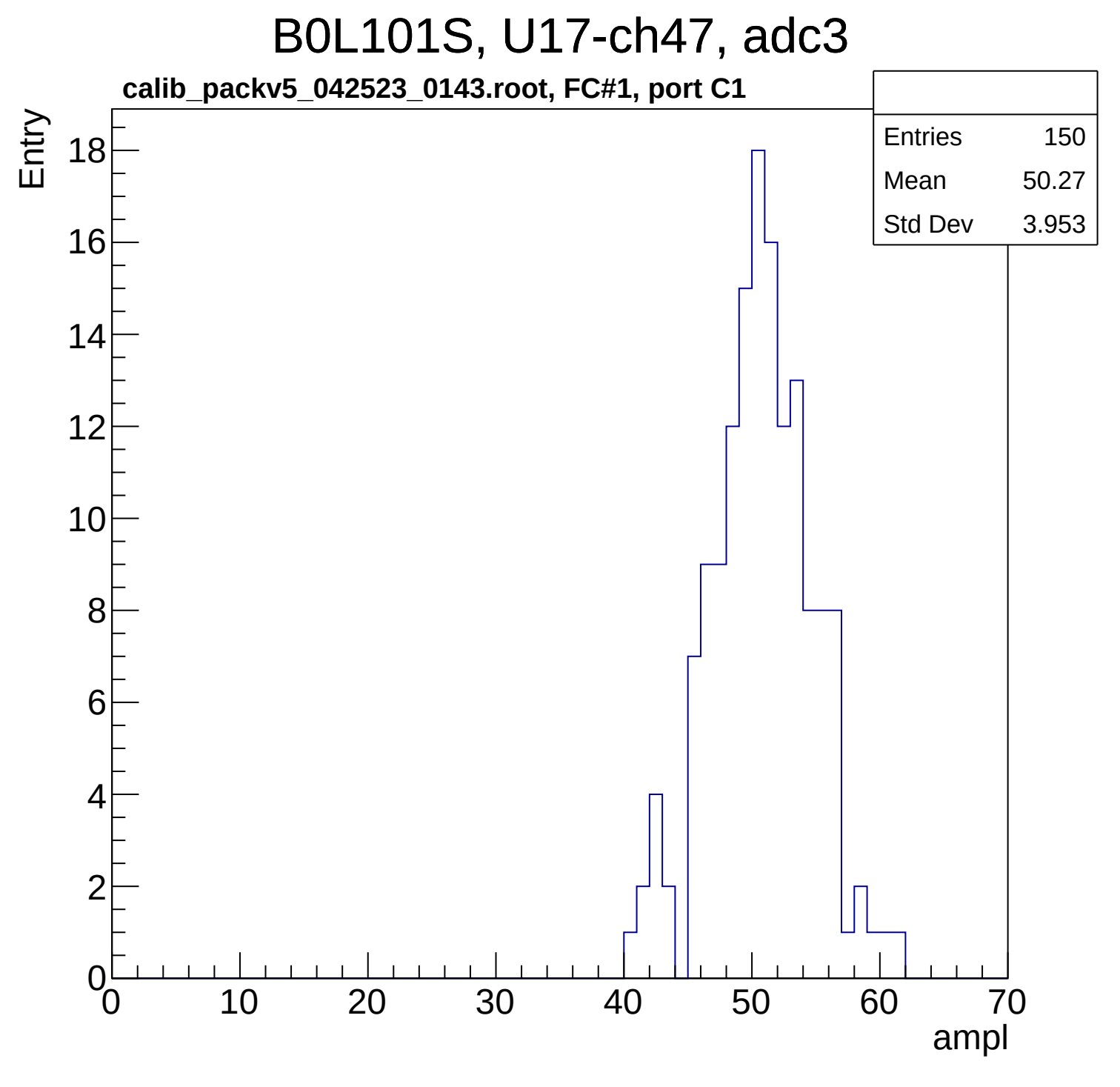

B0L101S, U17-ch46, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

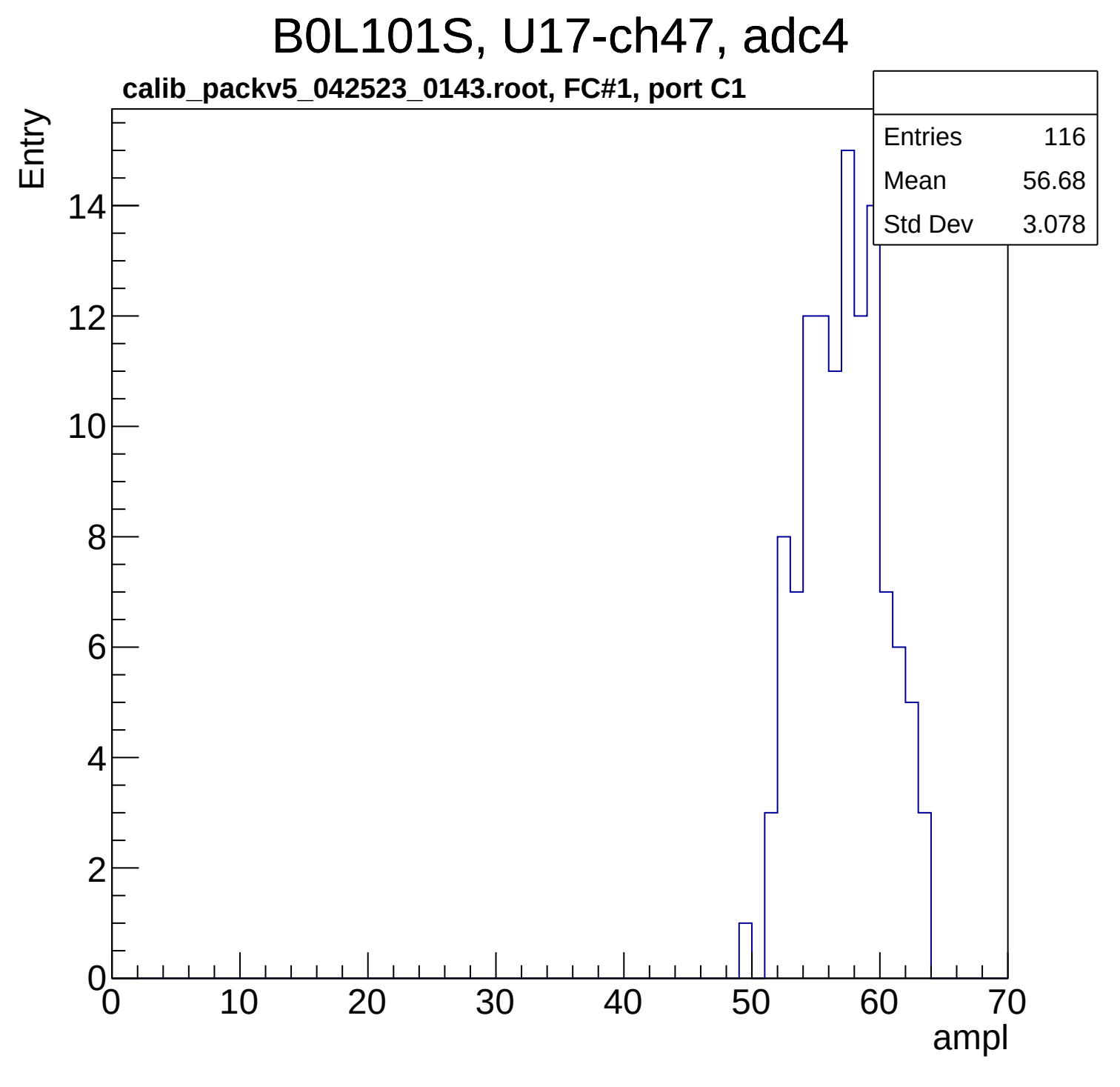

B0L101S, U17-ch46, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

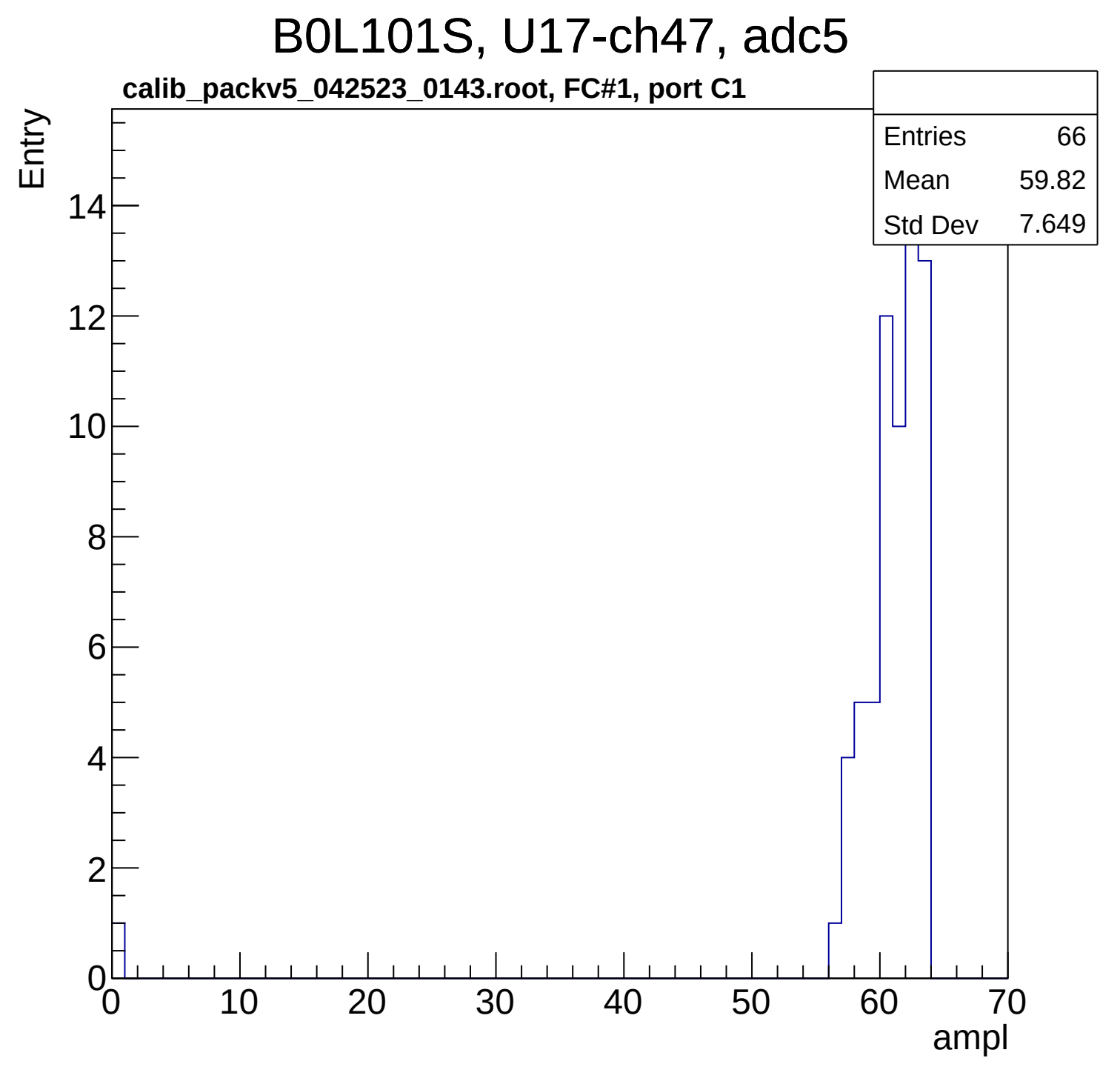

B0L101S, U17-ch47, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 29.96
Std Dev 4.675
Gaus mean : 30.5776
Gaus Width: 3.8624
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

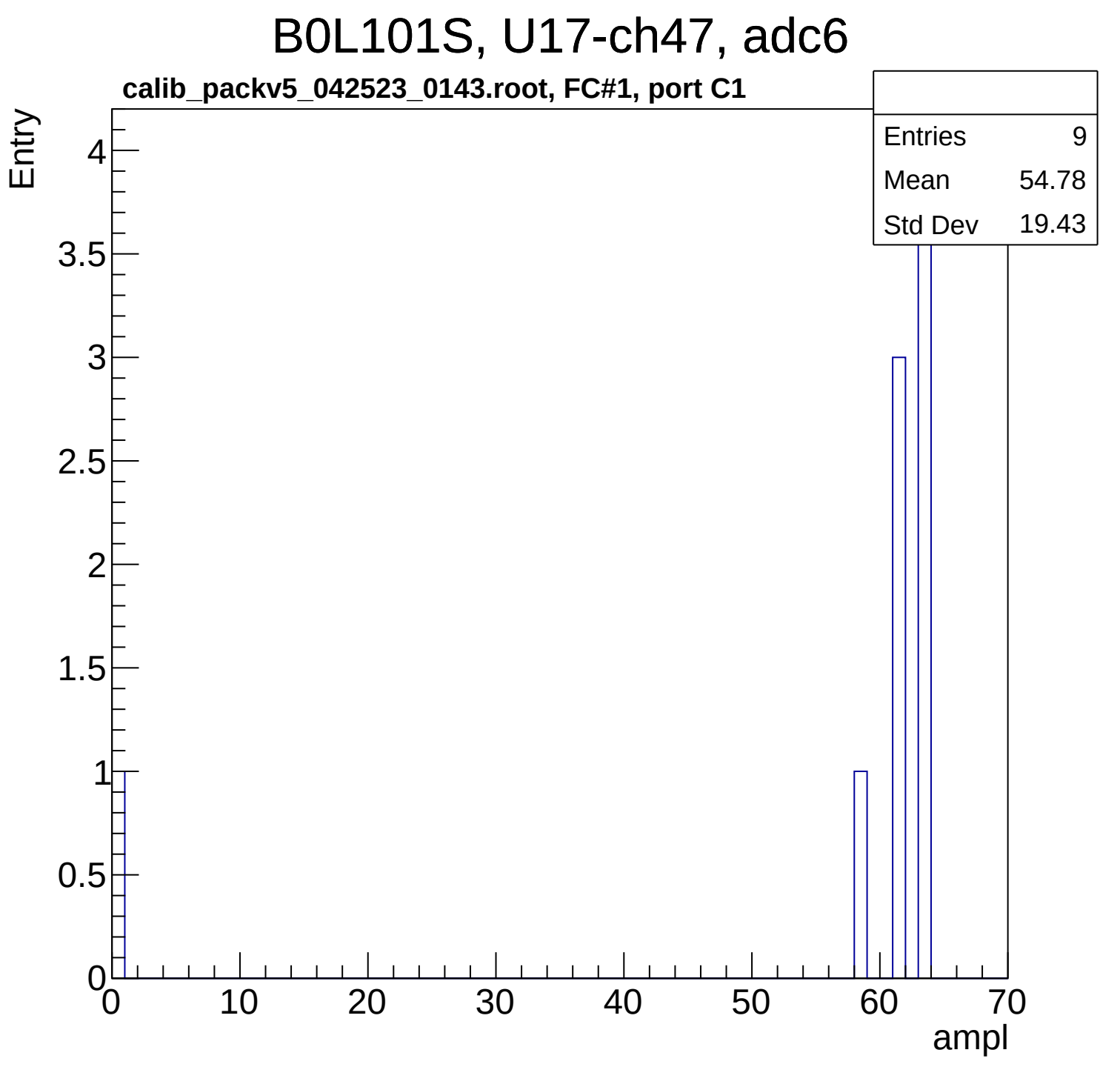

B0L101S, U17-ch47, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 36.95
Std Dev 3.977
Gaus mean : 37.9335
Gaus Width: 3.4939
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

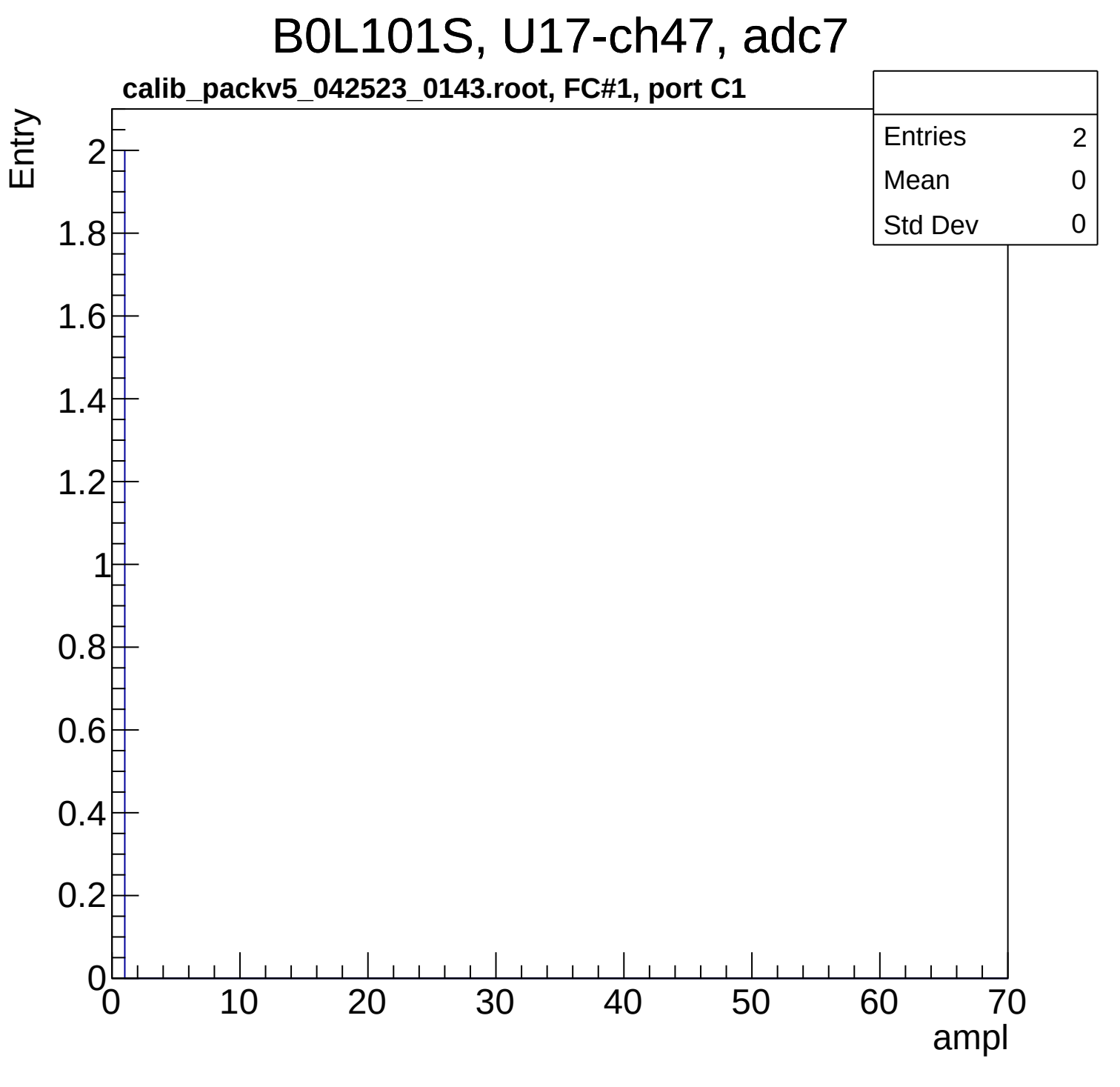

B0L101S, U17-ch47, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 43.57
Std Dev 3.387
Gaus mean : 44.3502
Gaus Width: 3.3272
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

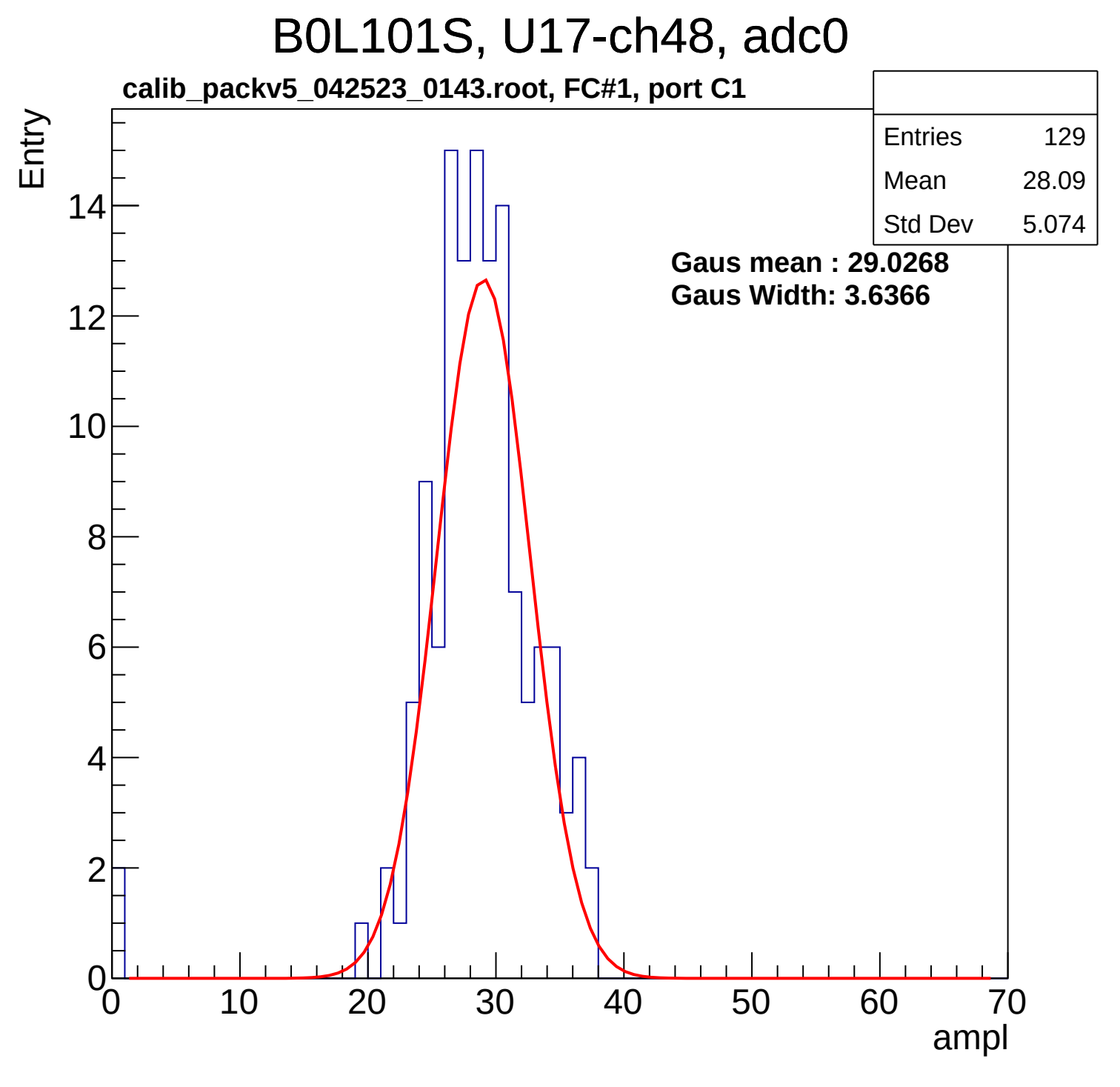

B0L101S, U17-ch47, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 50.27
Std Dev 3.953
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

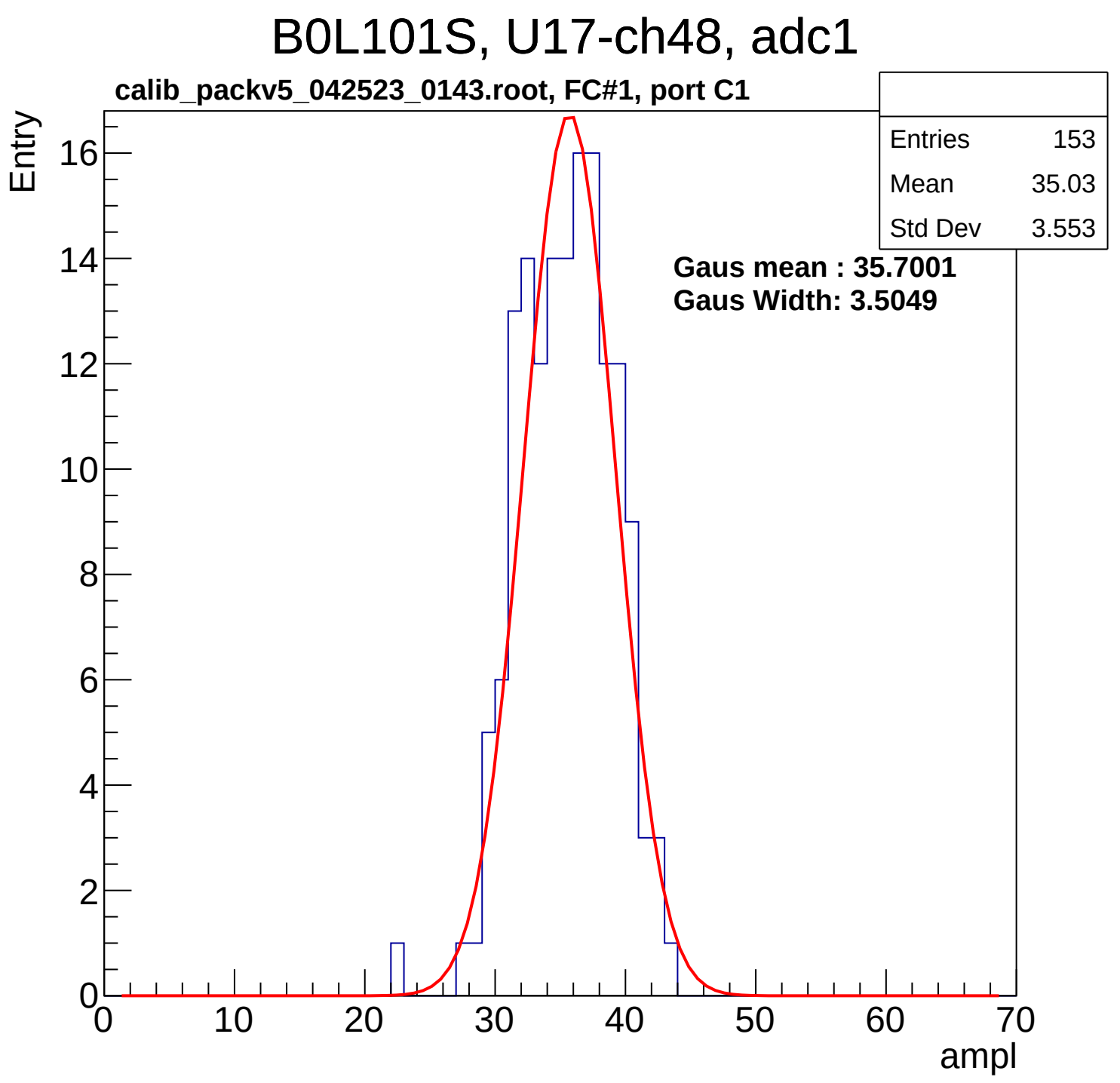

B0L101S, U17-ch47, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 56.68
Std Dev 3.078
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

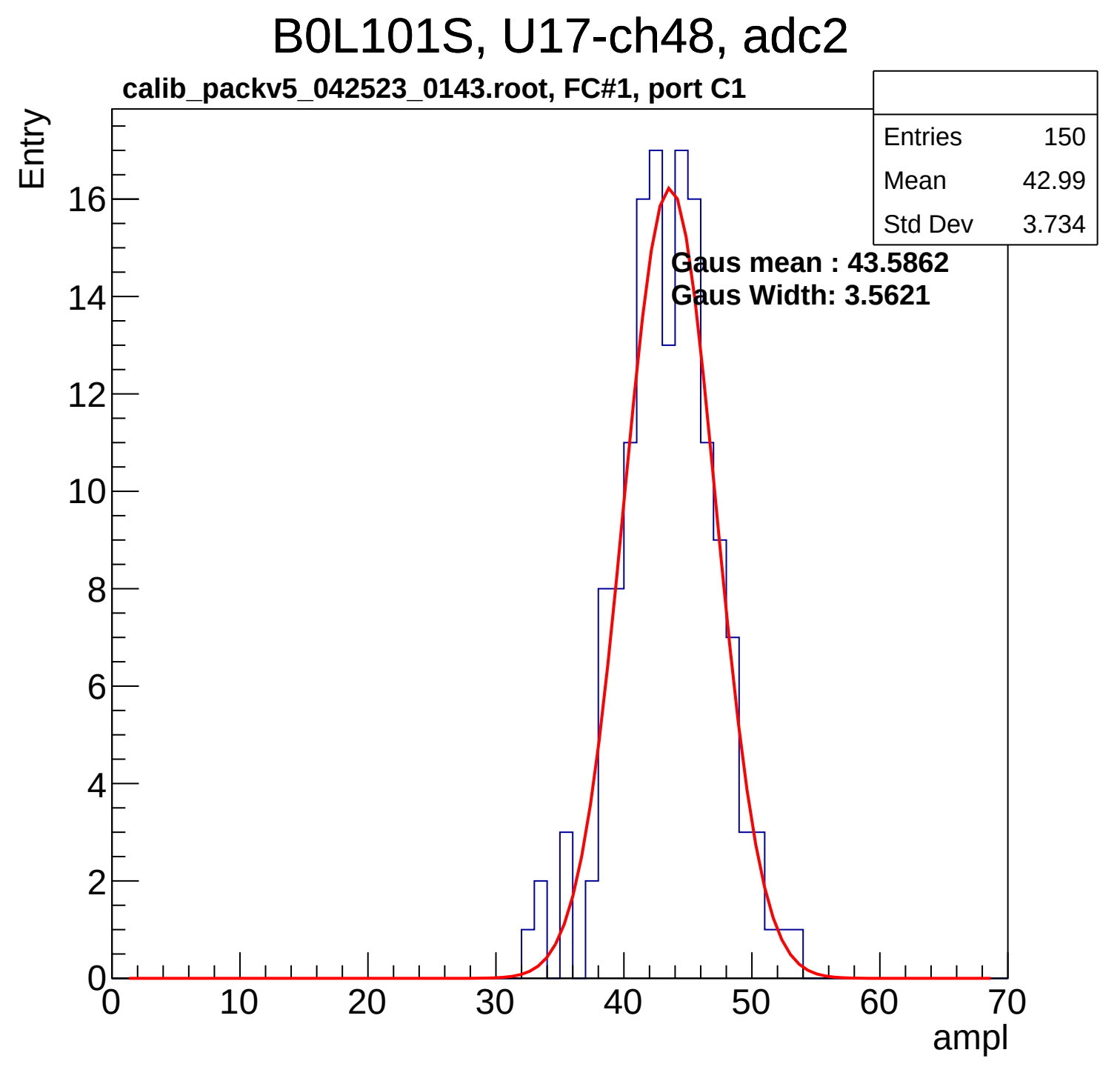

B0L101S, U17-ch47, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 66
Mean 59.82
Std Dev 7.649
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

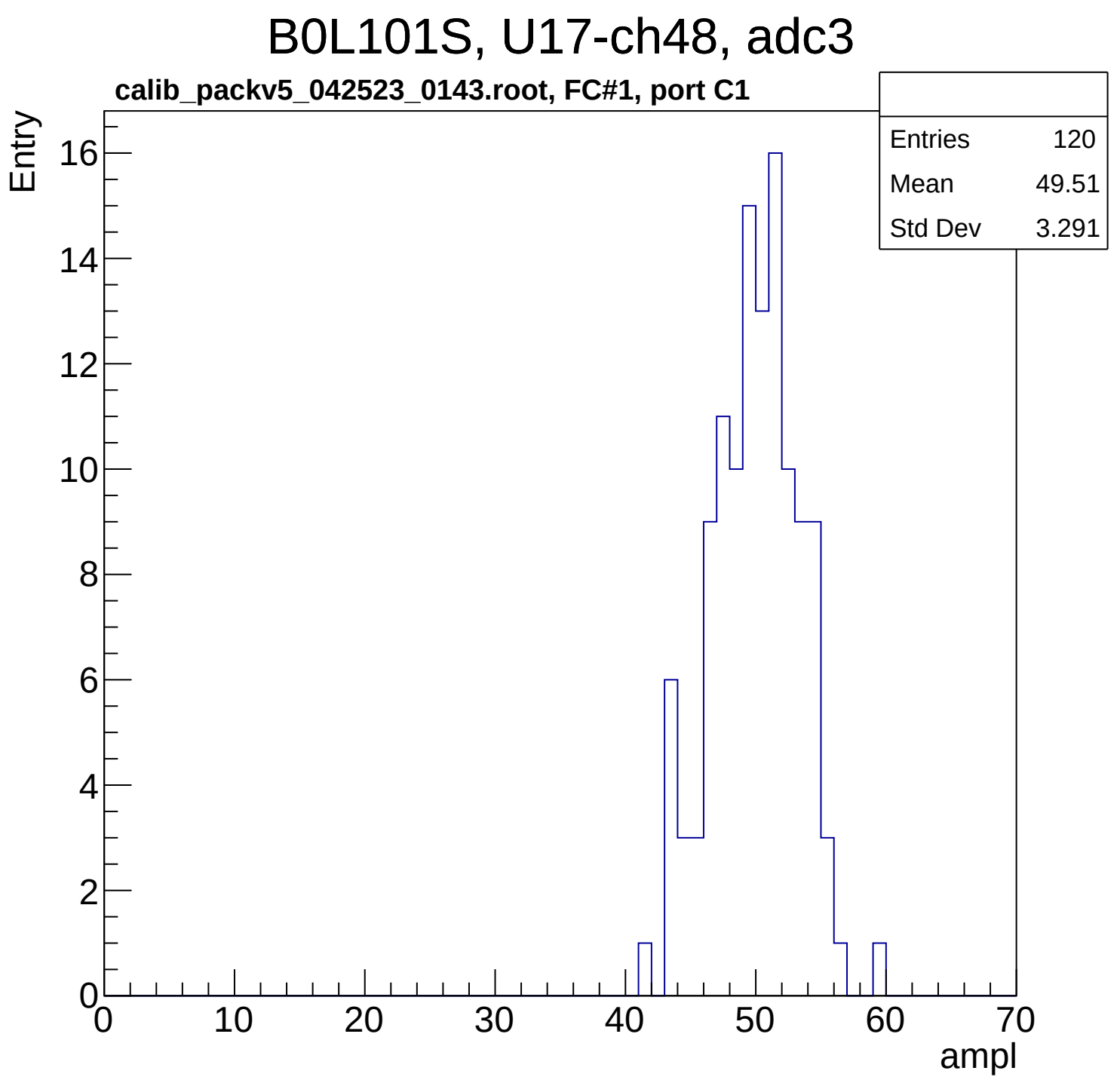

B0L101S, U17-ch47, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 9
Mean 54.78
Std Dev 19.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

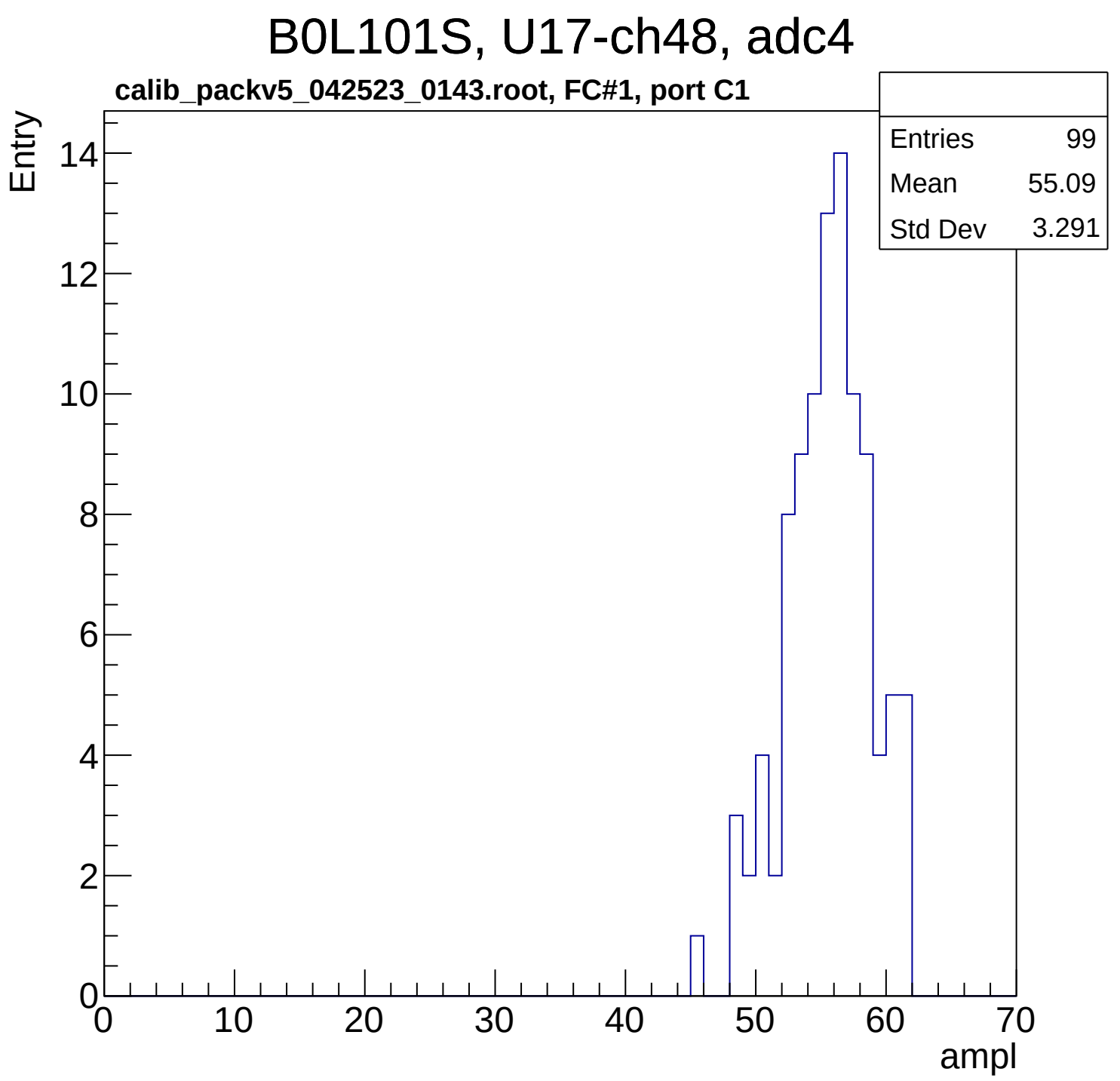

B0L101S, U17-ch47, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

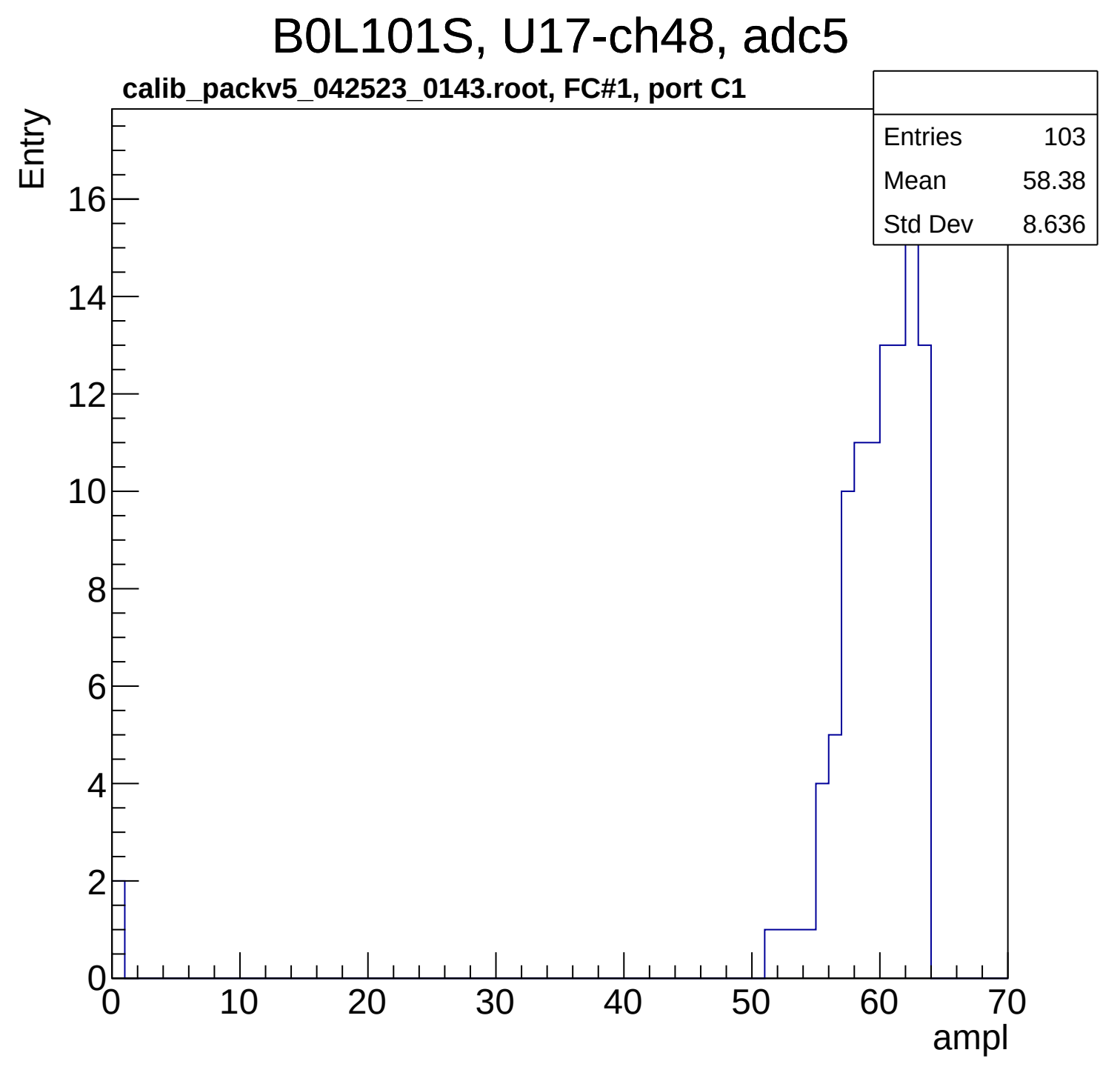

B0L101S, U17-ch48, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 28.09
Std Dev 5.074
Gaus mean : 29.0268
Gaus Width: 3.6366
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

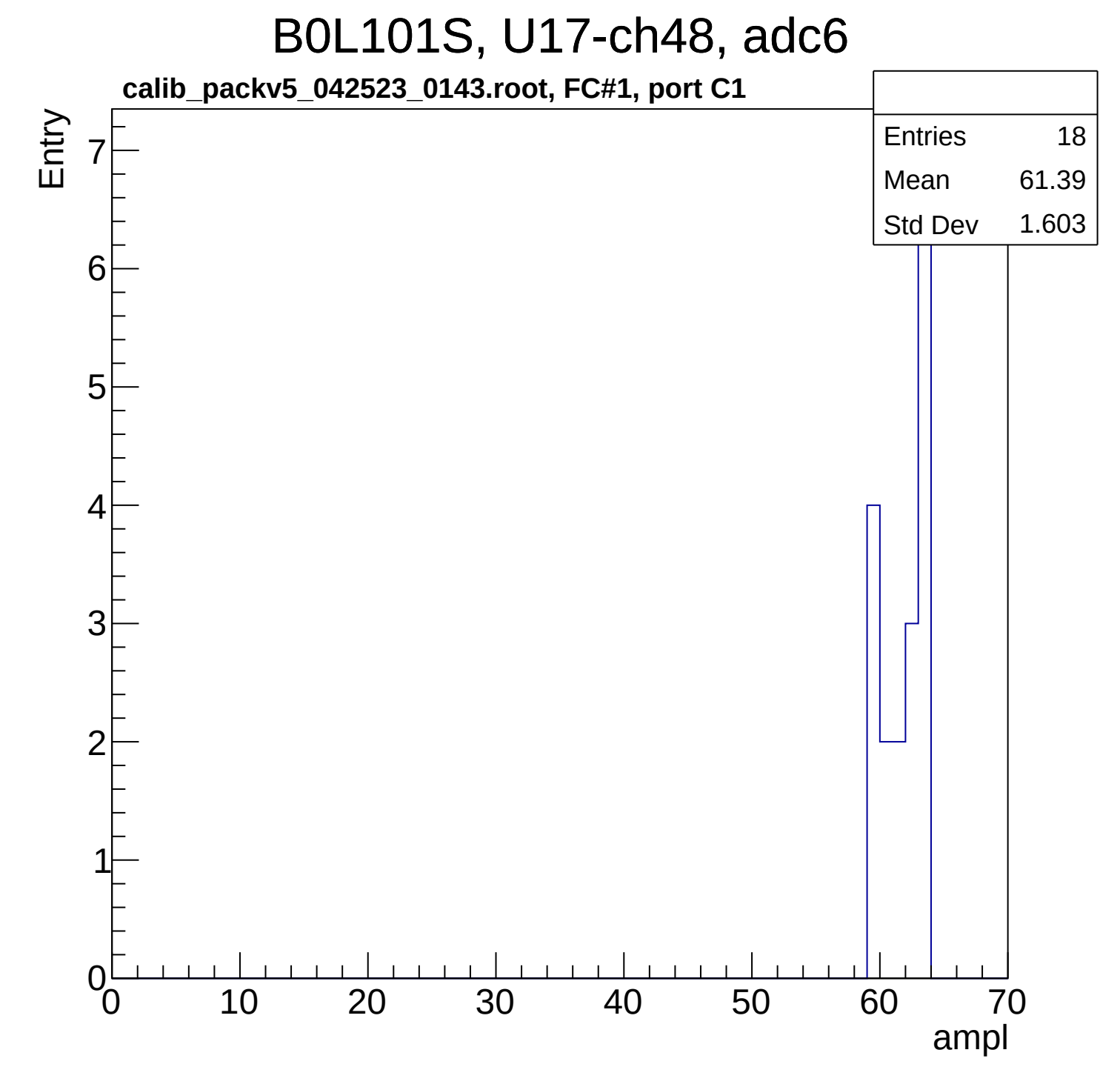

B0L101S, U17-ch48, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 35.03
Std Dev 3.553
Gaus mean : 35.7001
Gaus Width: 3.5049
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

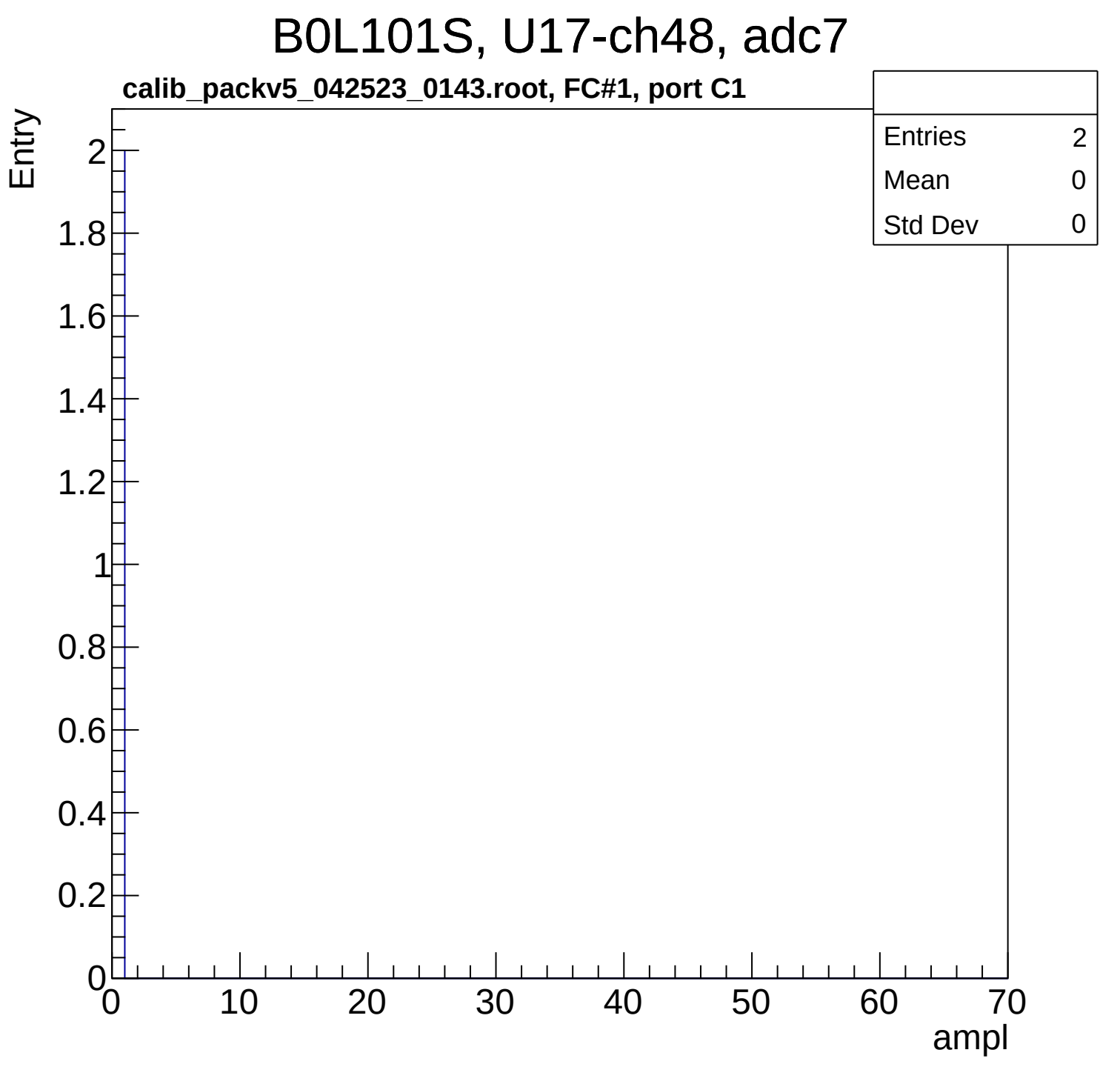

B0L101S, U17-ch48, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 42.99
Std Dev 3.734
Gaus mean : 43.5862
Gaus Width: 3.5621
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

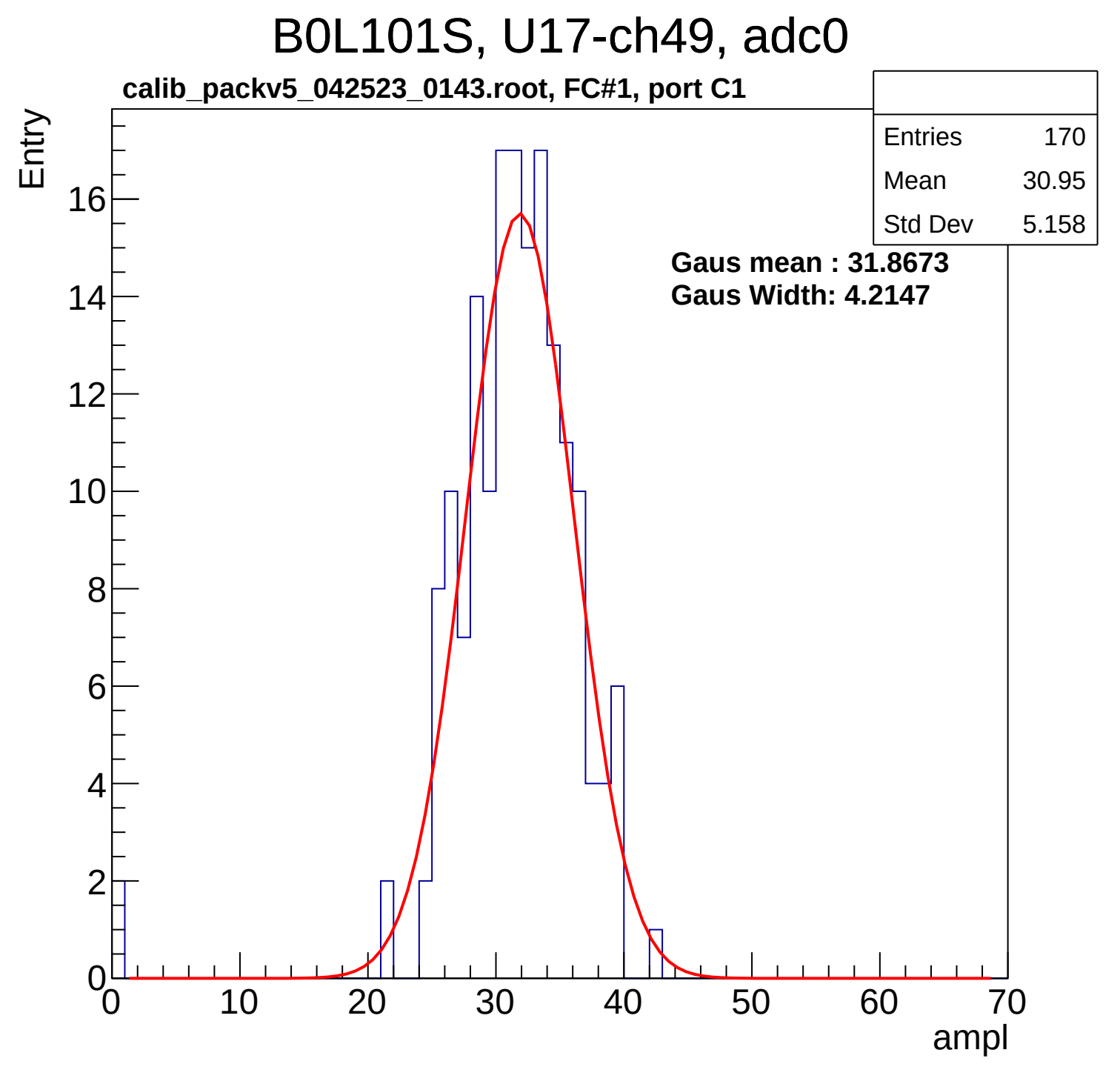

B0L101S, U17-ch48, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 49.51
Std Dev 3.291
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

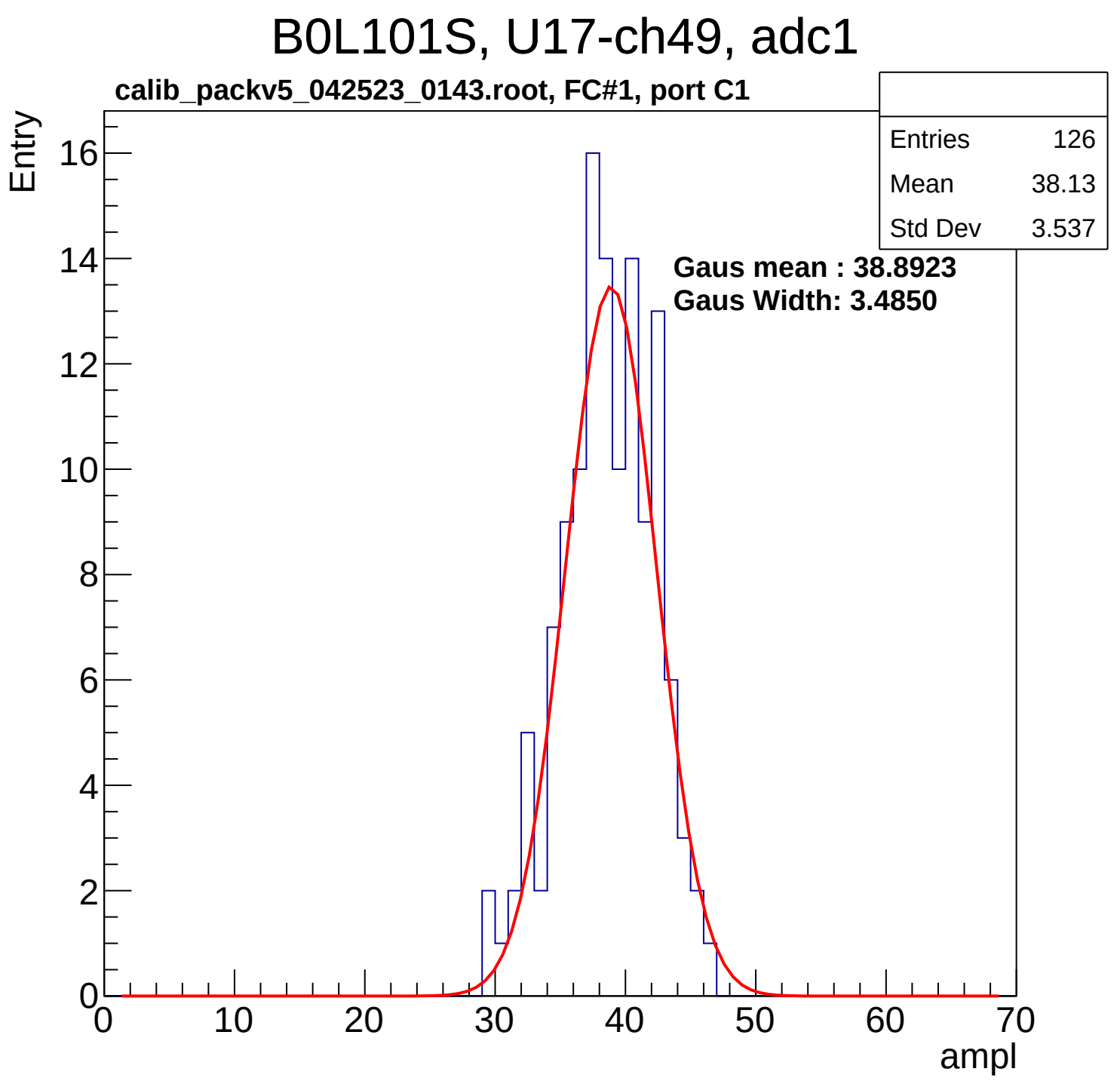

B0L101S, U17-ch48, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 99
Mean 55.09
Std Dev 3.291
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

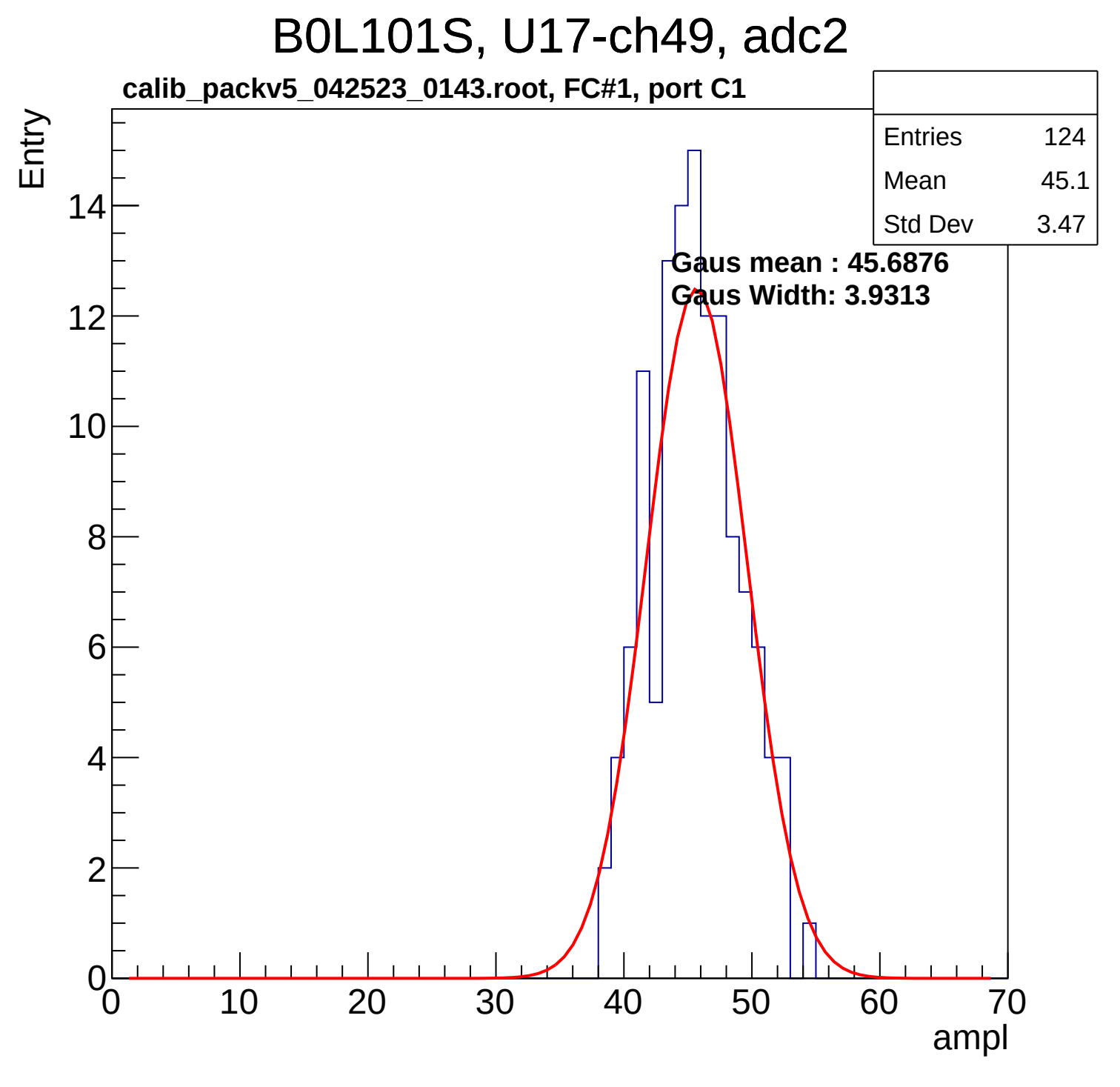

B0L101S, U17-ch48, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 58.38
Std Dev 8.636
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

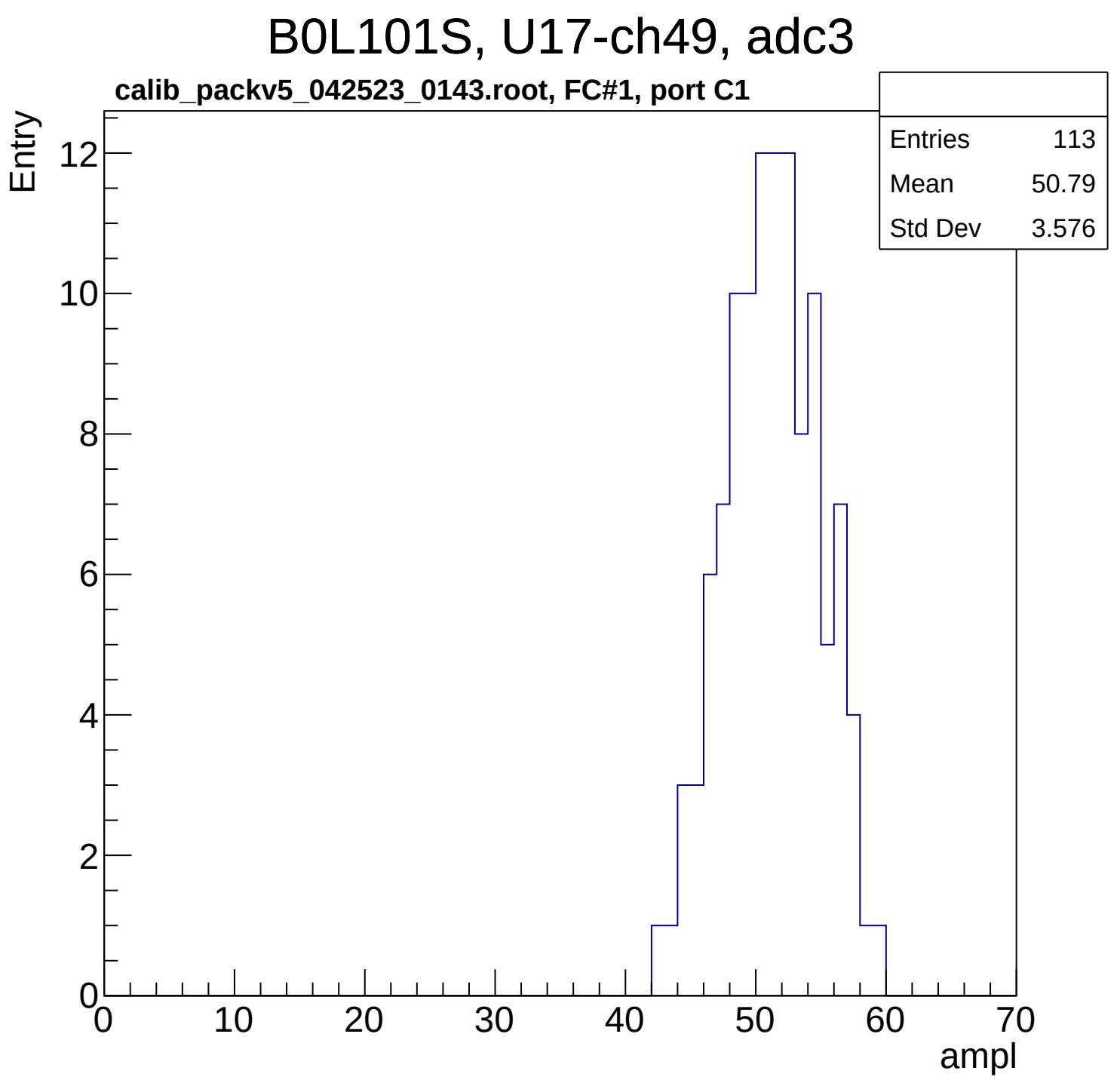

B0L101S, U17-ch48, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 18
Mean 61.39
Std Dev 1.603
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

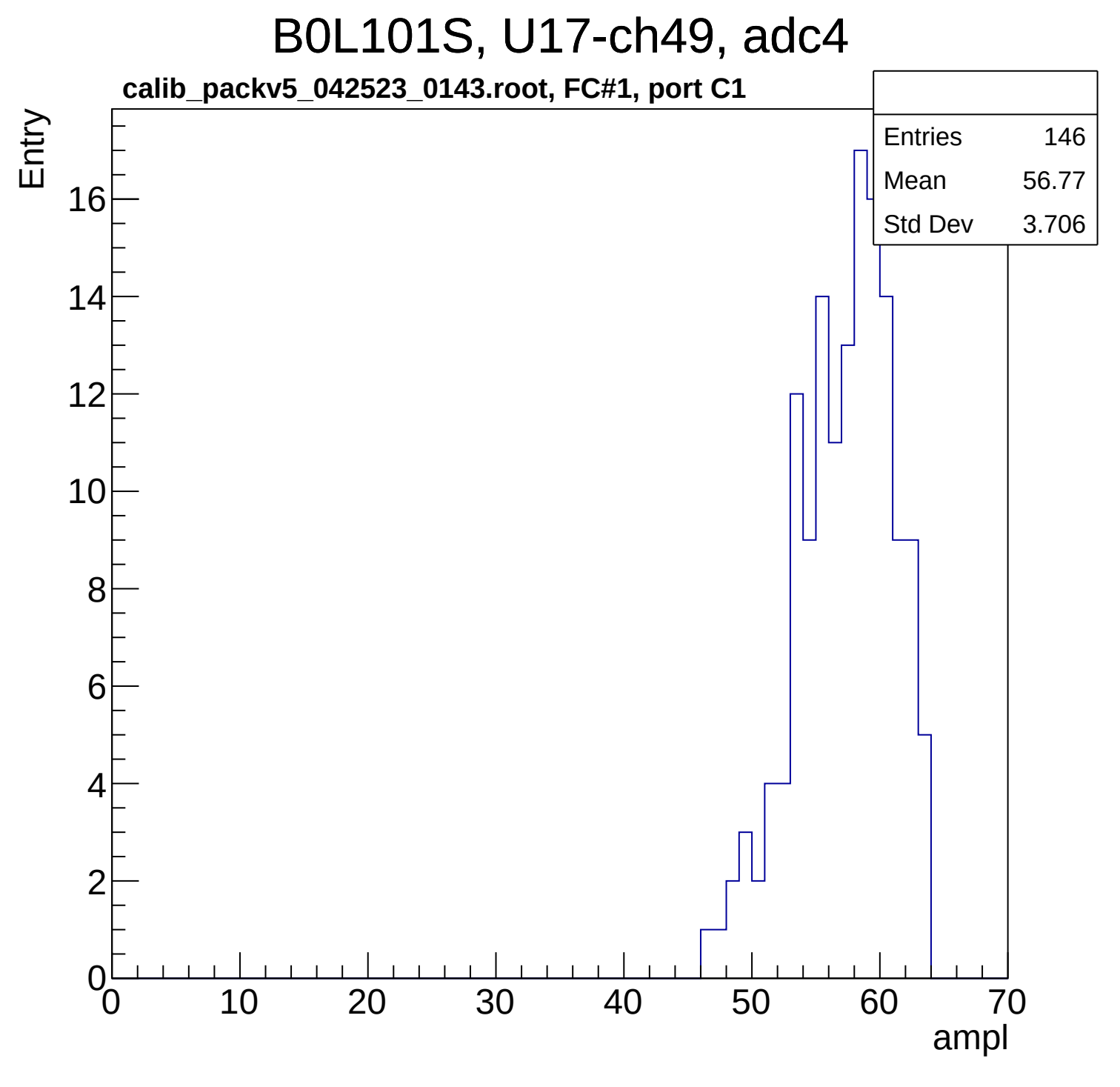

B0L101S, U17-ch48, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

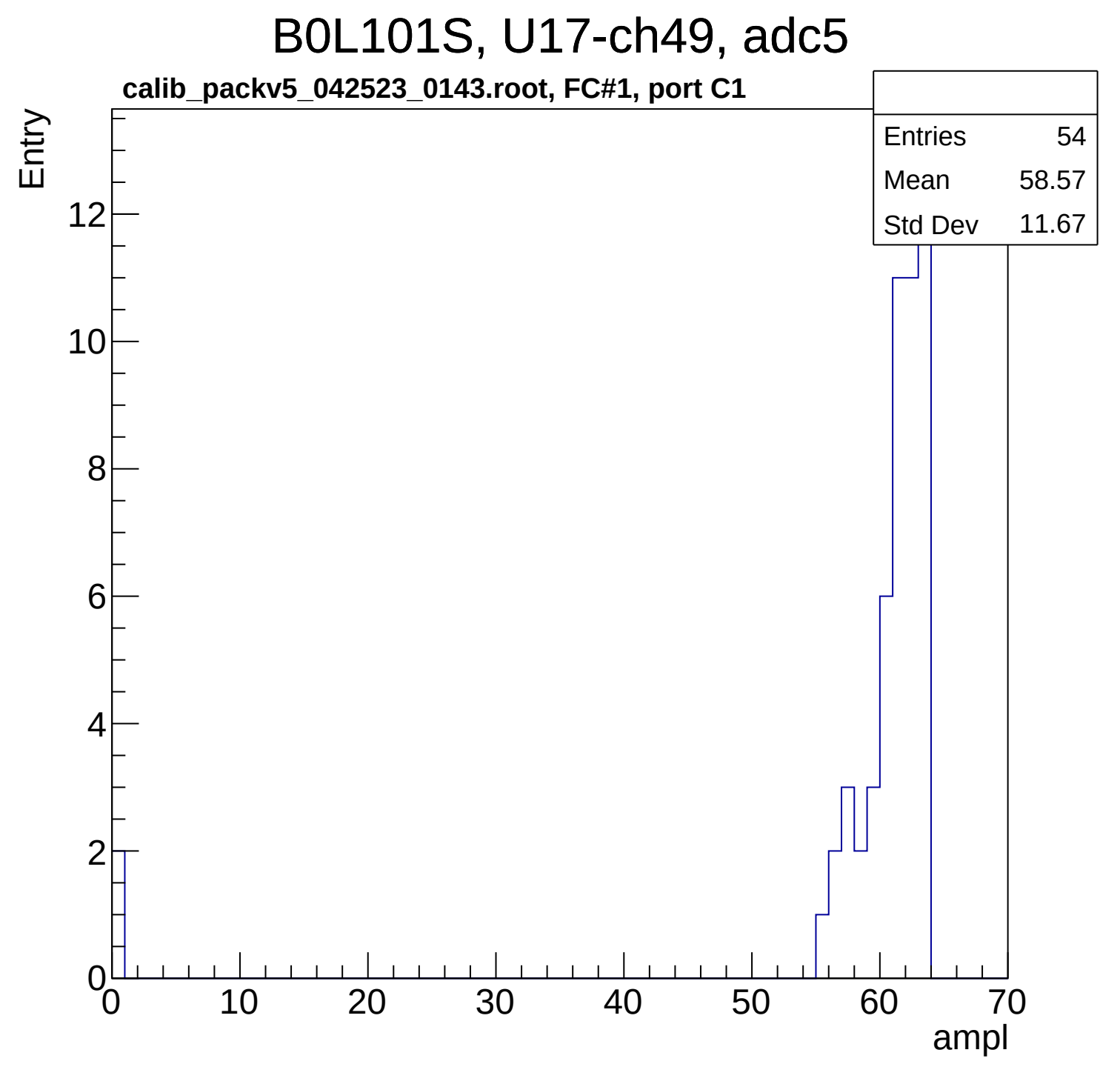

B0L101S, U17-ch49, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 170
Mean 30.95
Std Dev 5.158
Gaus mean : 31.8673
Gaus Width: 4.2147
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

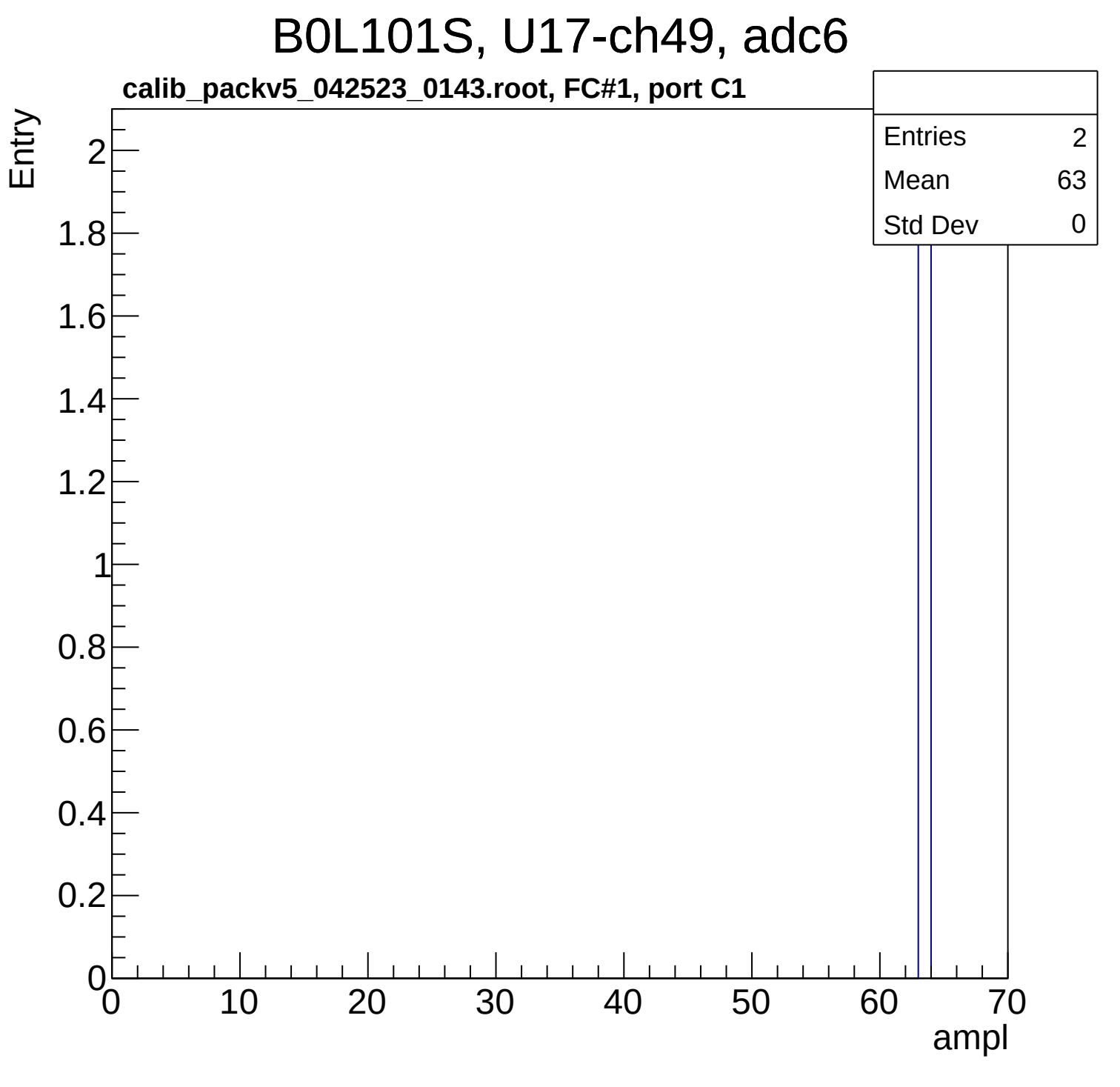

B0L101S, U17-ch49, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 38.13
Std Dev 3.537
Gaus mean : 38.8923
Gaus Width: 3.4850
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

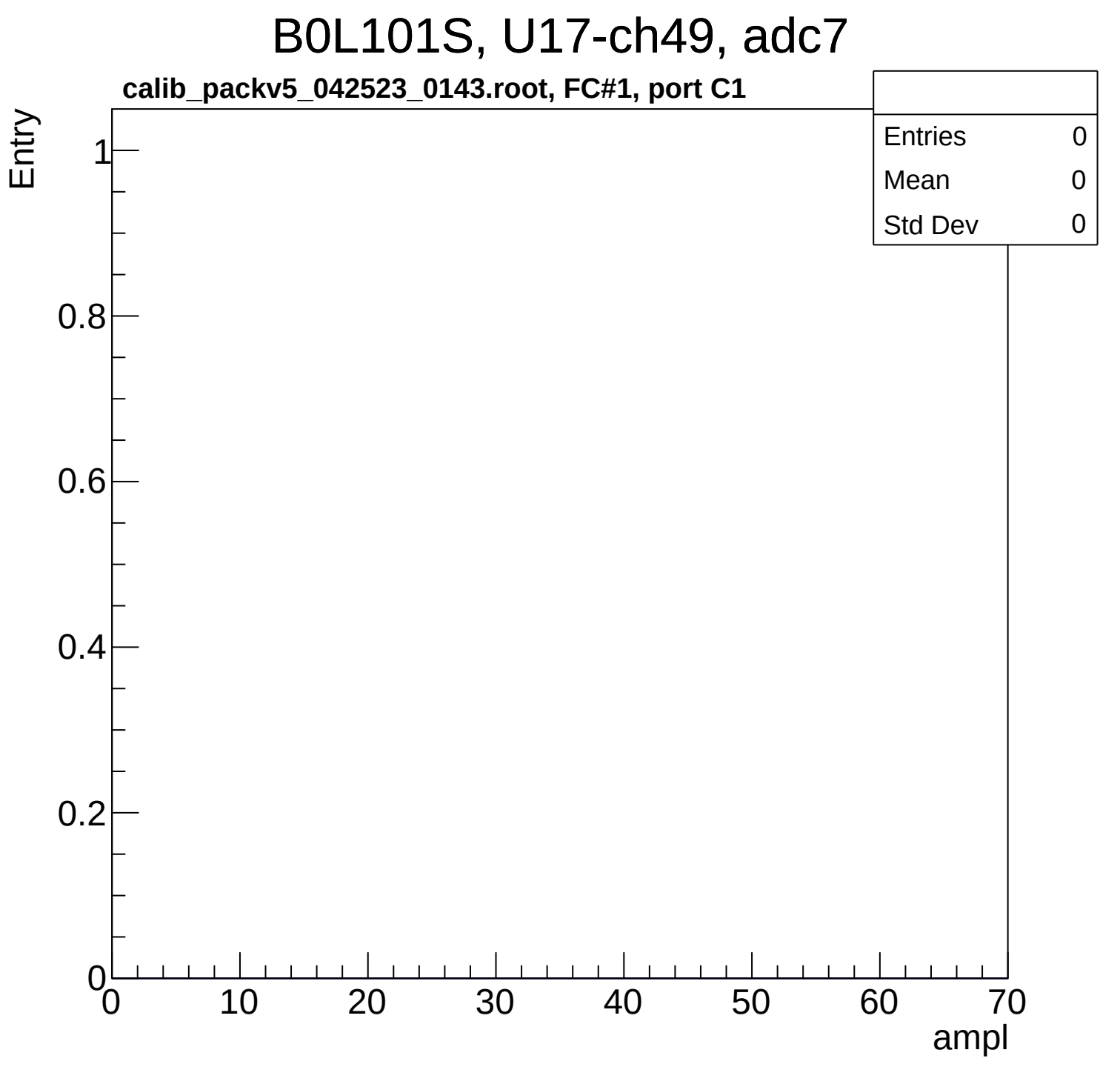

B0L101S, U17-ch49, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 45.1
Std Dev 3.47
Gaus mean : 45.6876
Gaus Width: 3.9313
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

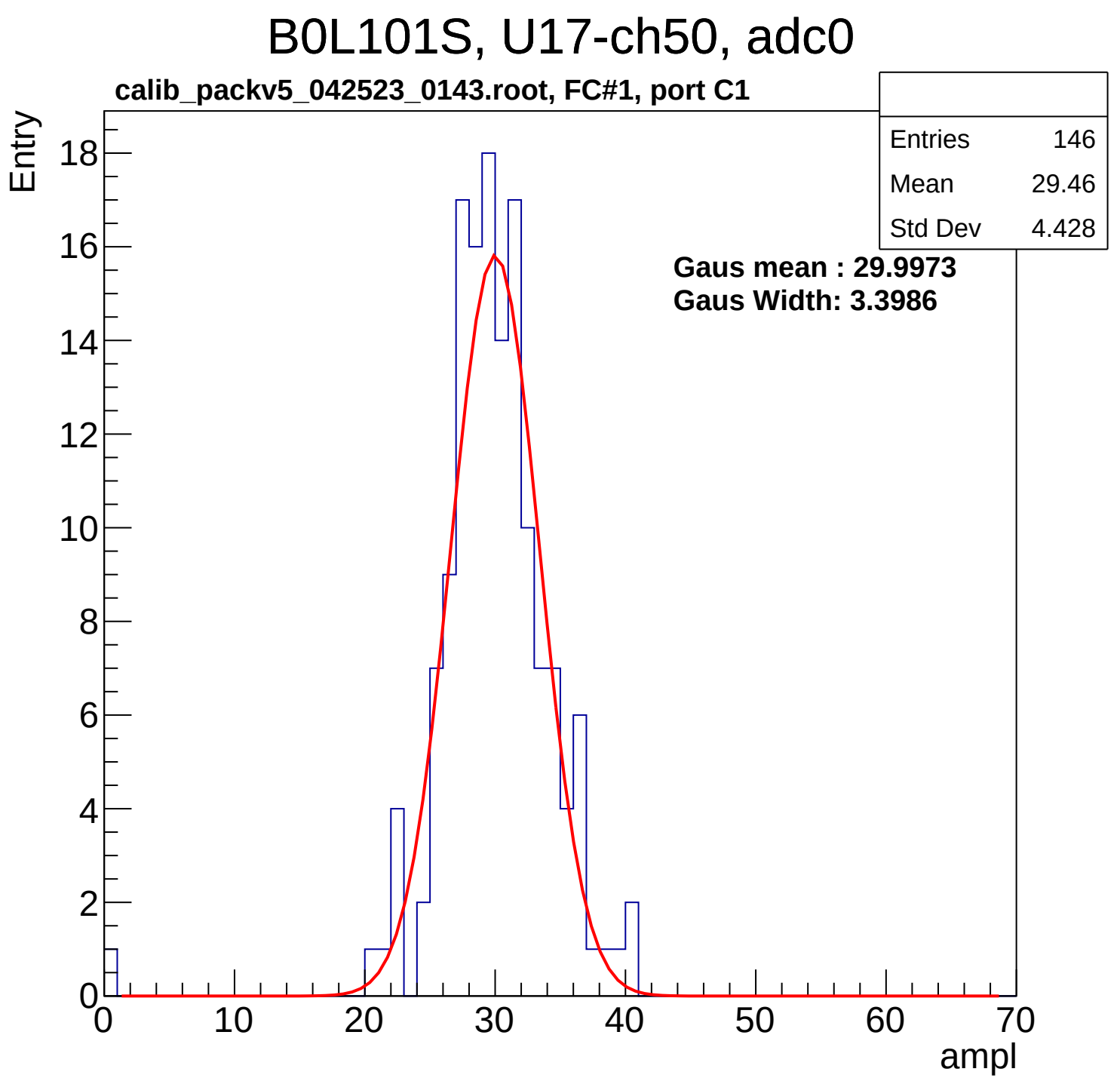

B0L101S, U17-ch49, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 50.79
Std Dev 3.576
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

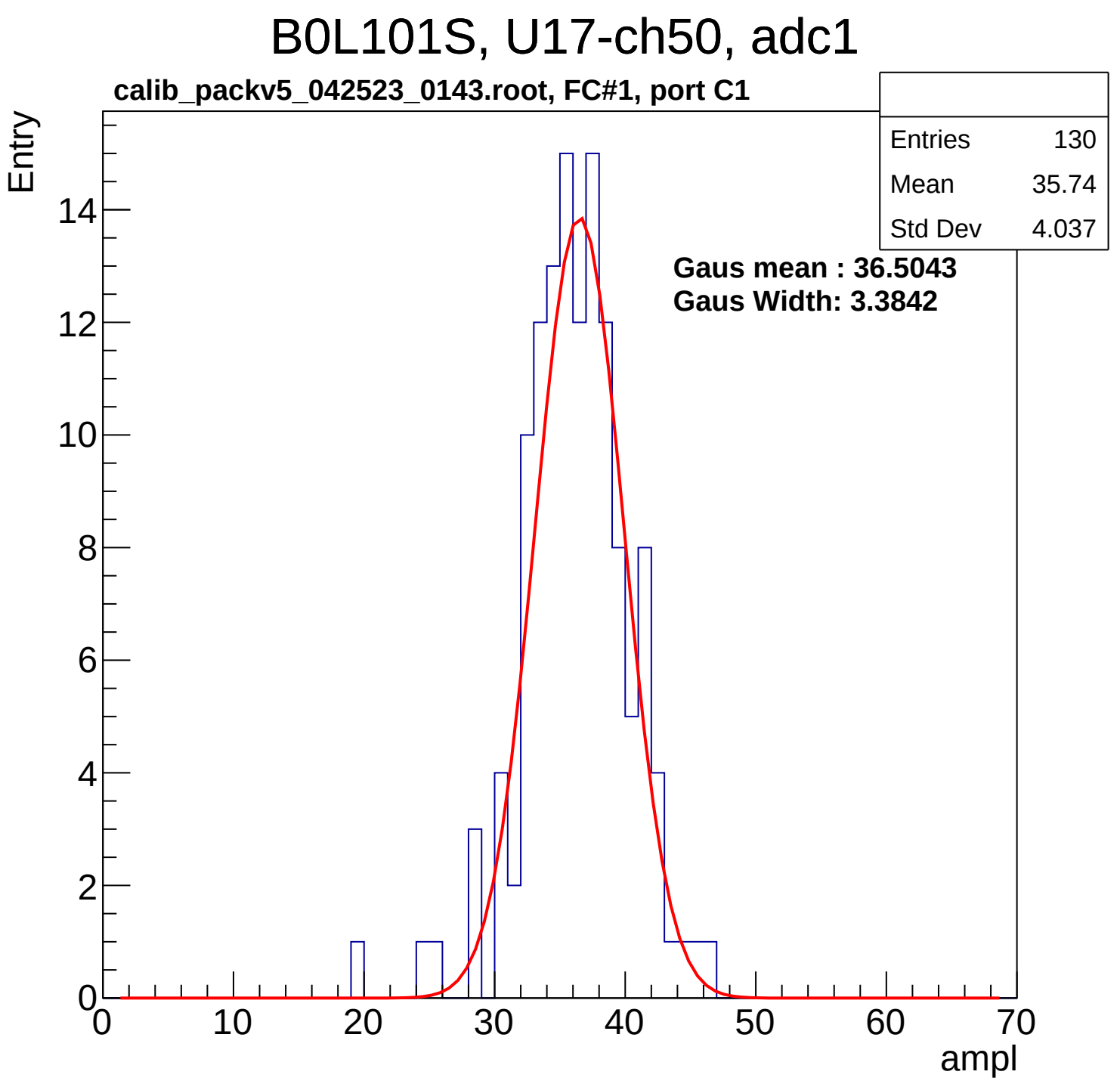

B0L101S, U17-ch49, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 56.77
Std Dev 3.706
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

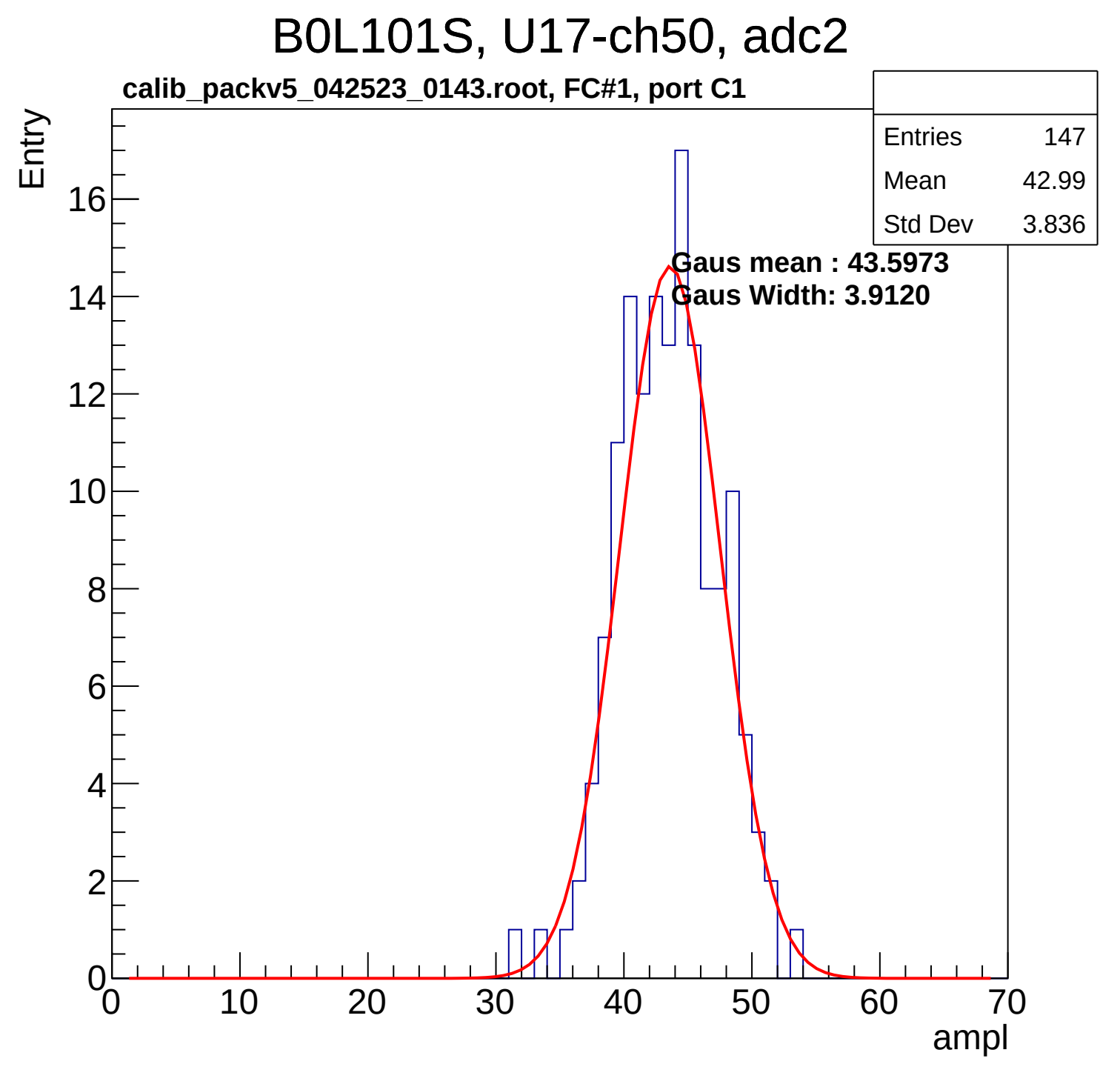

B0L101S, U17-ch49, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 54
Mean 58.57
Std Dev 11.67
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

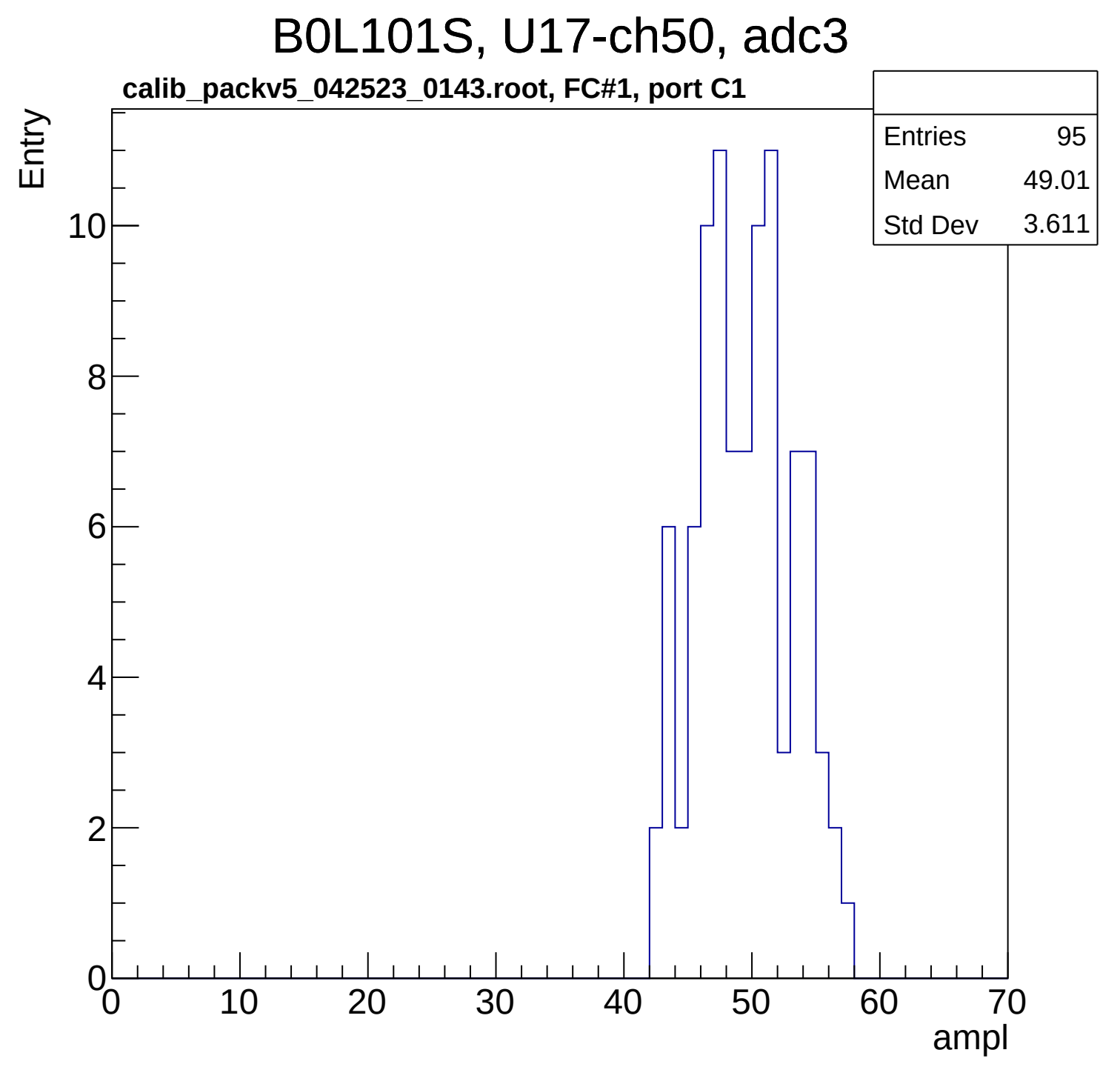

B0L101S, U17-ch49, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

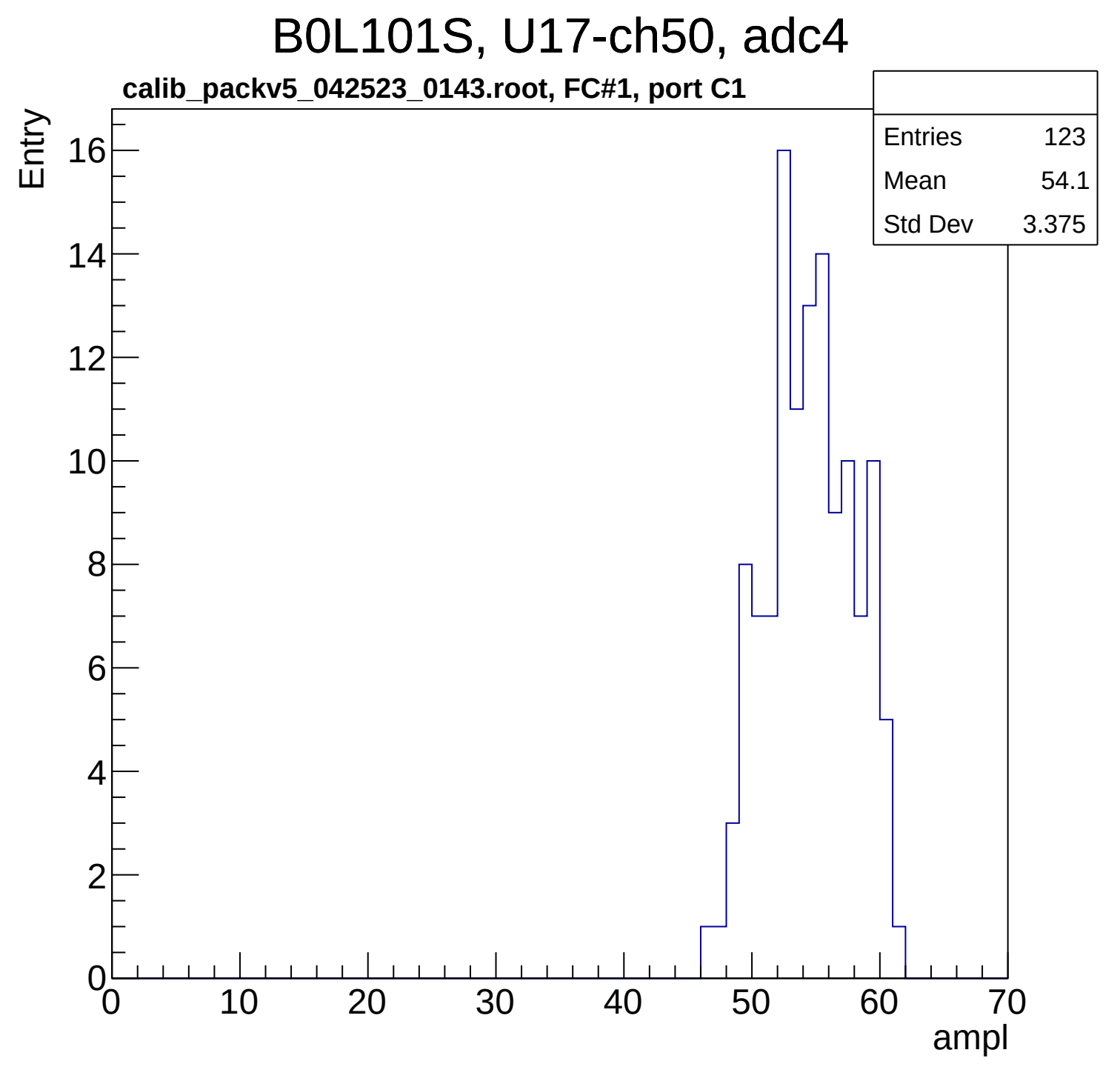

B0L101S, U17-ch49, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

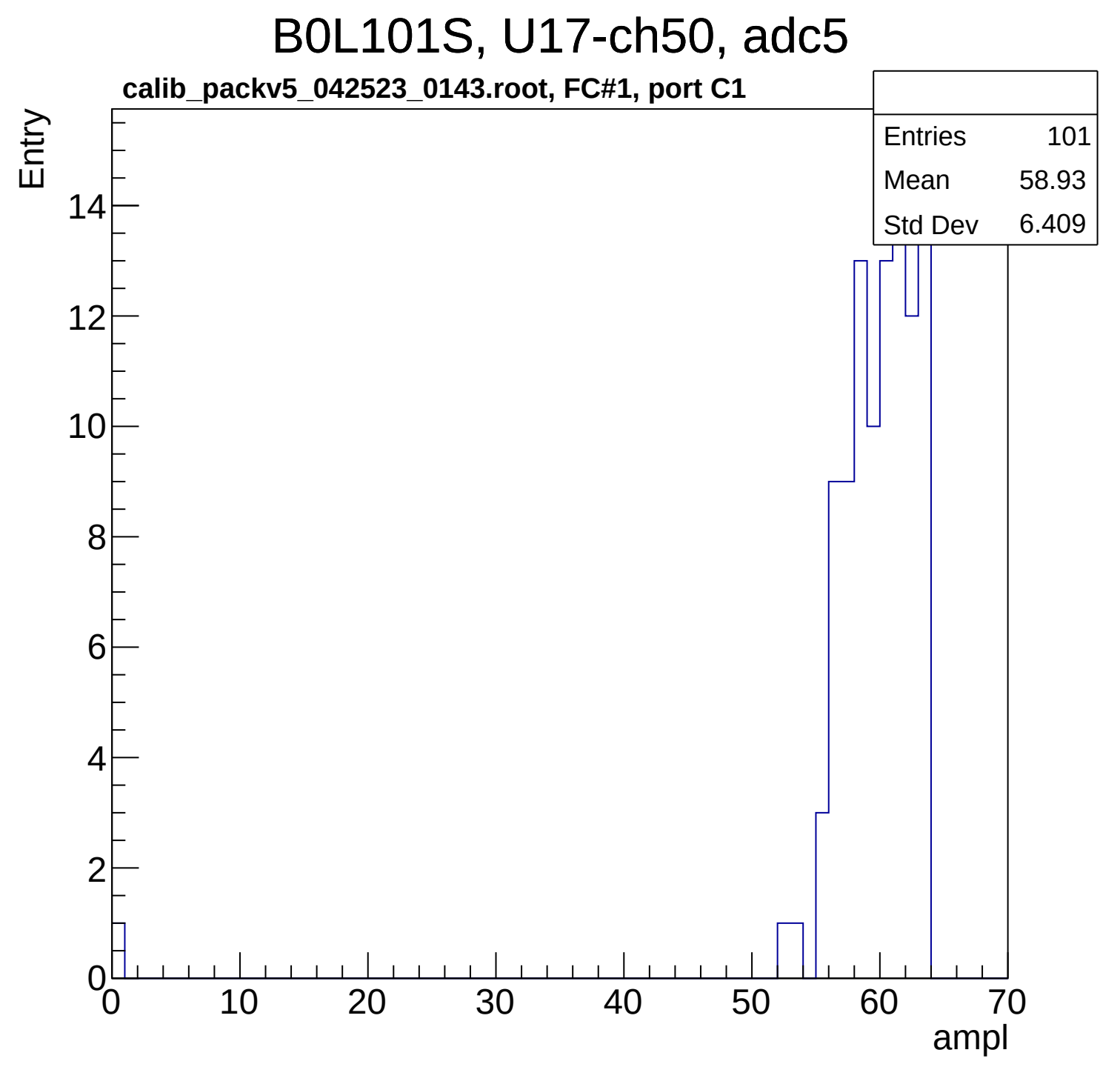

B0L101S, U17-ch50, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 29.46
Std Dev 4.428
Gaus mean : 29.9973
Gaus Width: 3.3986
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

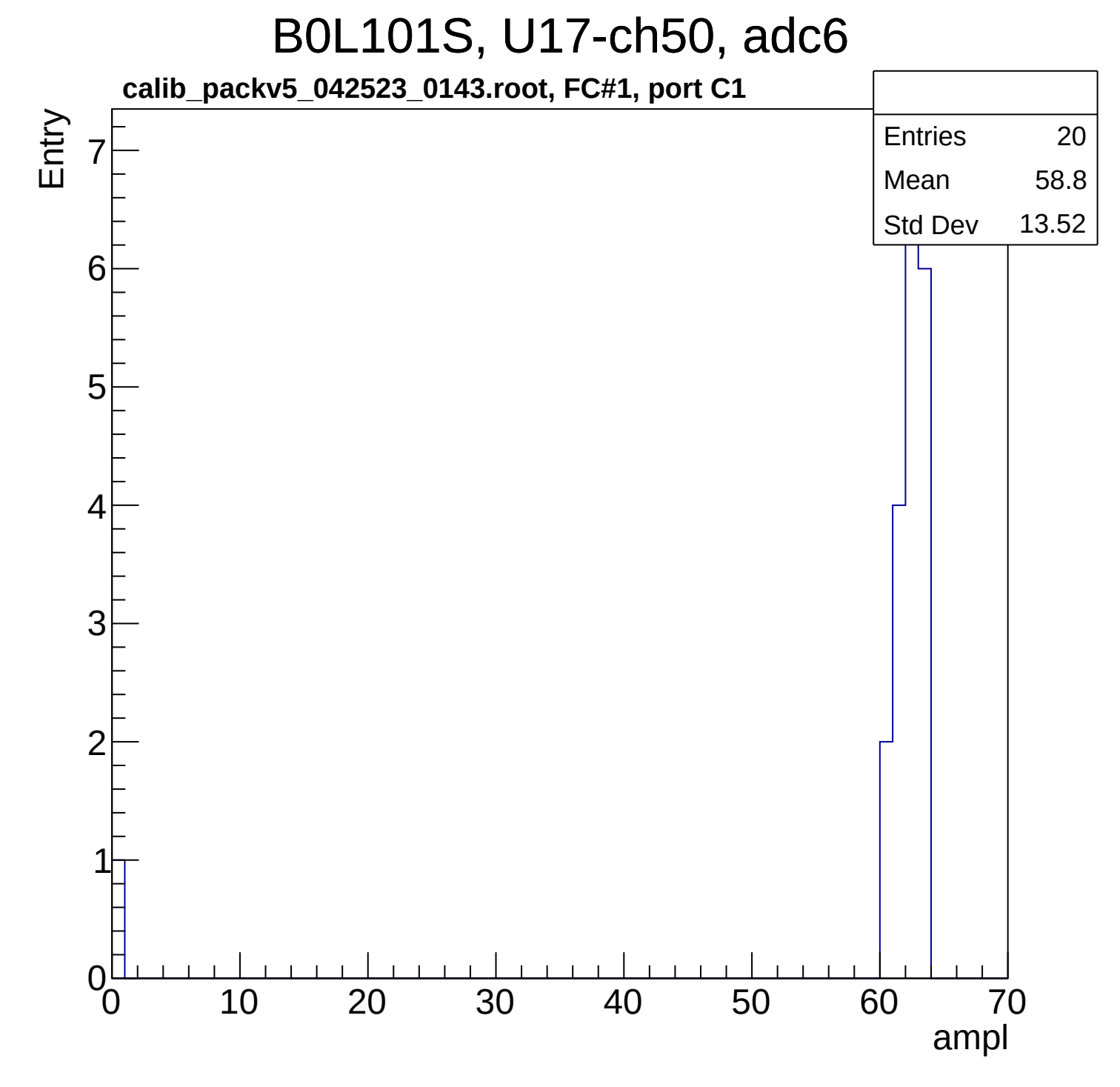

B0L101S, U17-ch50, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 35.74
Std Dev 4.037
Gaus mean : 36.5043
Gaus Width: 3.3842
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

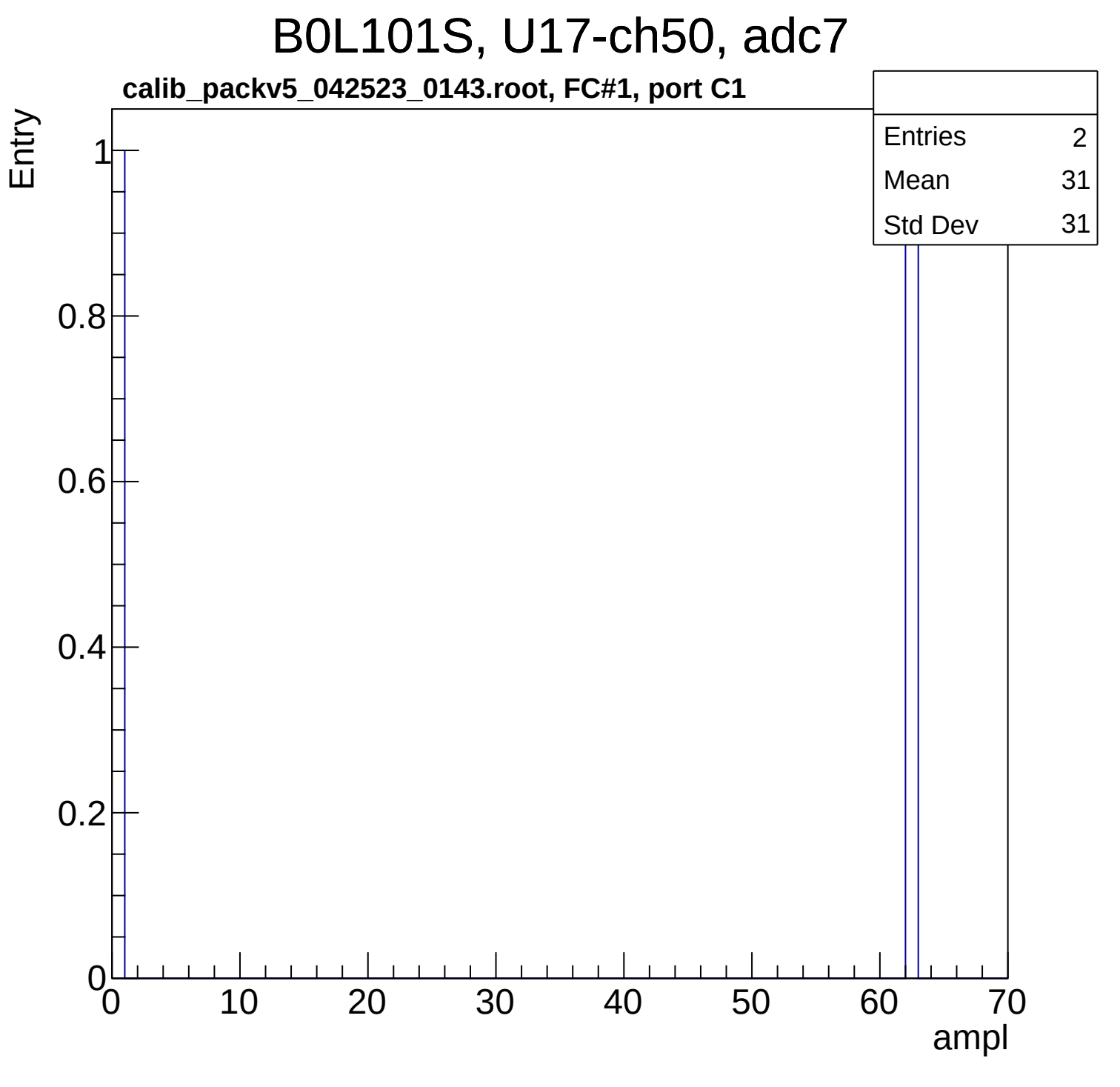

B0L101S, U17-ch50, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 42.99
Std Dev 3.836
Gaus mean : 43.5973
Gaus Width: 3.9120
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

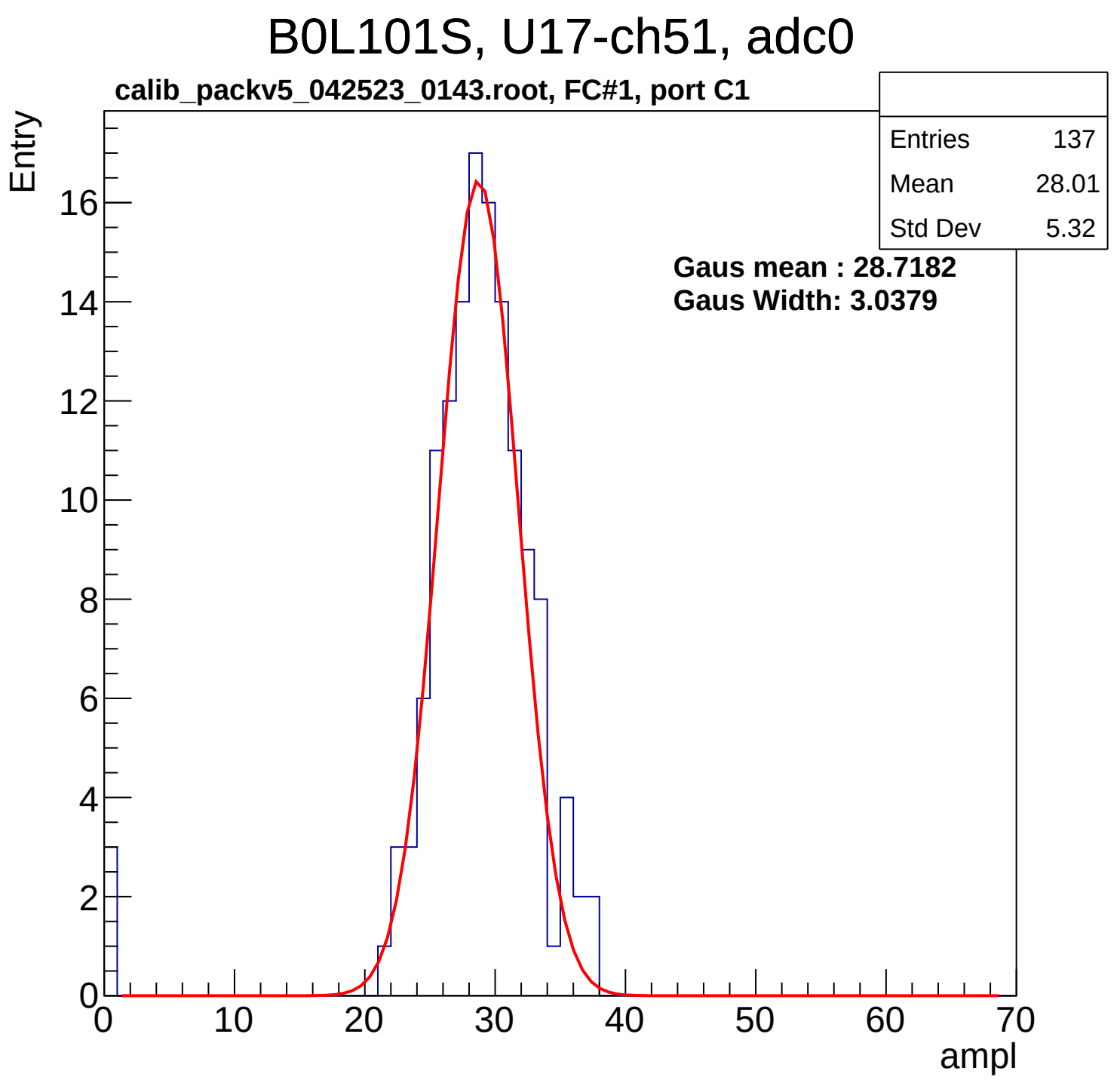

B0L101S, U17-ch50, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 95
Mean 49.01
Std Dev 3.611
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

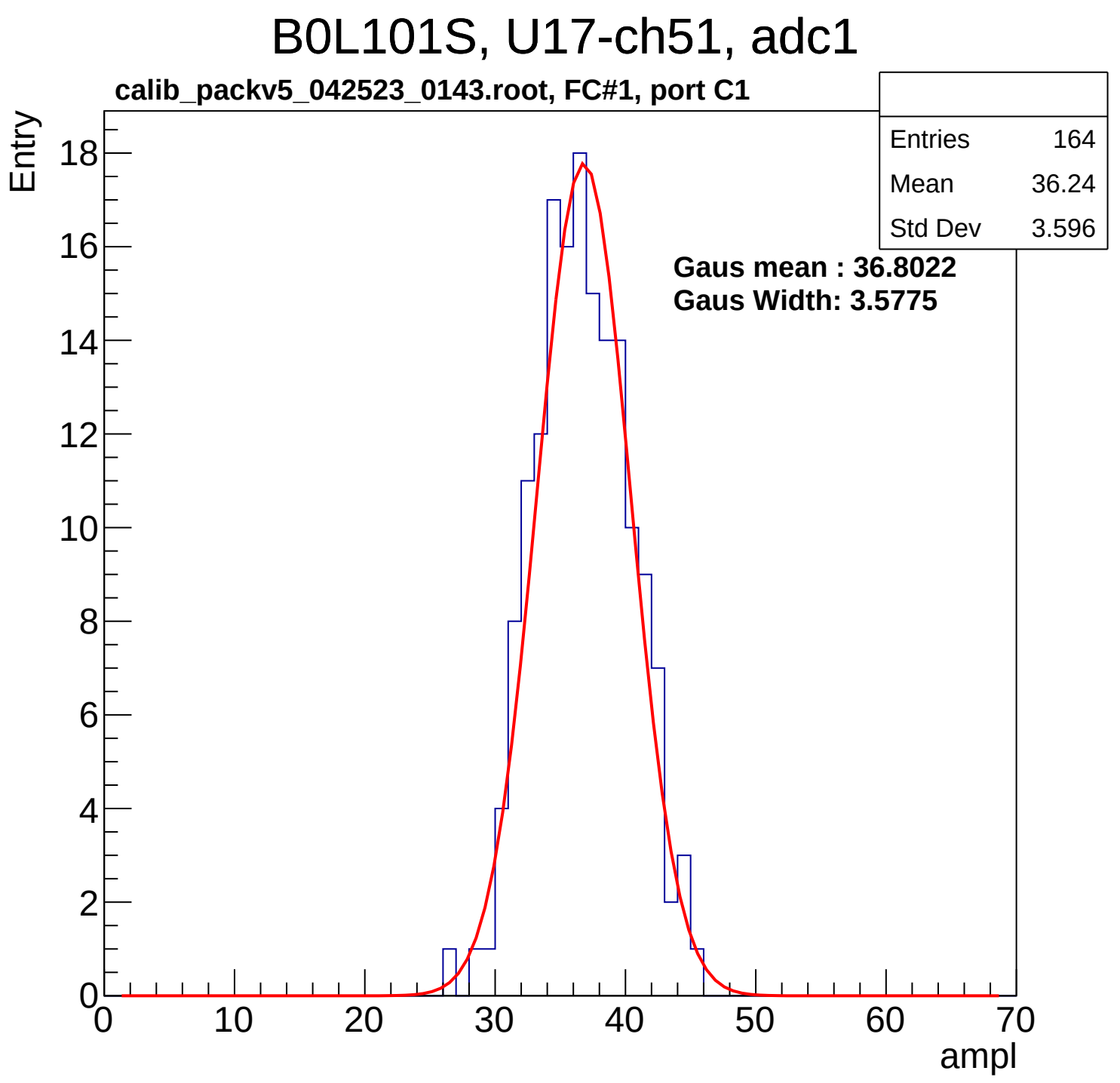

B0L101S, U17-ch50, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 54.1
Std Dev 3.375
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

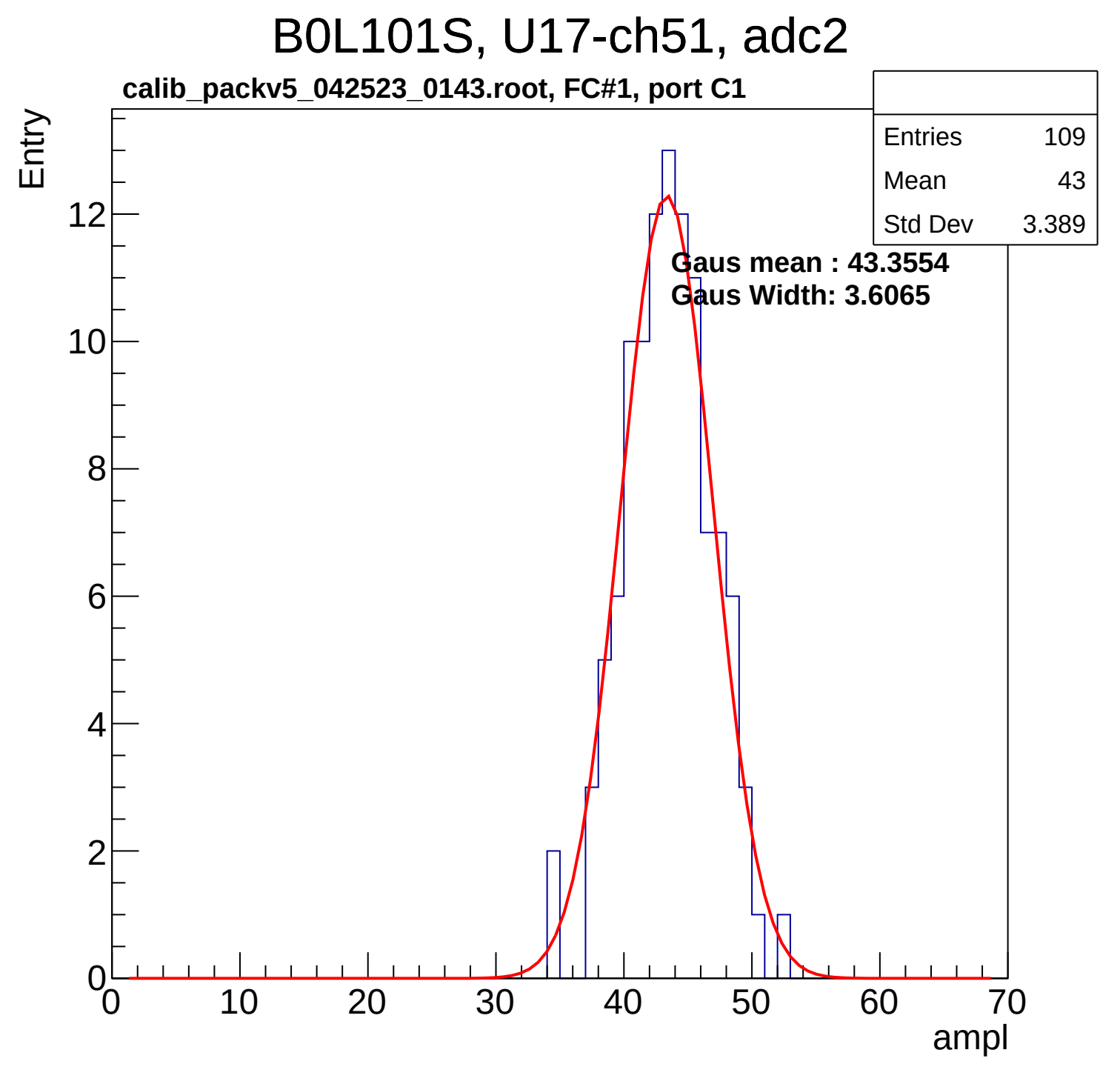

B0L101S, U17-ch50, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 58.93
Std Dev 6.409
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

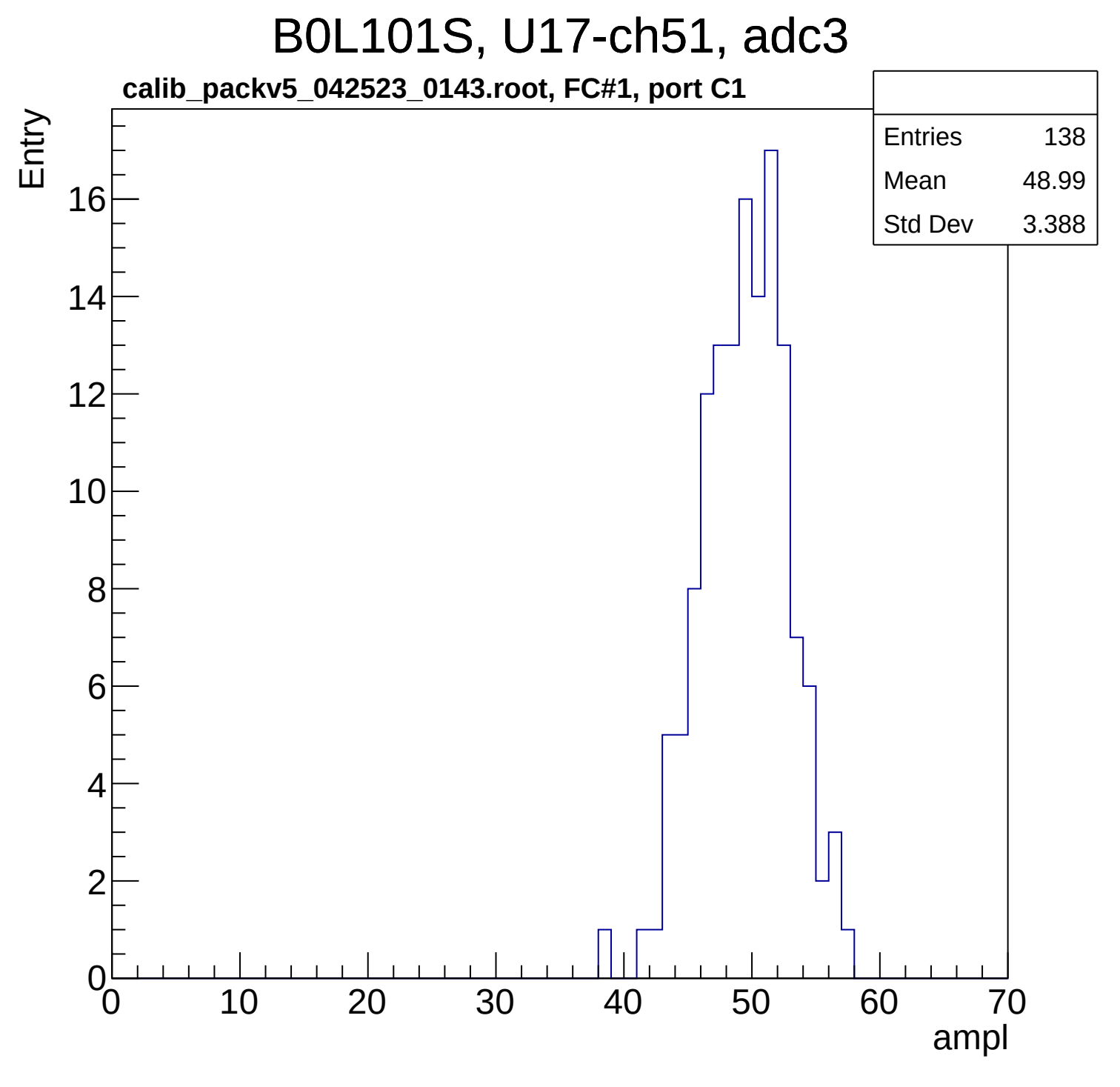

B0L101S, U17-ch50, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 20
Mean 58.8
Std Dev 13.52
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

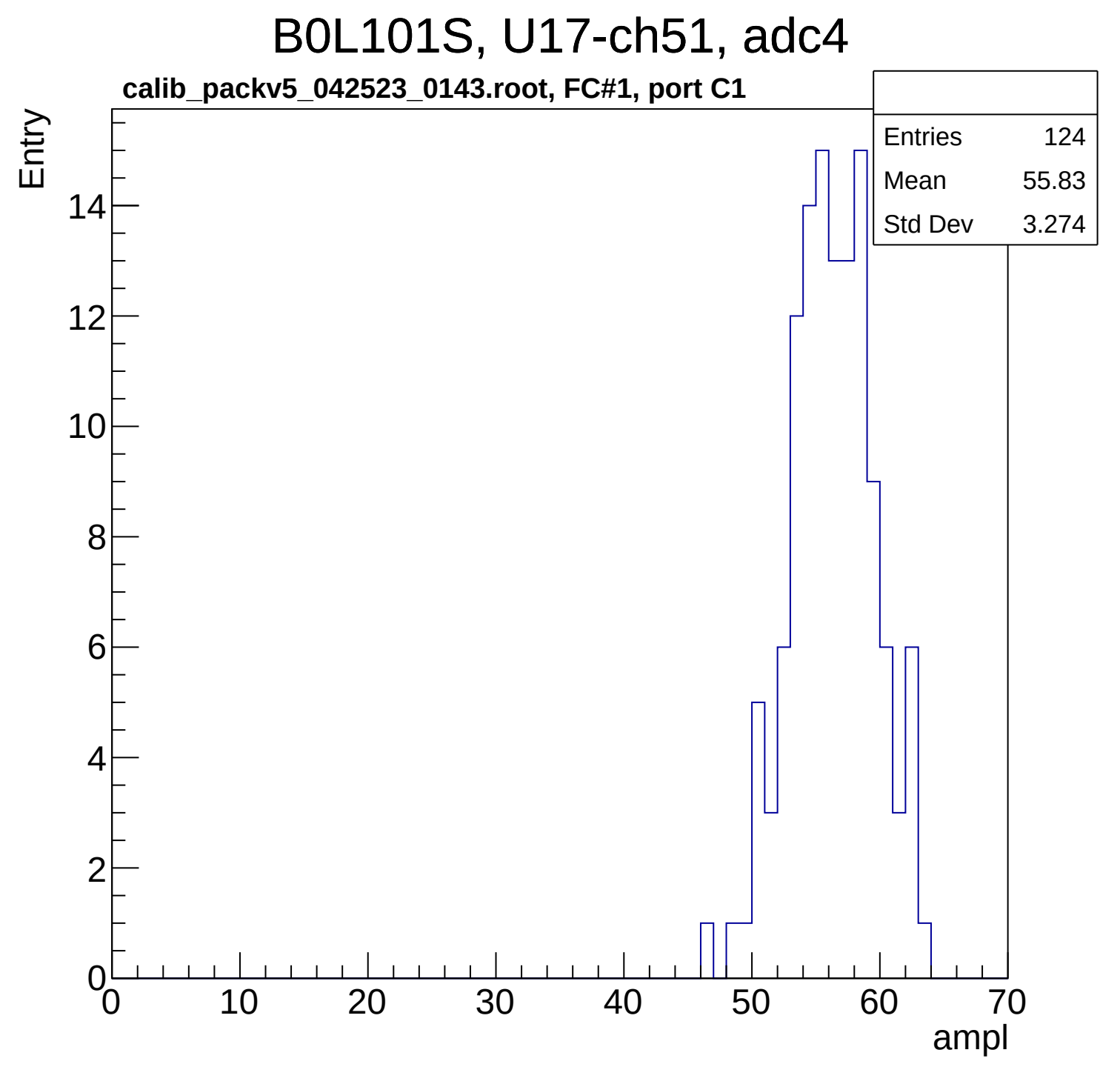

B0L101S, U17-ch50, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

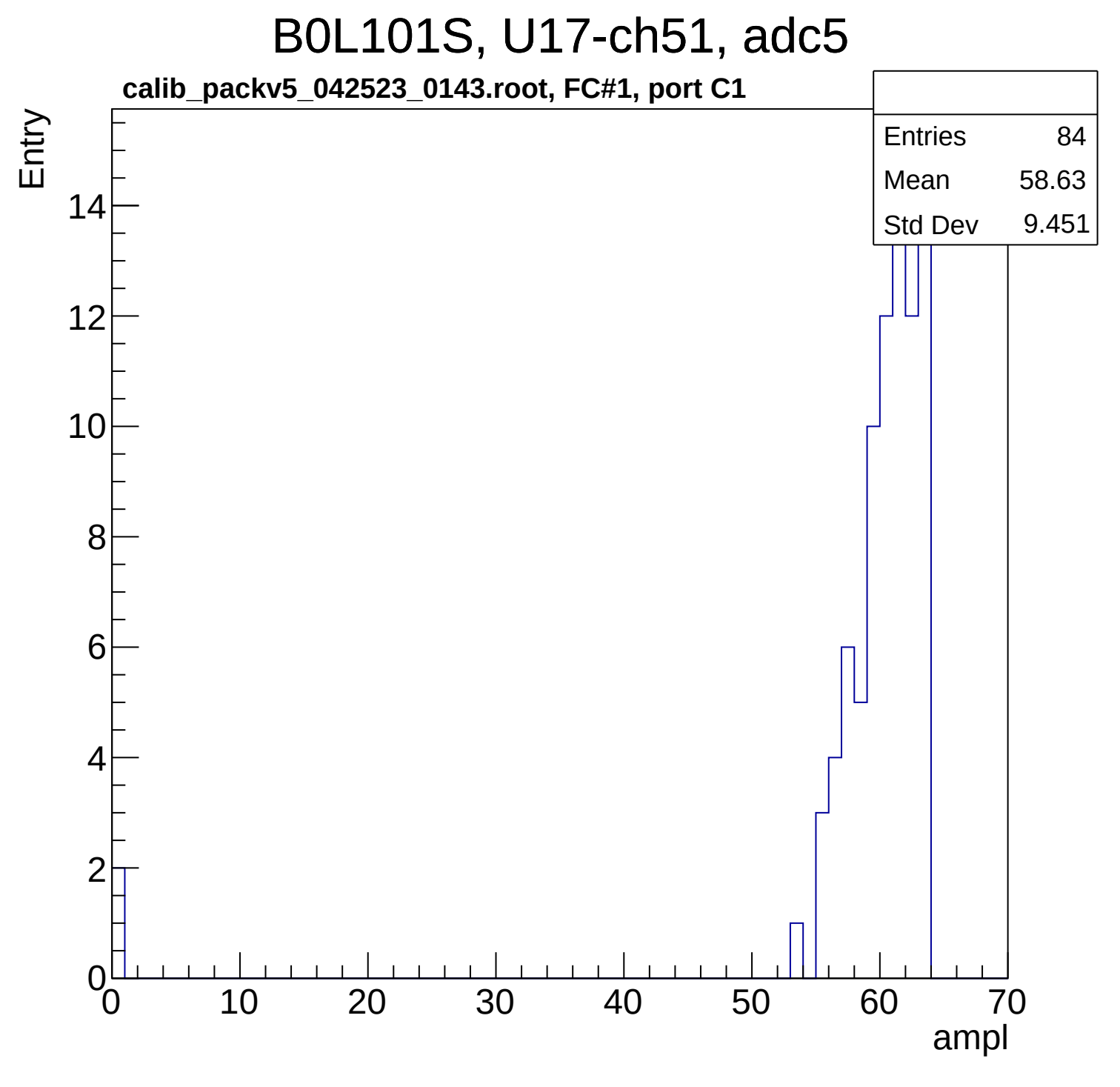

B0L101S, U17-ch51, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 28.01
Std Dev 5.32
Gaus mean : 28.7182
Gaus Width: 3.0379
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

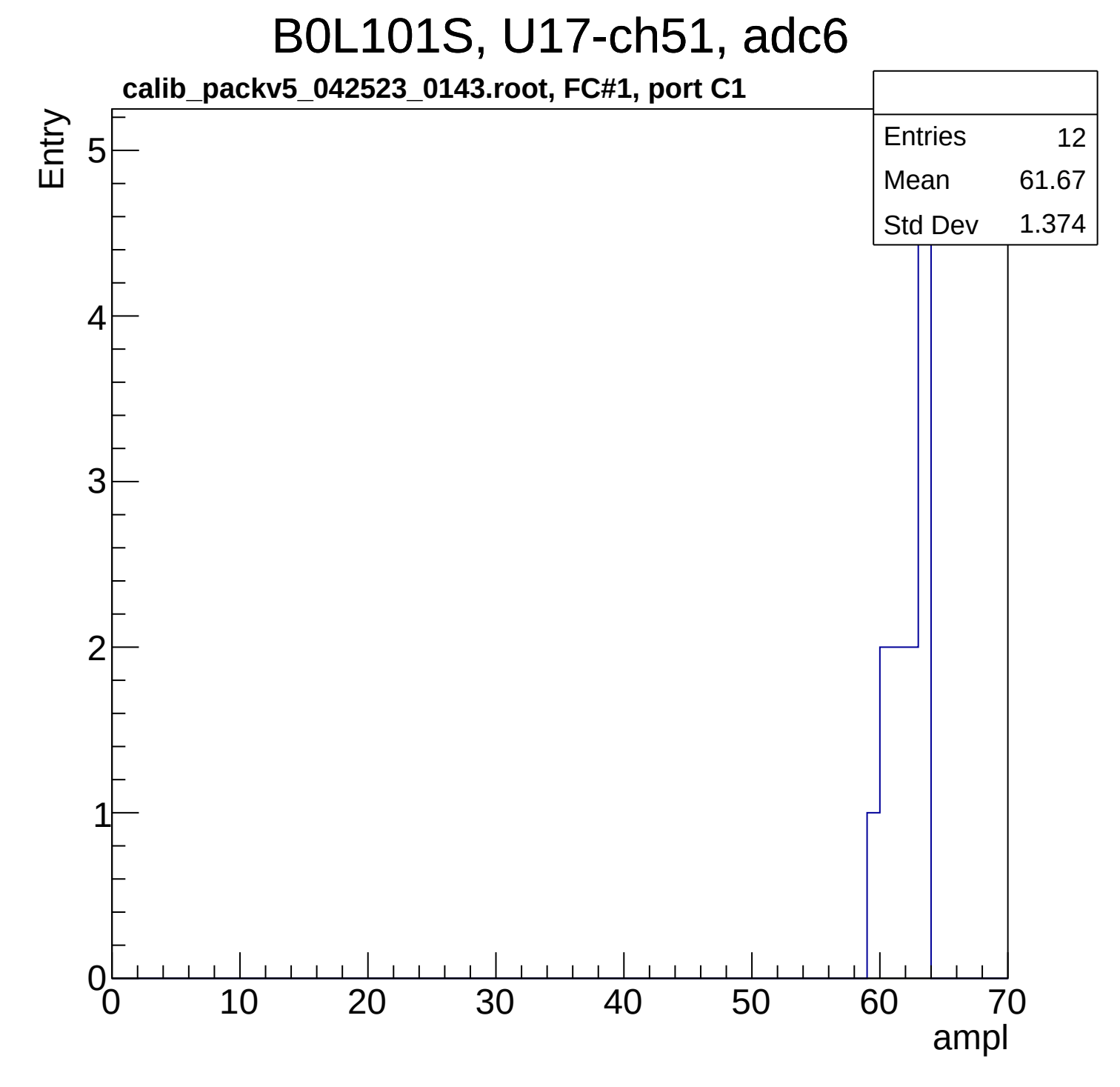

B0L101S, U17-ch51, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 164
Mean 36.24
Std Dev 3.596
Gaus mean : 36.8022
Gaus Width: 3.5775
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

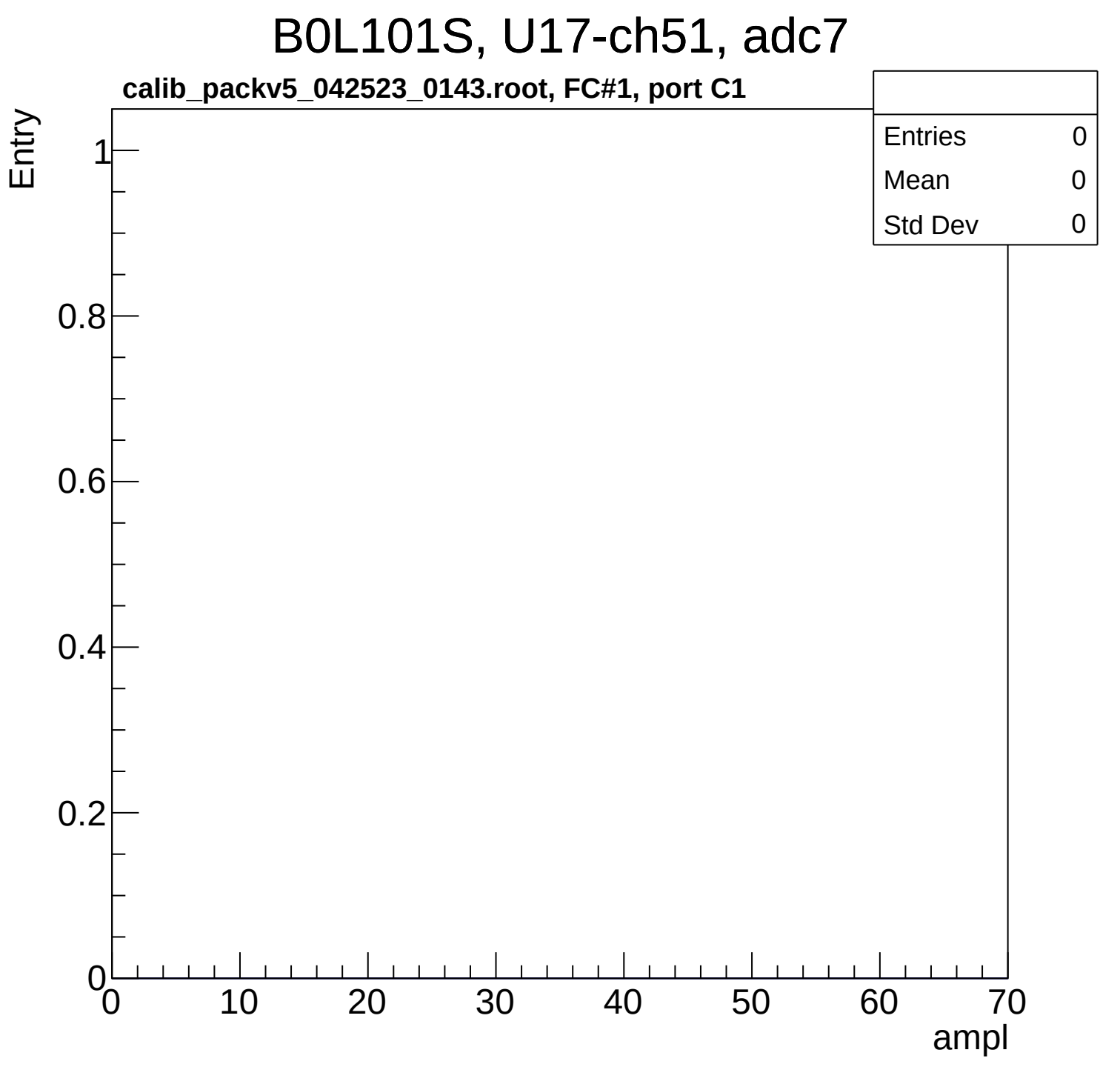

B0L101S, U17-ch51, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 43
Std Dev 3.389
Gaus mean : 43.3554
Gaus Width: 3.6065
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

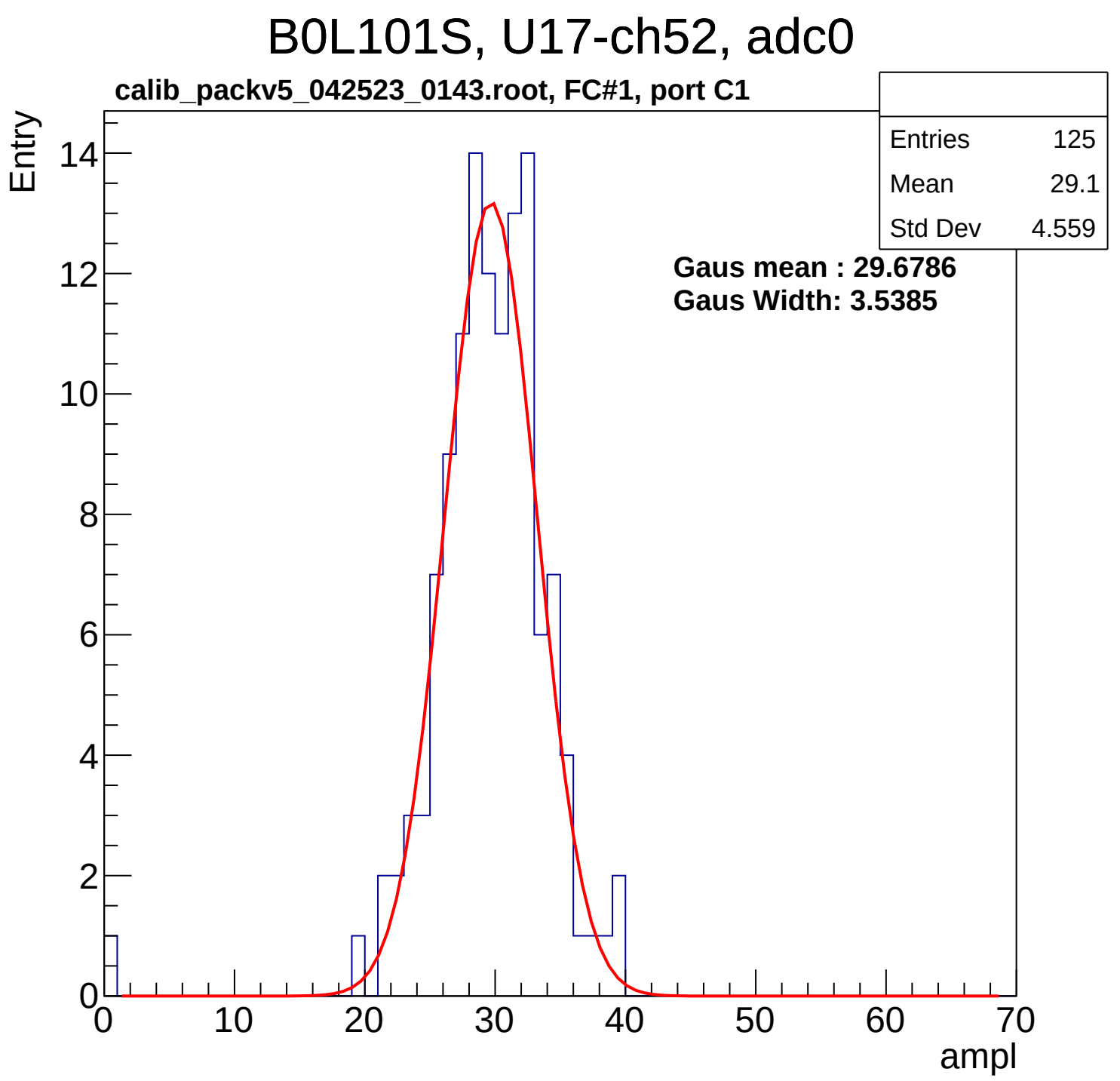

B0L101S, U17-ch51, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 48.99
Std Dev 3.388
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

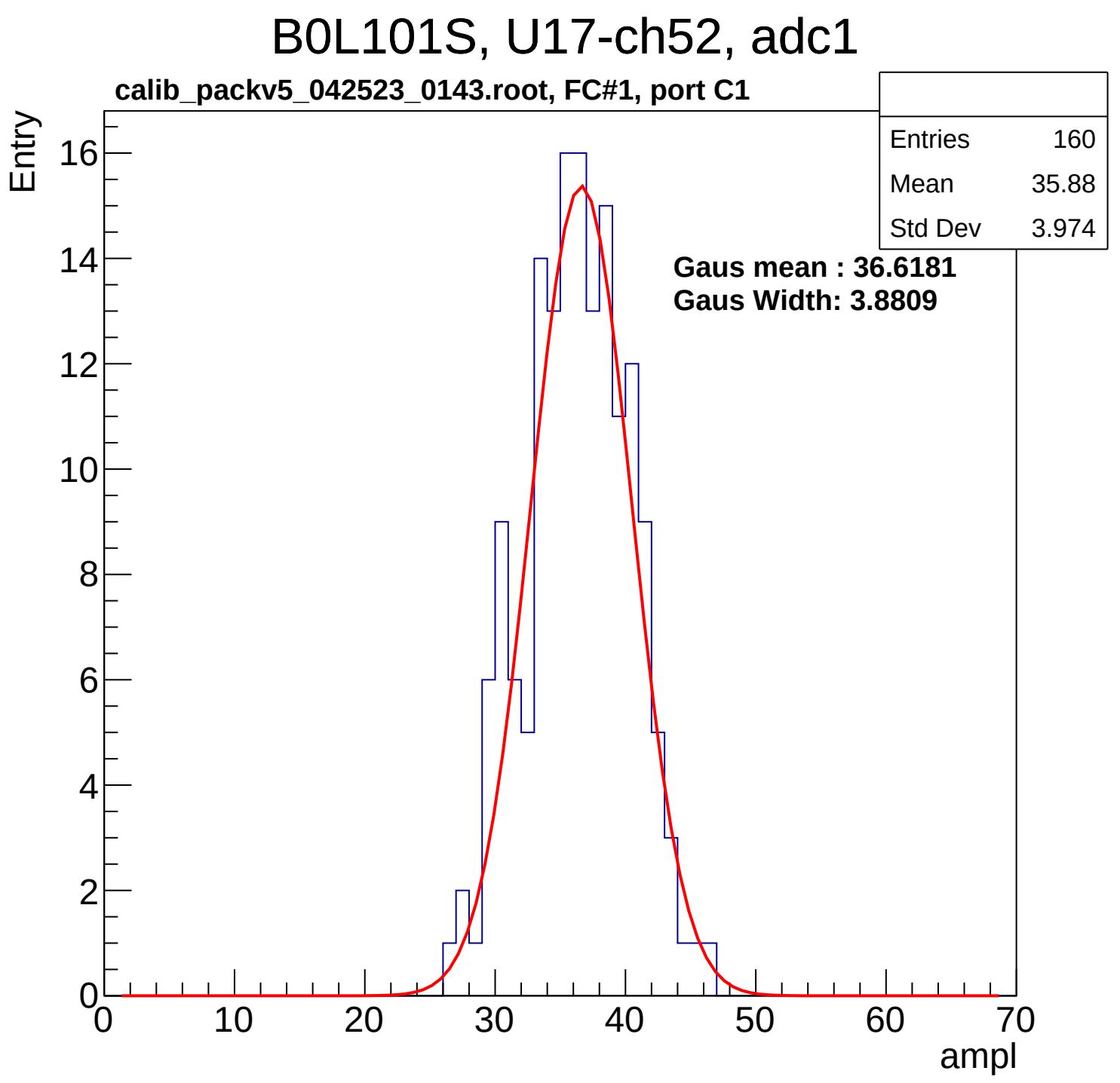

B0L101S, U17-ch51, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 55.83
Std Dev 3.274
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

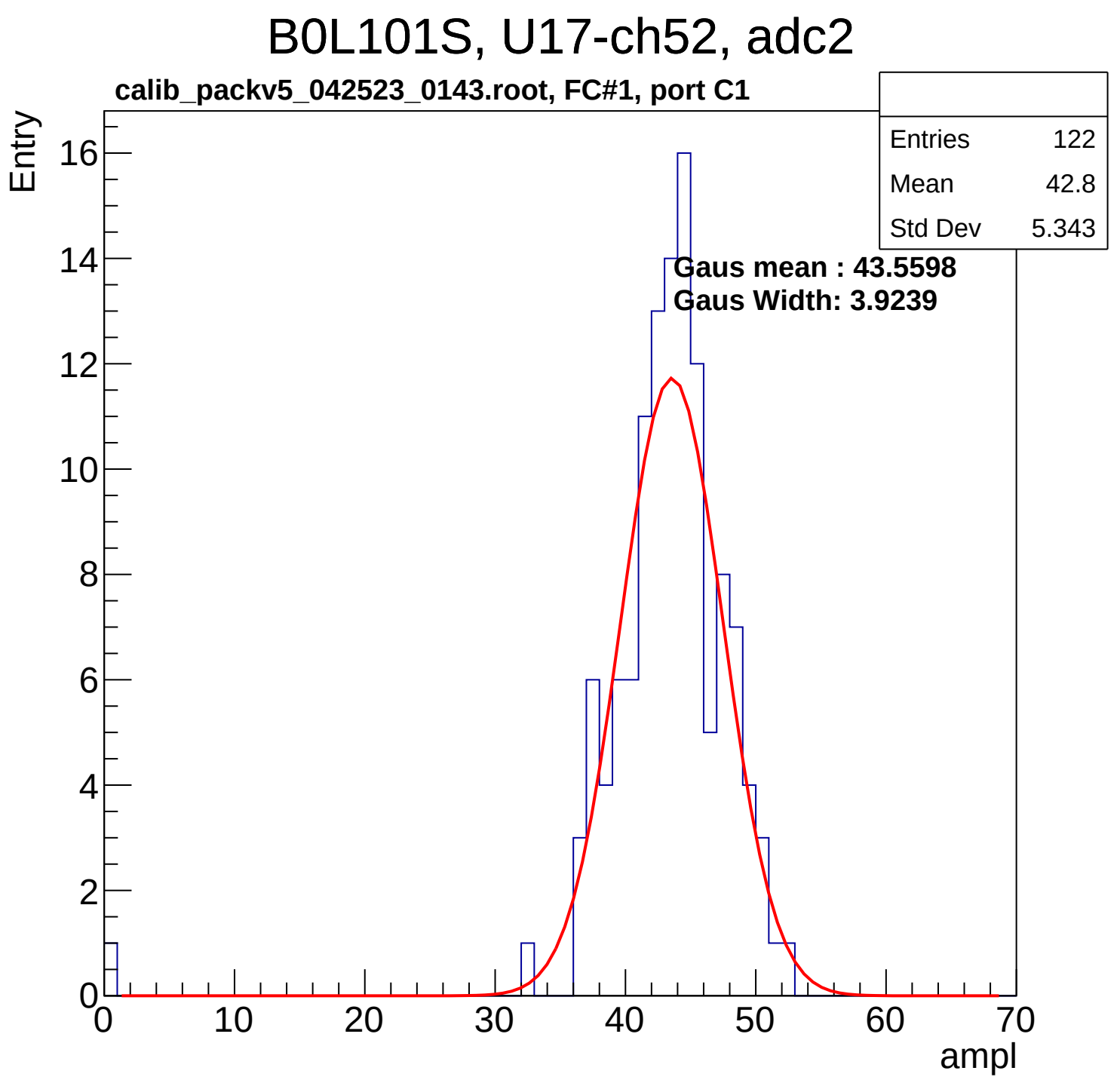

B0L101S, U17-ch51, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 84
Mean 58.63
Std Dev 9.451
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

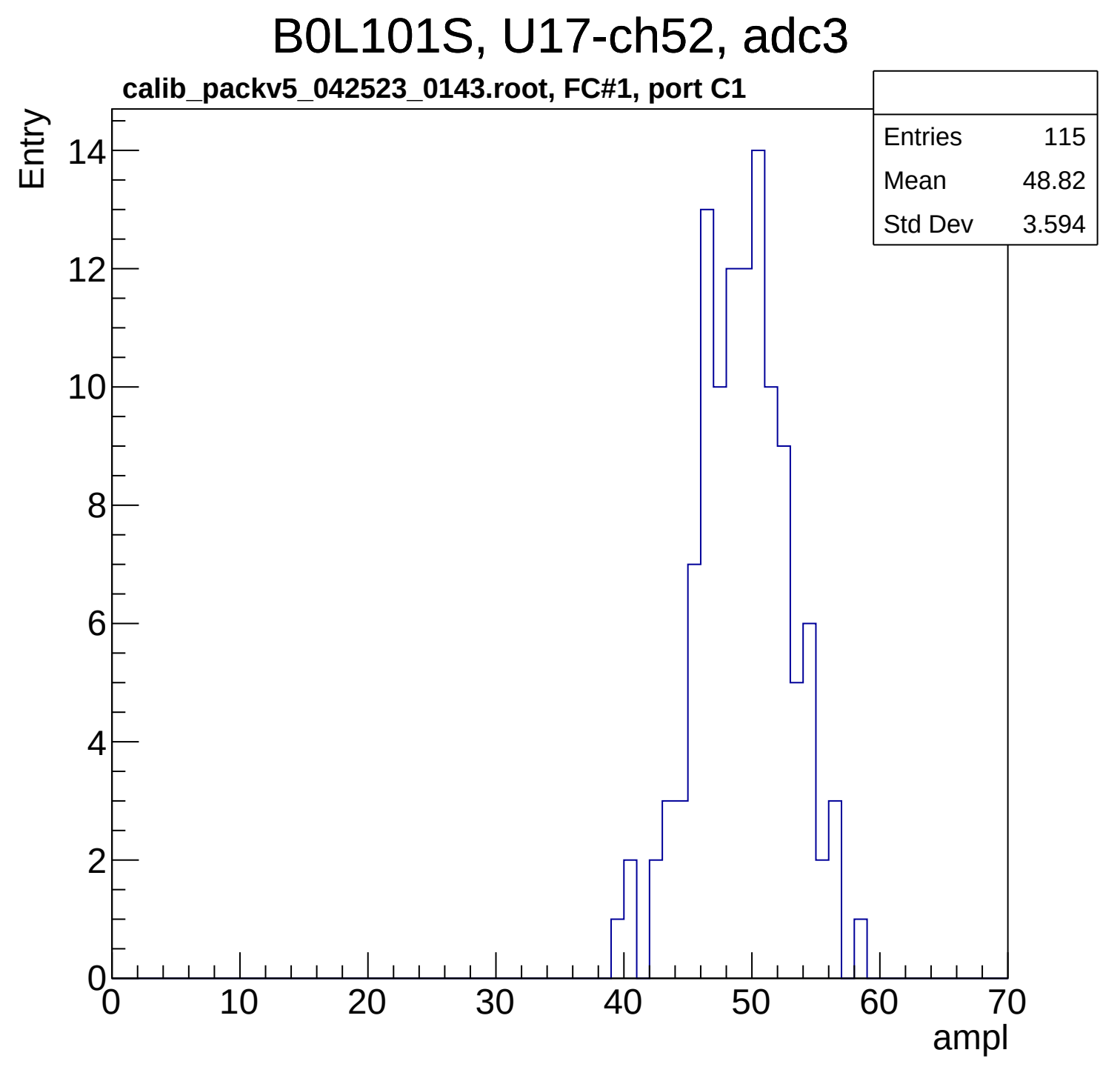

B0L101S, U17-ch51, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.67
Std Dev 1.374
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

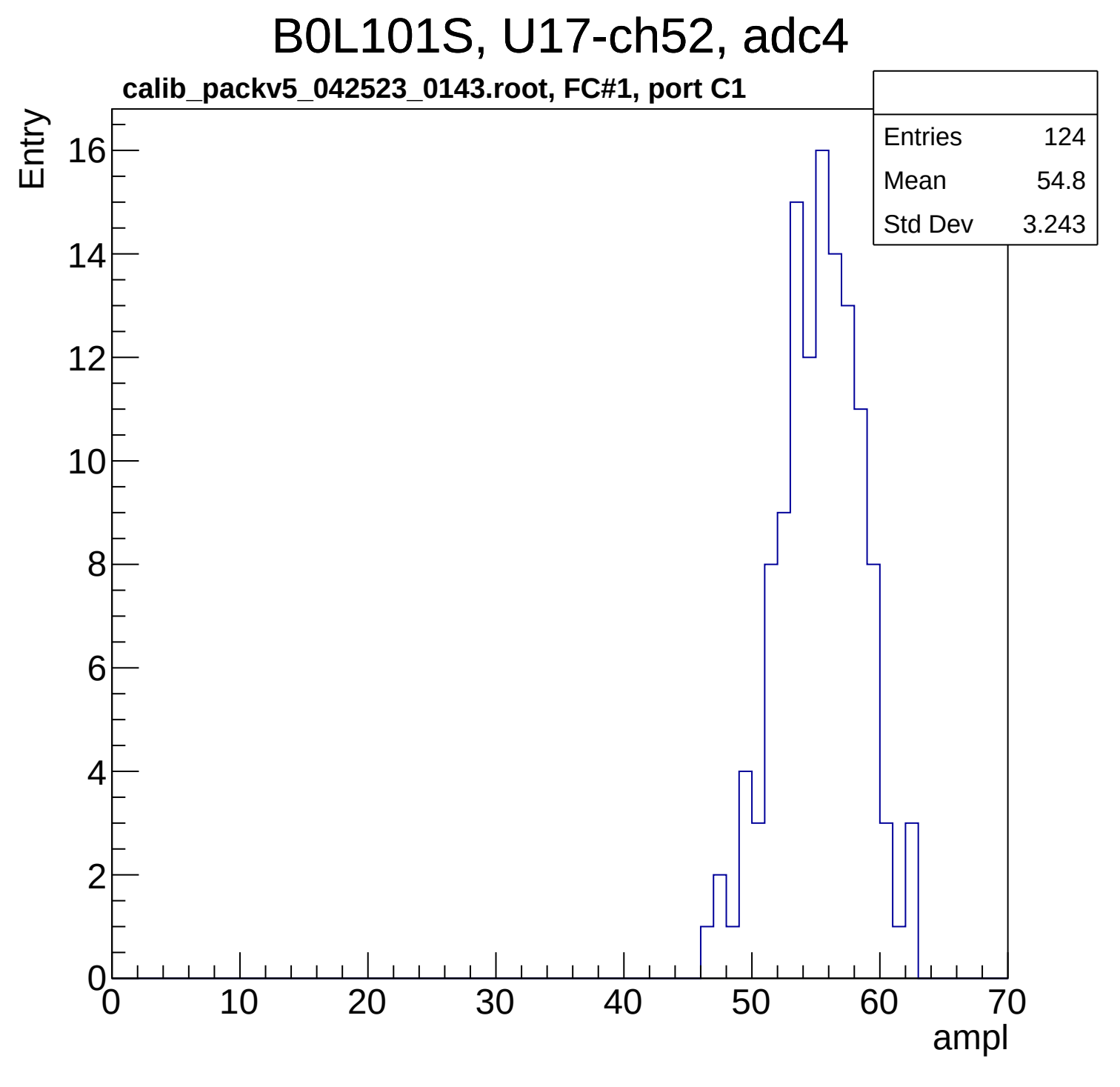

B0L101S, U17-ch51, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

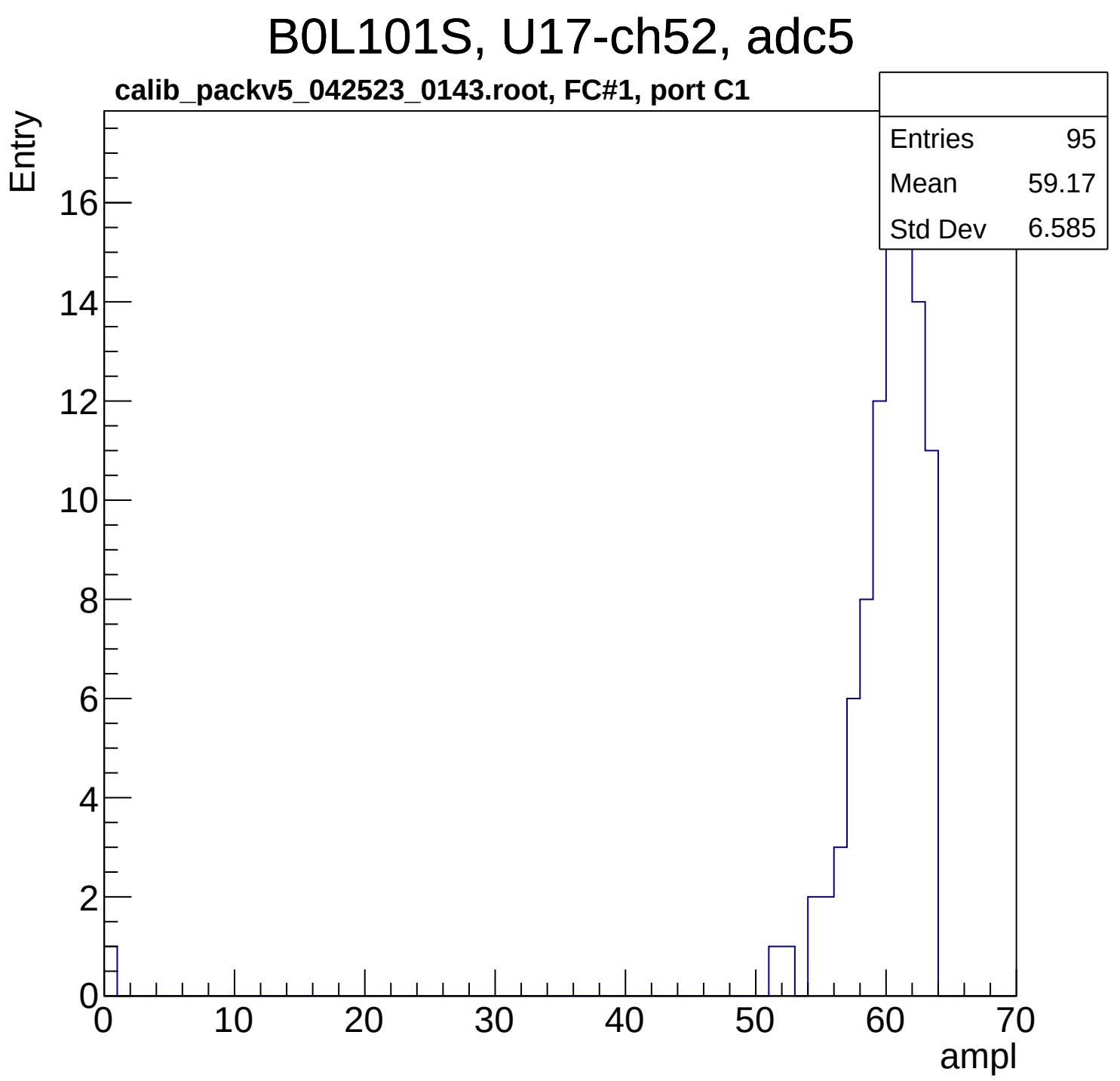

B0L101S, U17-ch52, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 29.1
Std Dev 4.559
Gaus mean : 29.6786
Gaus Width: 3.5385
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

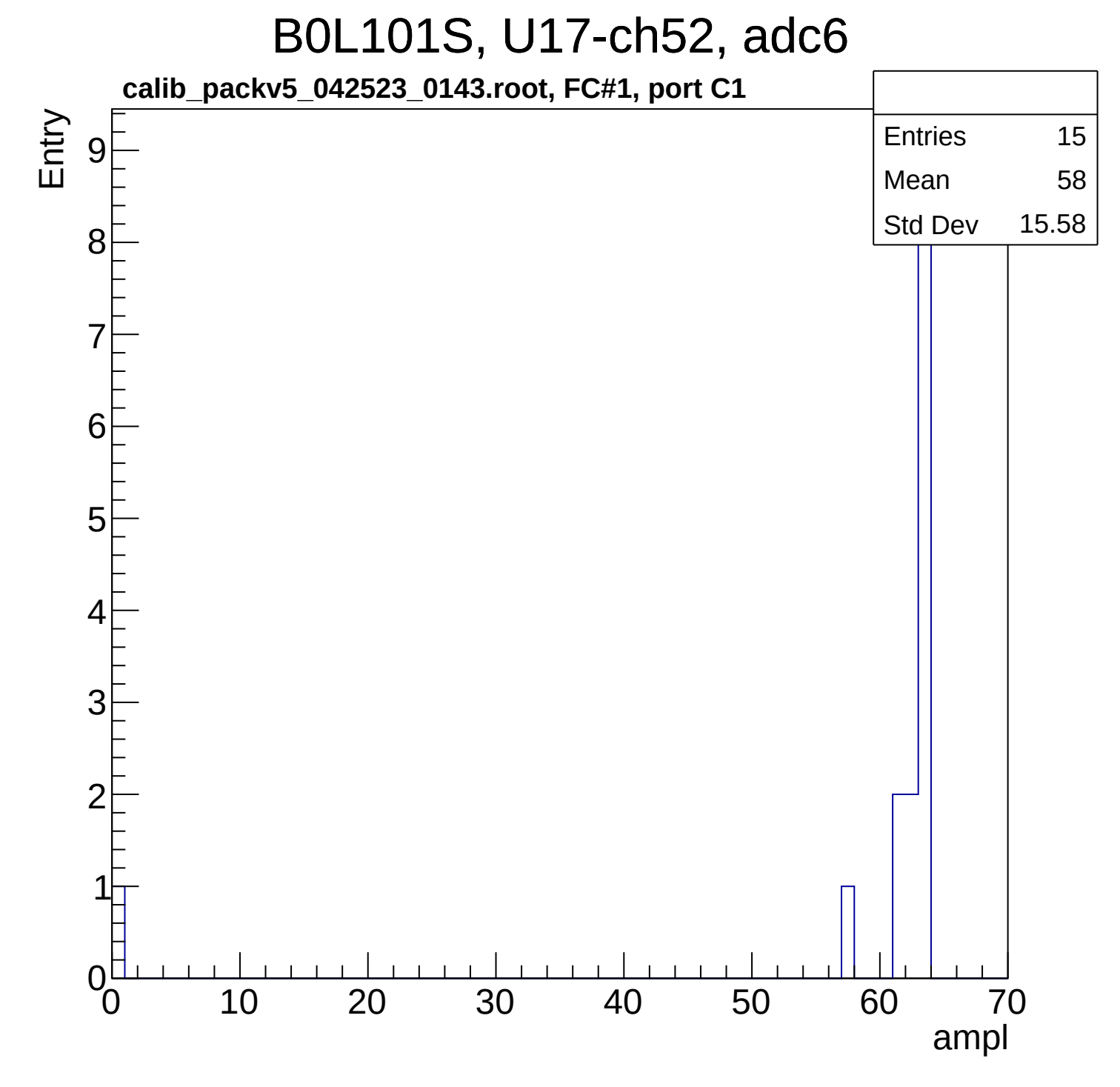

B0L101S, U17-ch52, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 160
Mean 35.88
Std Dev 3.974
Gaus mean : 36.6181
Gaus Width: 3.8809
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

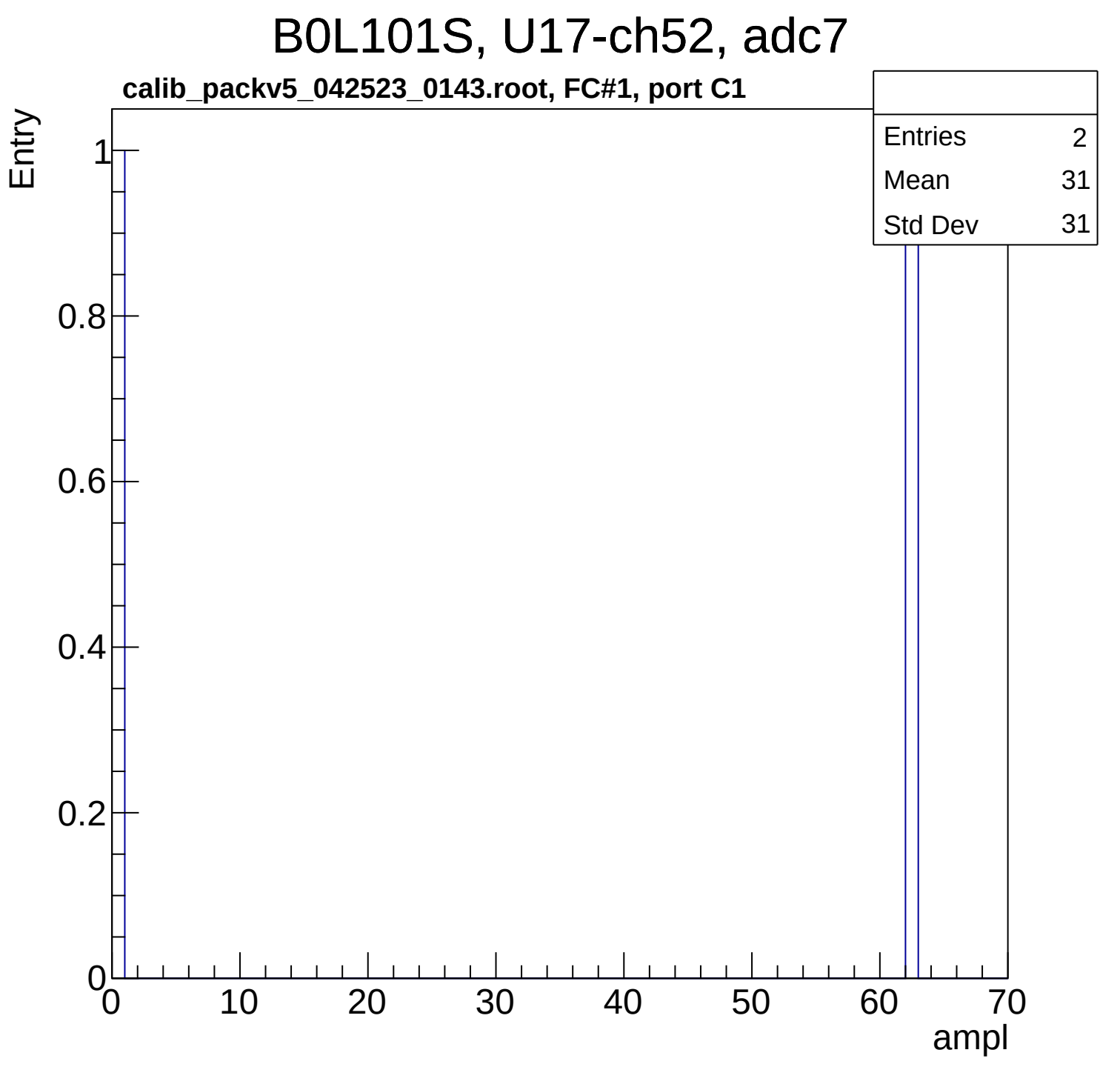

B0L101S, U17-ch52, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 42.8
Std Dev 5.343
Gaus mean : 43.5598
Gaus Width: 3.9239
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

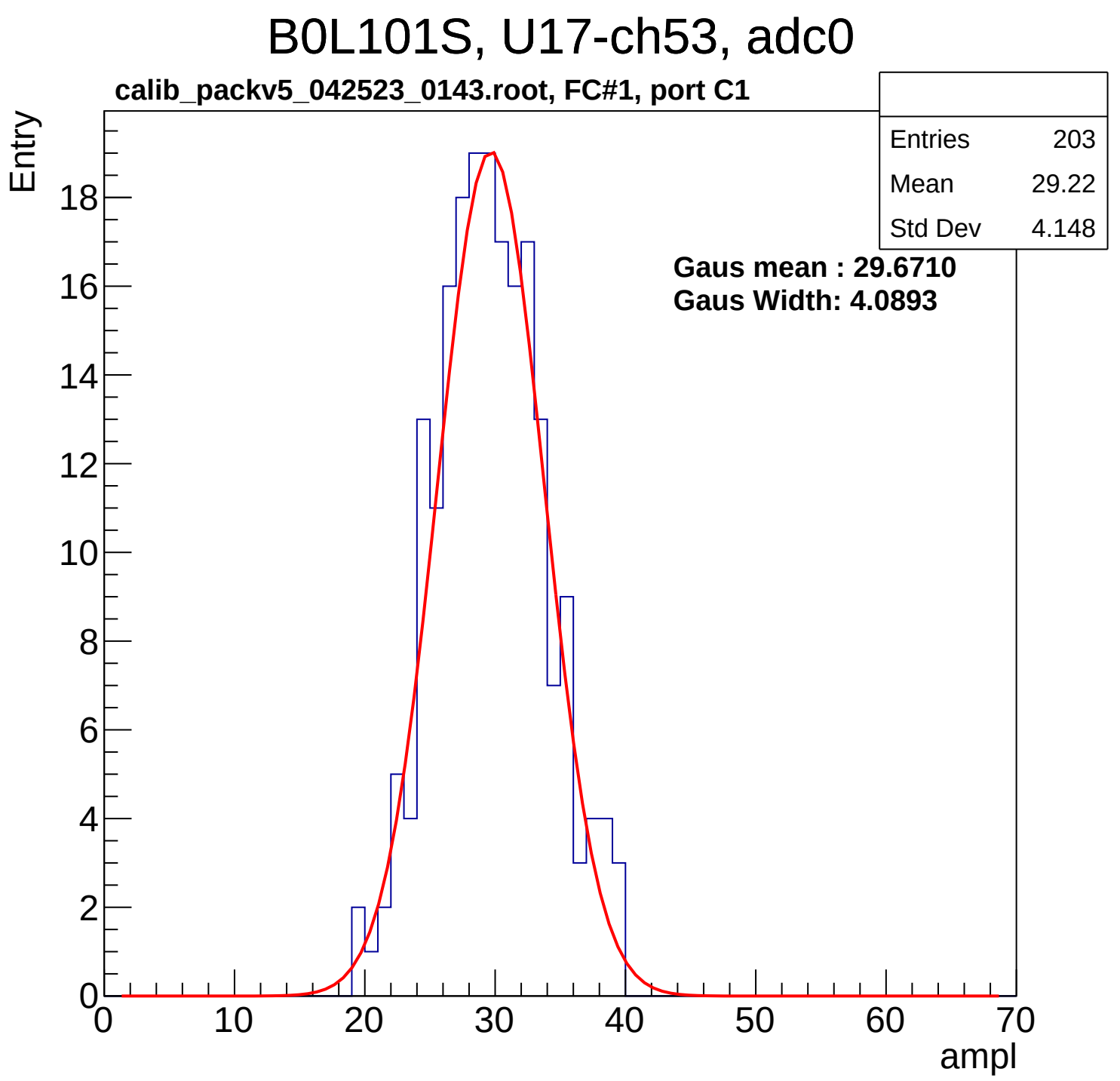

B0L101S, U17-ch52, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 48.82
Std Dev 3.594
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

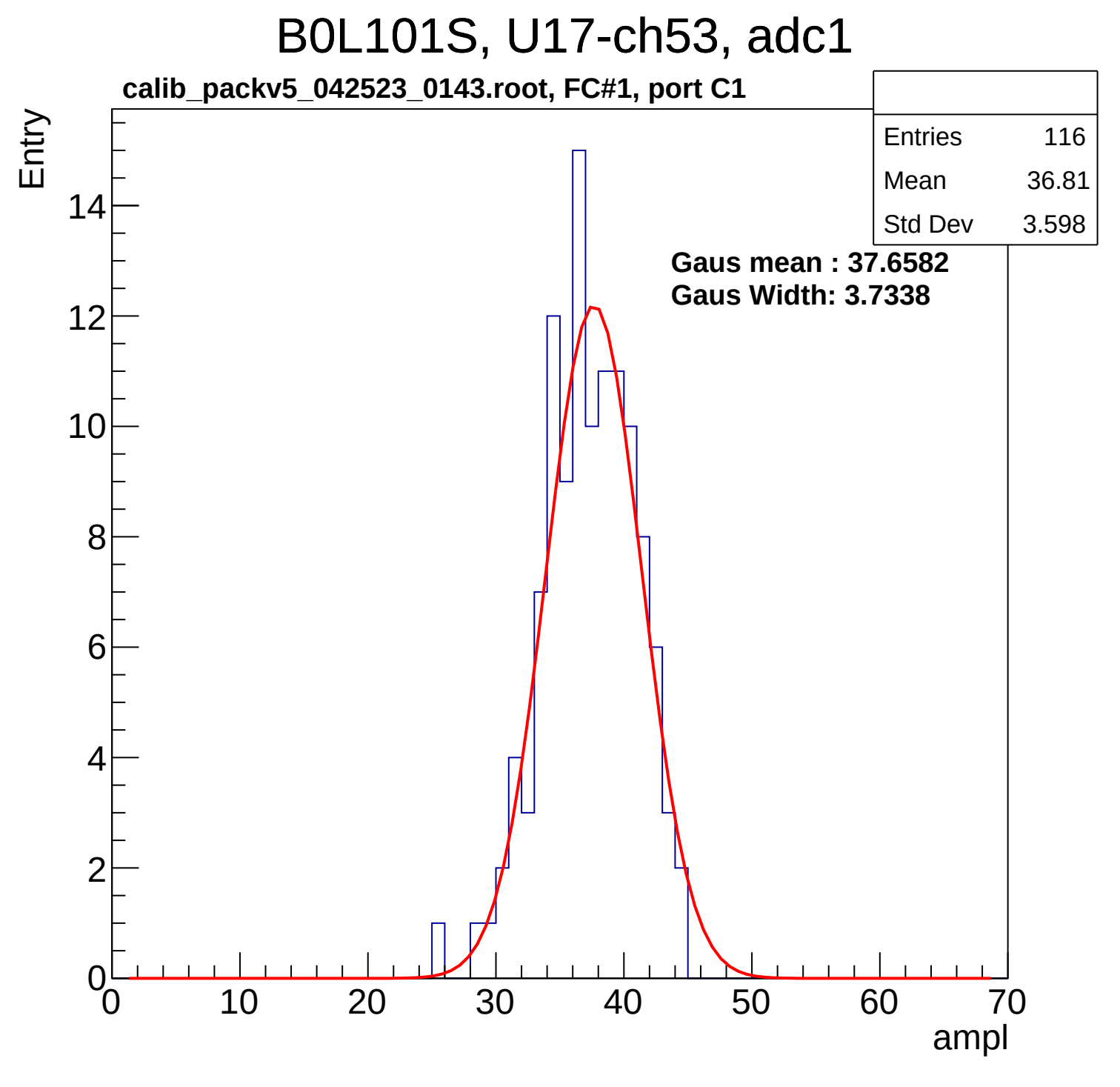

B0L101S, U17-ch52, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 54.8
Std Dev 3.243
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

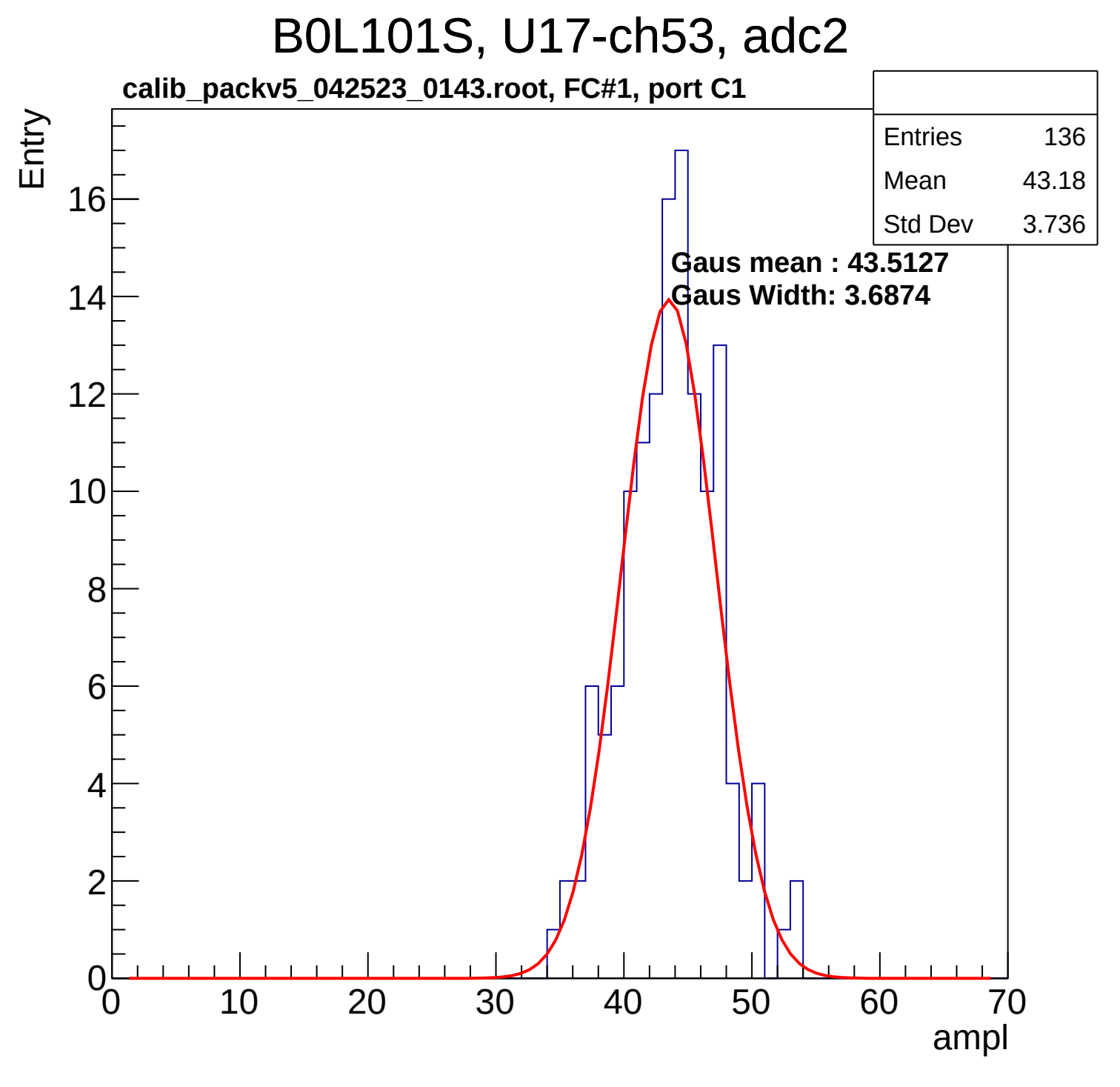

B0L101S, U17-ch52, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 95
Mean 59.17
Std Dev 6.585
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

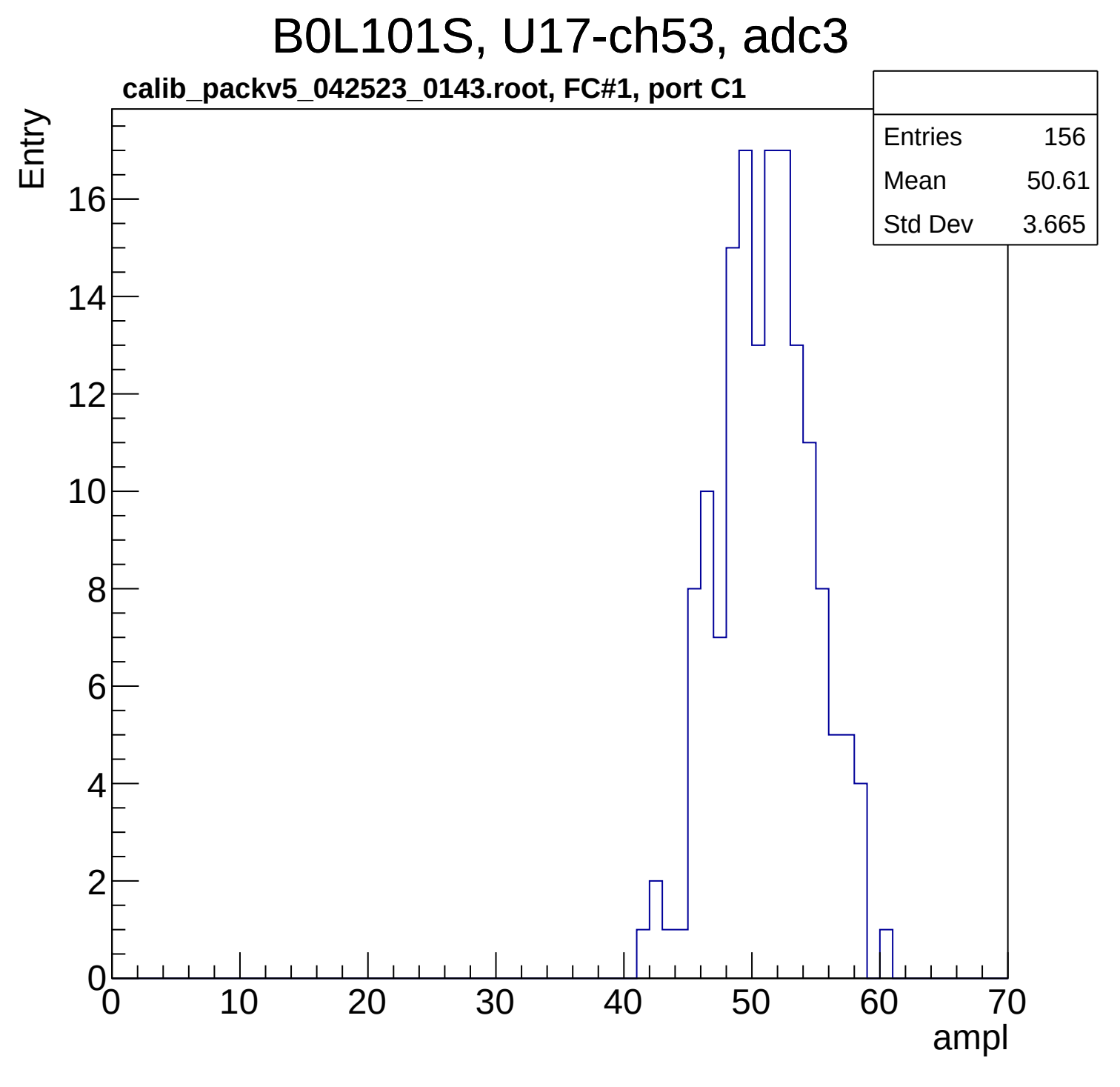

B0L101S, U17-ch52, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 15
Mean 58
Std Dev 15.58
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

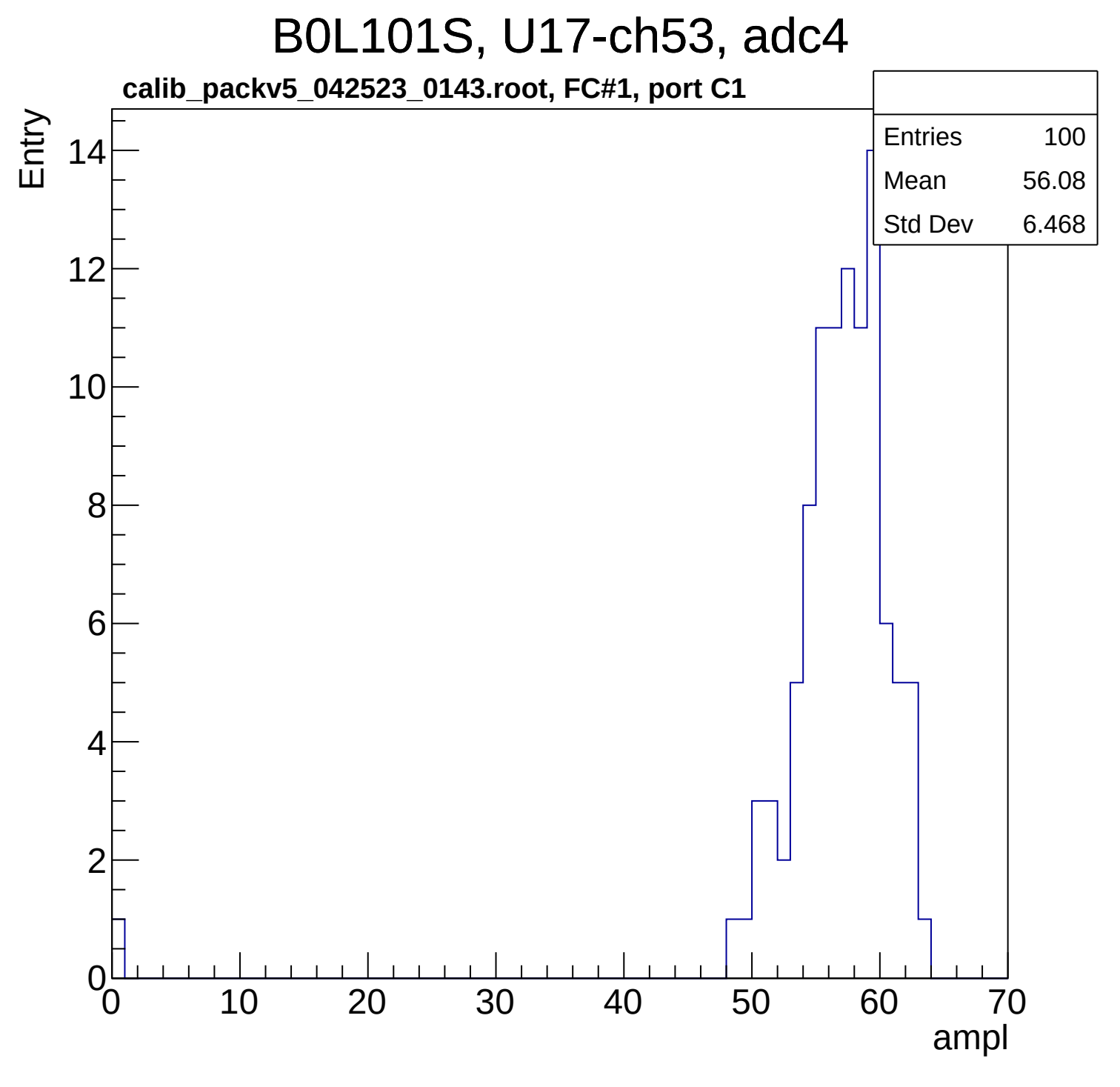

B0L101S, U17-ch52, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

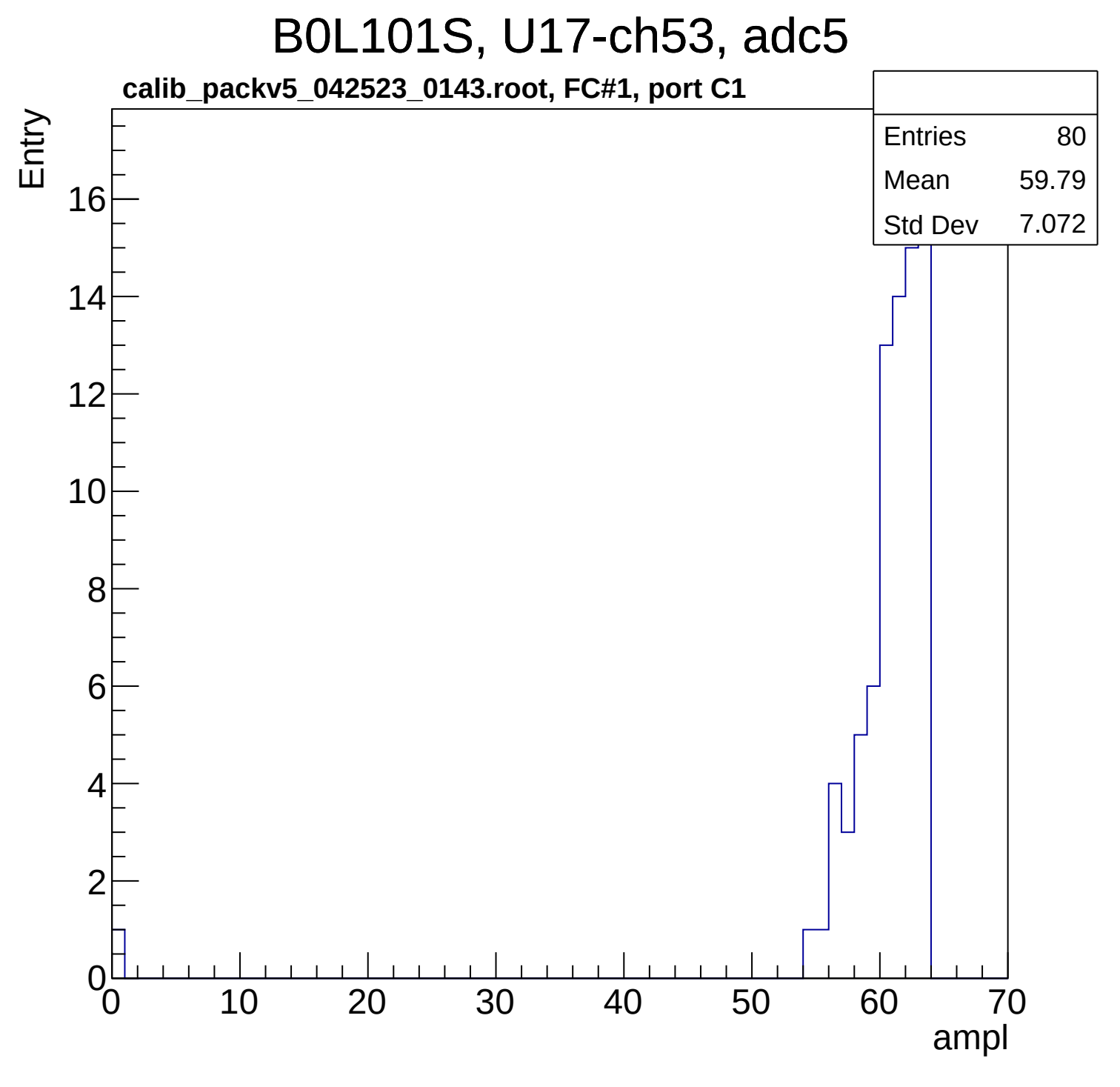

B0L101S, U17-ch53, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 203
Mean 29.22
Std Dev 4.148
Gaus mean : 29.6710
Gaus Width: 4.0893
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

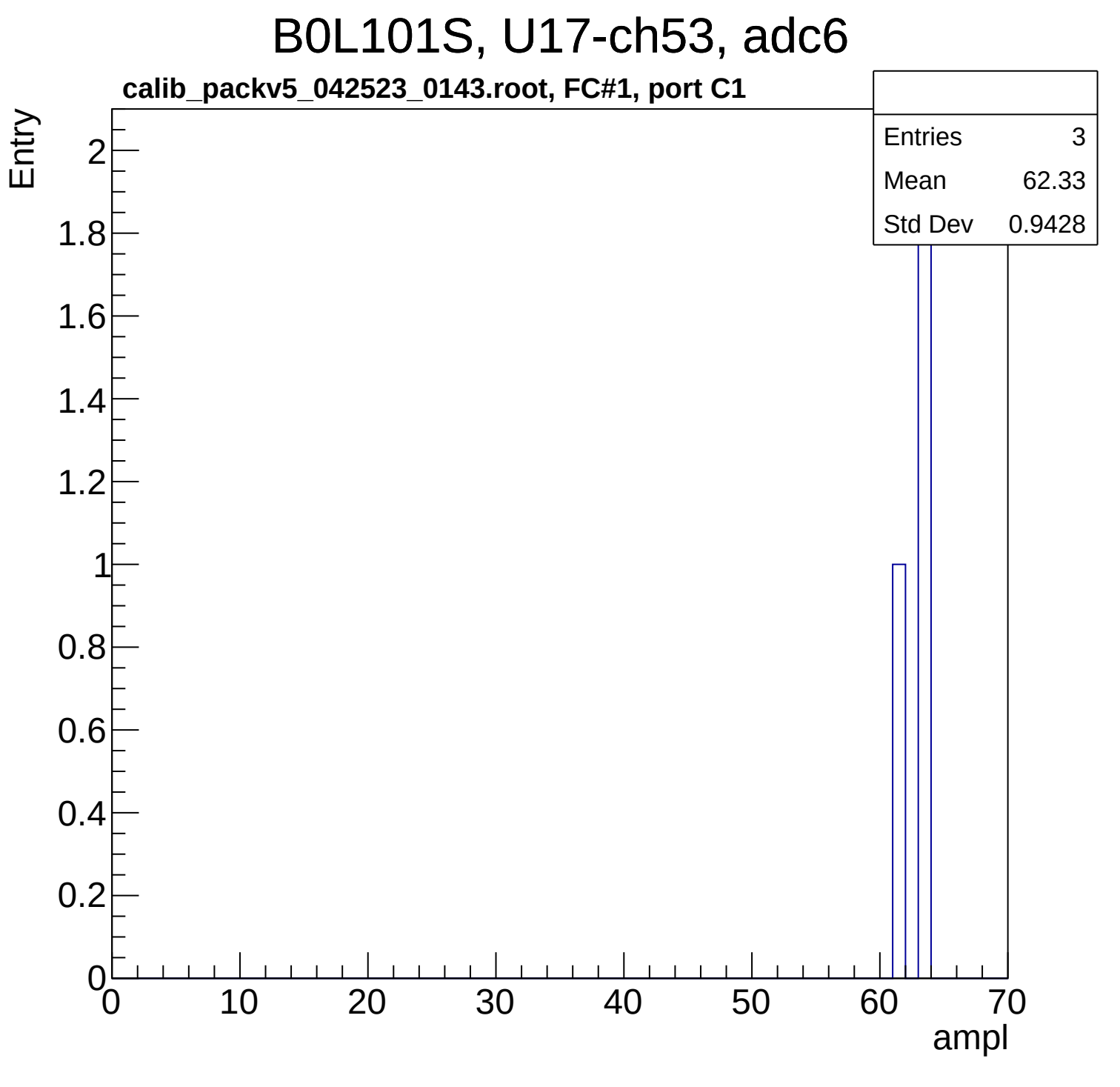

B0L101S, U17-ch53, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 36.81
Std Dev 3.598
Gaus mean : 37.6582
Gaus Width: 3.7338
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

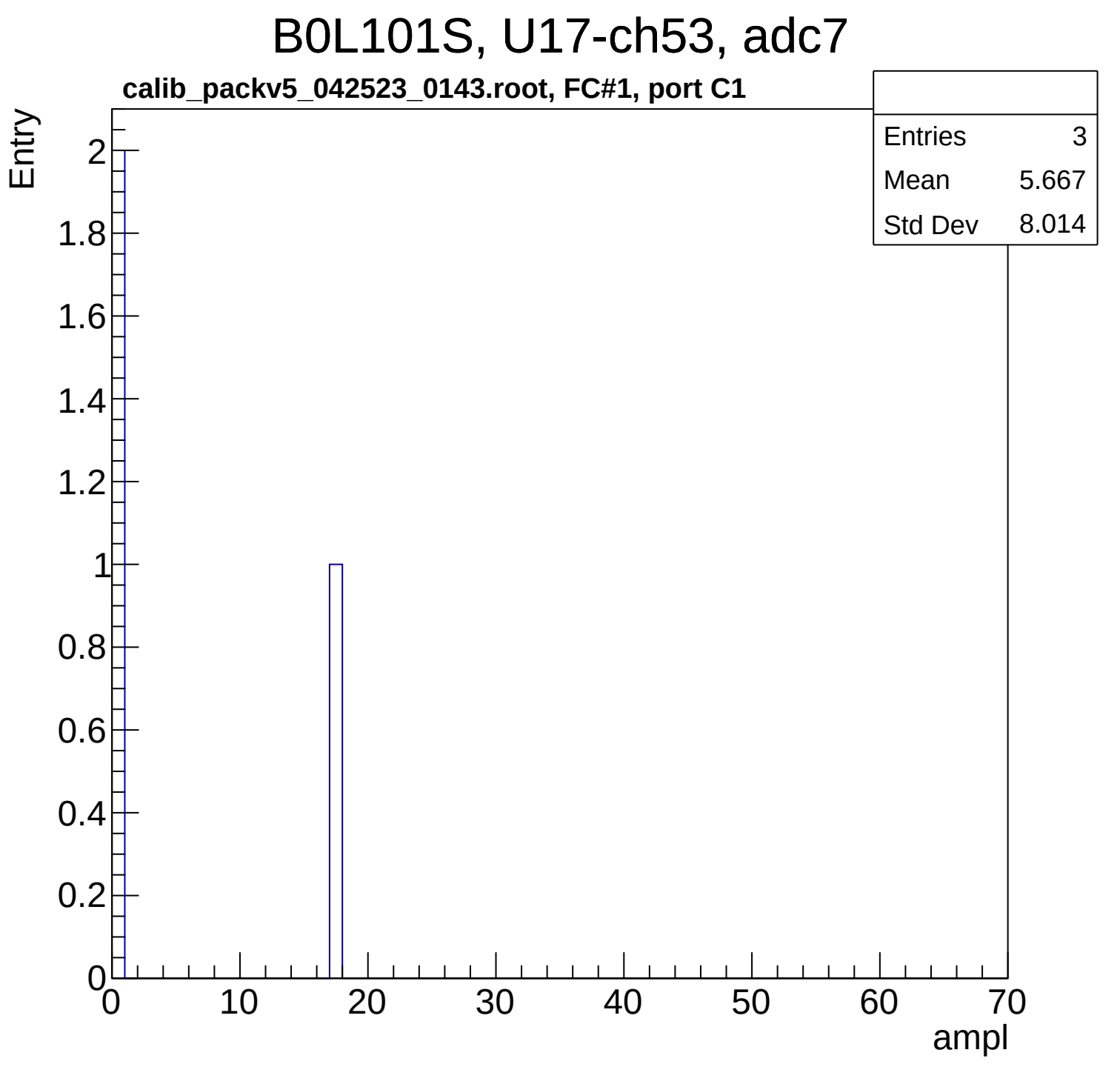

B0L101S, U17-ch53, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 43.18
Std Dev 3.736
Gaus mean : 43.5127
Gaus Width: 3.6874
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

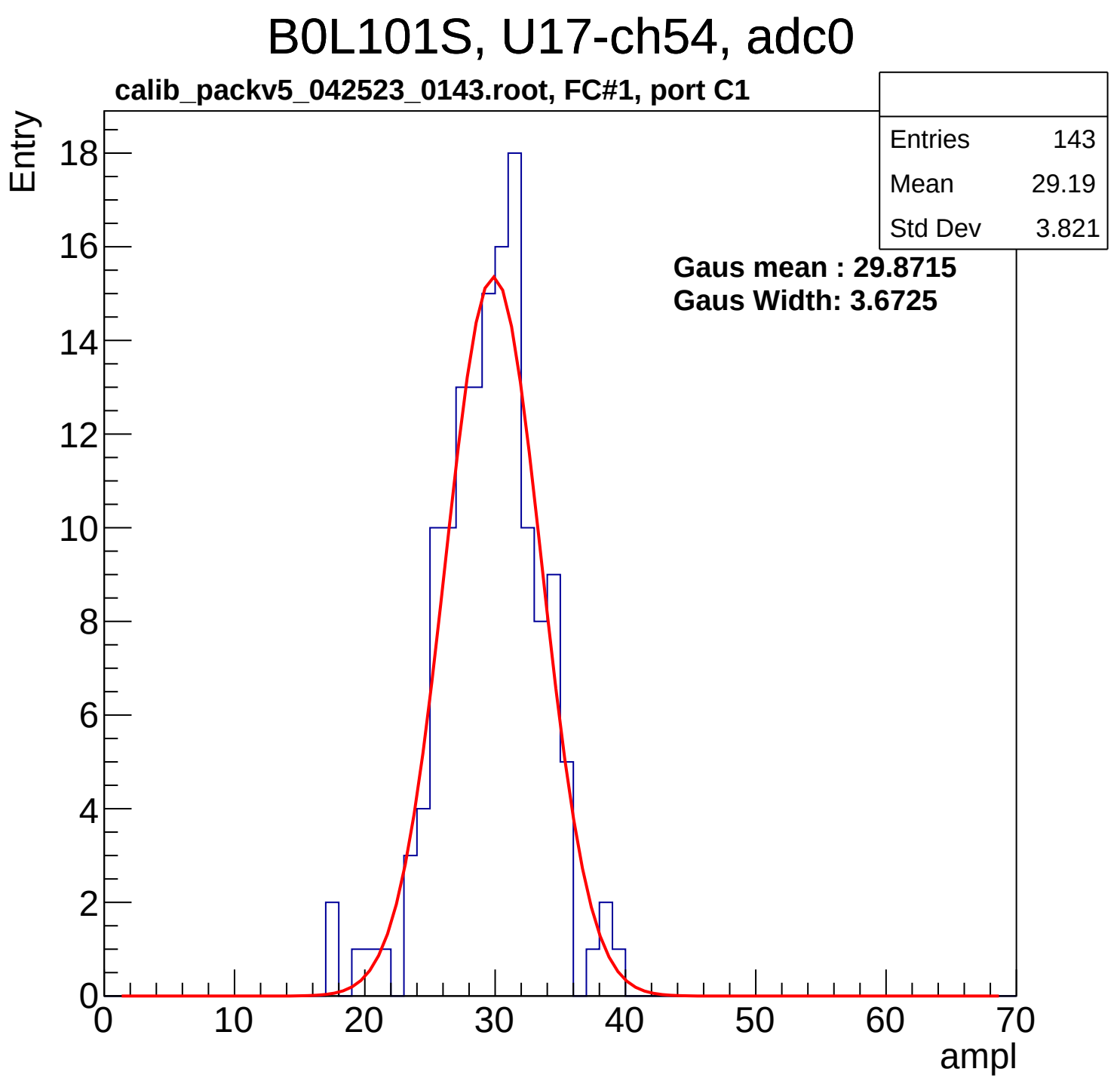

B0L101S, U17-ch53, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 50.61
Std Dev 3.665
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

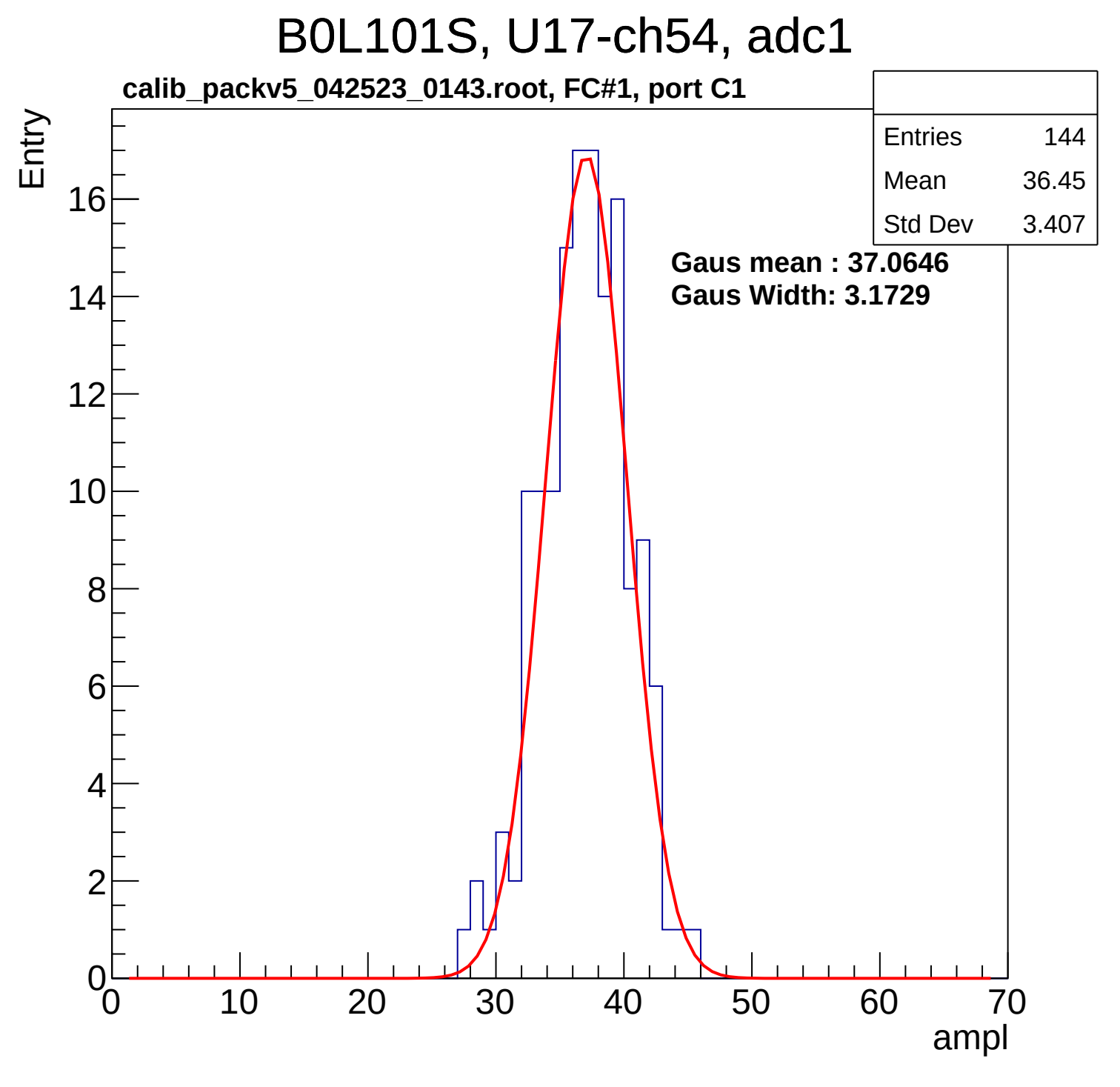

B0L101S, U17-ch53, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 56.08
Std Dev 6.468
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

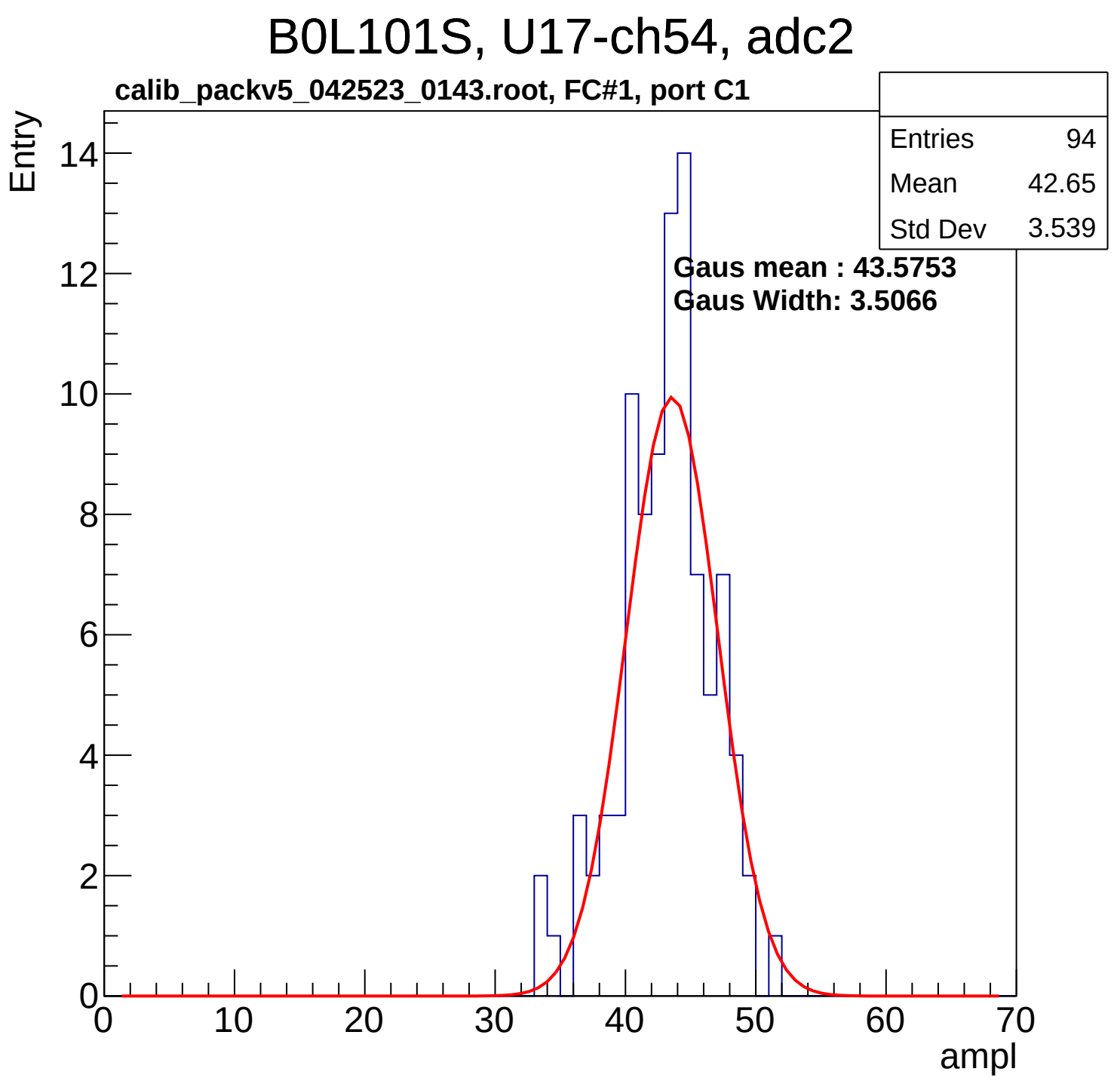

B0L101S, U17-ch53, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 80
Mean 59.79
Std Dev 7.072
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

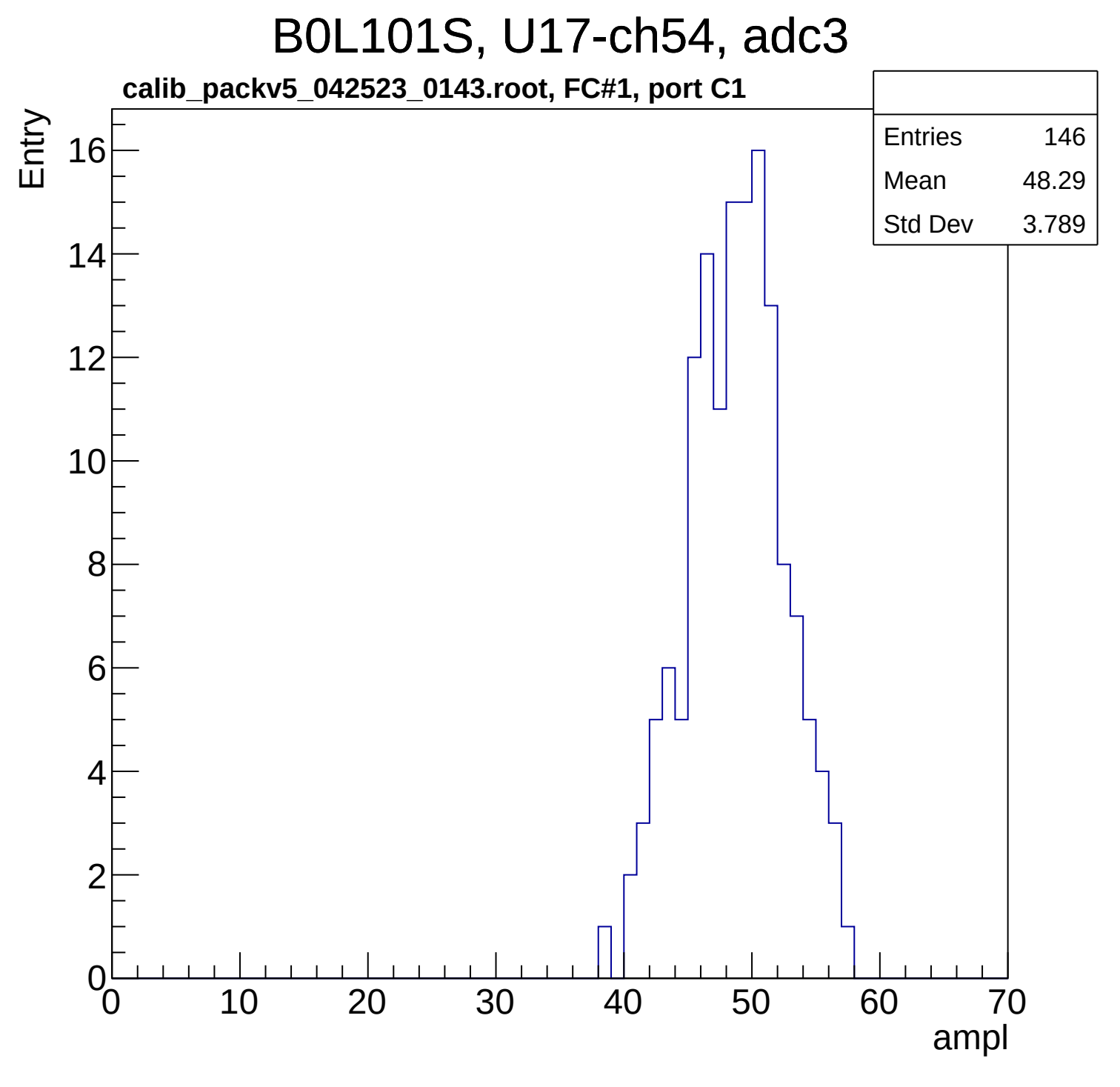

B0L101S, U17-ch53, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

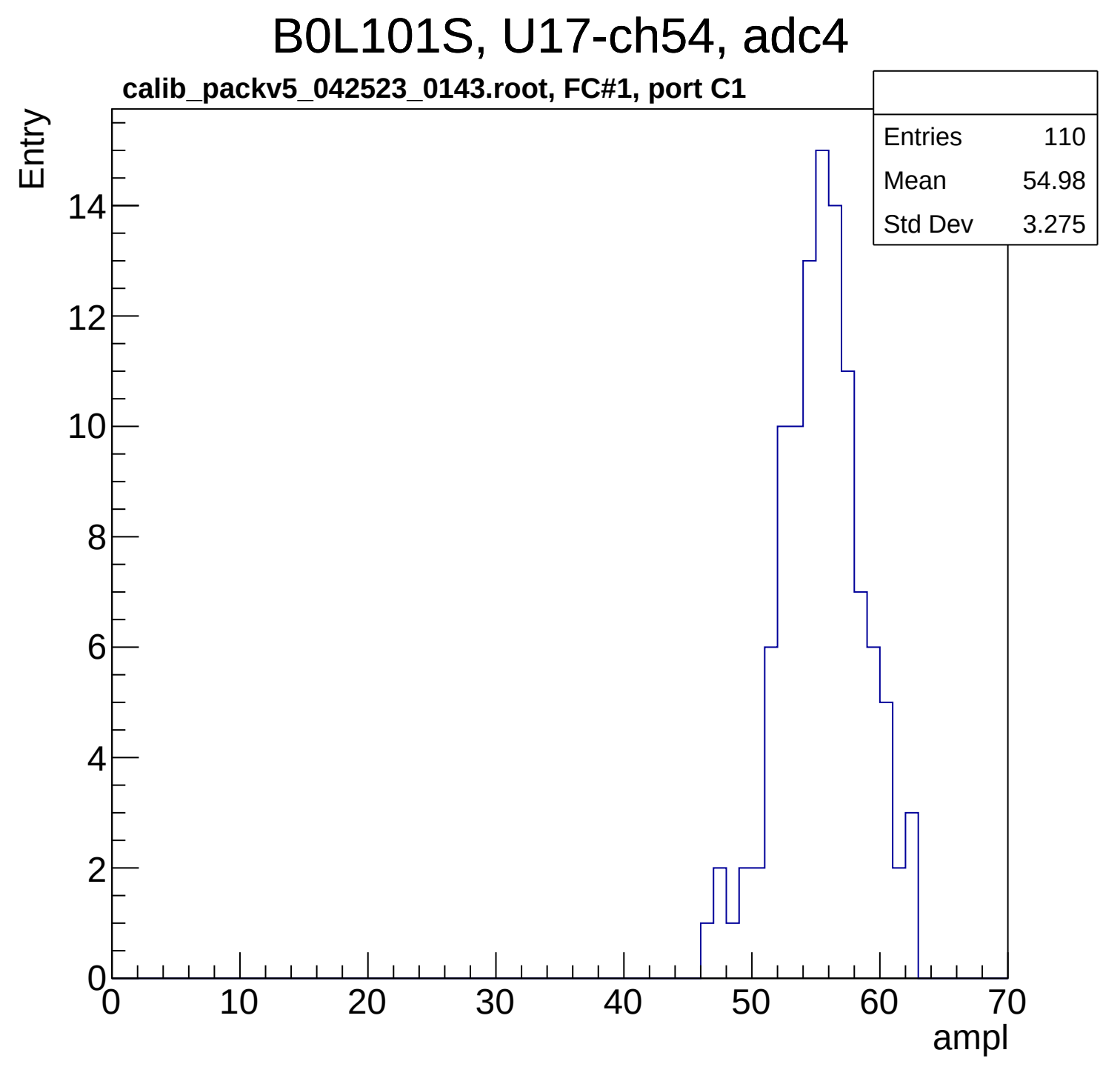

B0L101S, U17-ch53, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 5.667
Std Dev 8.014
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

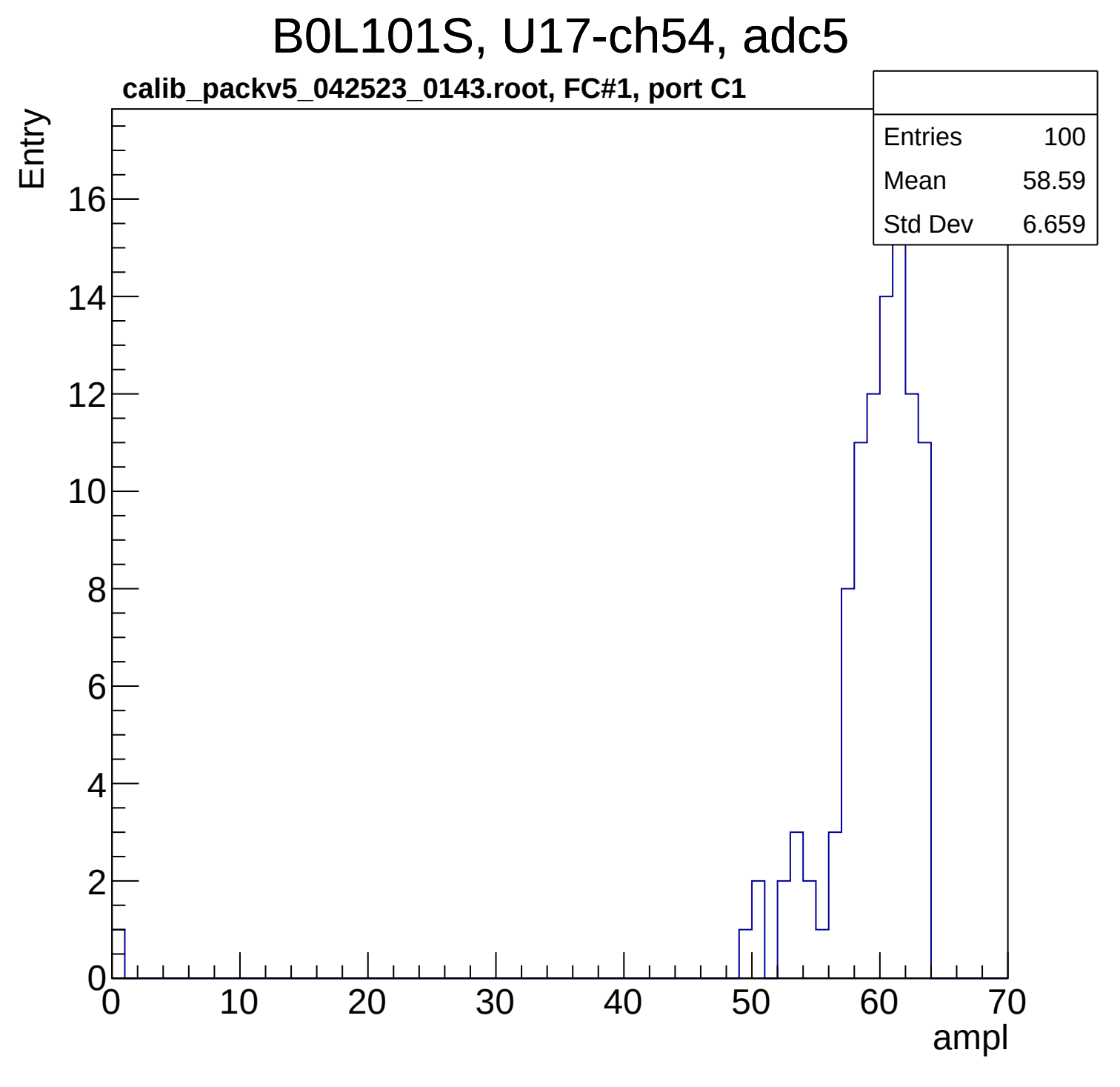

B0L101S, U17-ch54, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 29.19
Std Dev 3.821
Gaus mean : 29.8715
Gaus Width: 3.6725
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

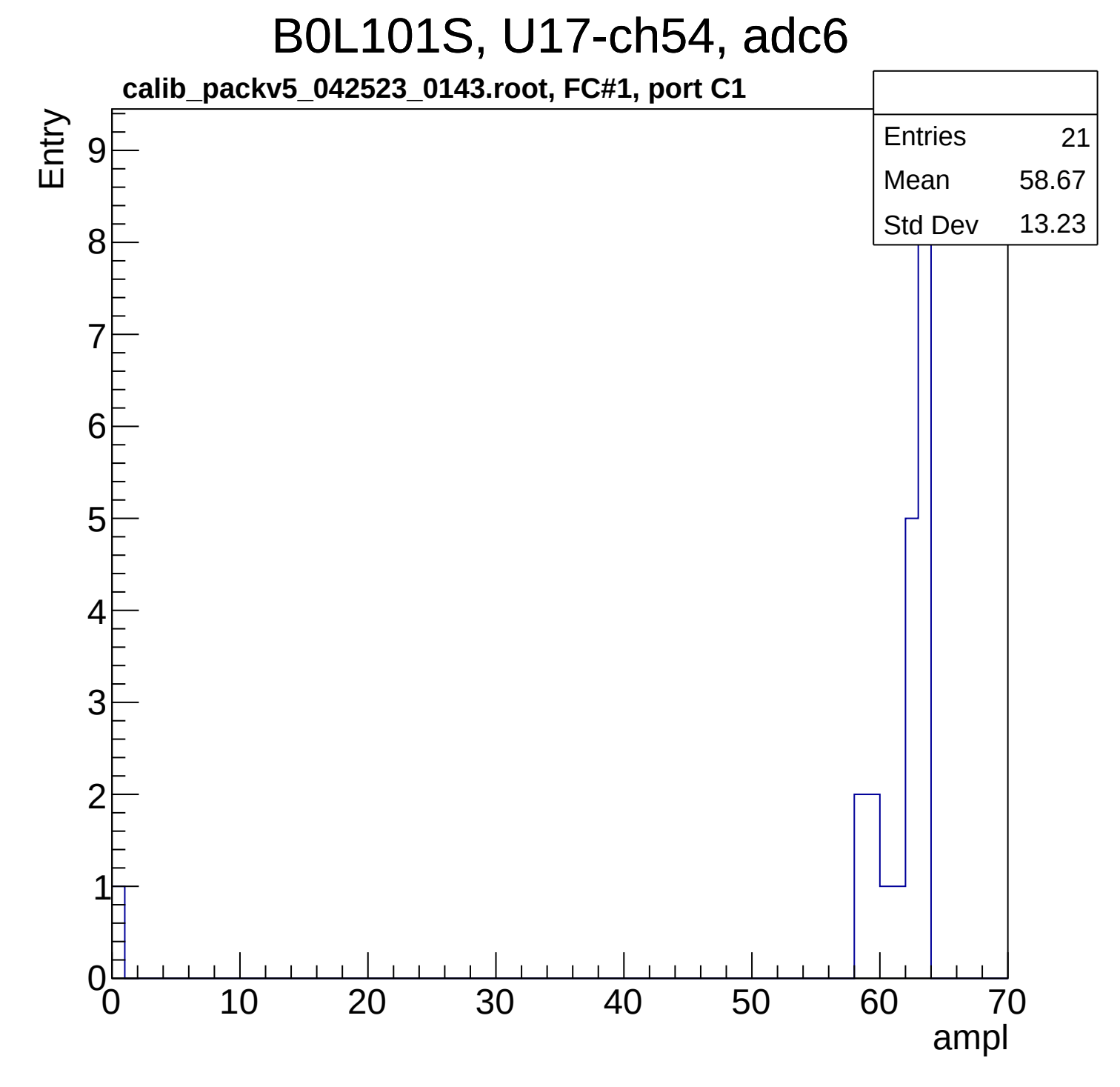

B0L101S, U17-ch54, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 36.45
Std Dev 3.407
Gaus mean : 37.0646
Gaus Width: 3.1729
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

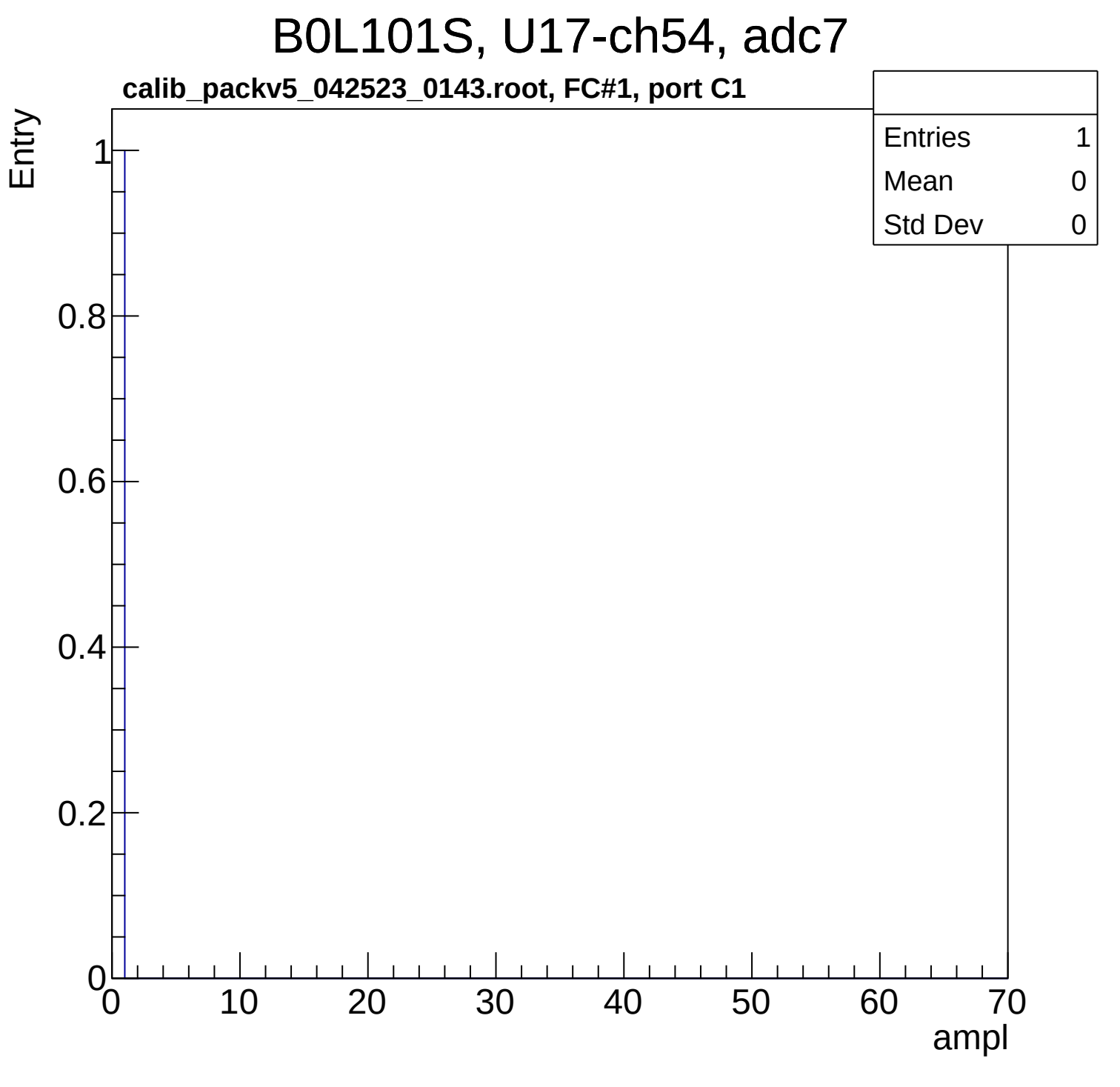

B0L101S, U17-ch54, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 94
Mean 42.65
Std Dev 3.539
Gaus mean : 43.5753
Gaus Width: 3.5066
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

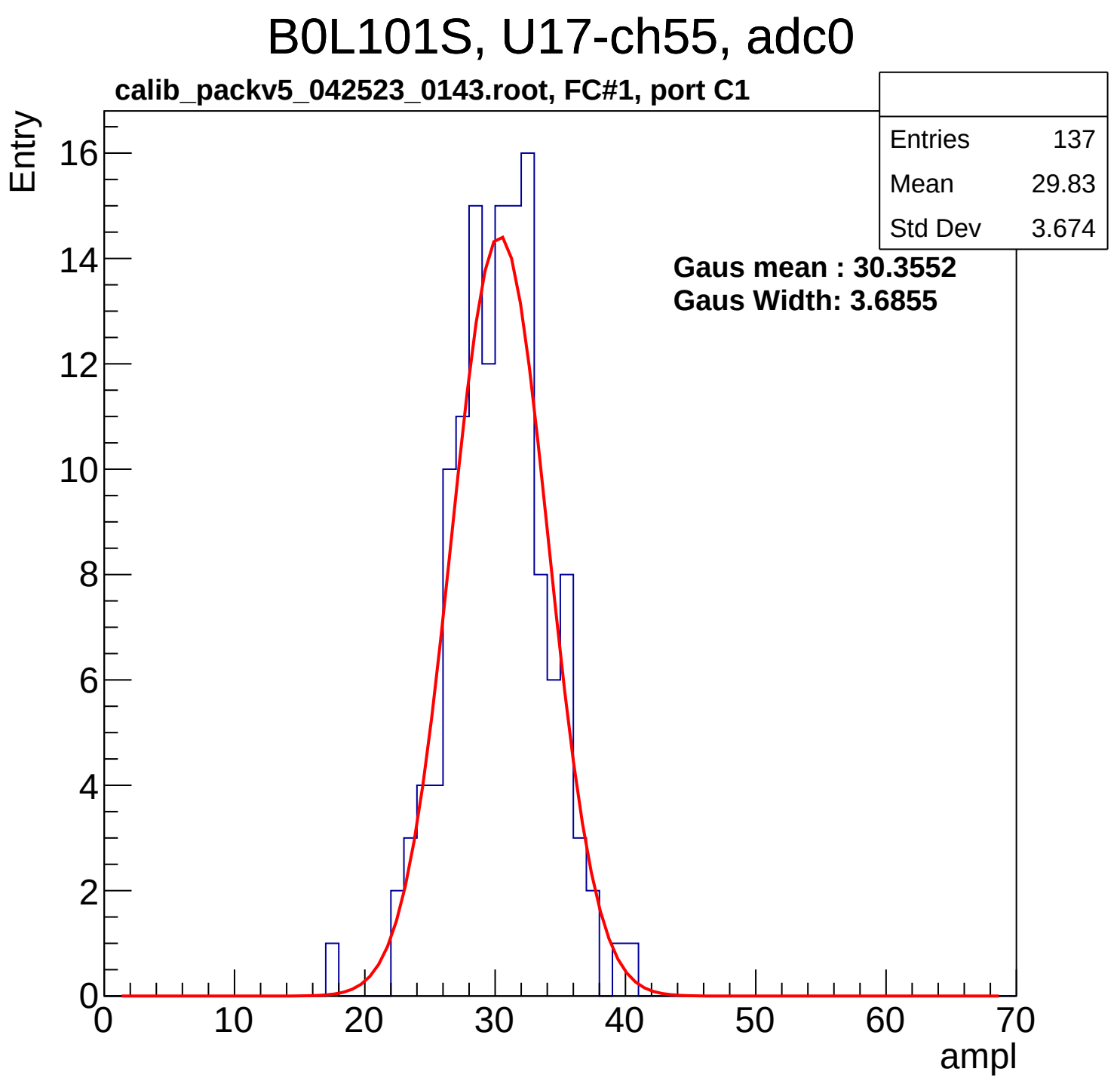

B0L101S, U17-ch54, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 48.29
Std Dev 3.789
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

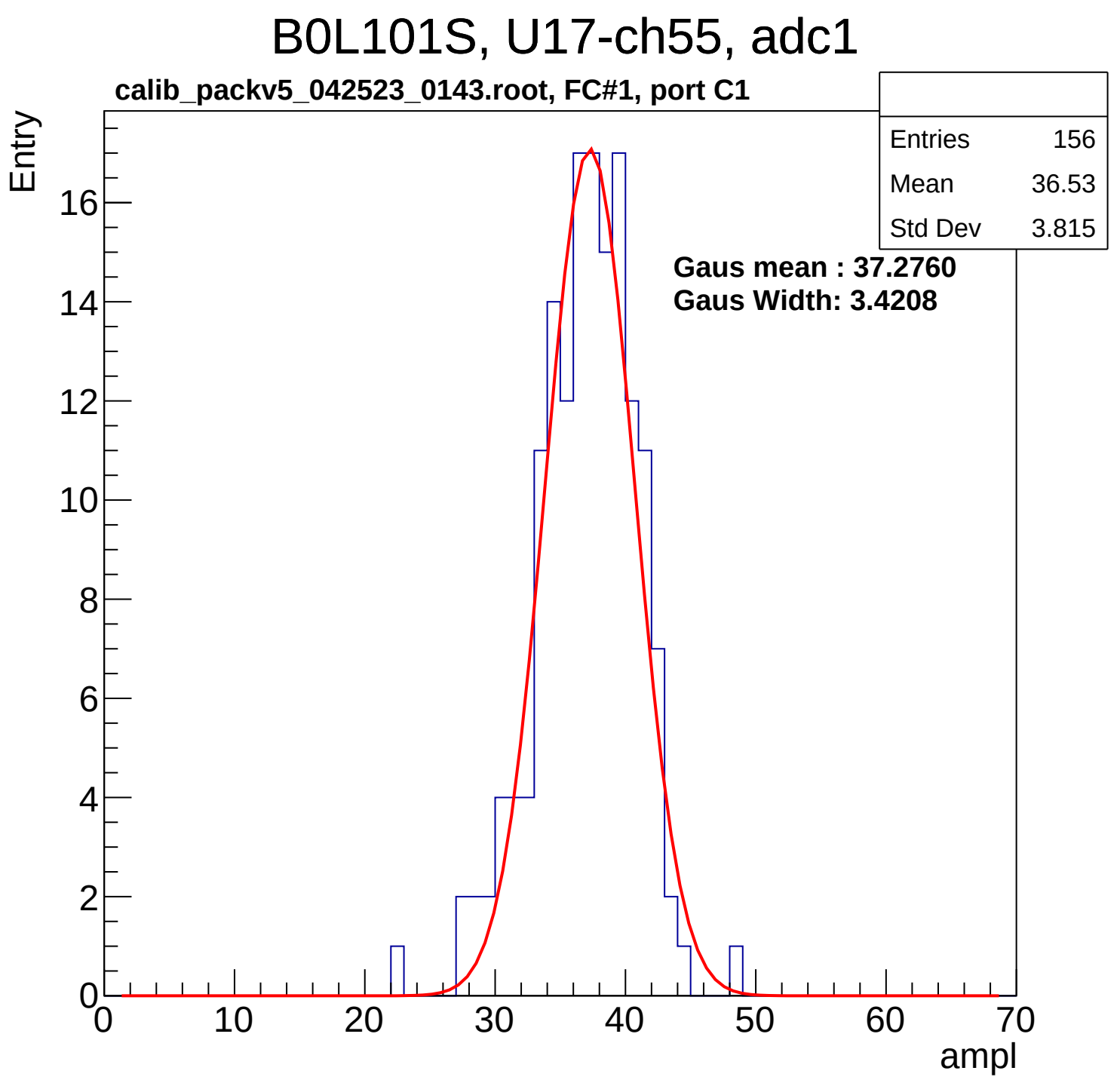

B0L101S, U17-ch54, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 54.98
Std Dev 3.275
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

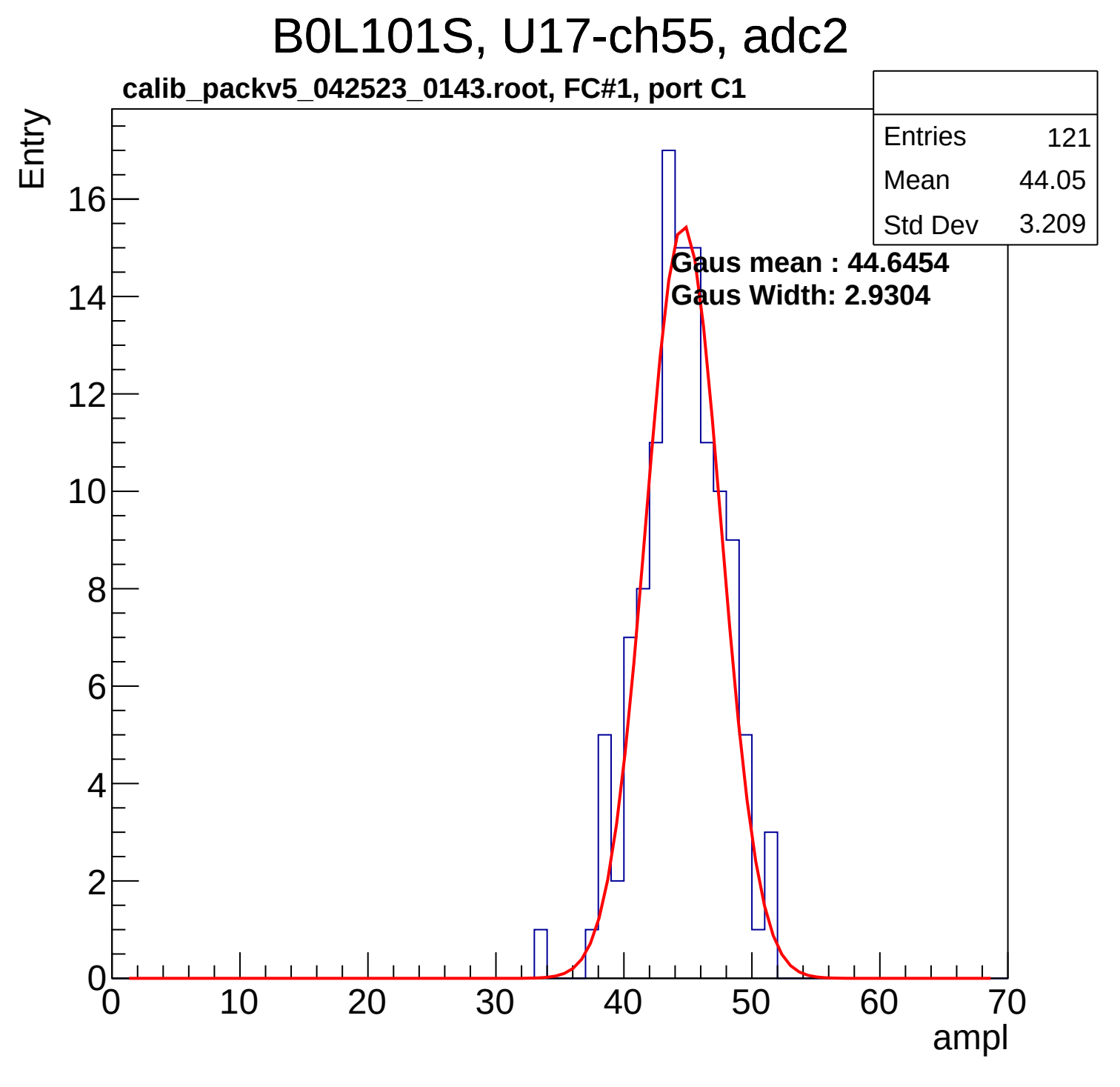

B0L101S, U17-ch54, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 58.59
Std Dev 6.659
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

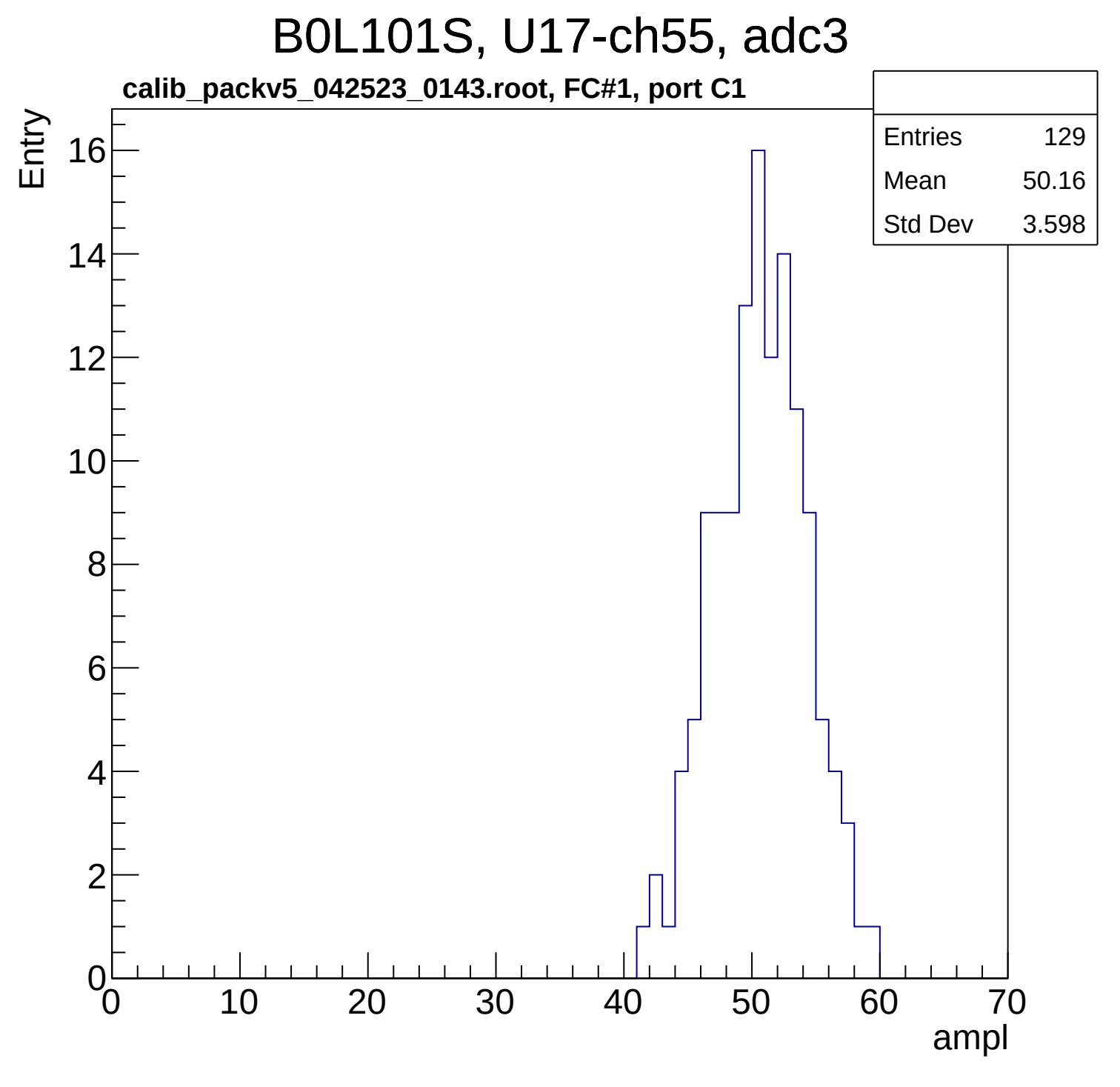

B0L101S, U17-ch54, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 21
Mean 58.67
Std Dev 13.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

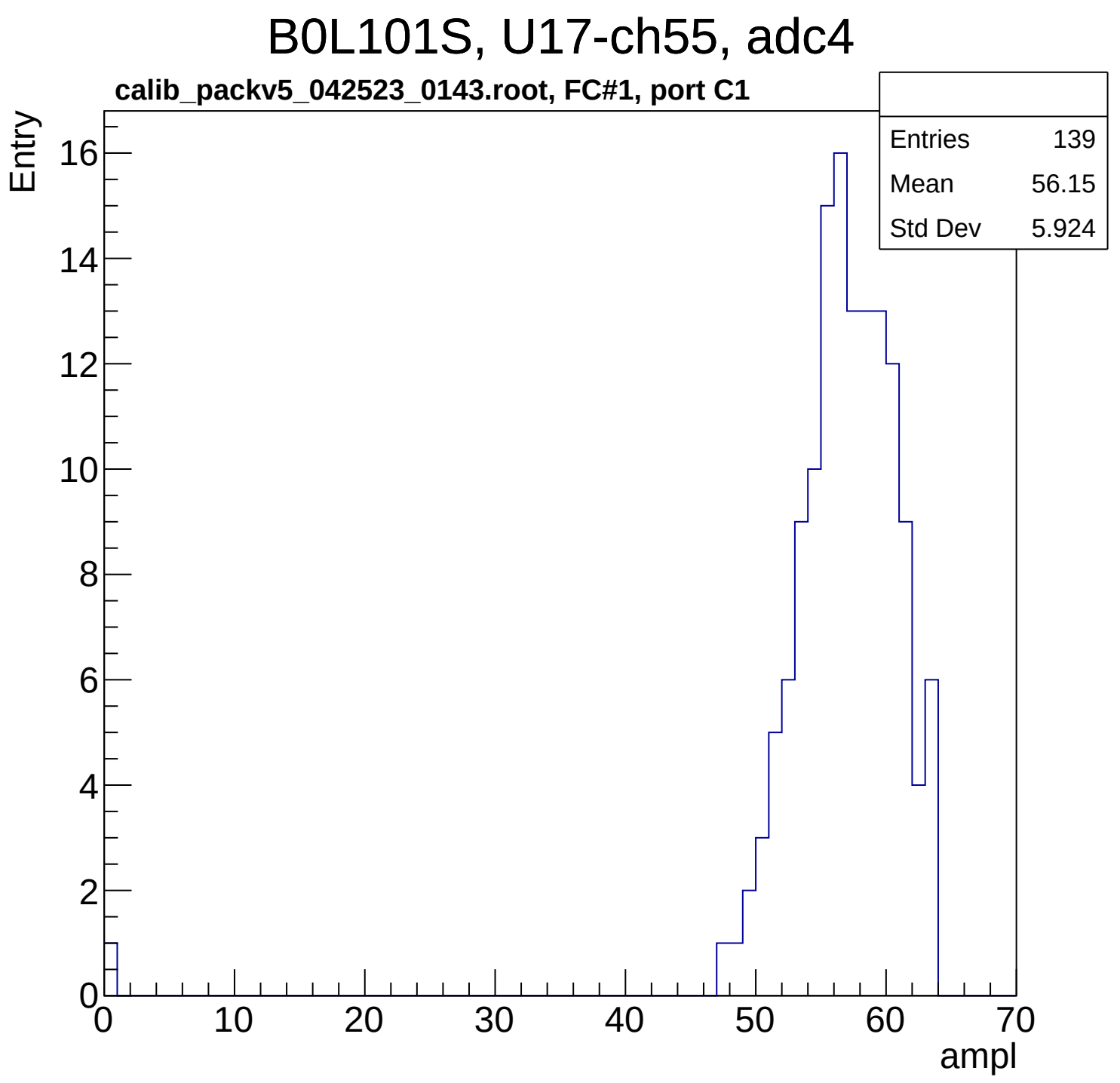

B0L101S, U17-ch54, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

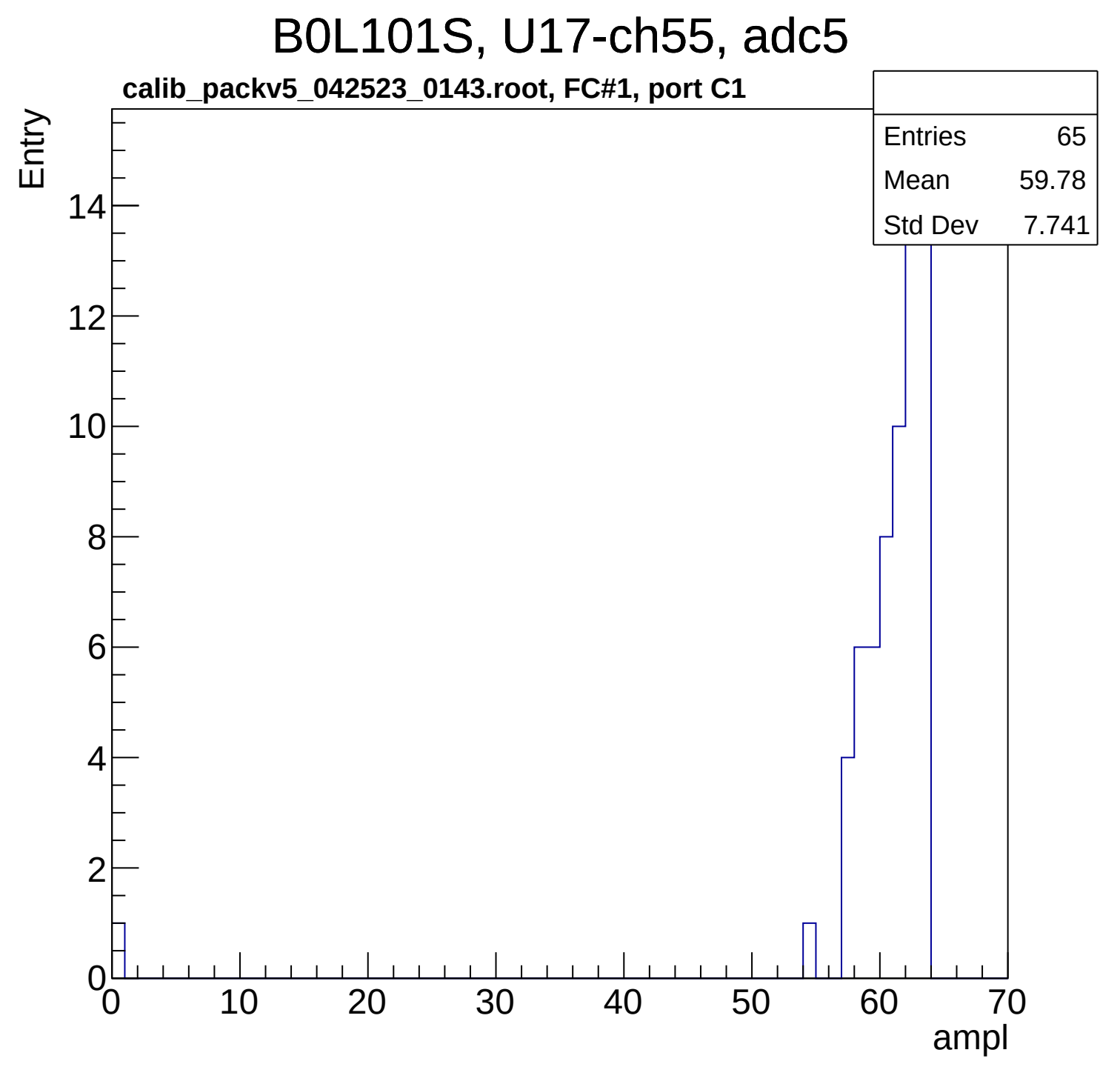

B0L101S, U17-ch55, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 29.83
Std Dev 3.674
Gaus mean : 30.3552
Gaus Width: 3.6855
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

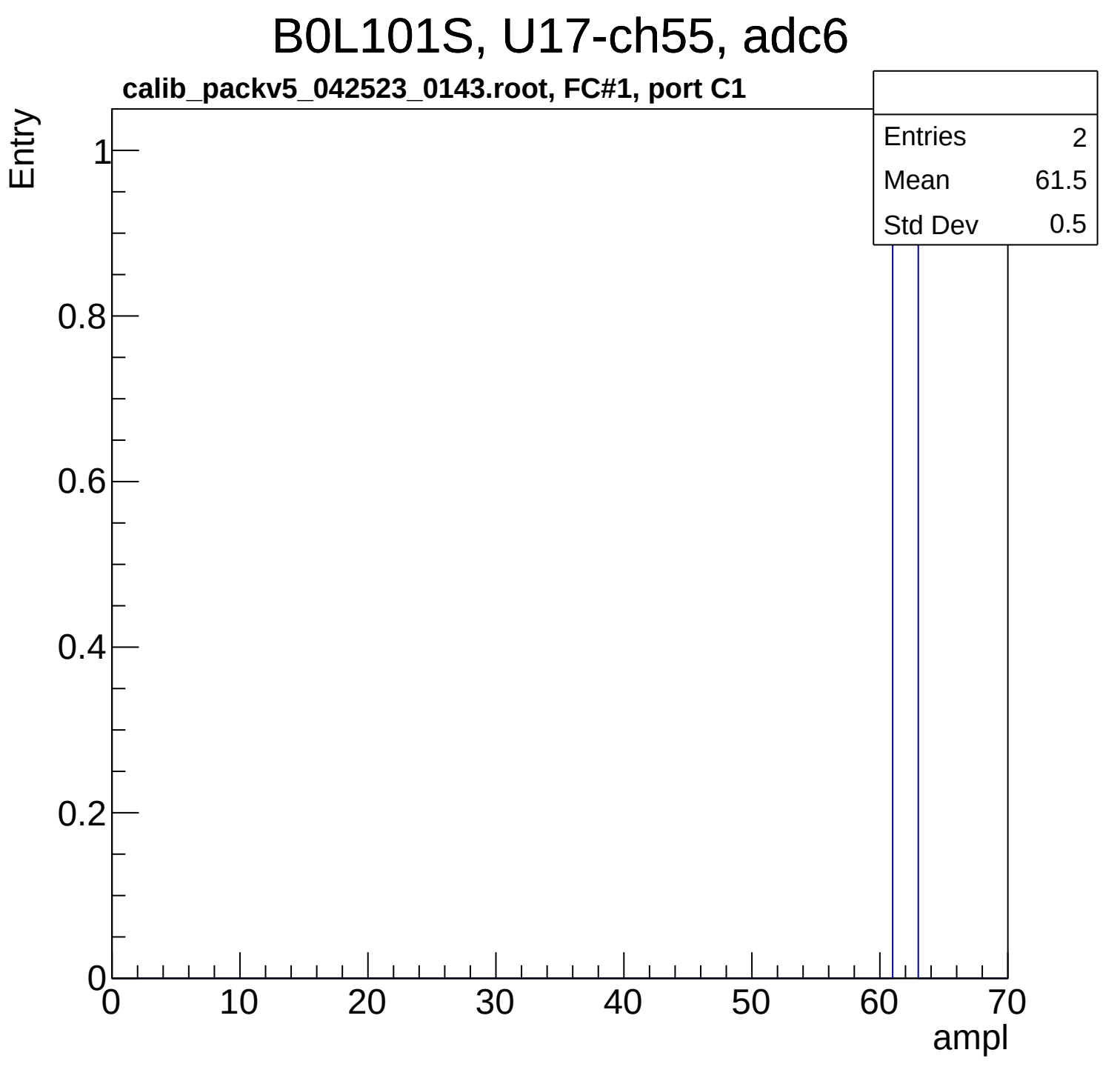

B0L101S, U17-ch55, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 36.53
Std Dev 3.815
Gaus mean : 37.2760
Gaus Width: 3.4208
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

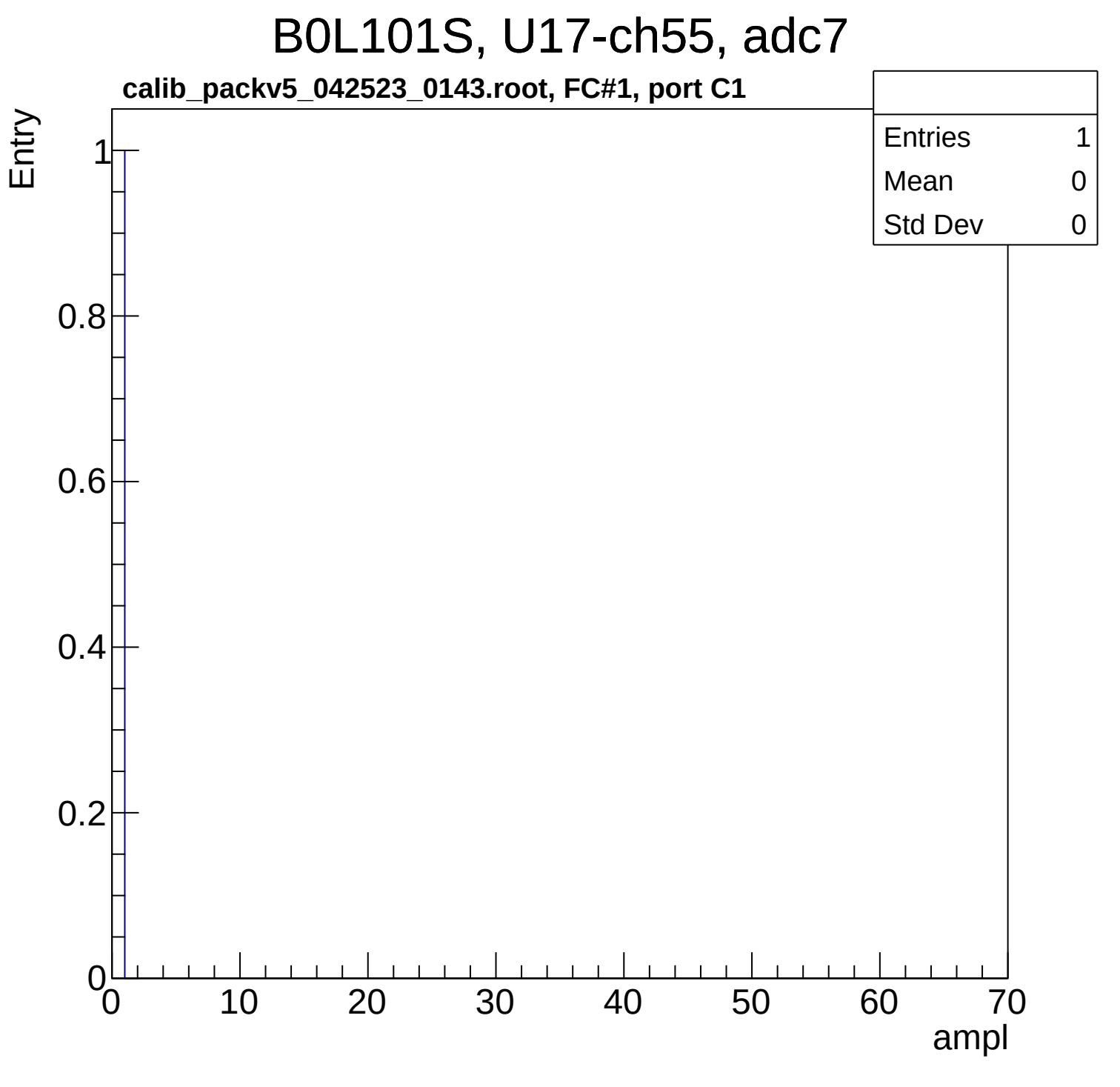

B0L101S, U17-ch55, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 44.05
Std Dev 3.209
Gaus mean : 44.6454
Gaus Width: 2.9304
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

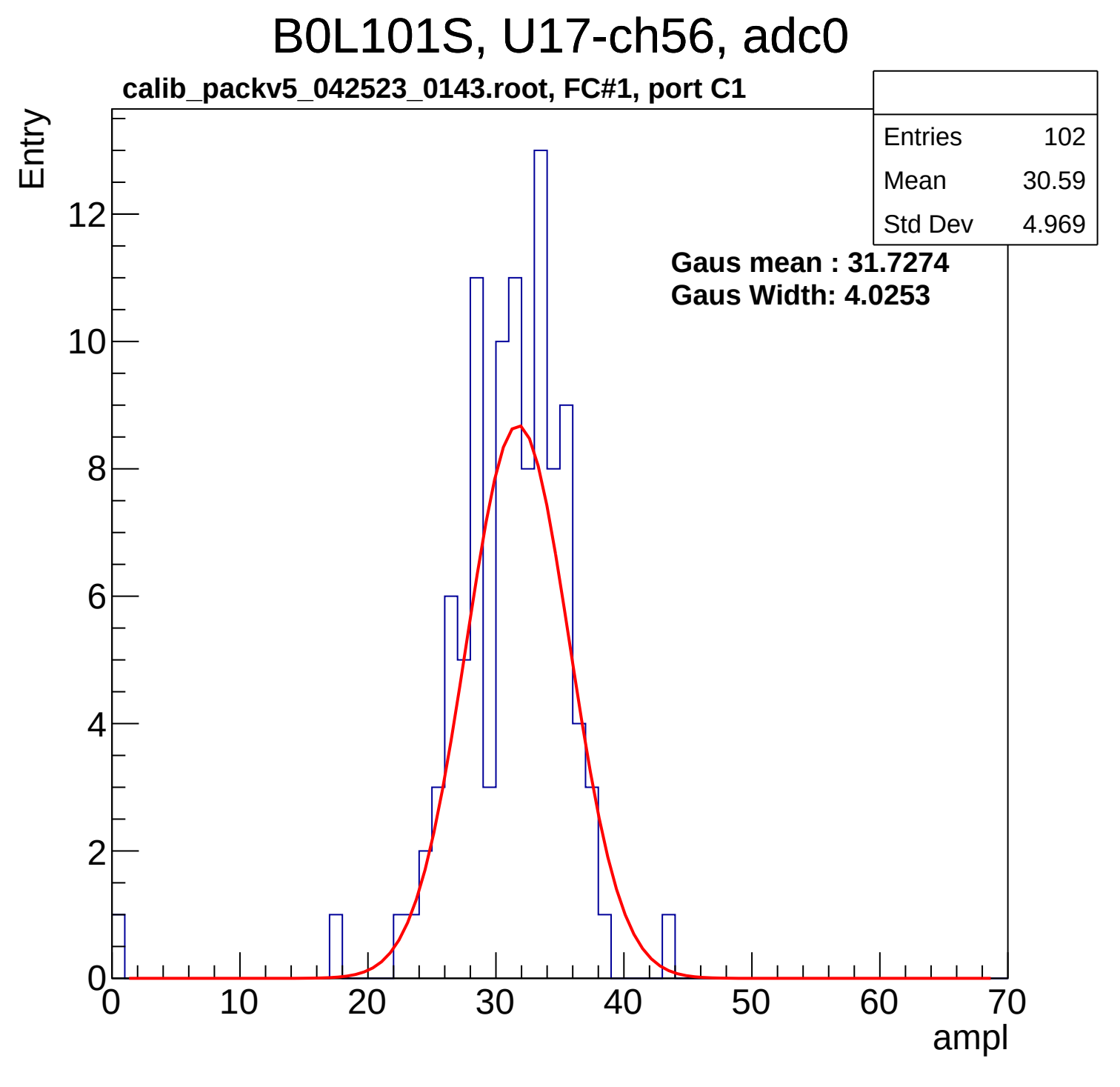

B0L101S, U17-ch55, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 50.16
Std Dev 3.598
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

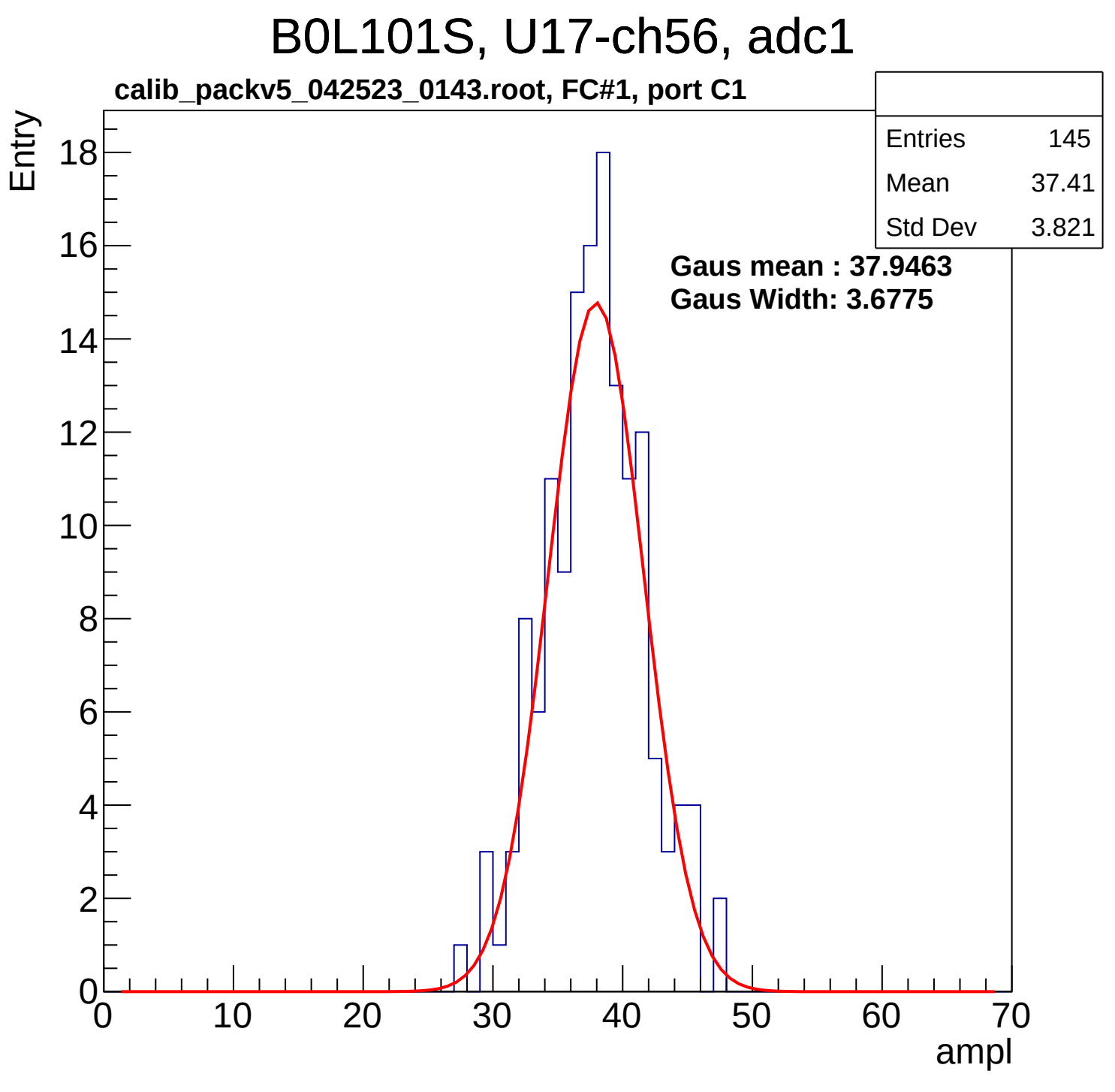

B0L101S, U17-ch55, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 56.15
Std Dev 5.924
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

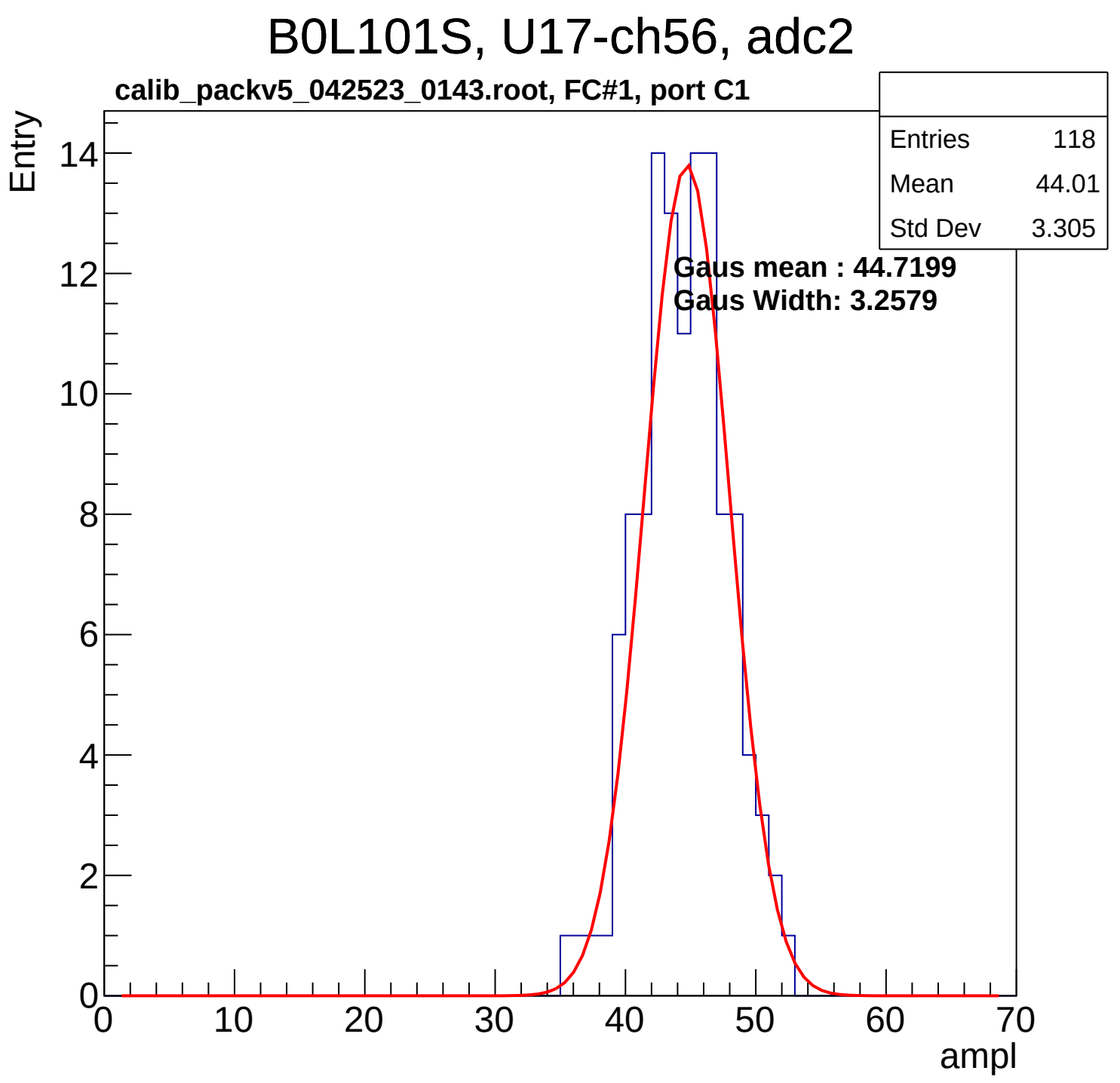

B0L101S, U17-ch55, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 65
Mean 59.78
Std Dev 7.741
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

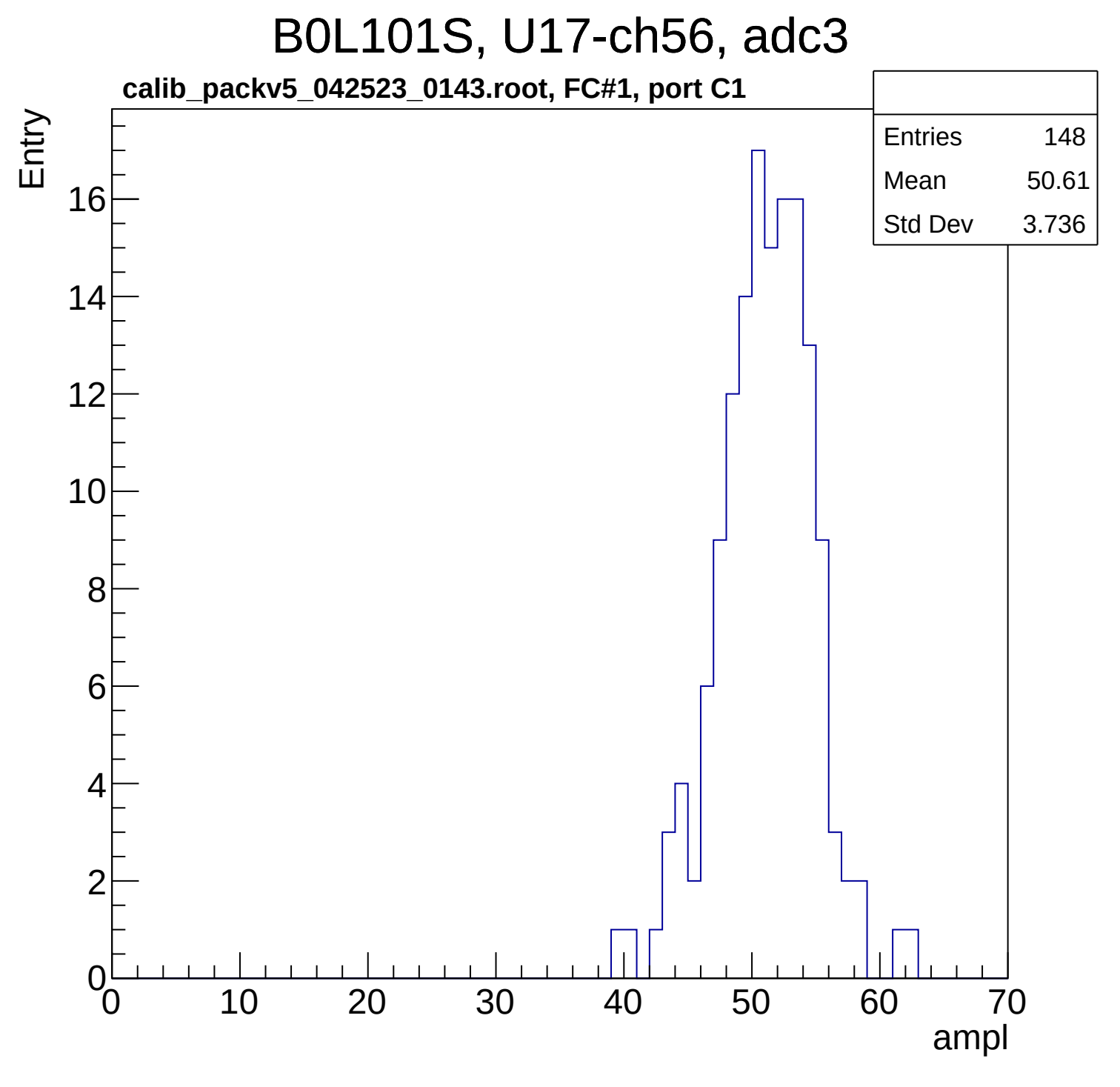

B0L101S, U17-ch55, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

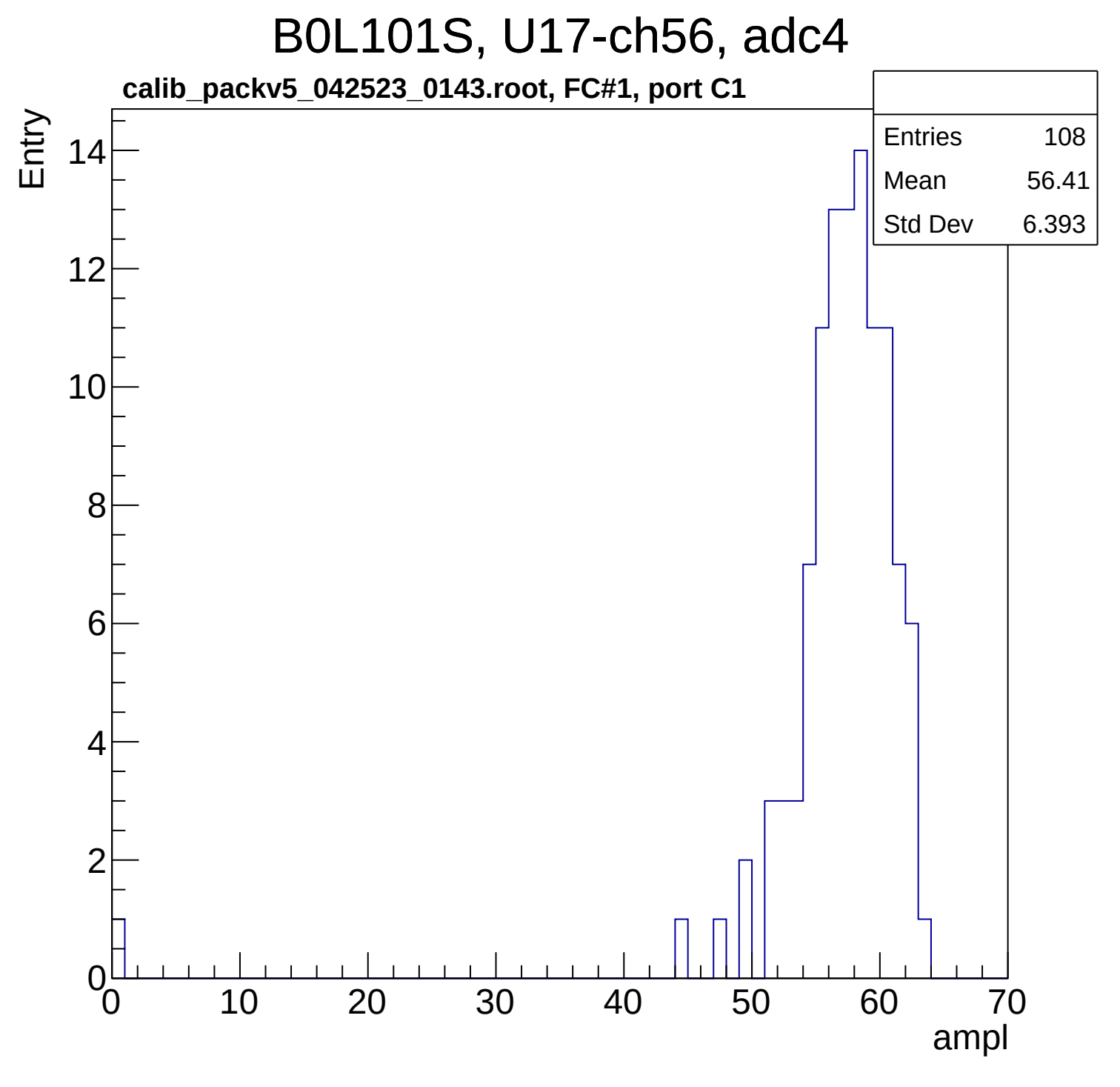

B0L101S, U17-ch55, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

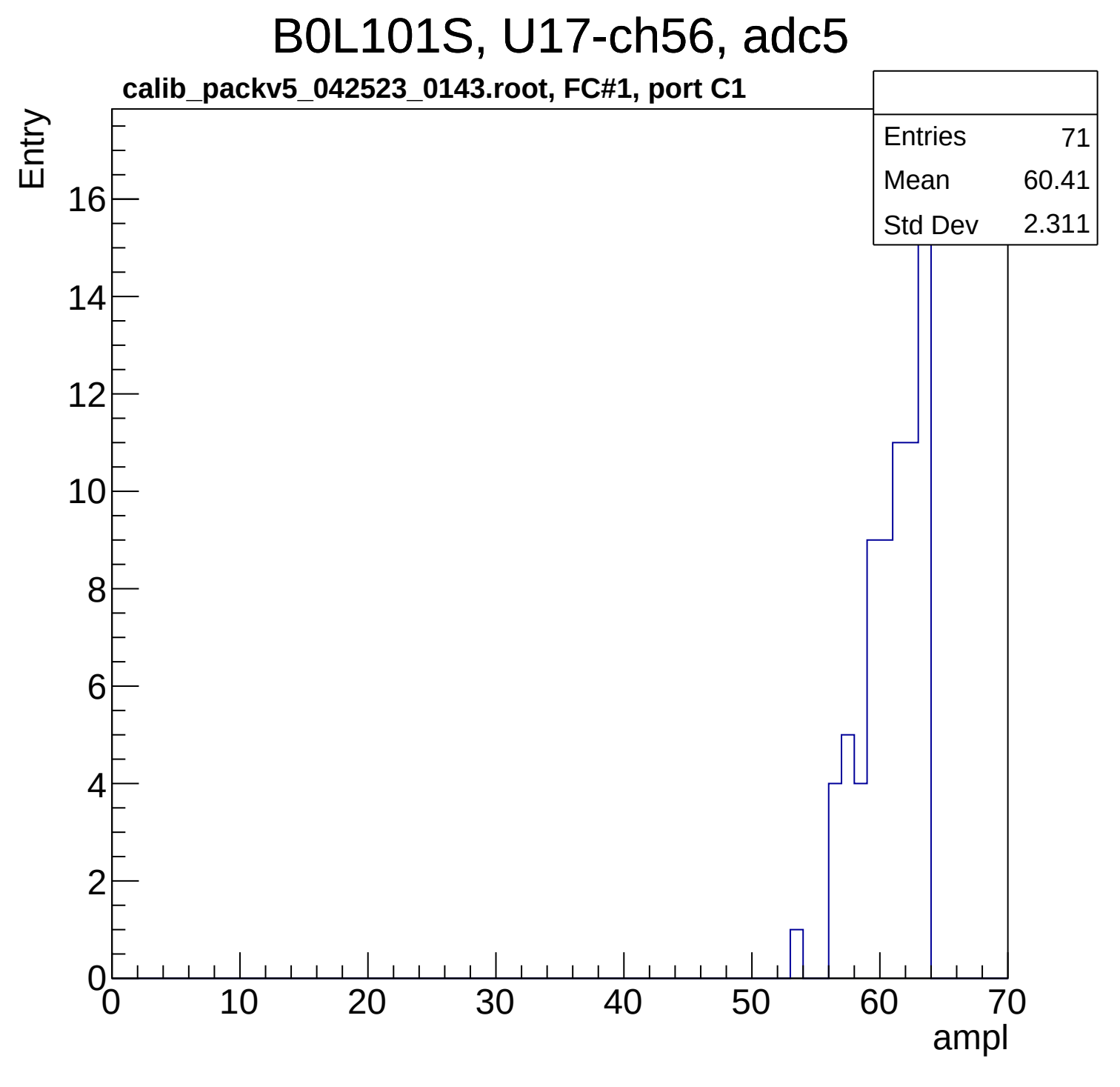

B0L101S, U17-ch56, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 30.59
Std Dev 4.969
Gaus mean : 31.7274
Gaus Width: 4.0253
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

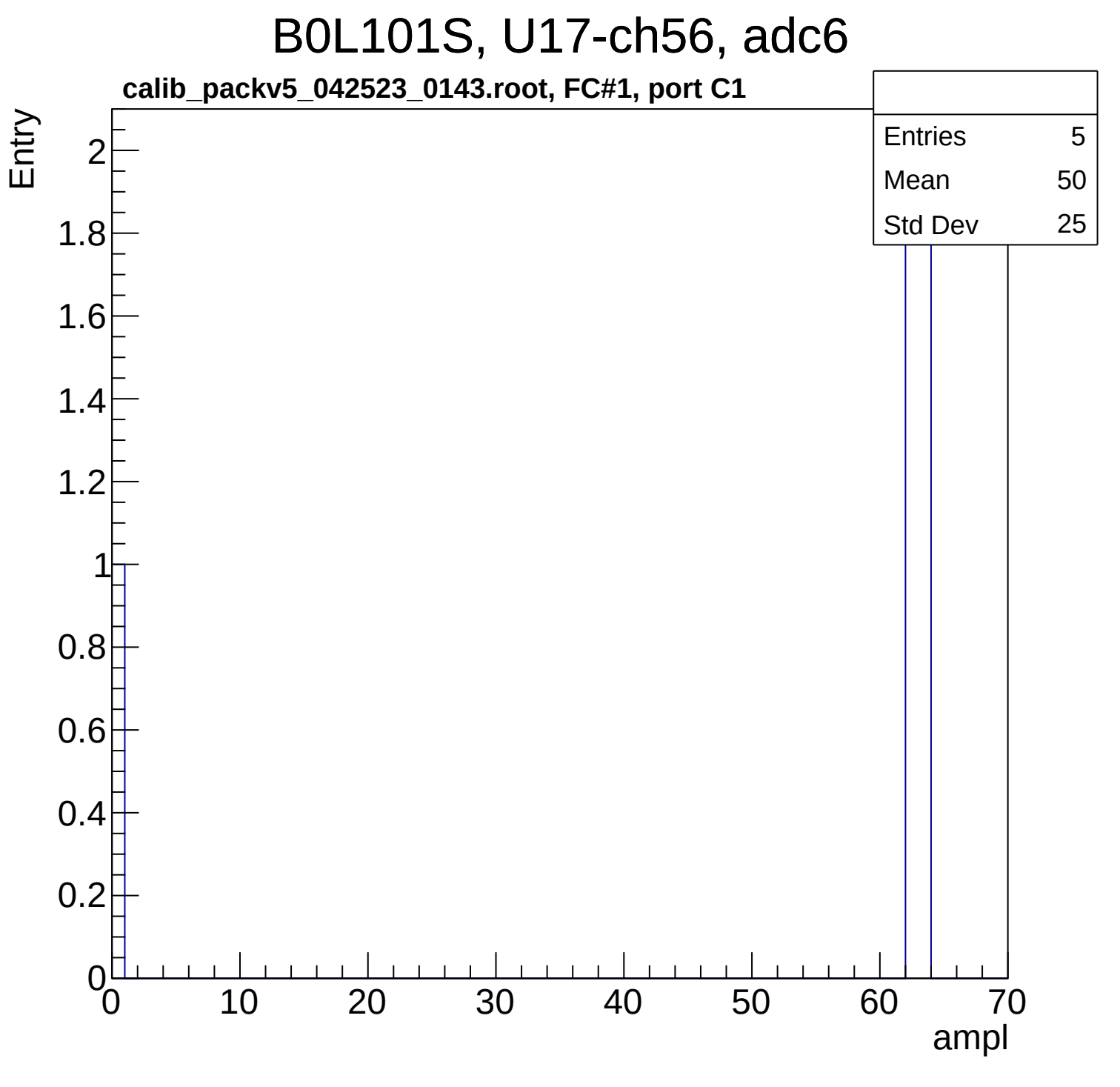

B0L101S, U17-ch56, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 37.41
Std Dev 3.821
Gaus mean : 37.9463
Gaus Width: 3.6775
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

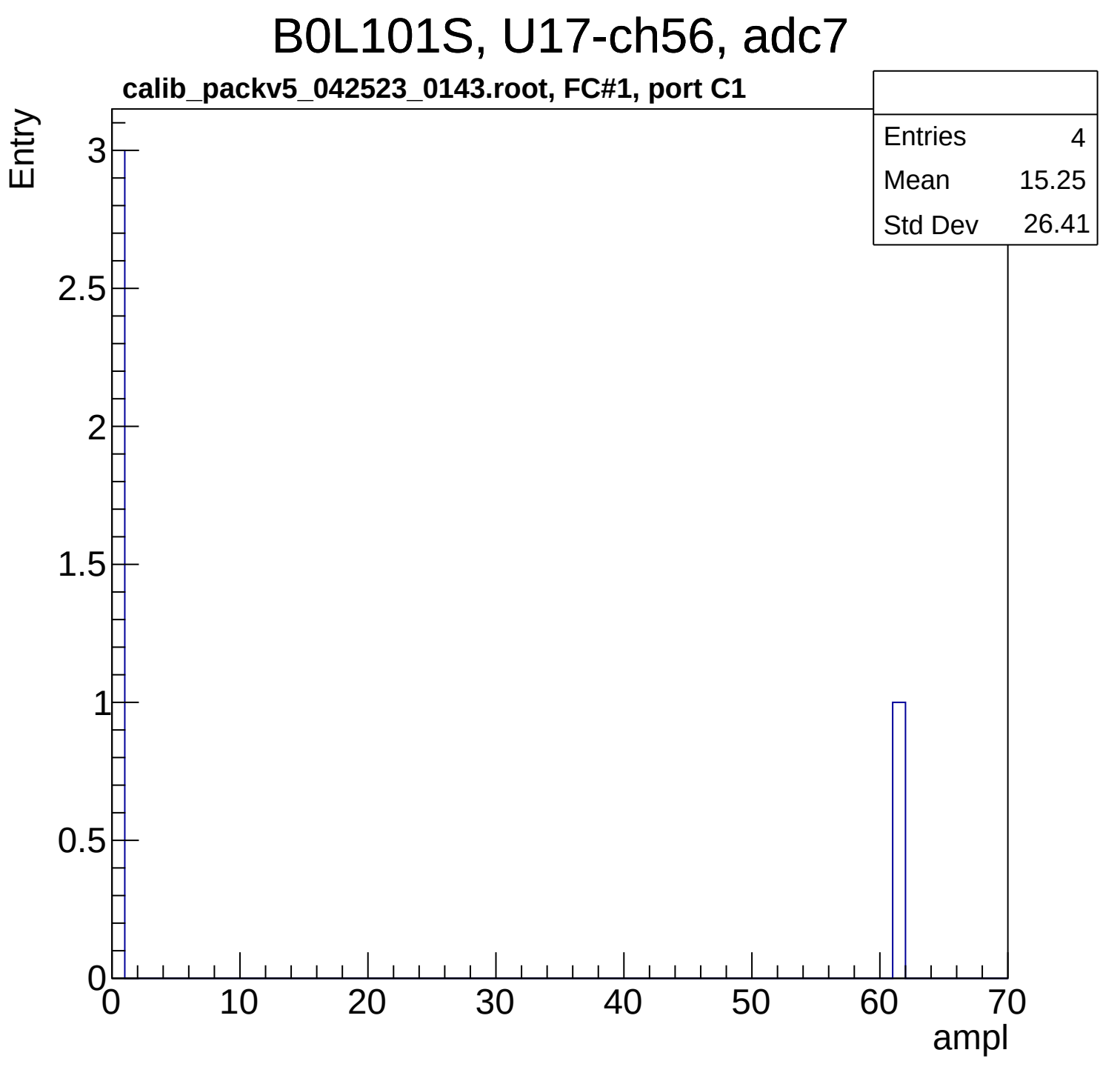

B0L101S, U17-ch56, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 44.01
Std Dev 3.305
Gaus mean : 44.7199
Gaus Width: 3.2579
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

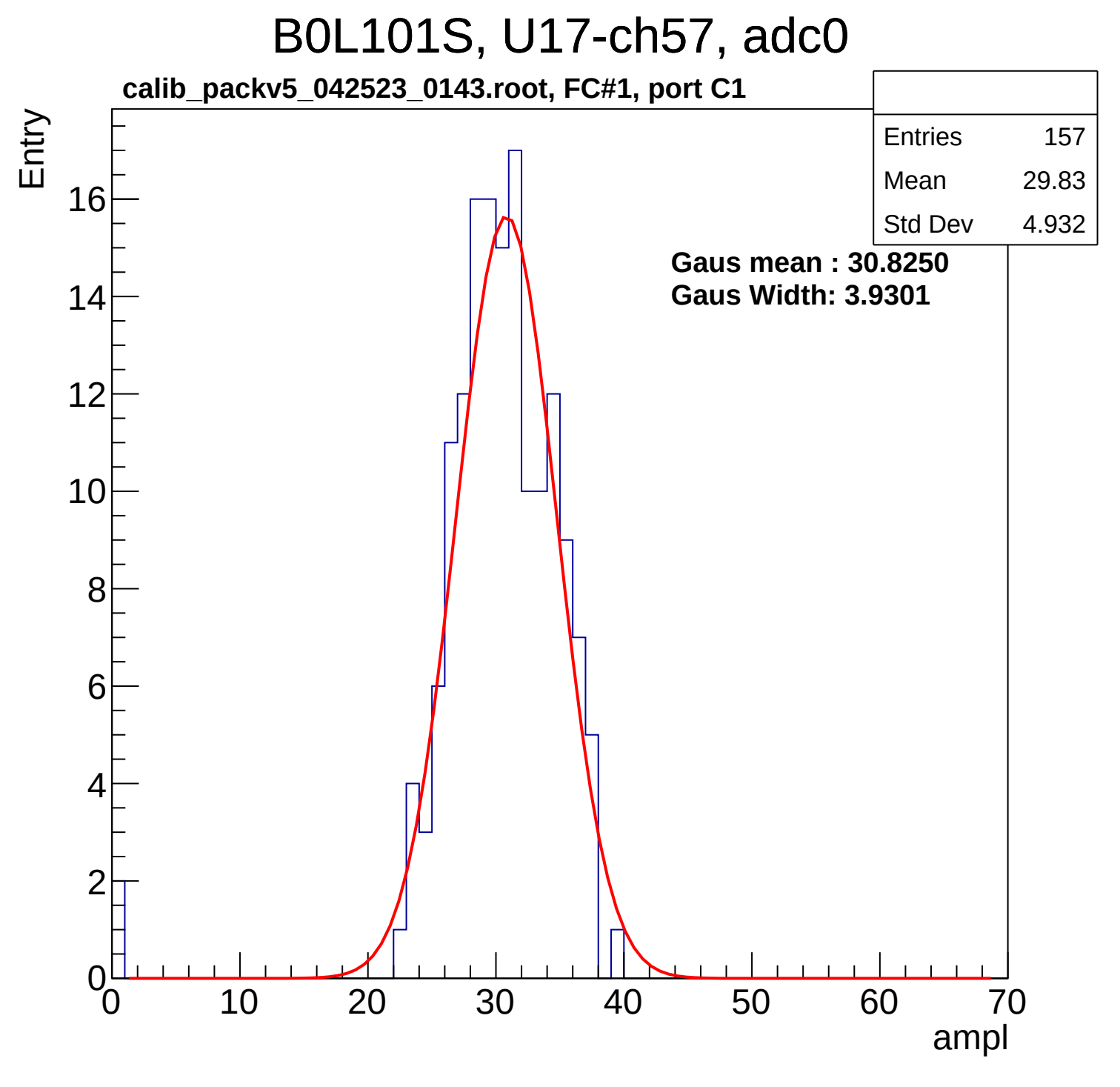

B0L101S, U17-ch56, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 50.61
Std Dev 3.736
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

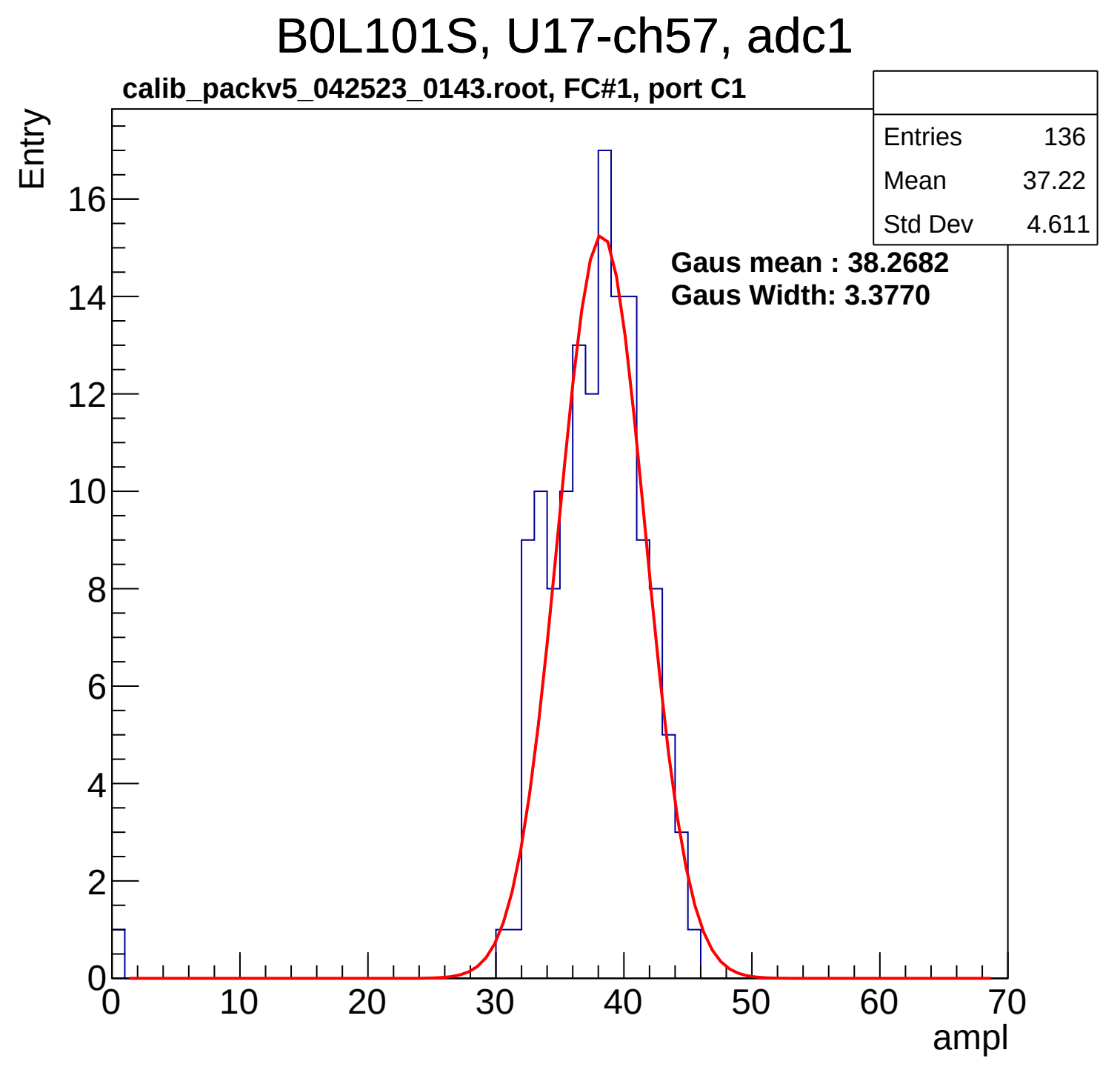

B0L101S, U17-ch56, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 108
Mean 56.41
Std Dev 6.393
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

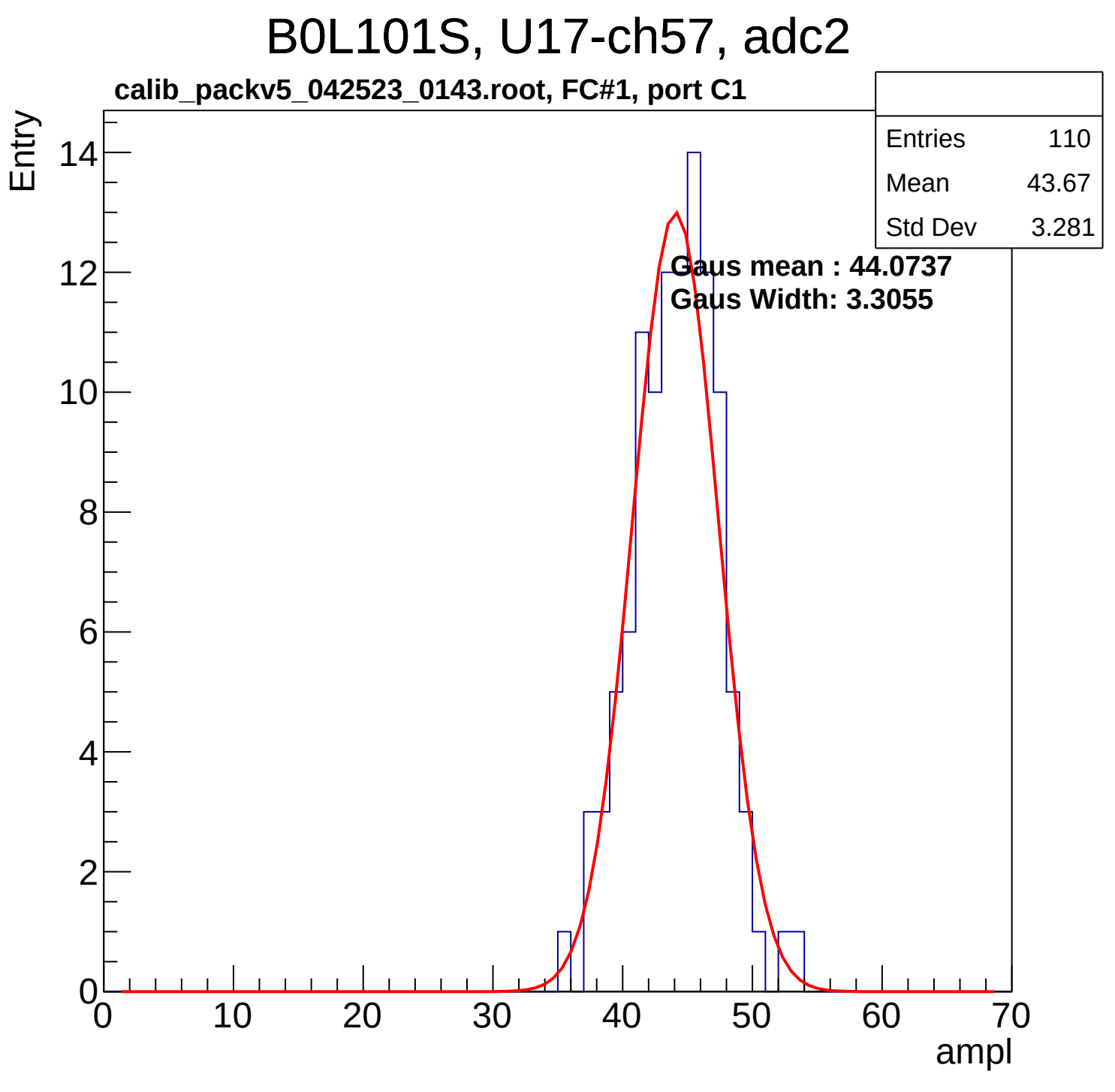

B0L101S, U17-ch56, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 71
Mean 60.41
Std Dev 2.311
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

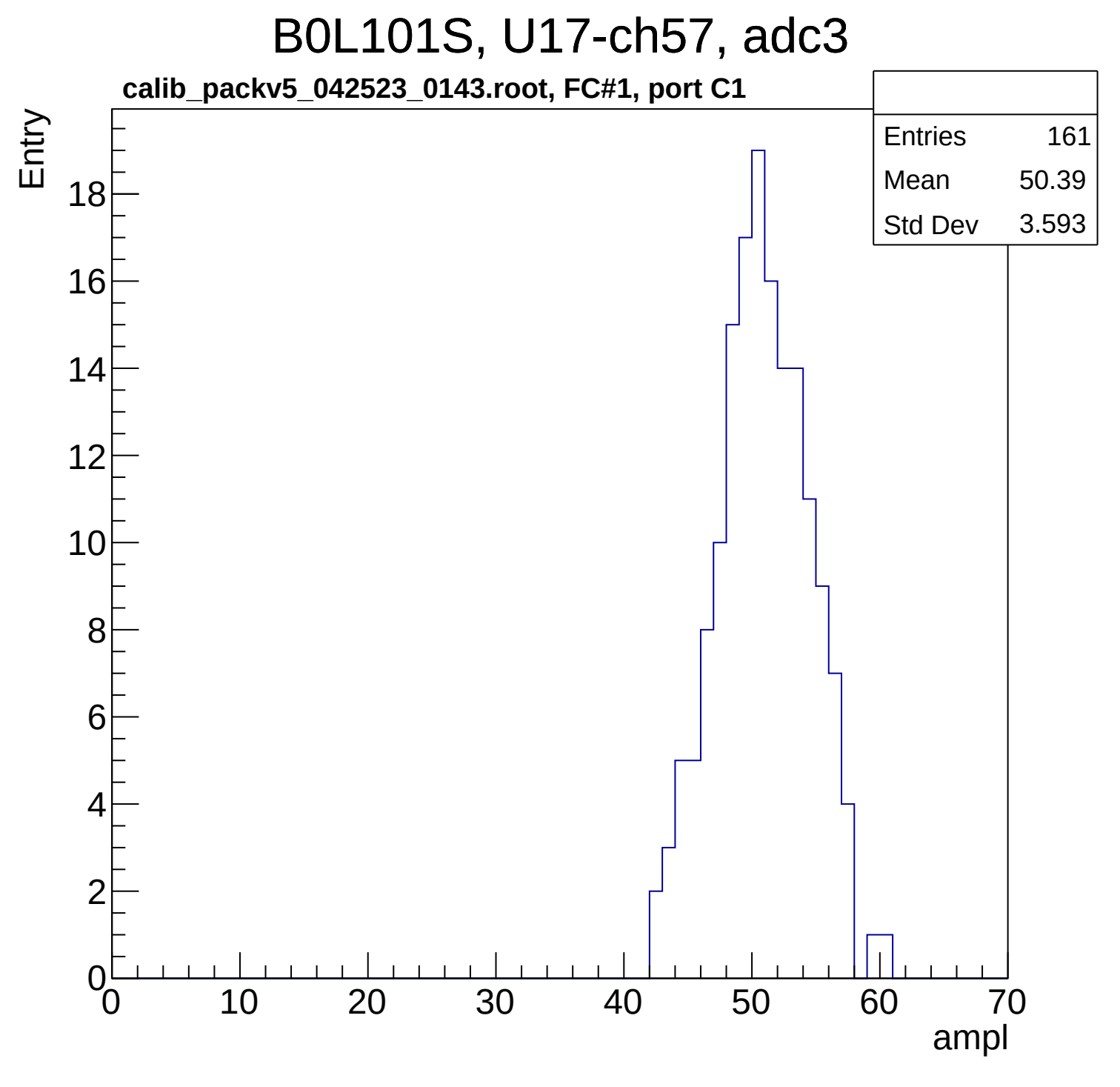

B0L101S, U17-ch56, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

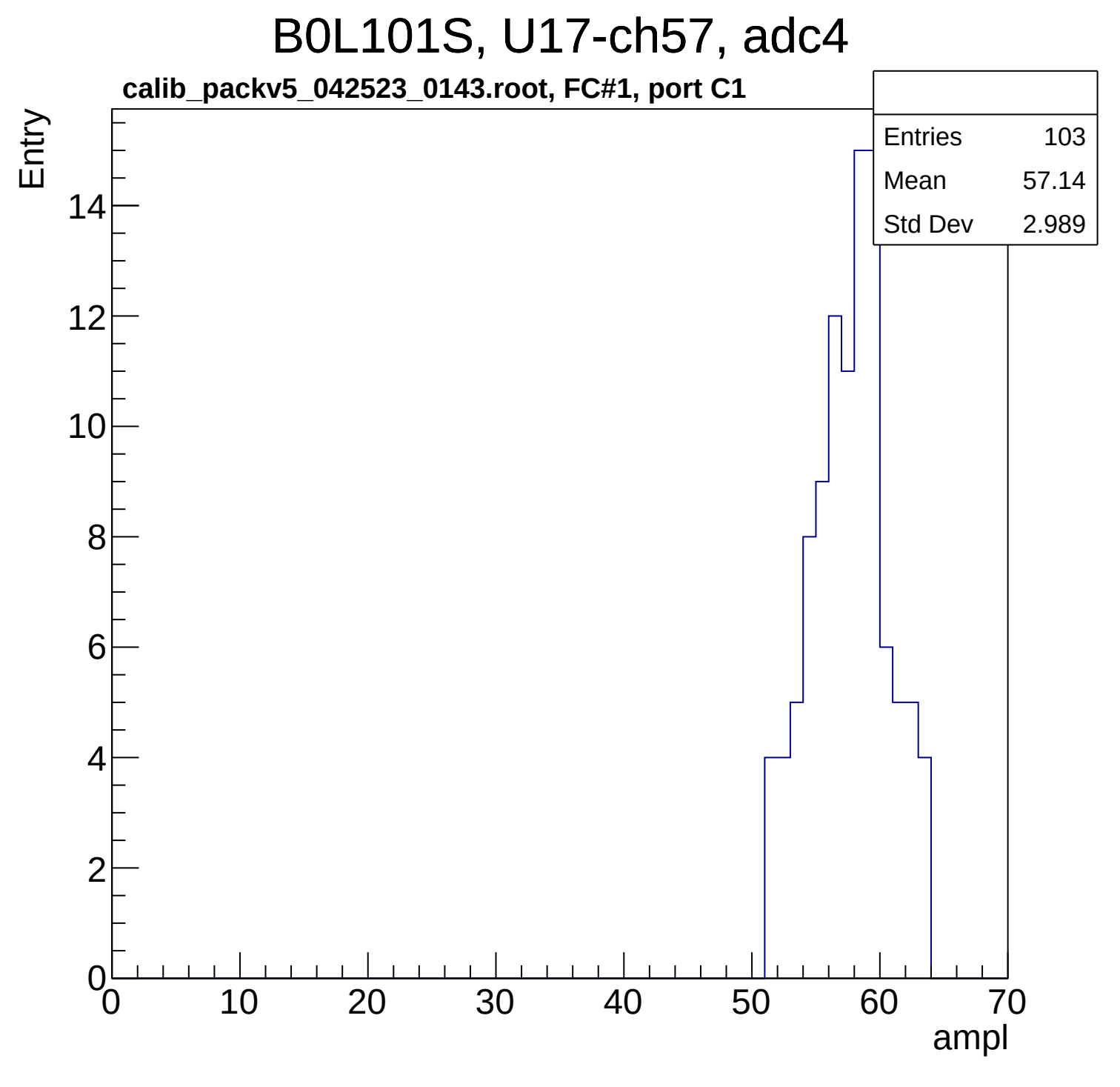

B0L101S, U17-ch56, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 15.25
Std Dev 26.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

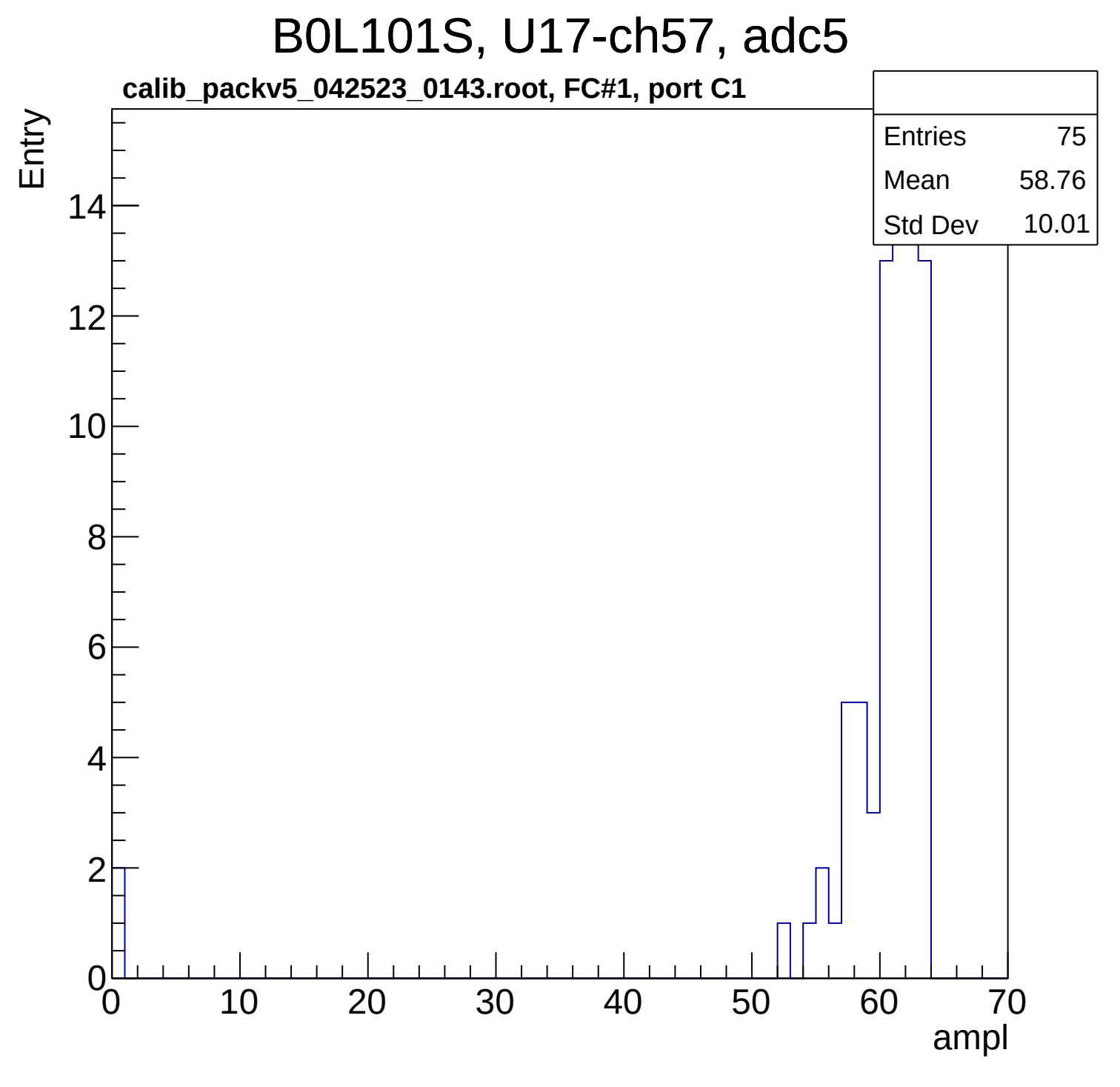

B0L101S, U17-ch57, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 29.83
Std Dev 4.932
Gaus mean : 30.8250
Gaus Width: 3.9301
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

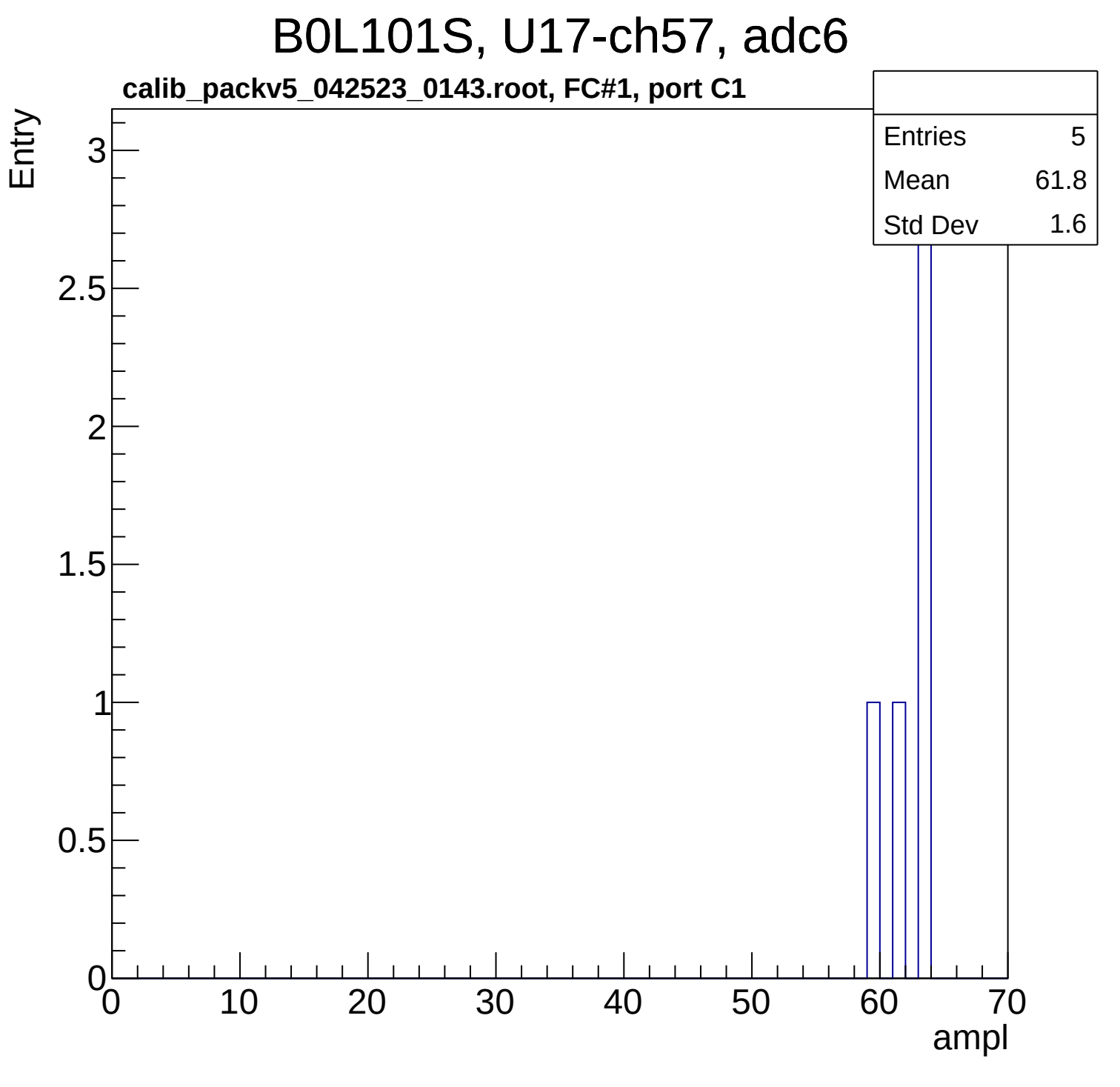

B0L101S, U17-ch57, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 37.22
Std Dev 4.611
Gaus mean : 38.2682
Gaus Width: 3.3770
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

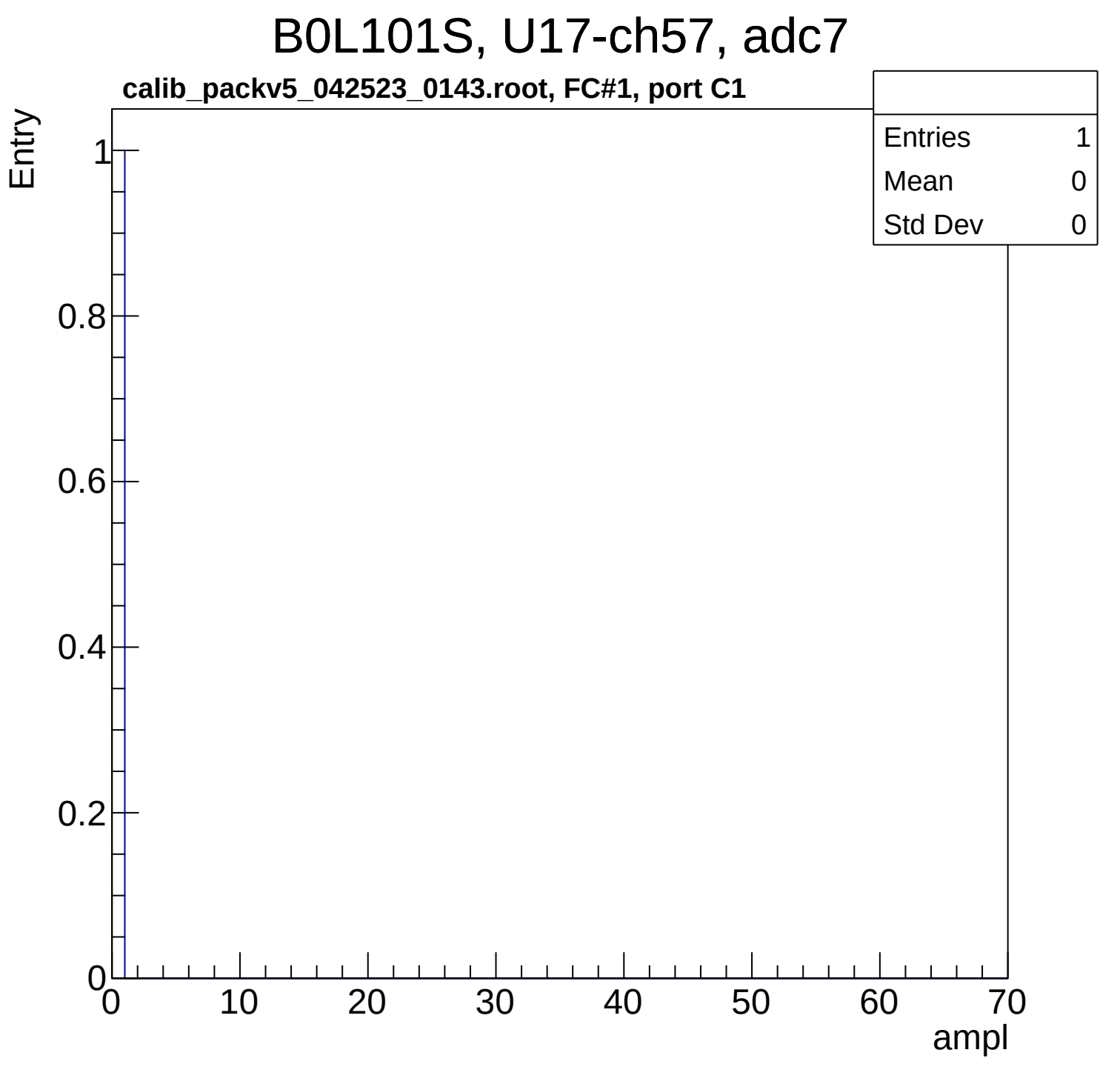

B0L101S, U17-ch57, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 43.67
Std Dev 3.281
Gaus mean : 44.0737
Gaus Width: 3.3055
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

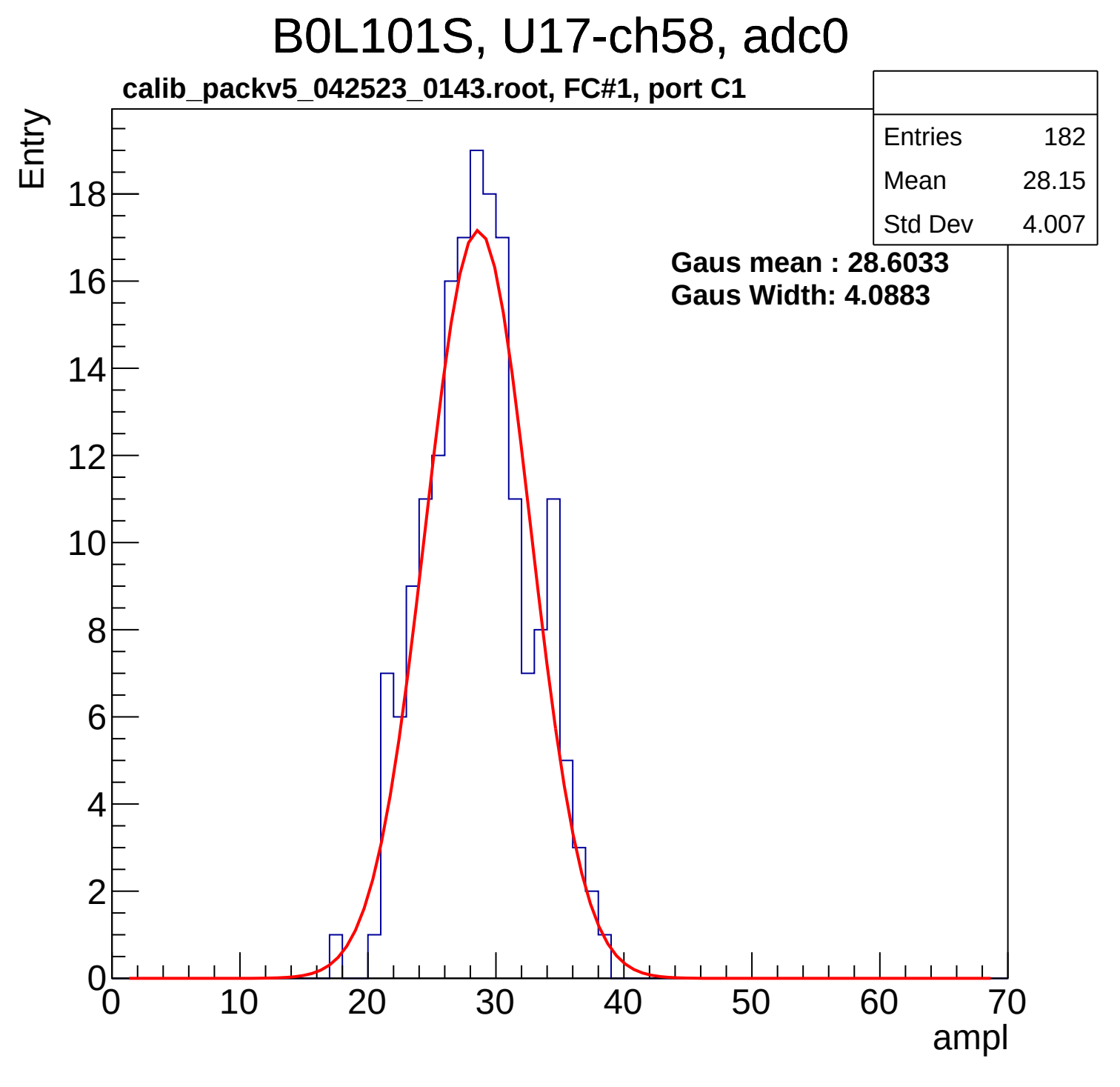

B0L101S, U17-ch57, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 161
Mean 50.39
Std Dev 3.593
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

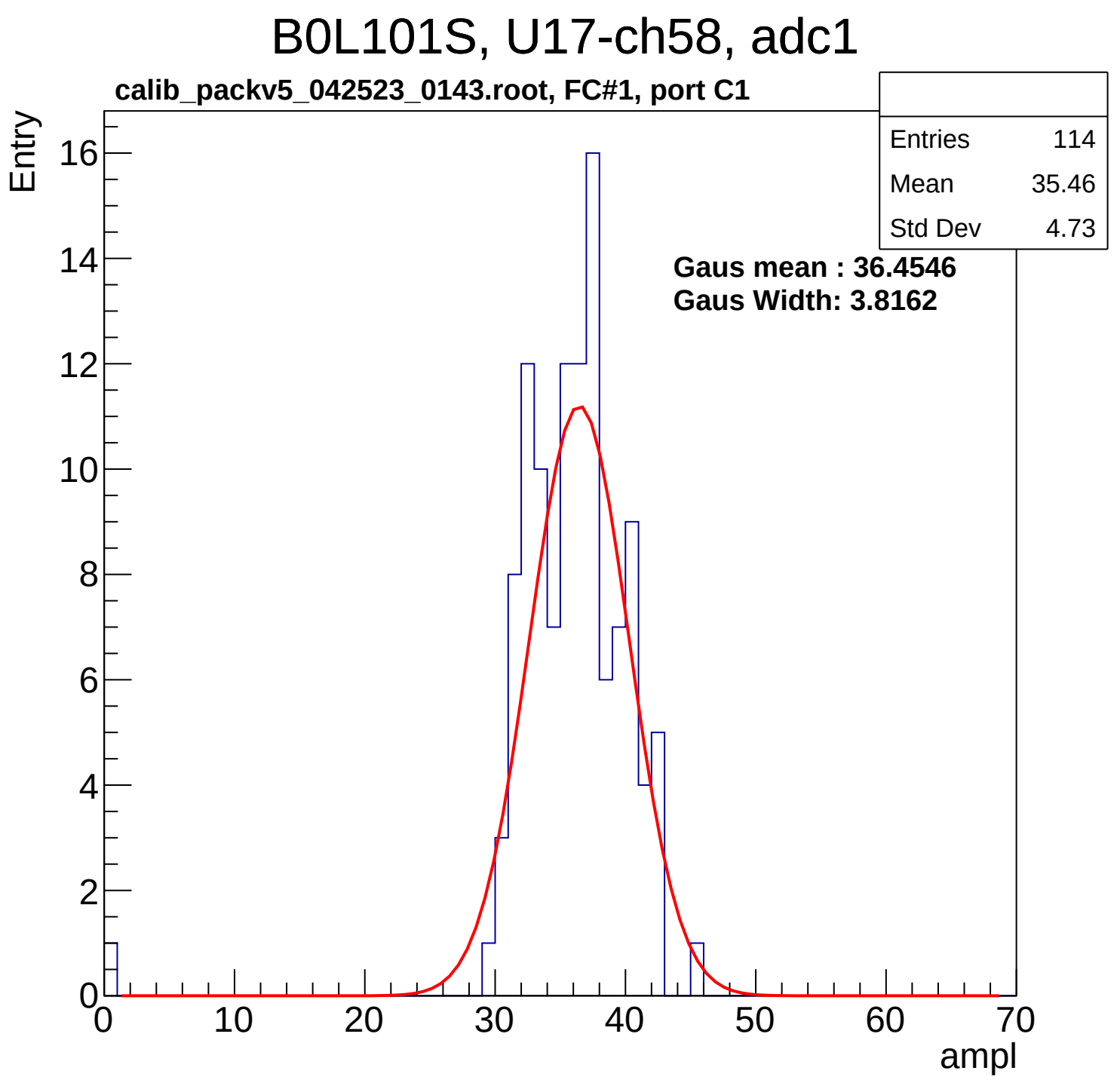

B0L101S, U17-ch57, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 57.14
Std Dev 2.989
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

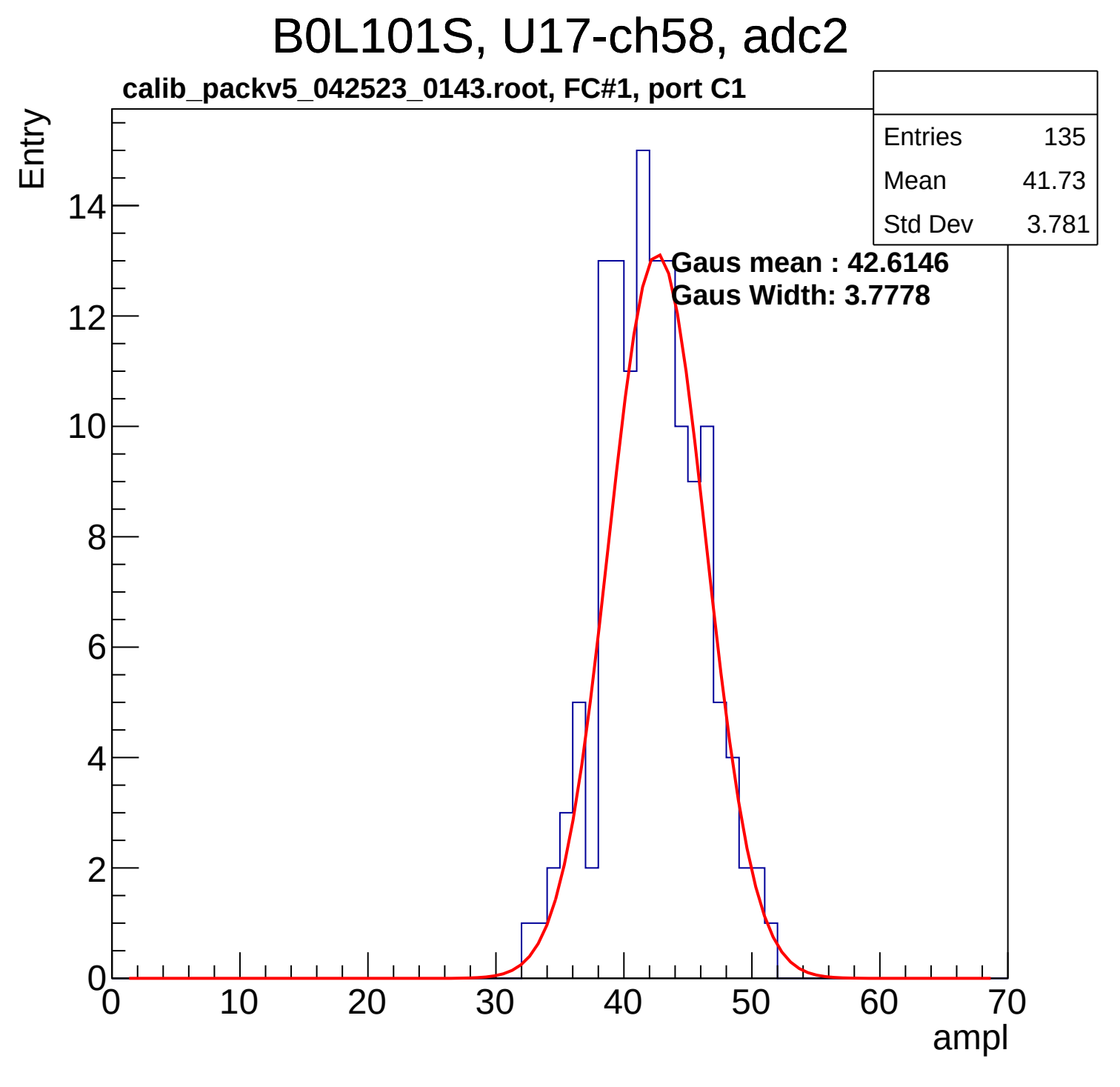

B0L101S, U17-ch57, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 75
Mean 58.76
Std Dev 10.01
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

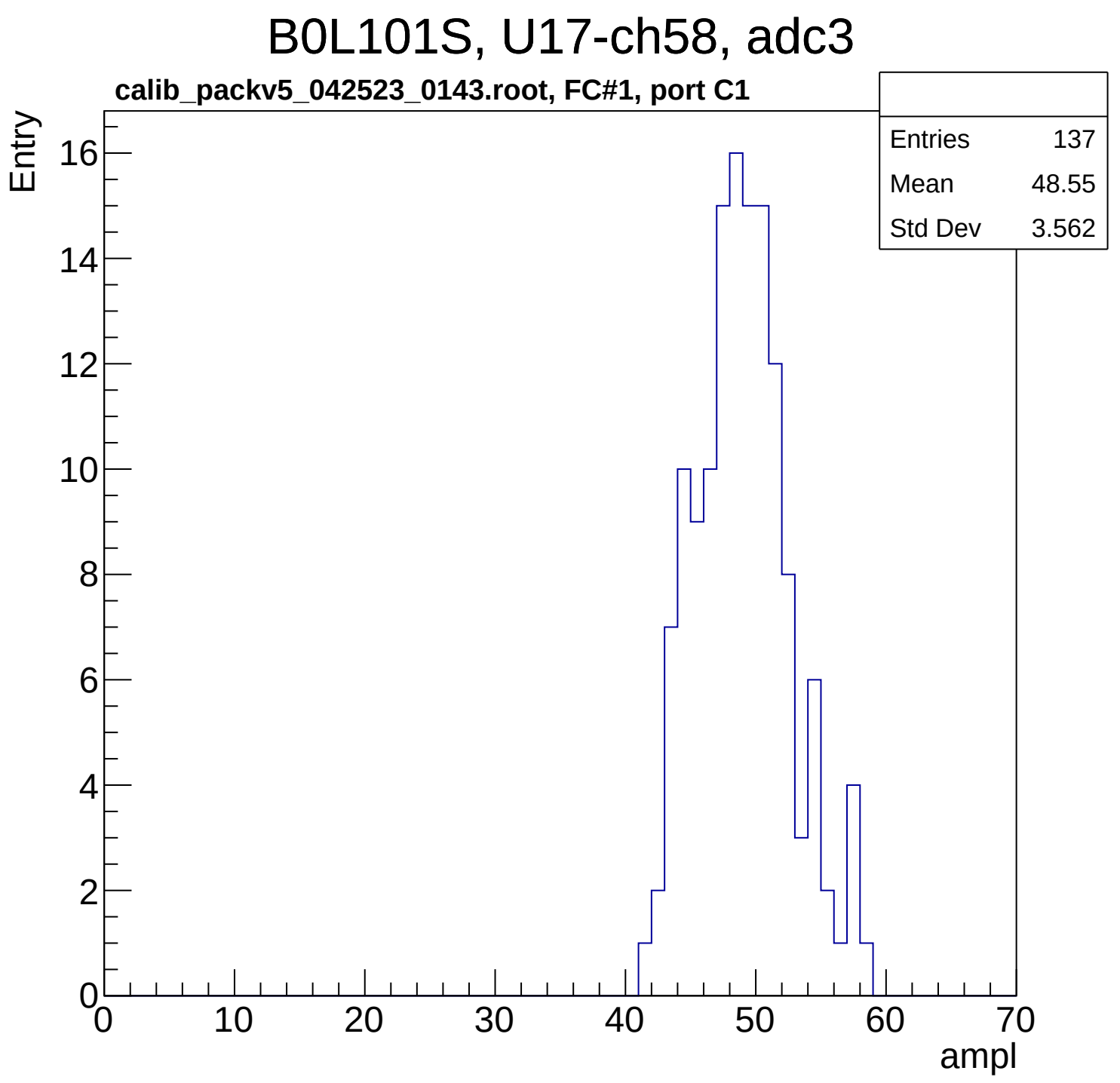

B0L101S, U17-ch57, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 61.8
Std Dev 1.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

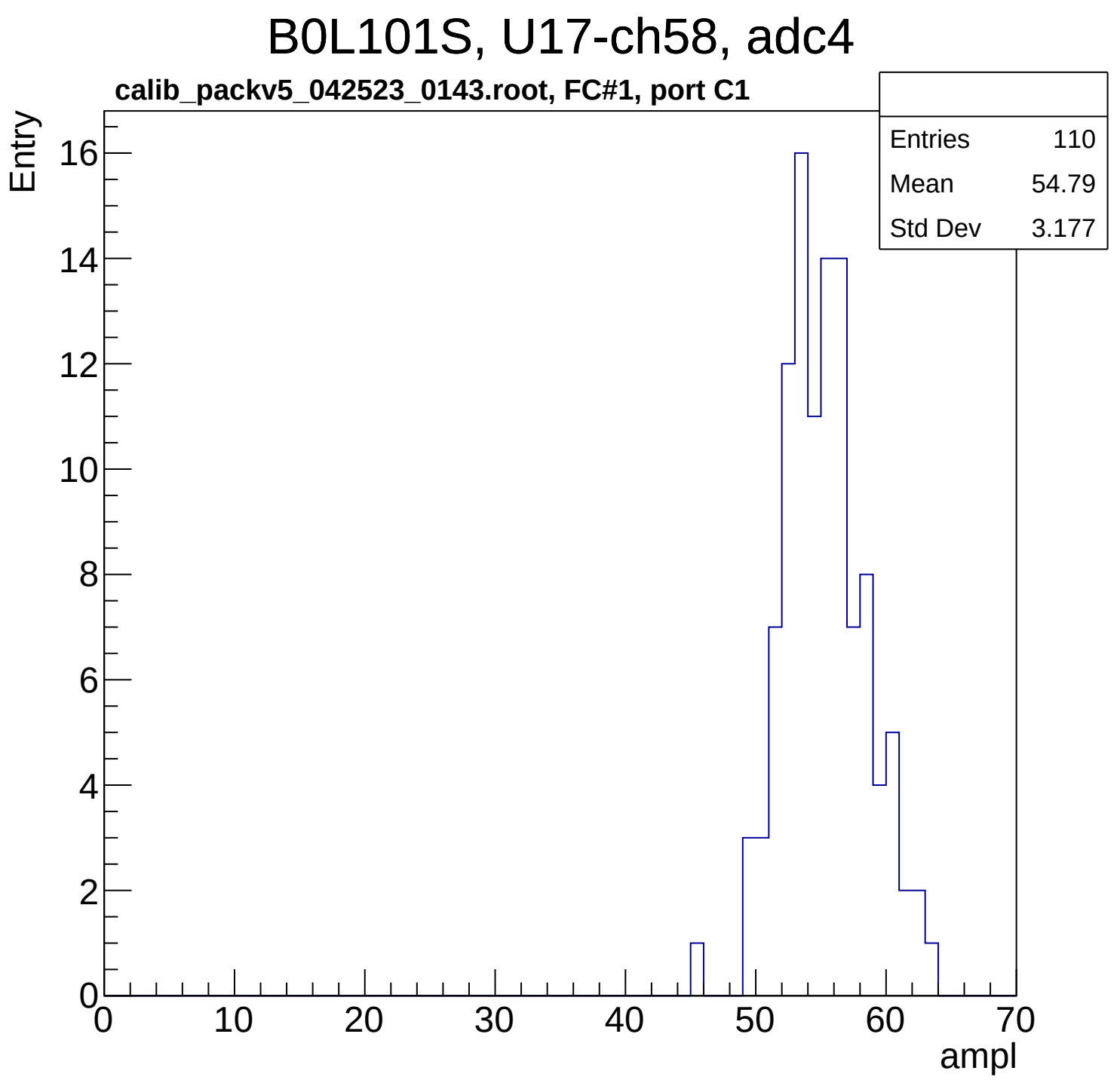

B0L101S, U17-ch57, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

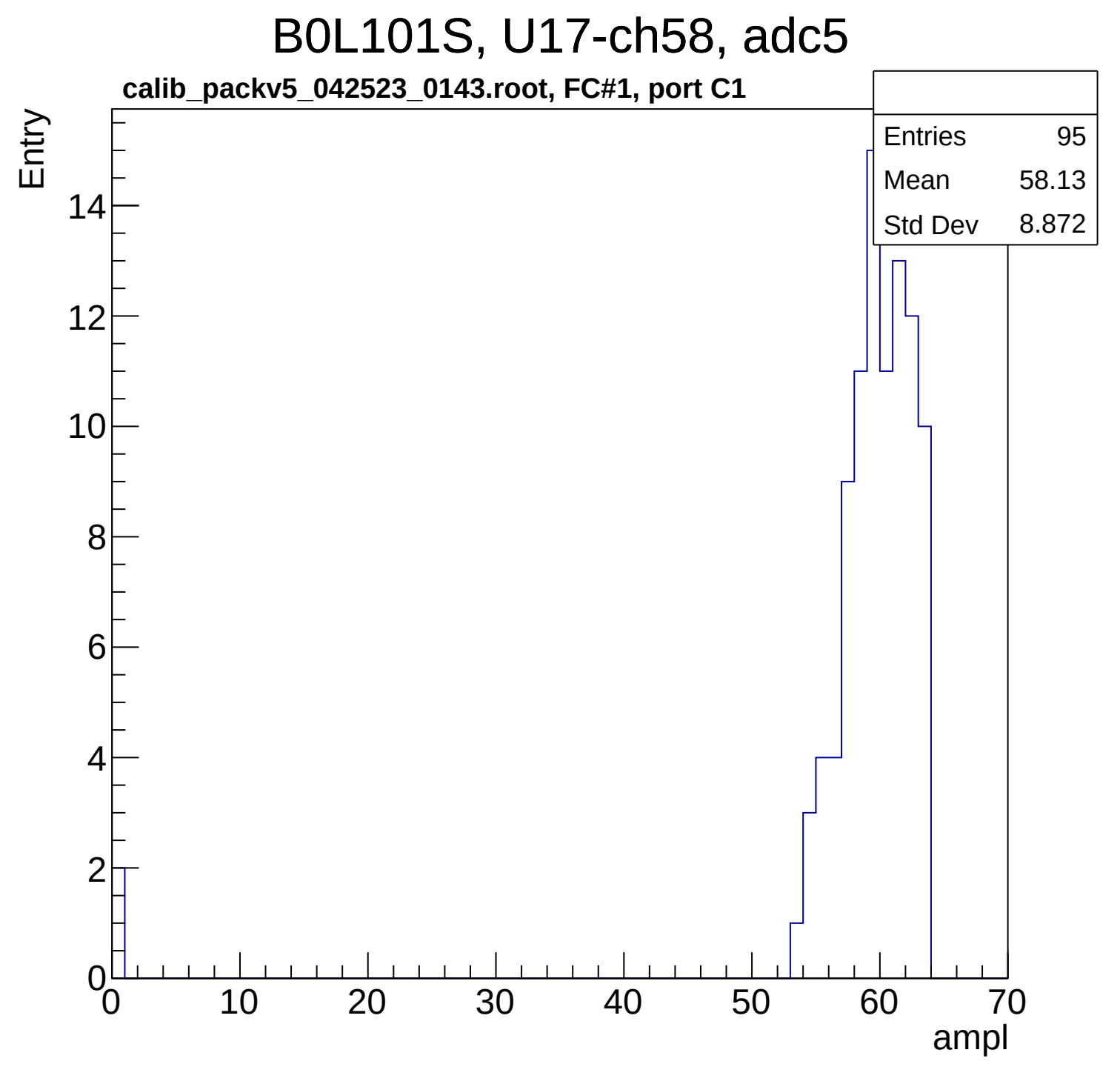

B0L101S, U17-ch58, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 182
Mean 28.15
Std Dev 4.007
Gaus mean : 28.6033
Gaus Width: 4.0883
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

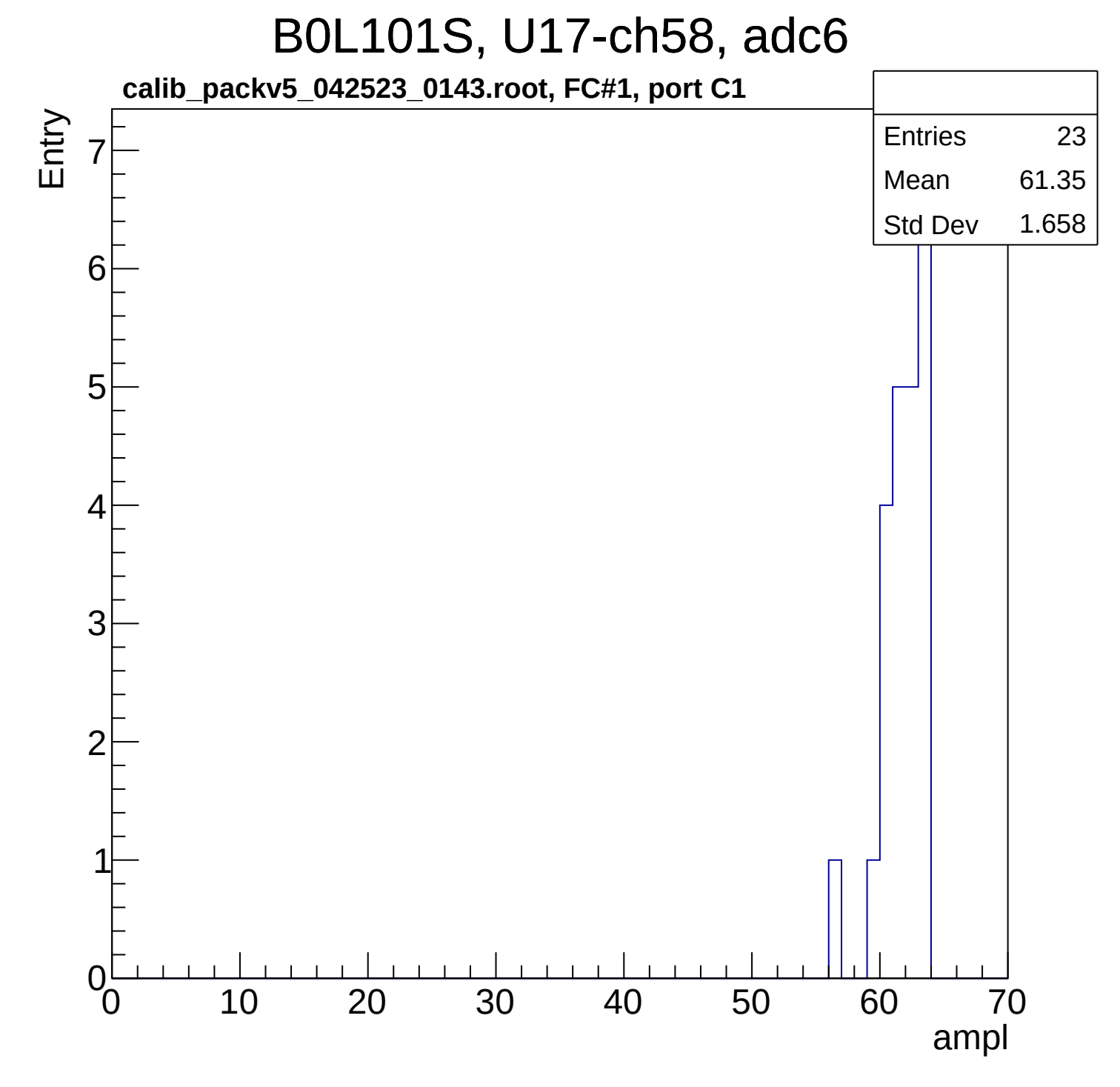

B0L101S, U17-ch58, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 35.46
Std Dev 4.73
Gaus mean : 36.4546
Gaus Width: 3.8162
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

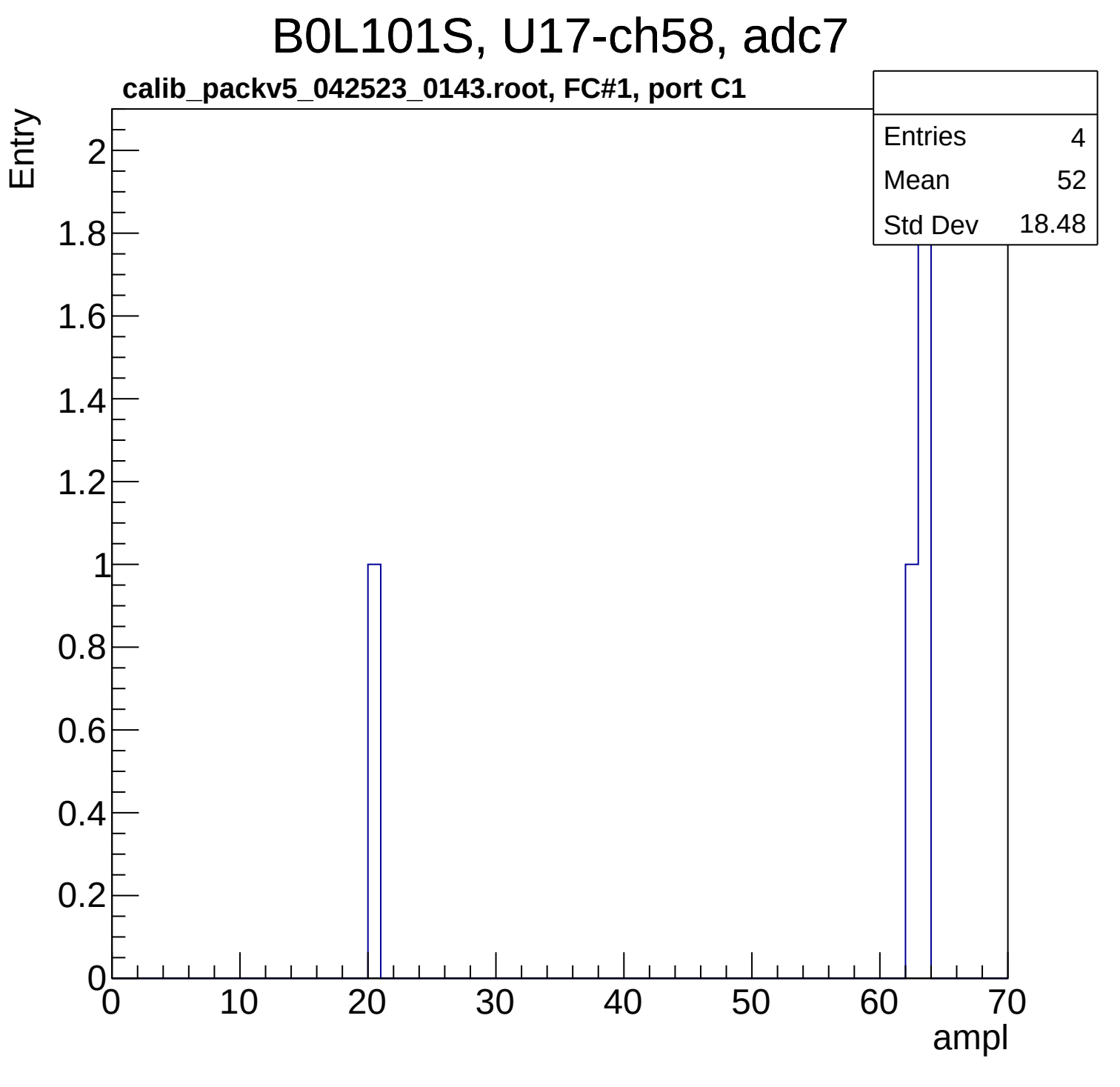

B0L101S, U17-ch58, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 41.73
Std Dev 3.781
Gaus mean : 42.6146
Gaus Width: 3.7778
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

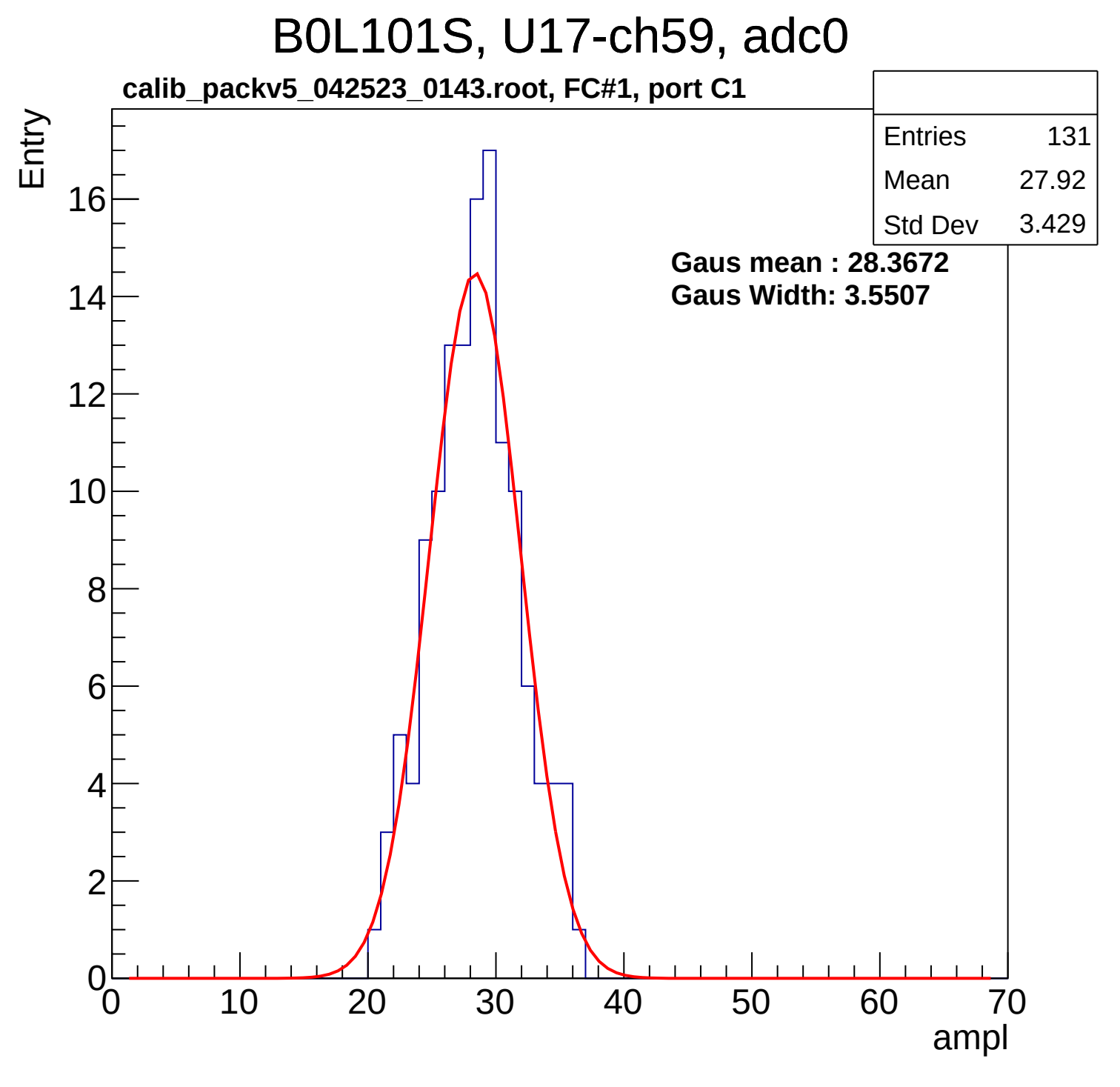

B0L101S, U17-ch58, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 48.55
Std Dev 3.562
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

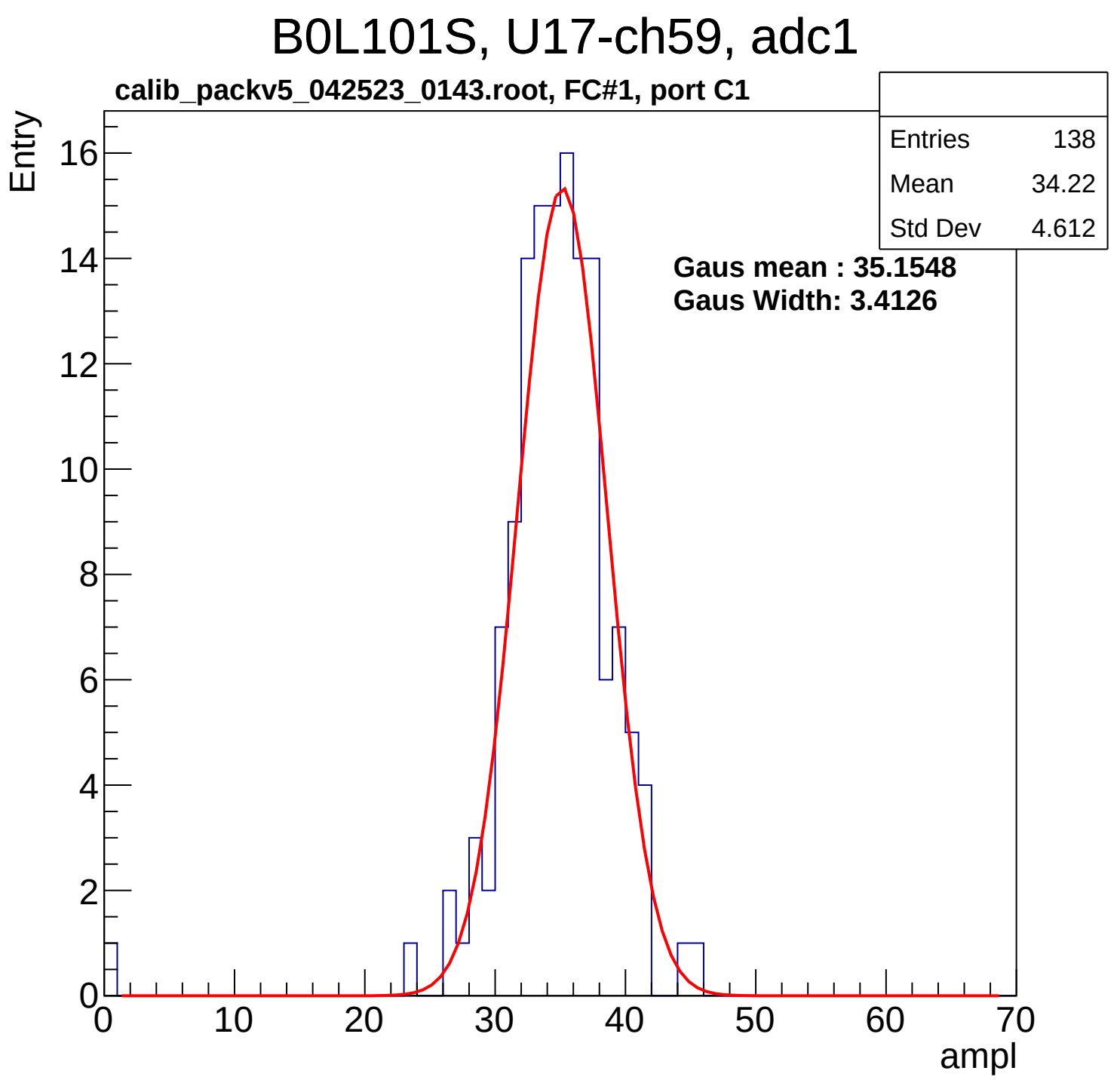

B0L101S, U17-ch58, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 54.79
Std Dev 3.177
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

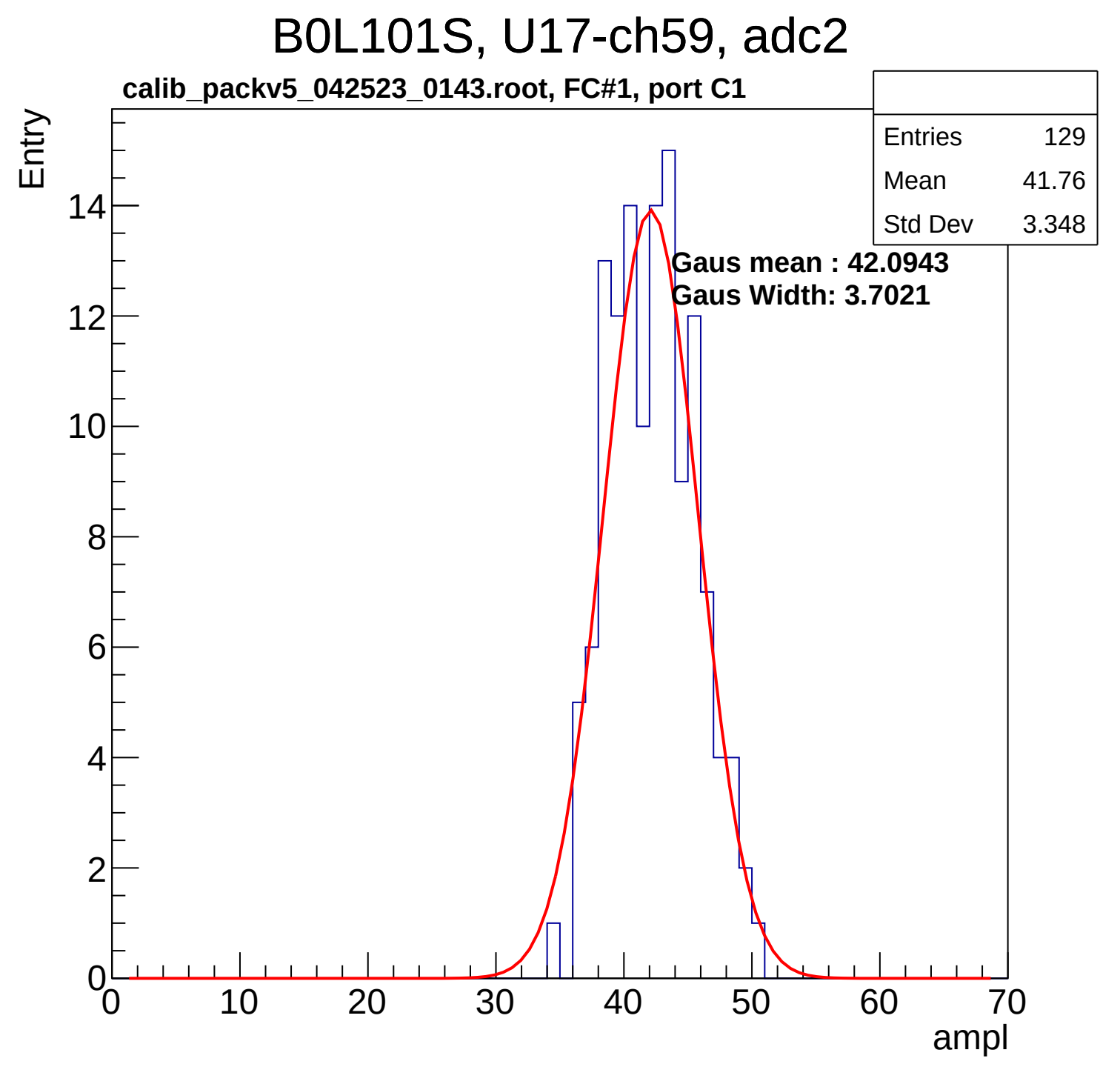

B0L101S, U17-ch58, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 95
Mean 58.13
Std Dev 8.872
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

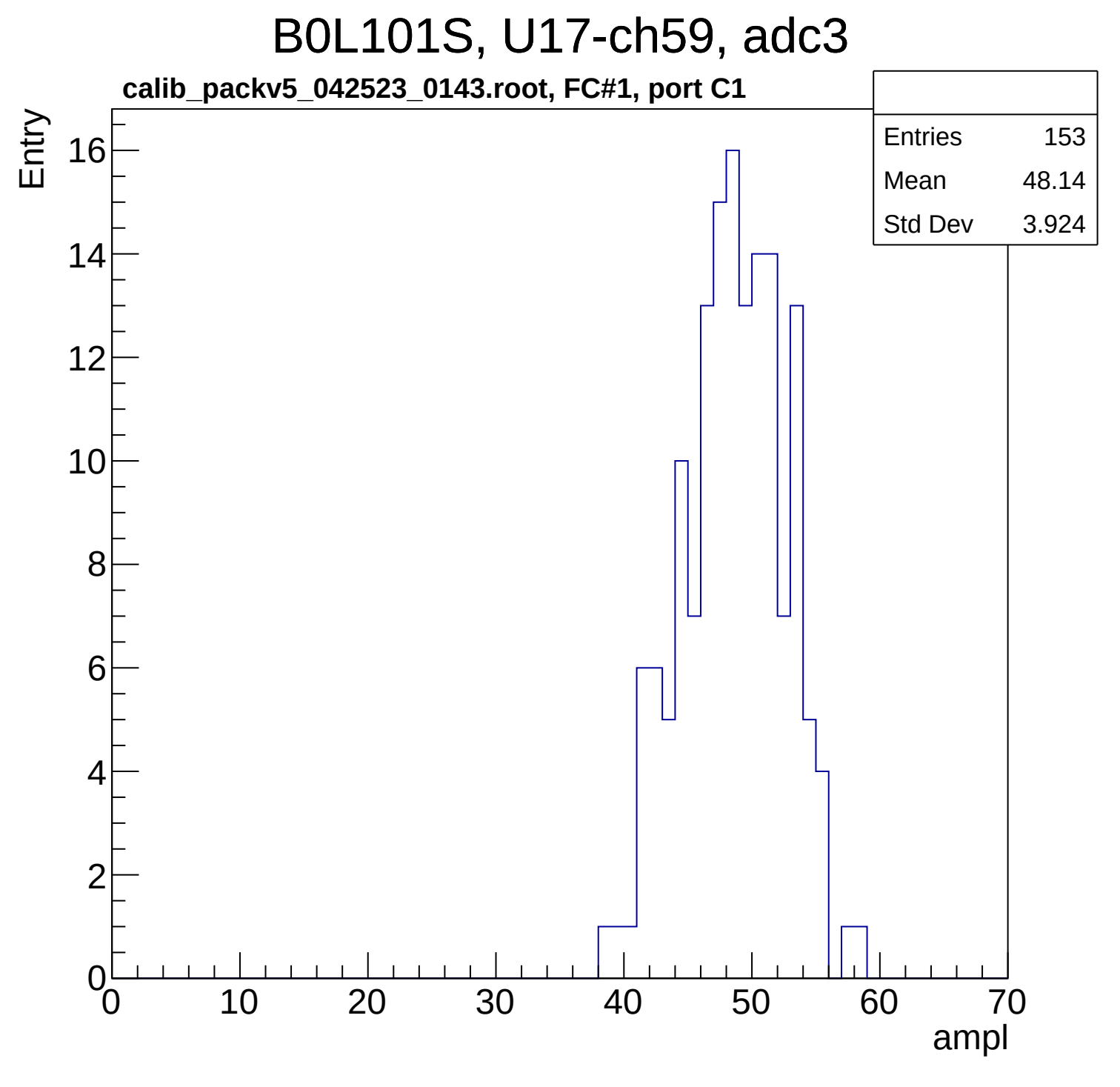

B0L101S, U17-ch58, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 23
Mean 61.35
Std Dev 1.658
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

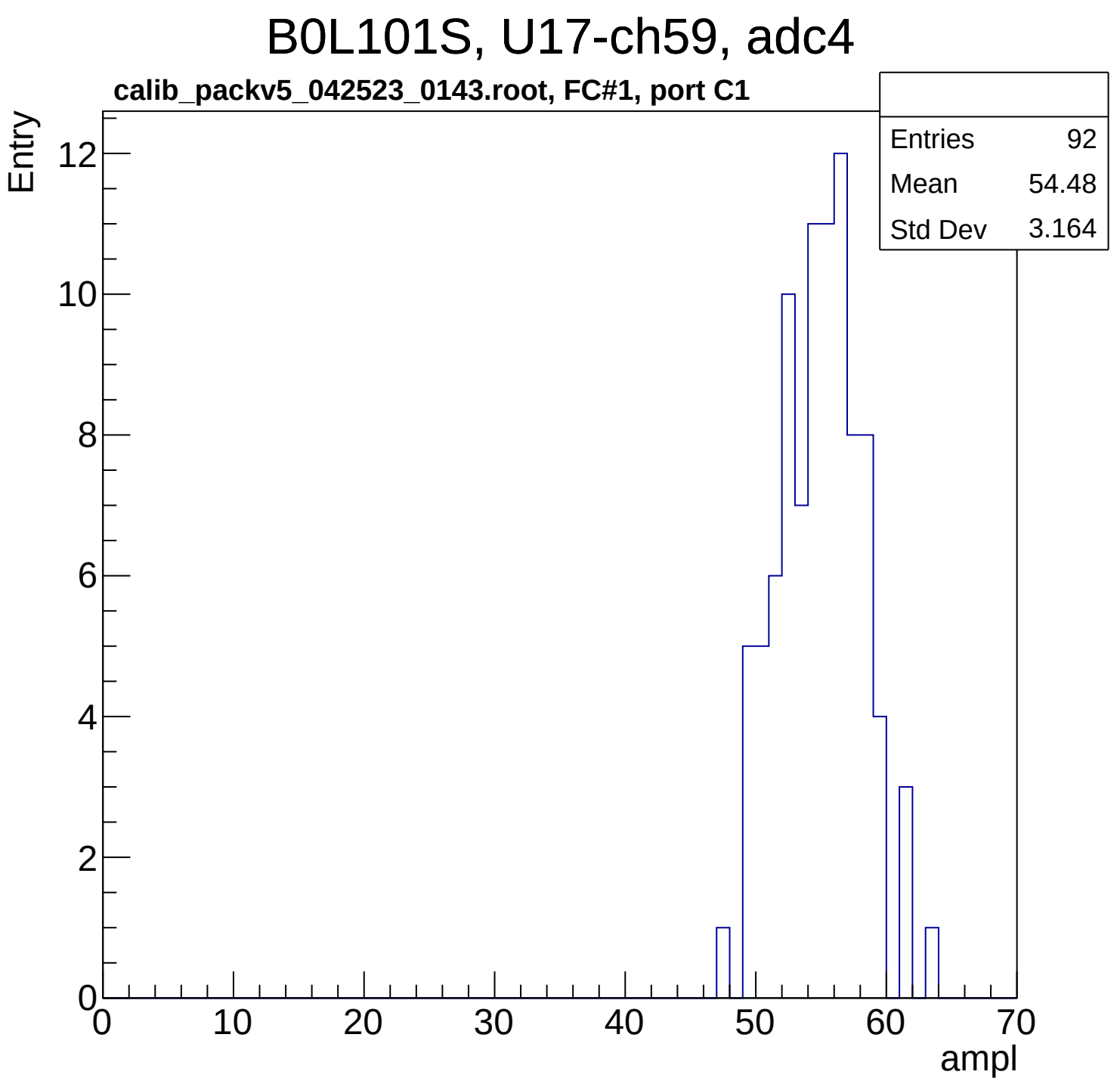

B0L101S, U17-ch58, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 52
Std Dev 18.48
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

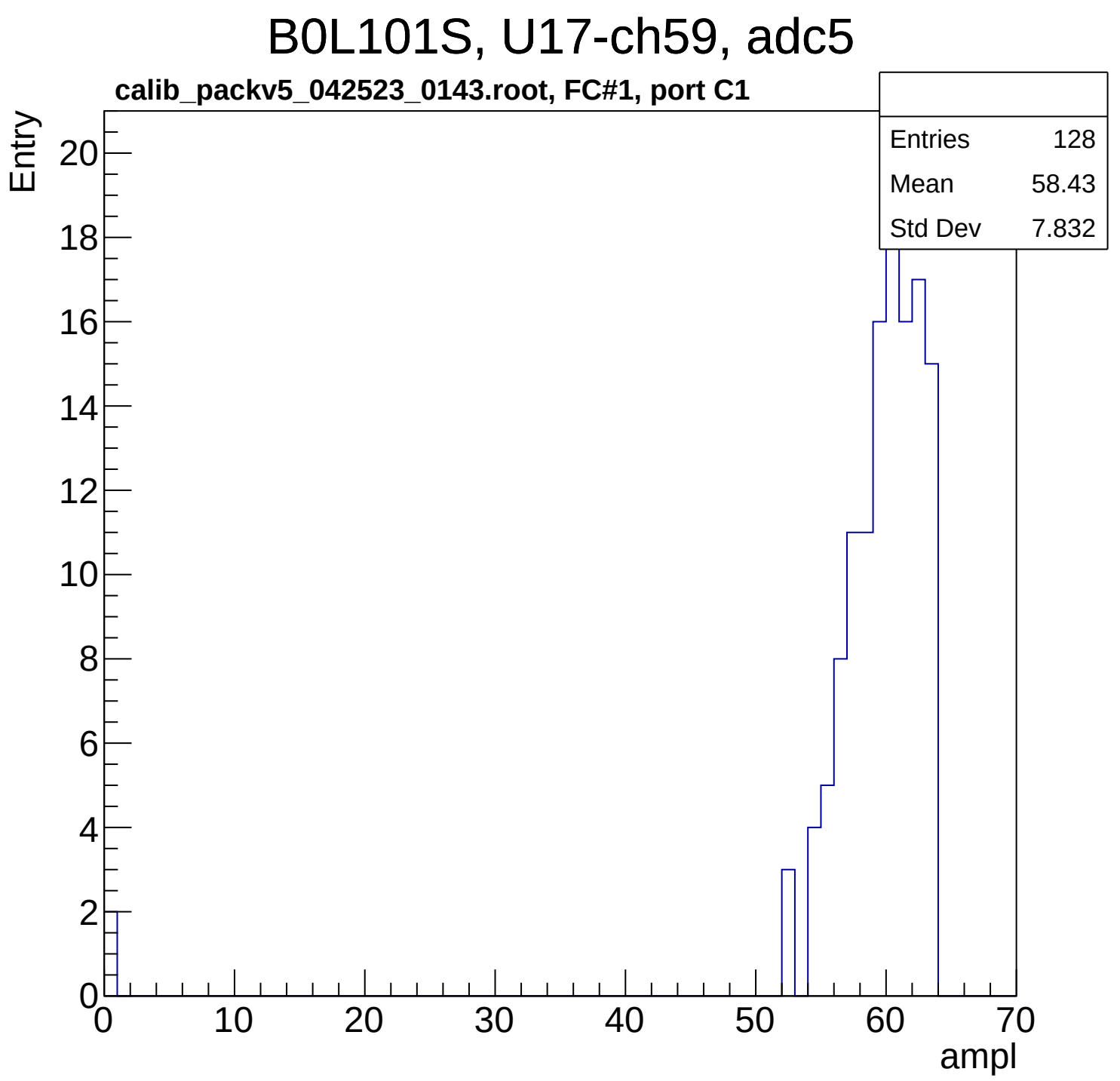

B0L101S, U17-ch59, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 27.92
Std Dev 3.429
Gaus mean : 28.3672
Gaus Width: 3.5507
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

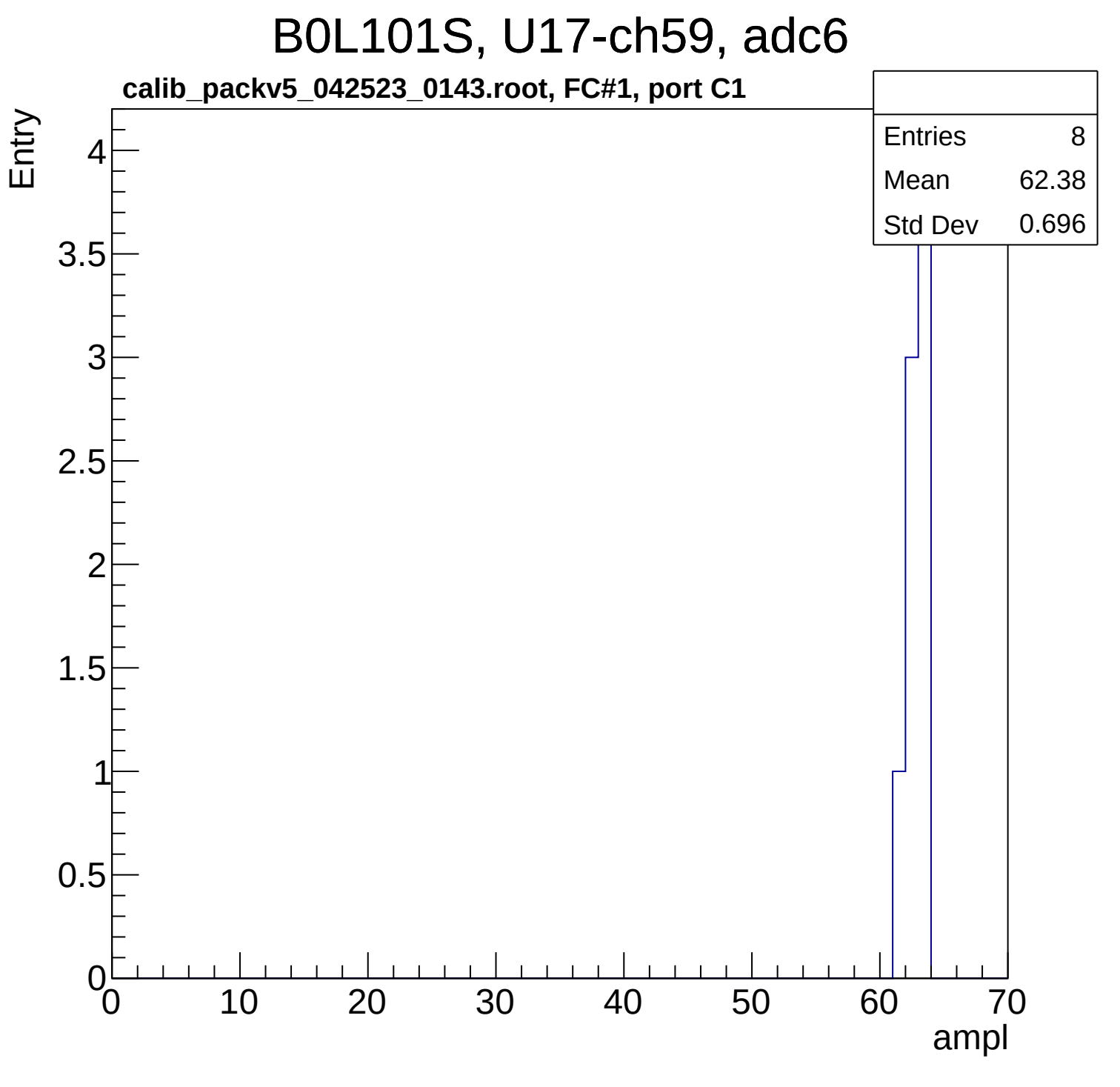

B0L101S, U17-ch59, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 34.22
Std Dev 4.612
Gaus mean : 35.1548
Gaus Width: 3.4126
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

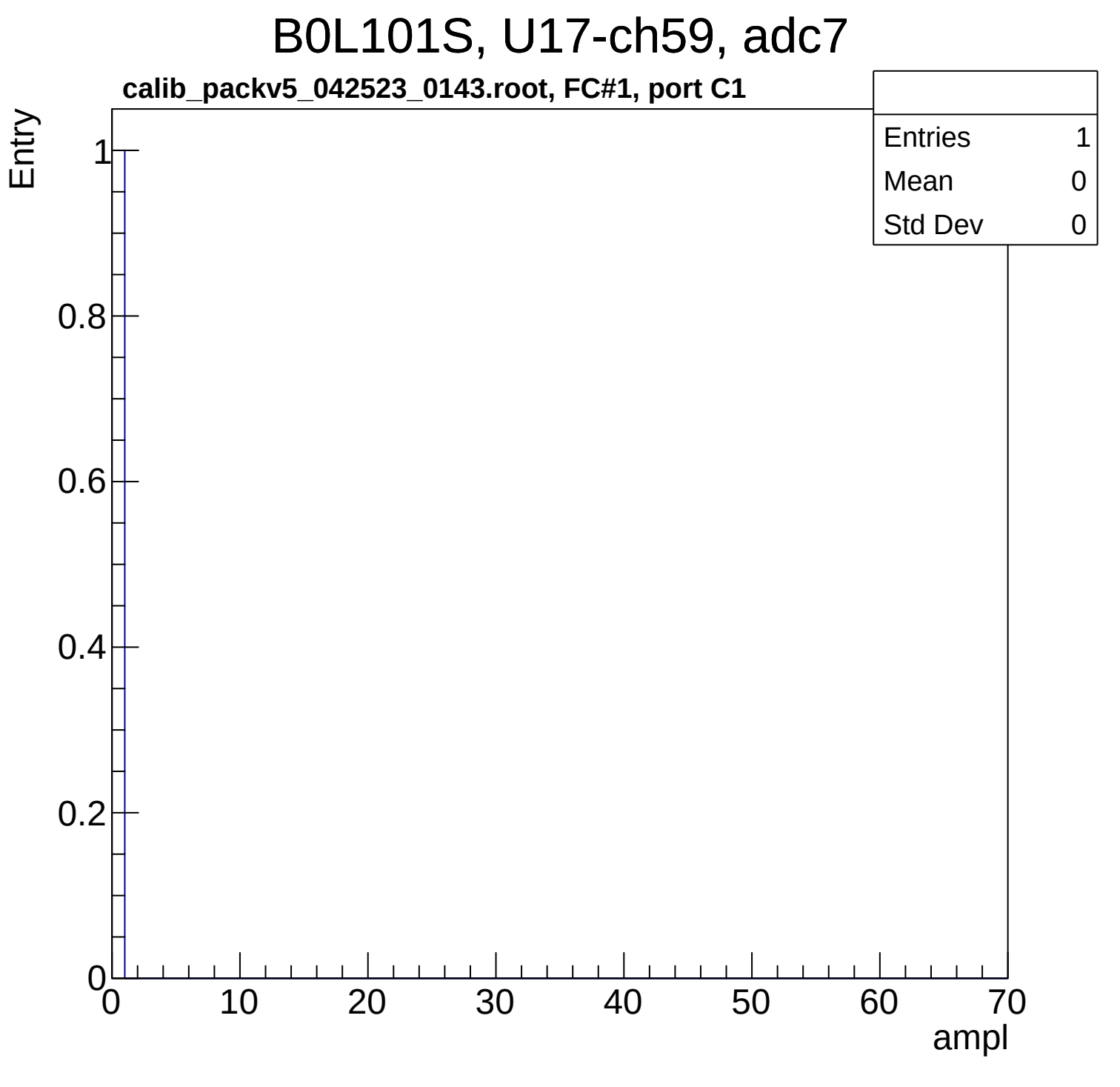

B0L101S, U17-ch59, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 41.76
Std Dev 3.348
Gaus mean : 42.0943
Gaus Width: 3.7021
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

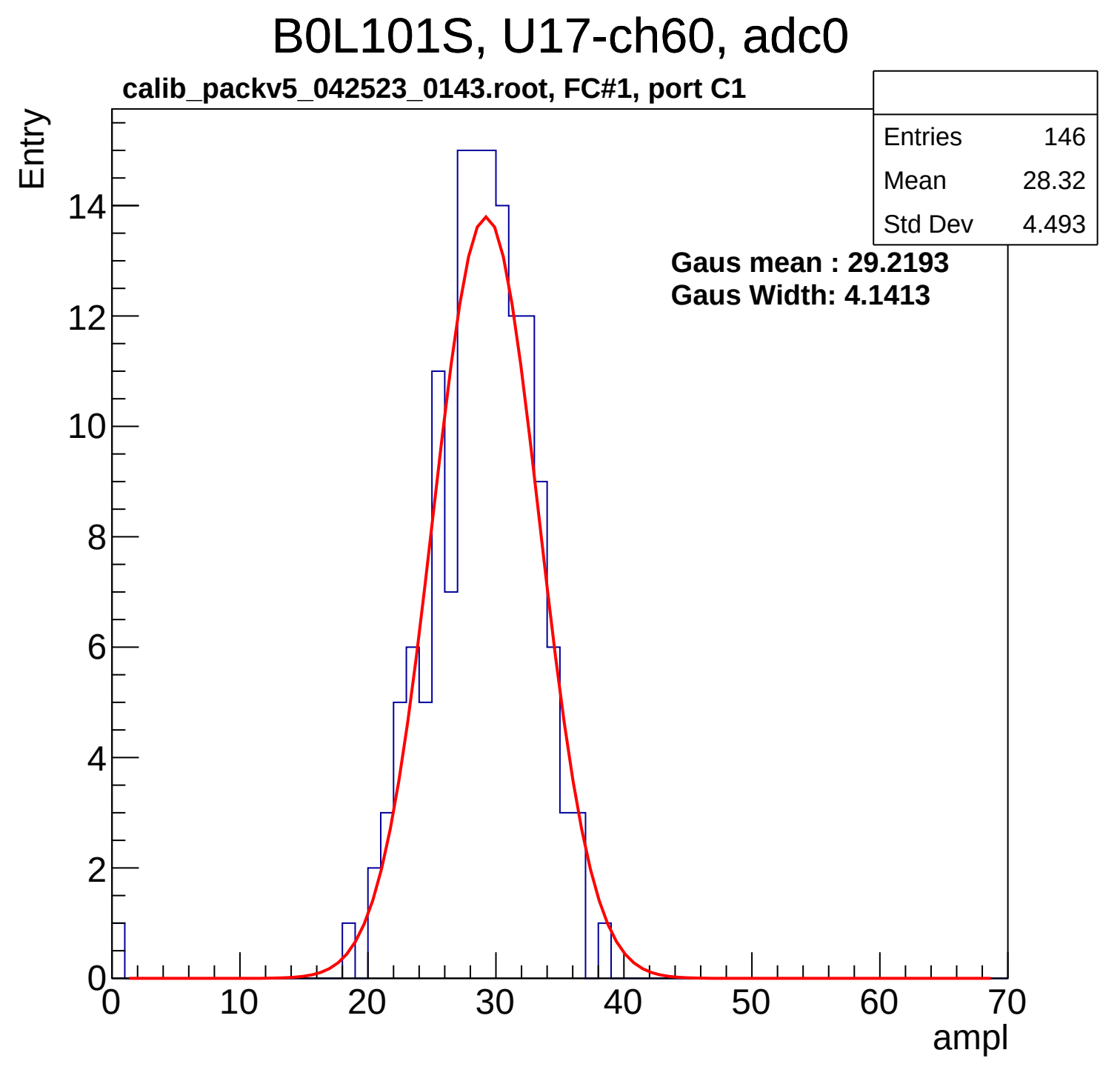

B0L101S, U17-ch59, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 48.14
Std Dev 3.924
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

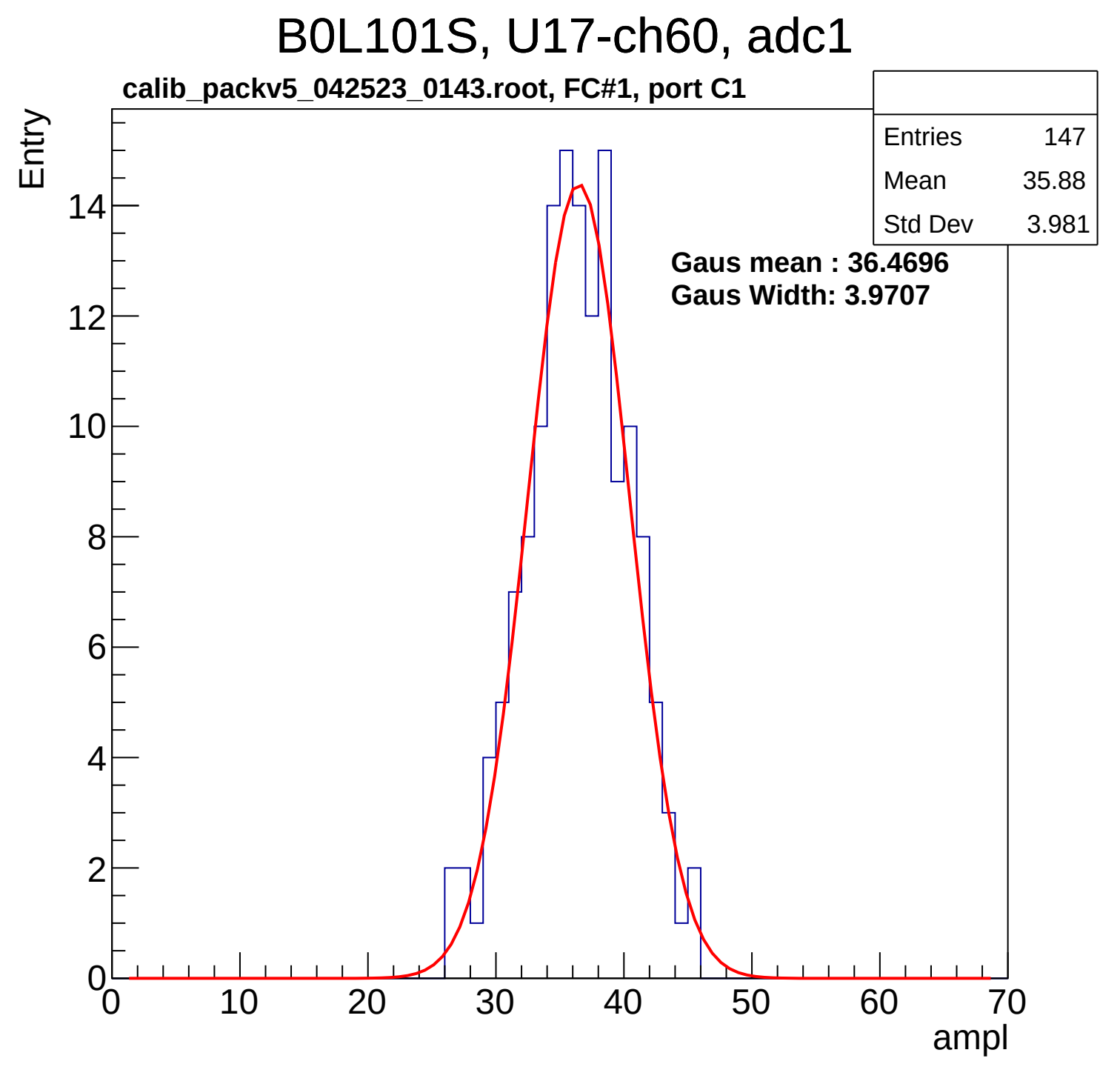

B0L101S, U17-ch59, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 92
Mean 54.48
Std Dev 3.164
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

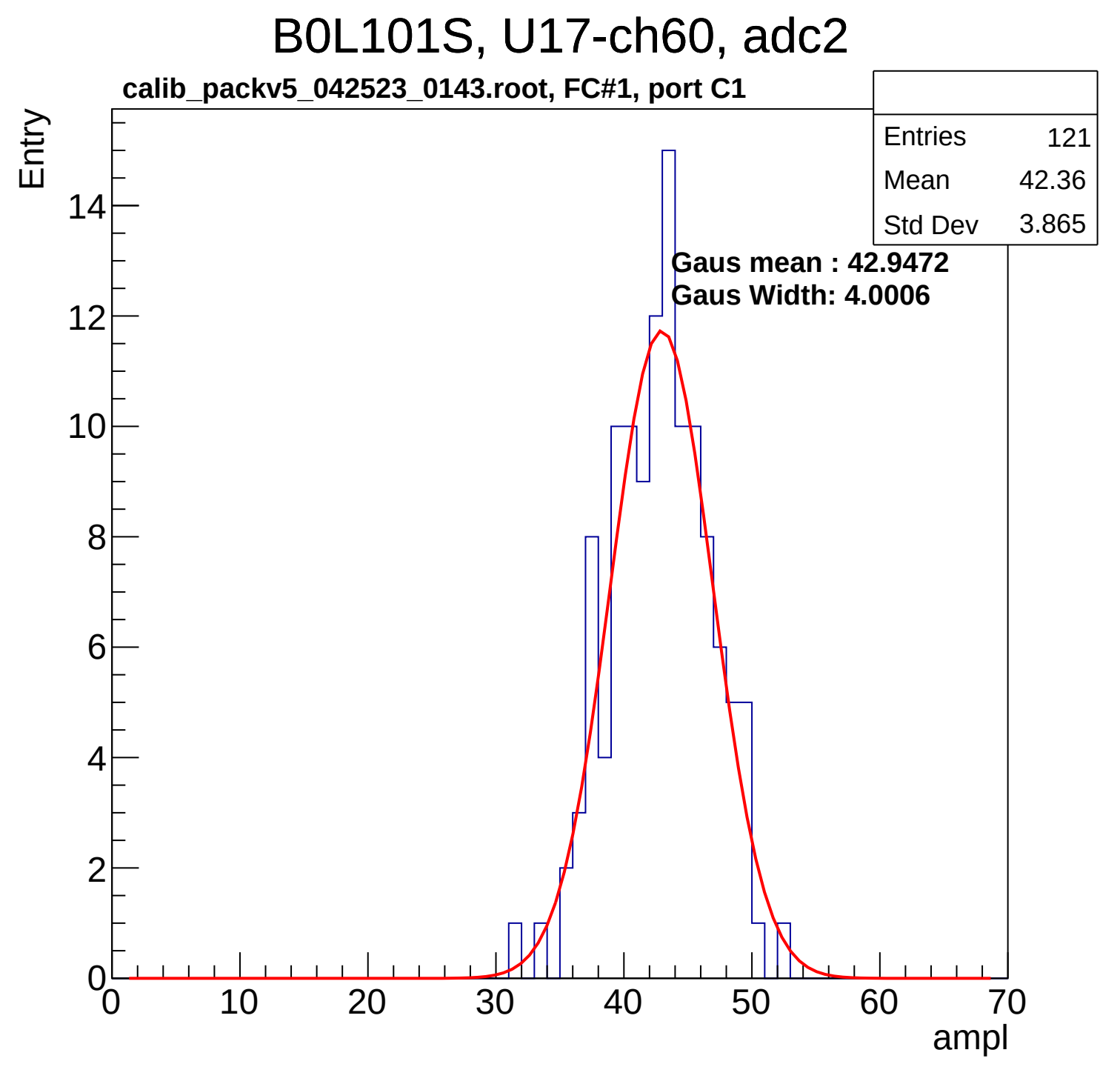

B0L101S, U17-ch59, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 58.43
Std Dev 7.832
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

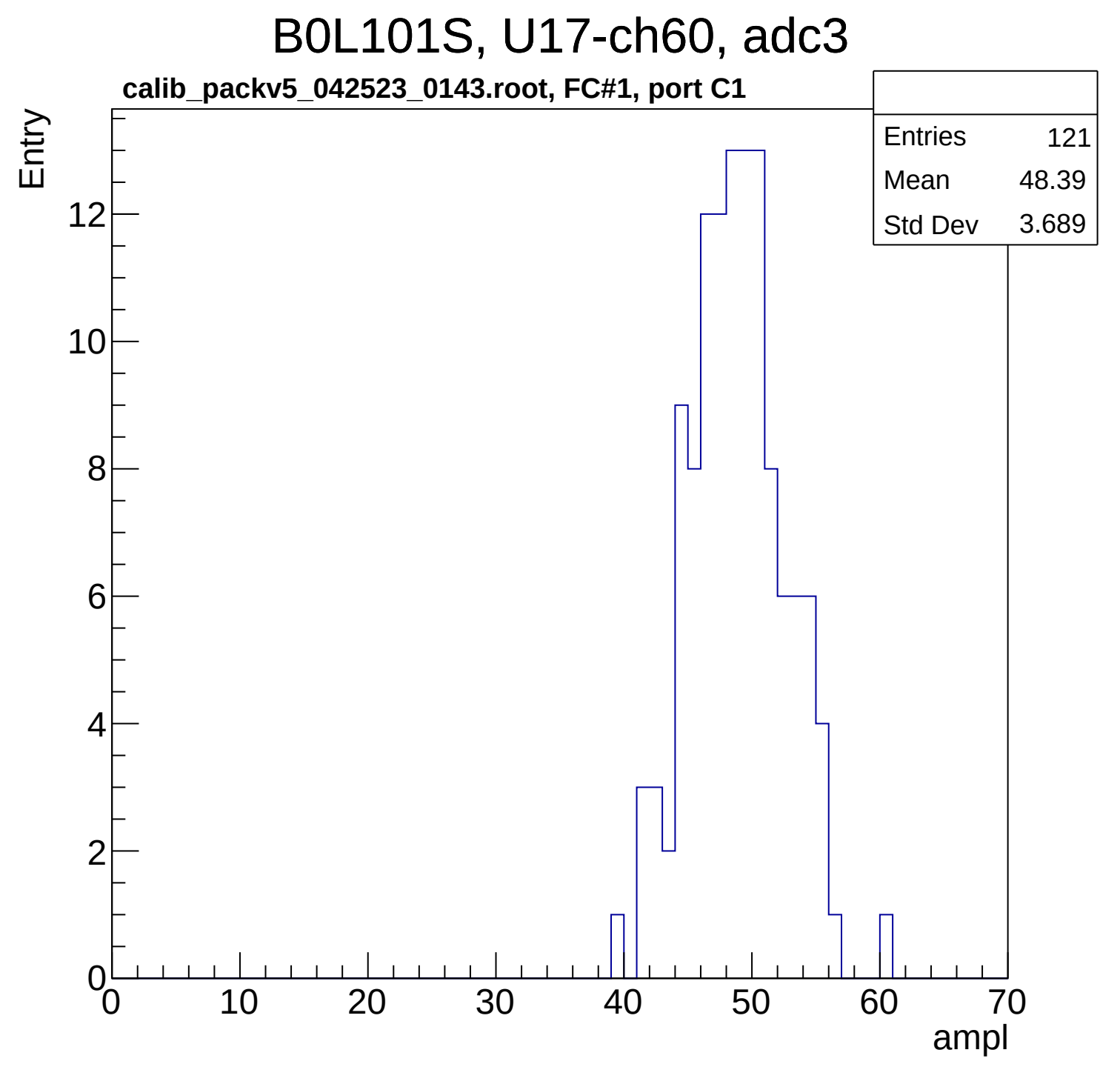

B0L101S, U17-ch59, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

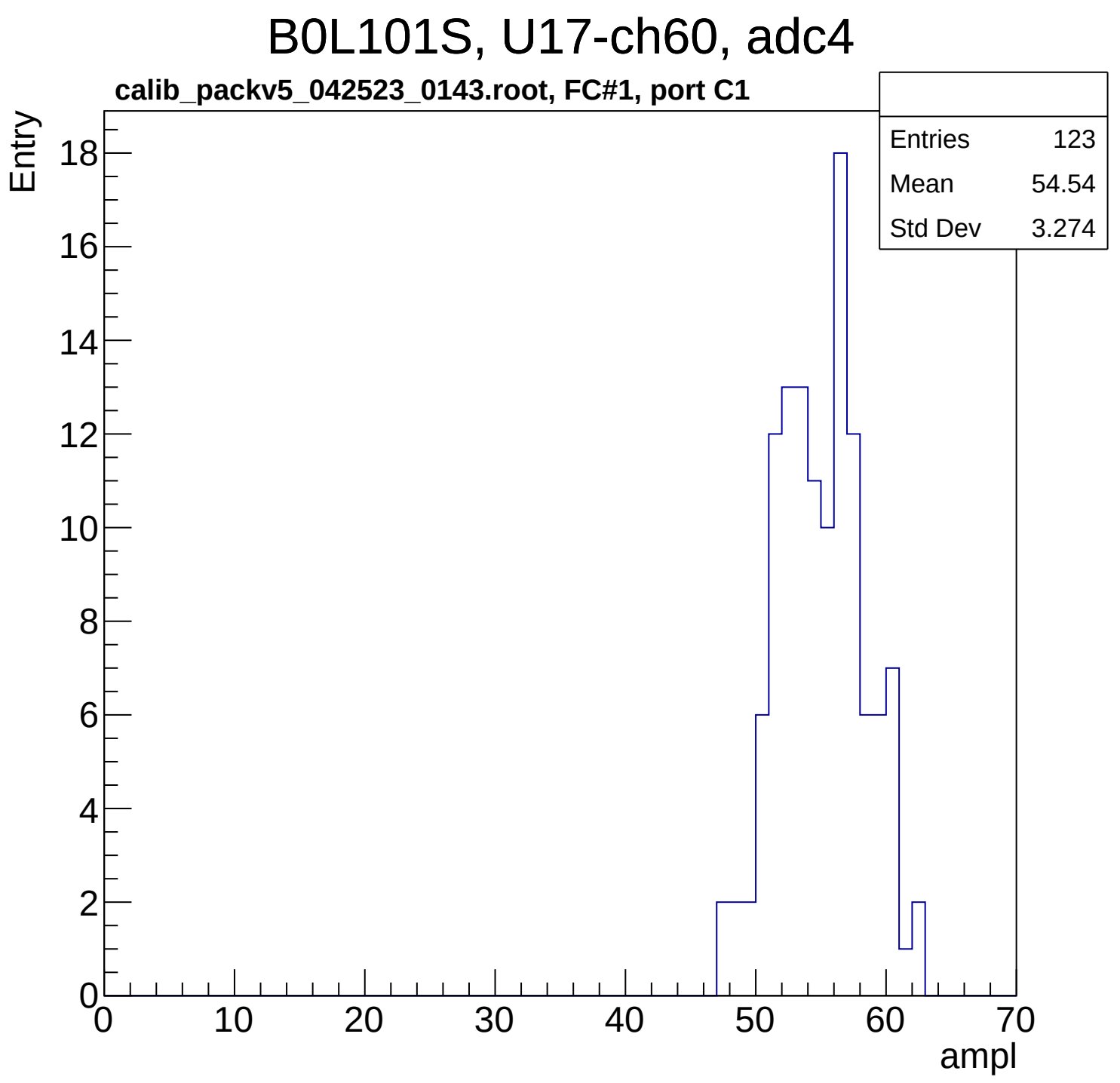

B0L101S, U17-ch59, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

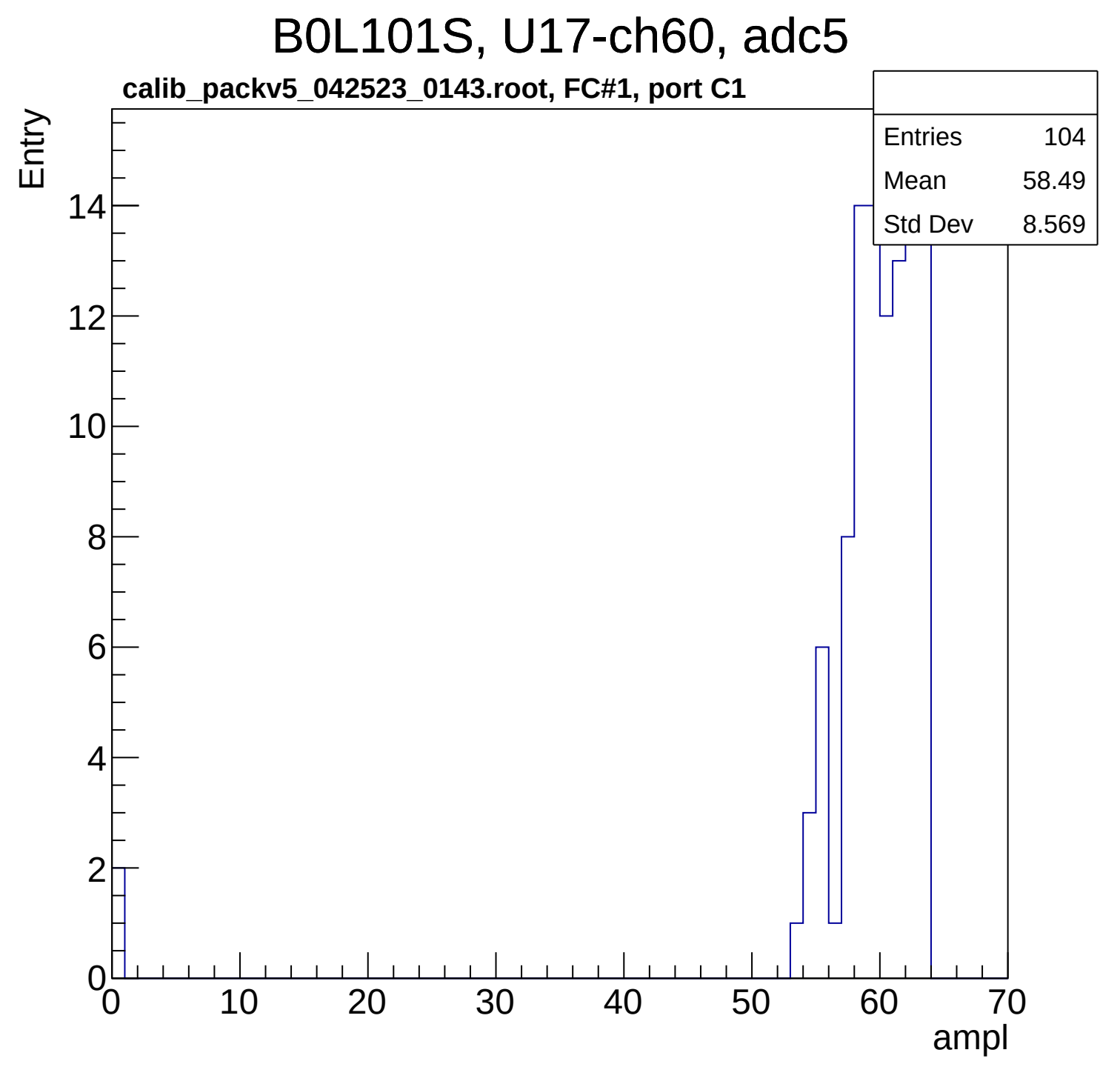

B0L101S, U17-ch60, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 28.32
Std Dev 4.493
Gaus mean : 29.2193
Gaus Width: 4.1413
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

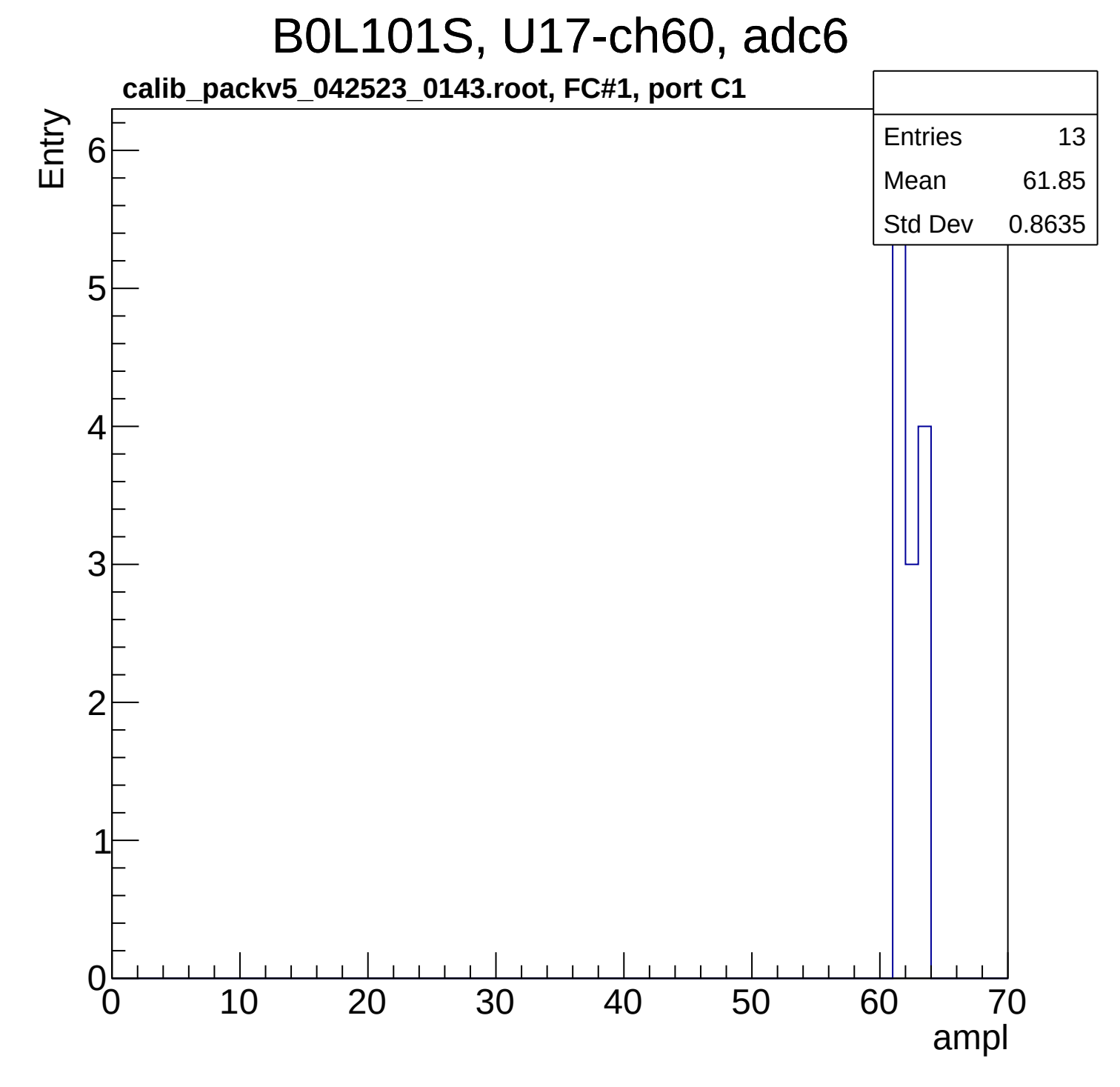

B0L101S, U17-ch60, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 35.88
Std Dev 3.981
Gaus mean : 36.4696
Gaus Width: 3.9707
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

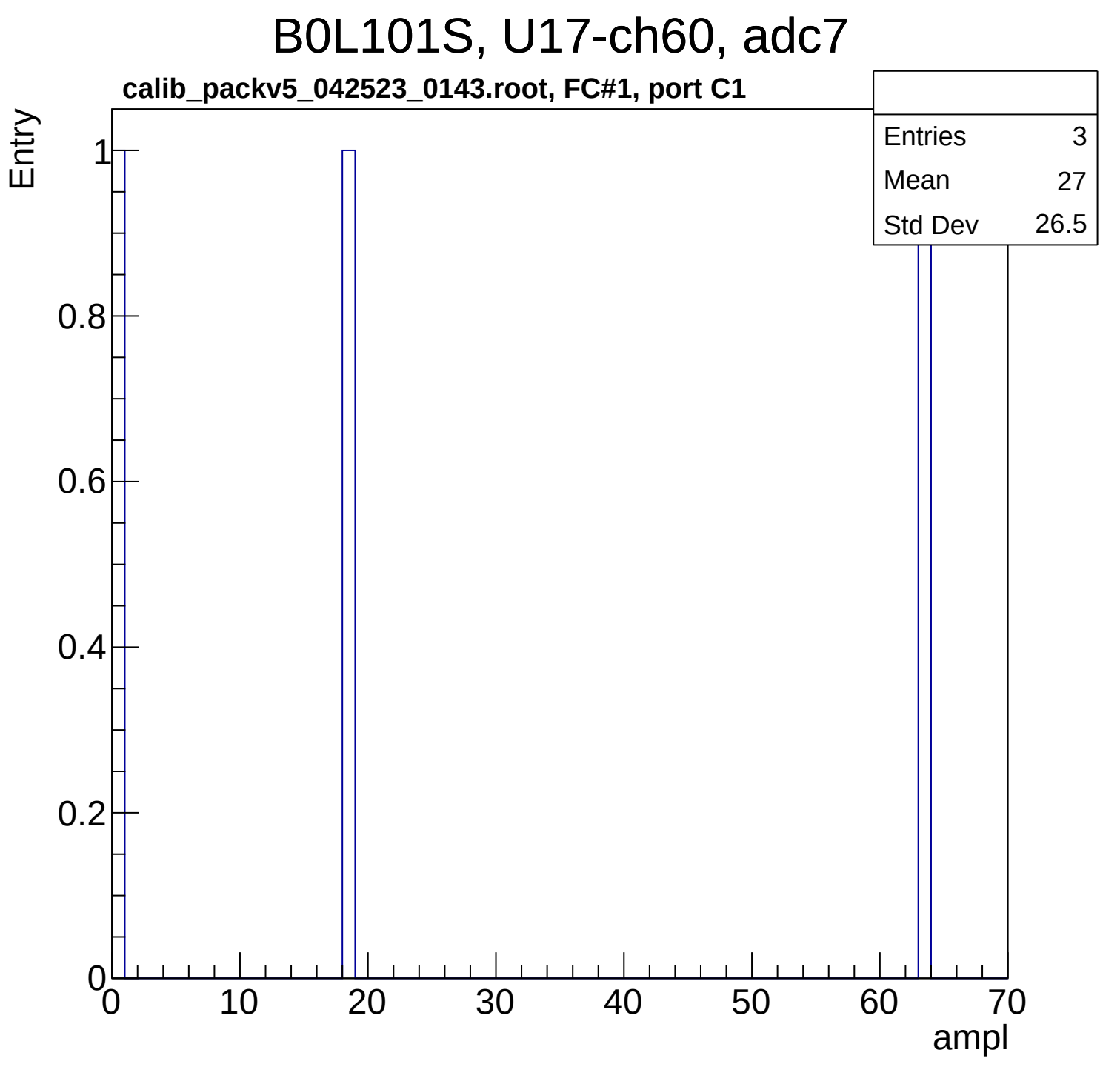

B0L101S, U17-ch60, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 42.36
Std Dev 3.865
Gaus mean : 42.9472
Gaus Width: 4.0006
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

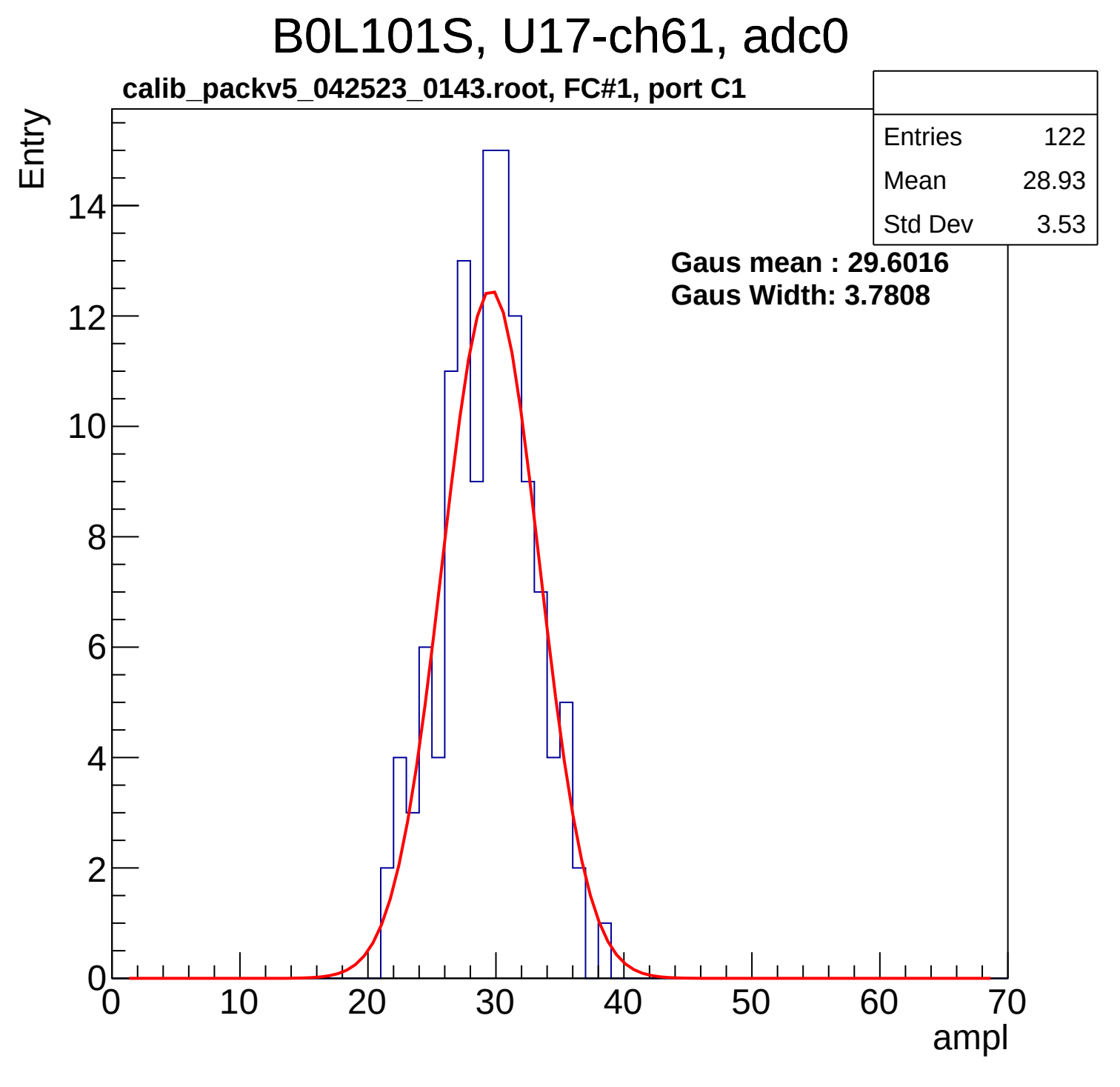

B0L101S, U17-ch60, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 48.39
Std Dev 3.689
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

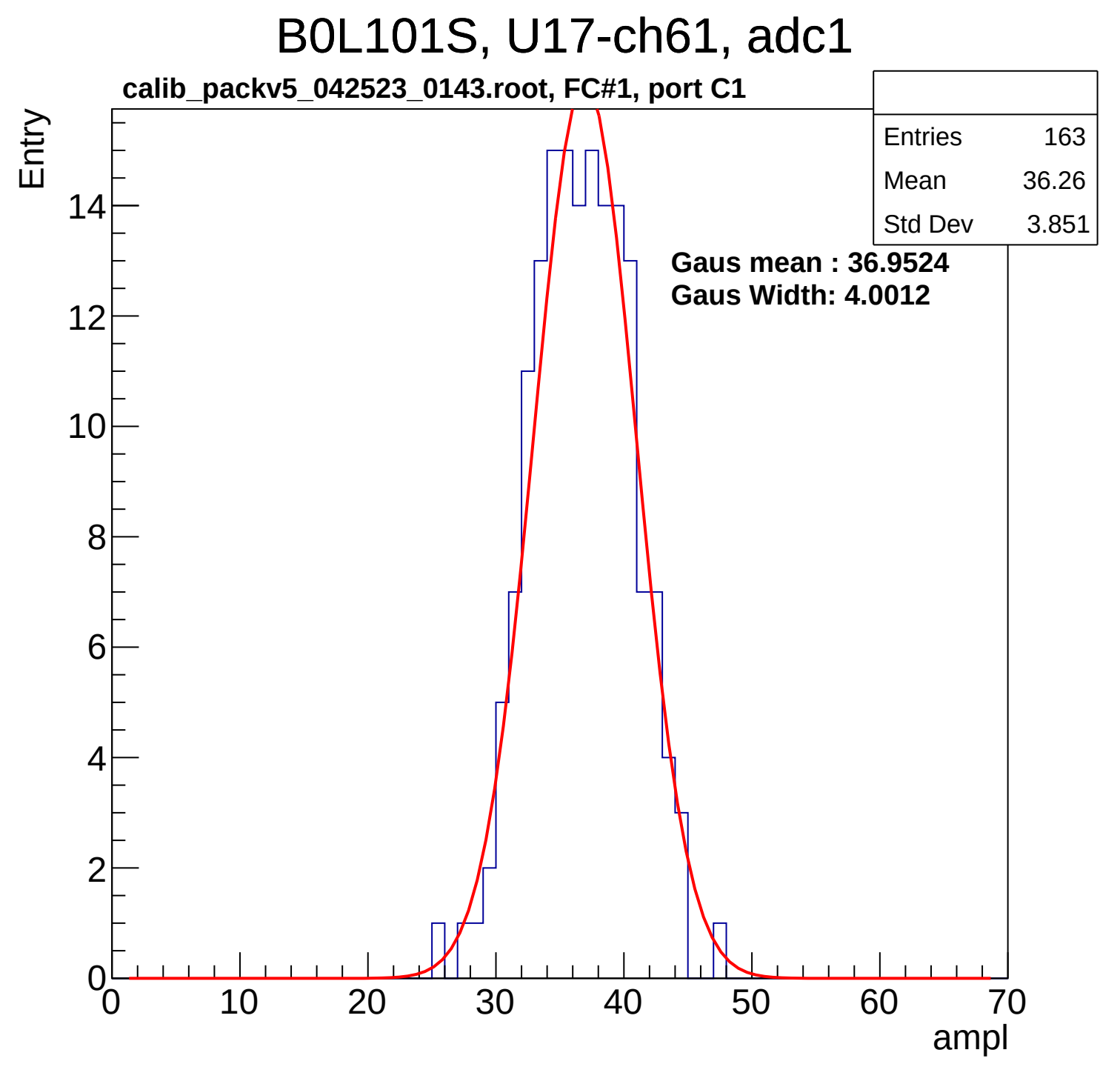

B0L101S, U17-ch60, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 54.54
Std Dev 3.274
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

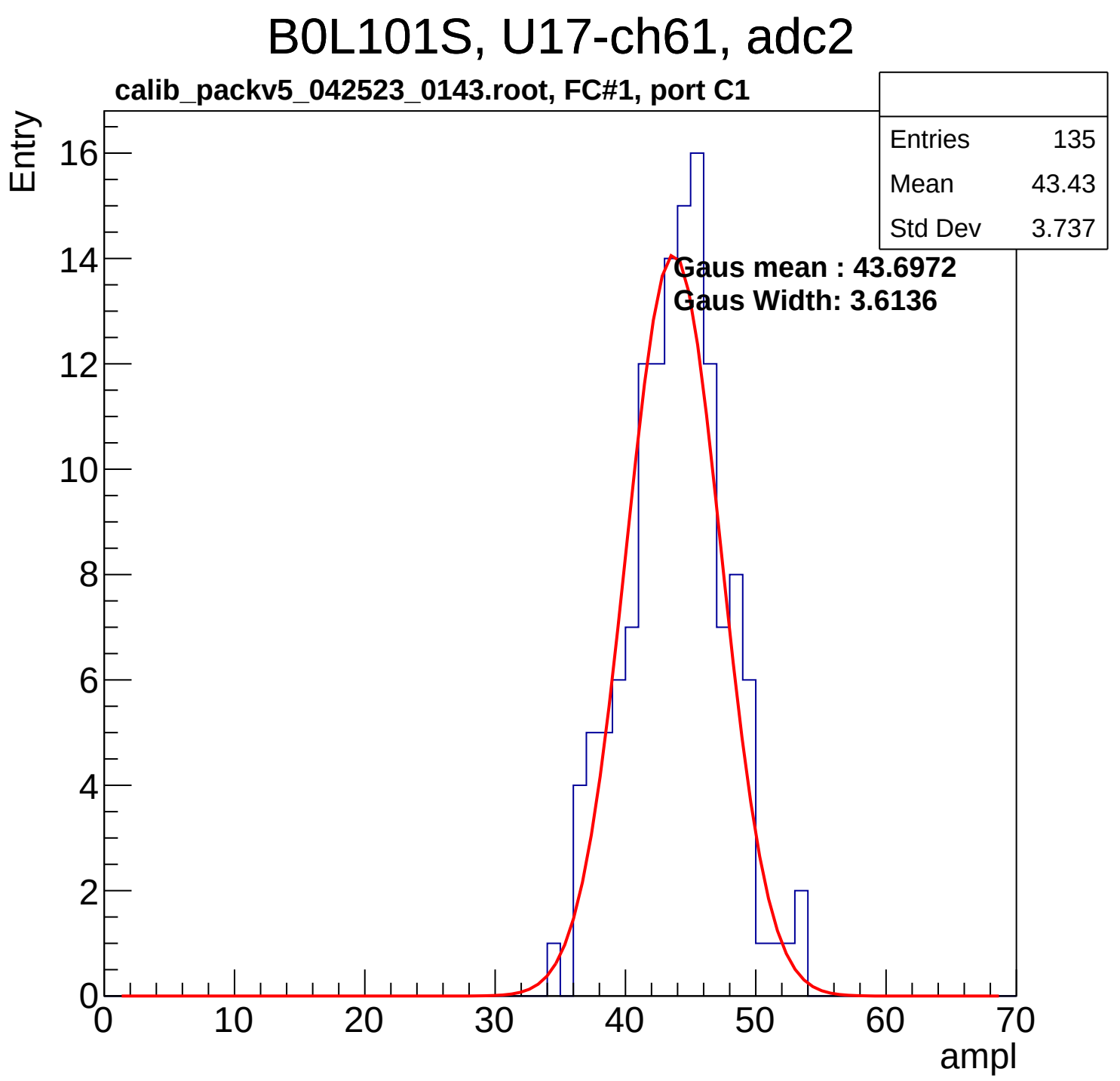

B0L101S, U17-ch60, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.49
Std Dev 8.569
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

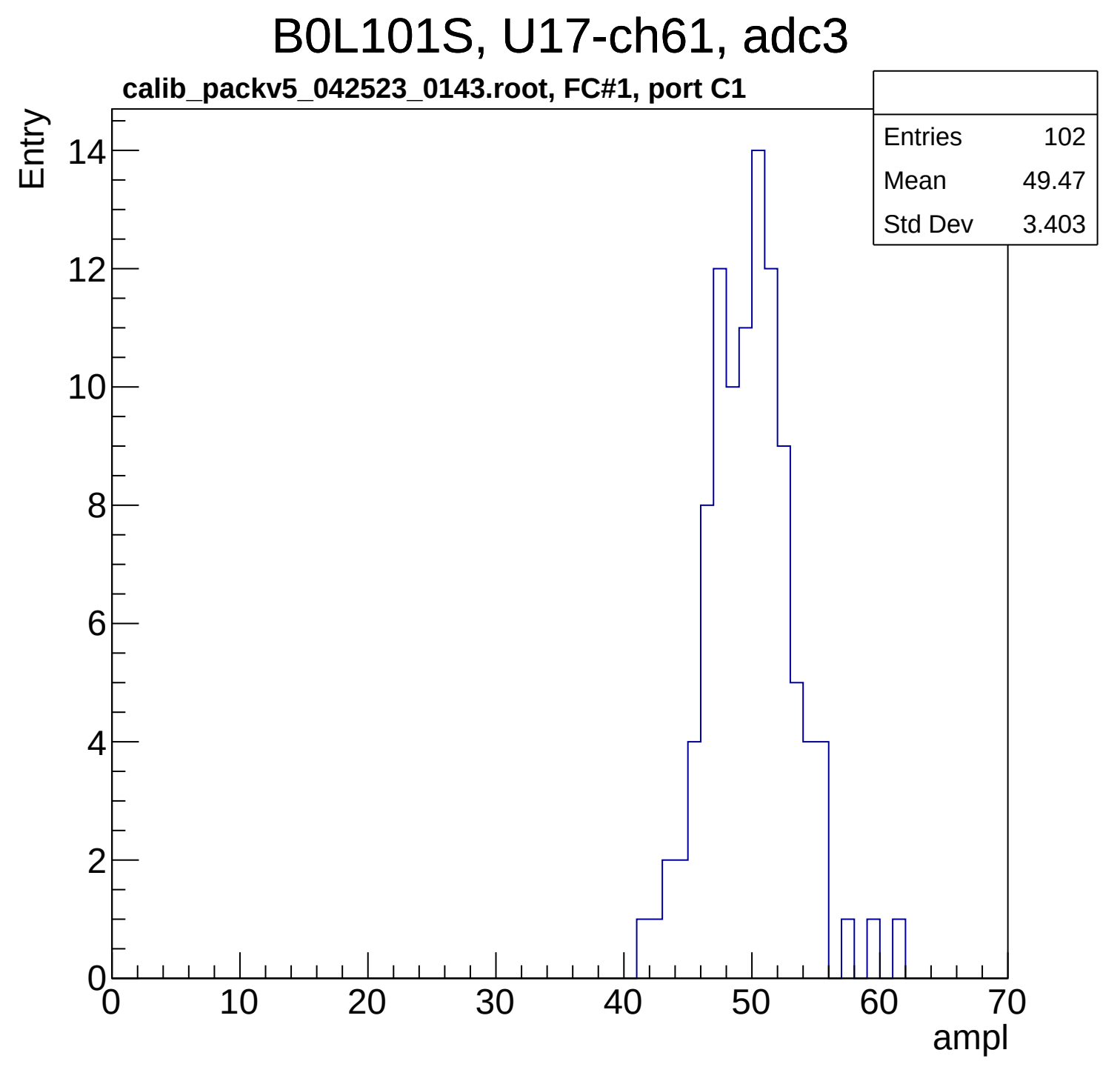

B0L101S, U17-ch60, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 13
Mean 61.85
Std Dev 0.8635
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

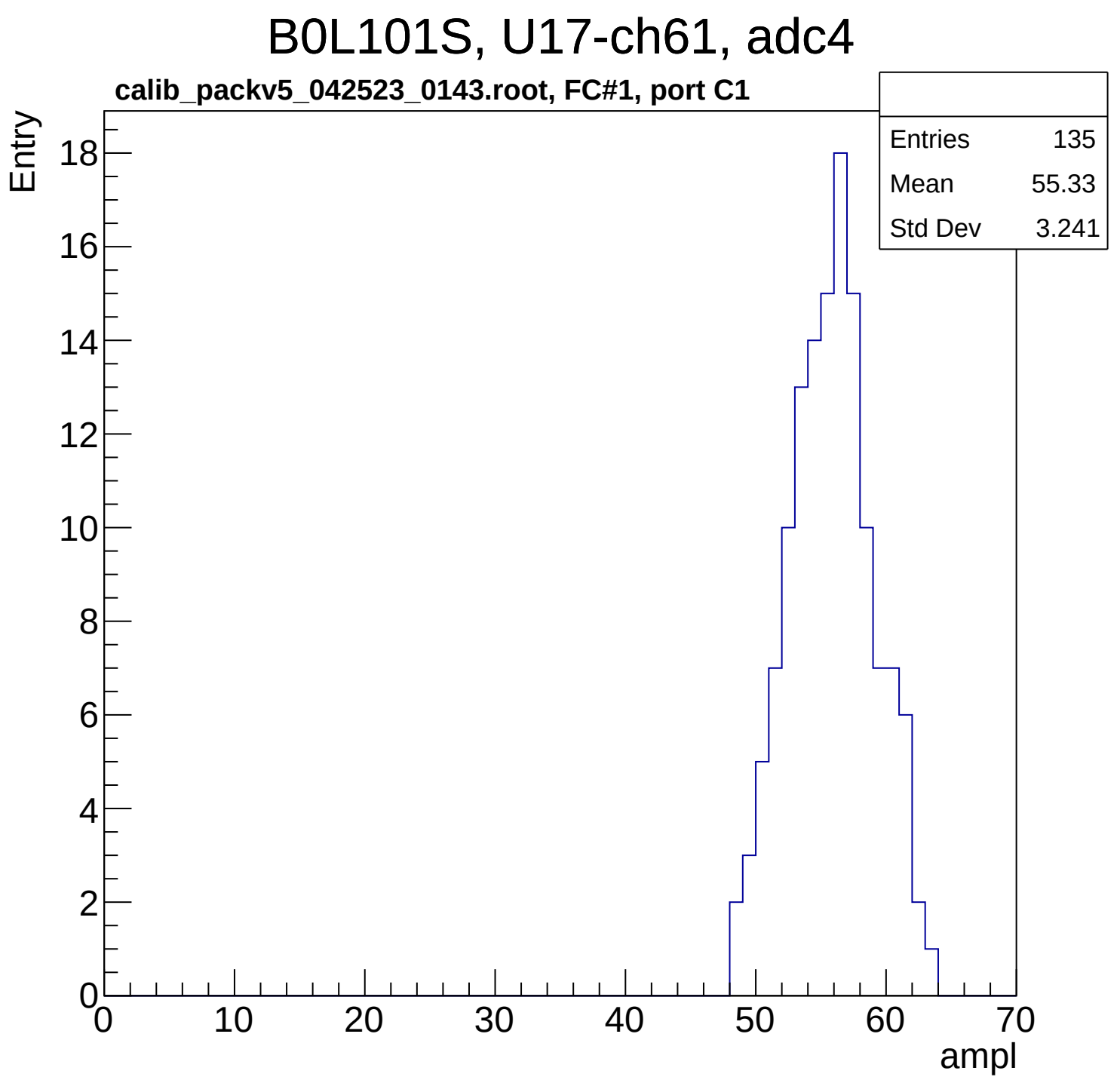

B0L101S, U17-ch60, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 27
Std Dev 26.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

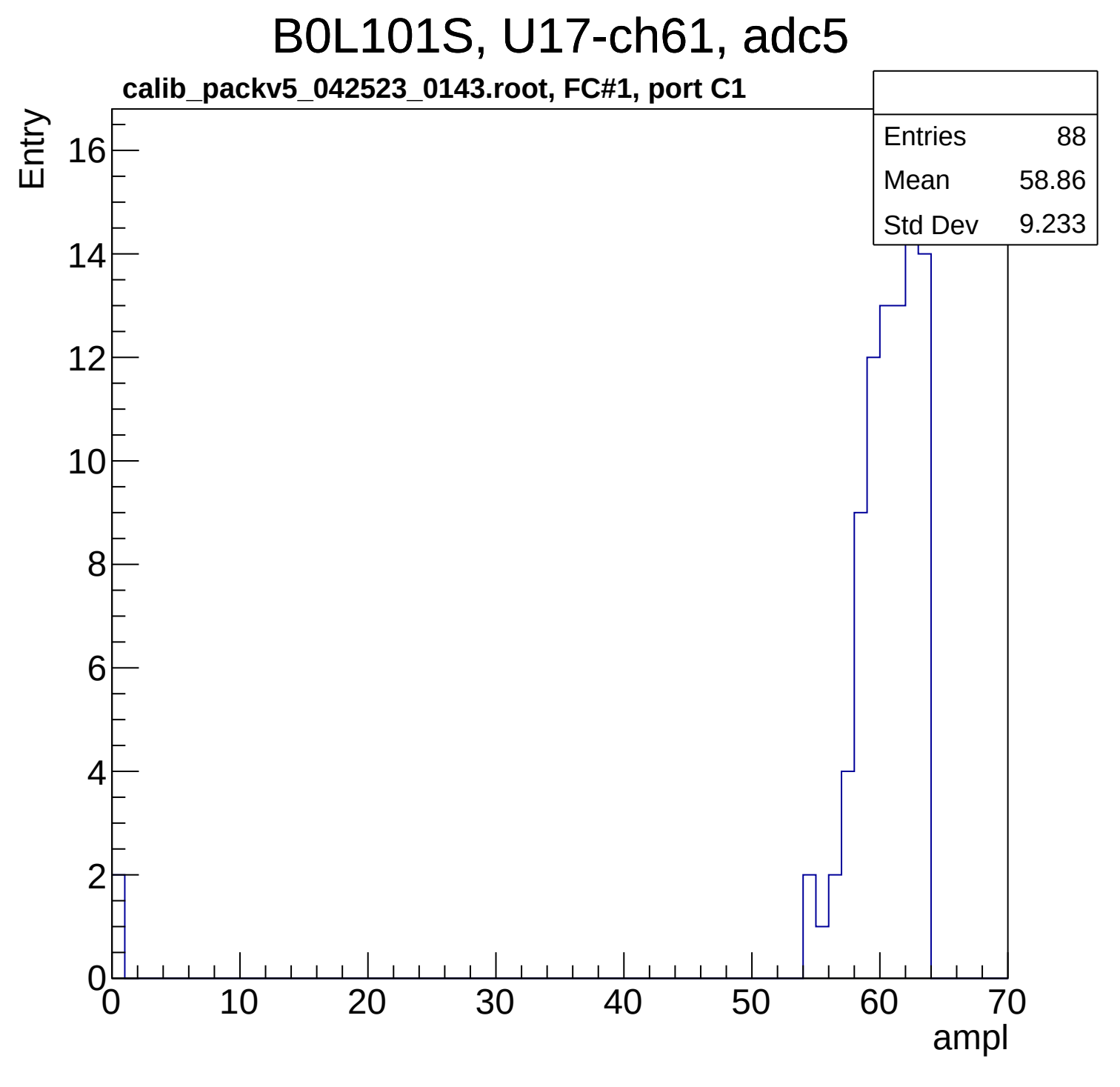

B0L101S, U17-ch61, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 28.93
Std Dev 3.53
Gaus mean : 29.6016
Gaus Width: 3.7808
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

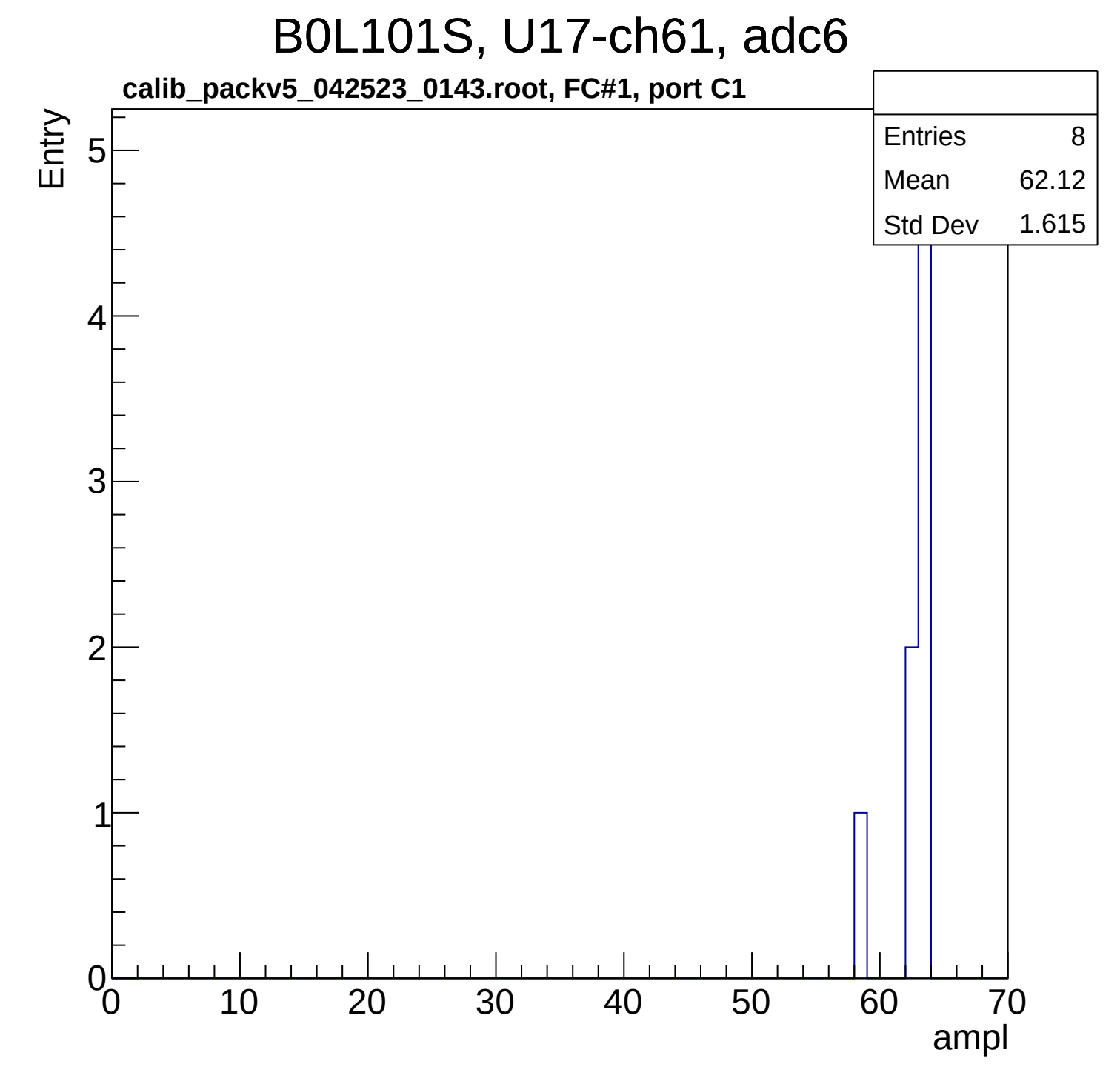

B0L101S, U17-ch61, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 36.26
Std Dev 3.851
Gaus mean : 36.9524
Gaus Width: 4.0012
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

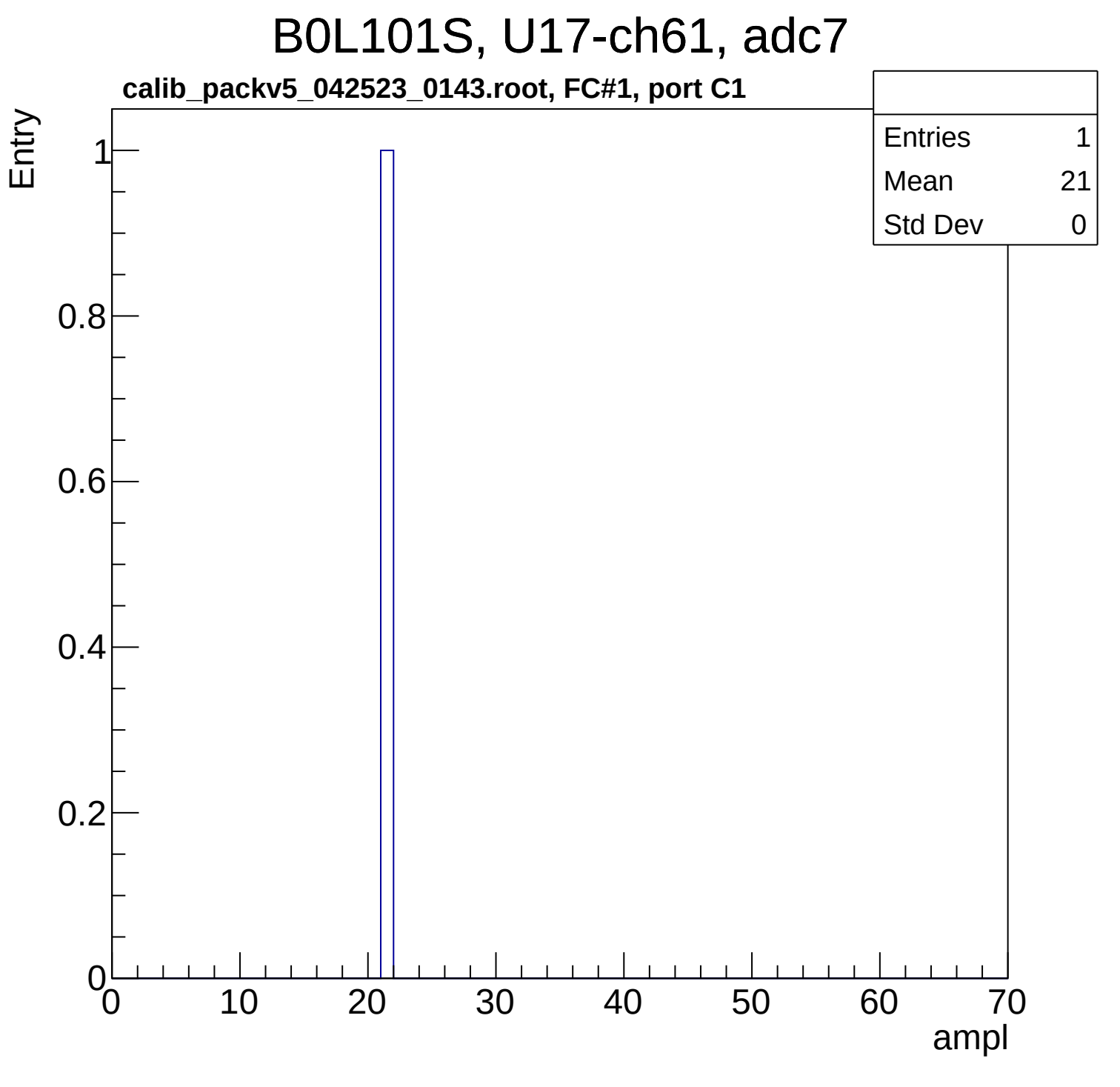

B0L101S, U17-ch61, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 43.43
Std Dev 3.737
Gaus mean : 43.6972
Gaus Width: 3.6136
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

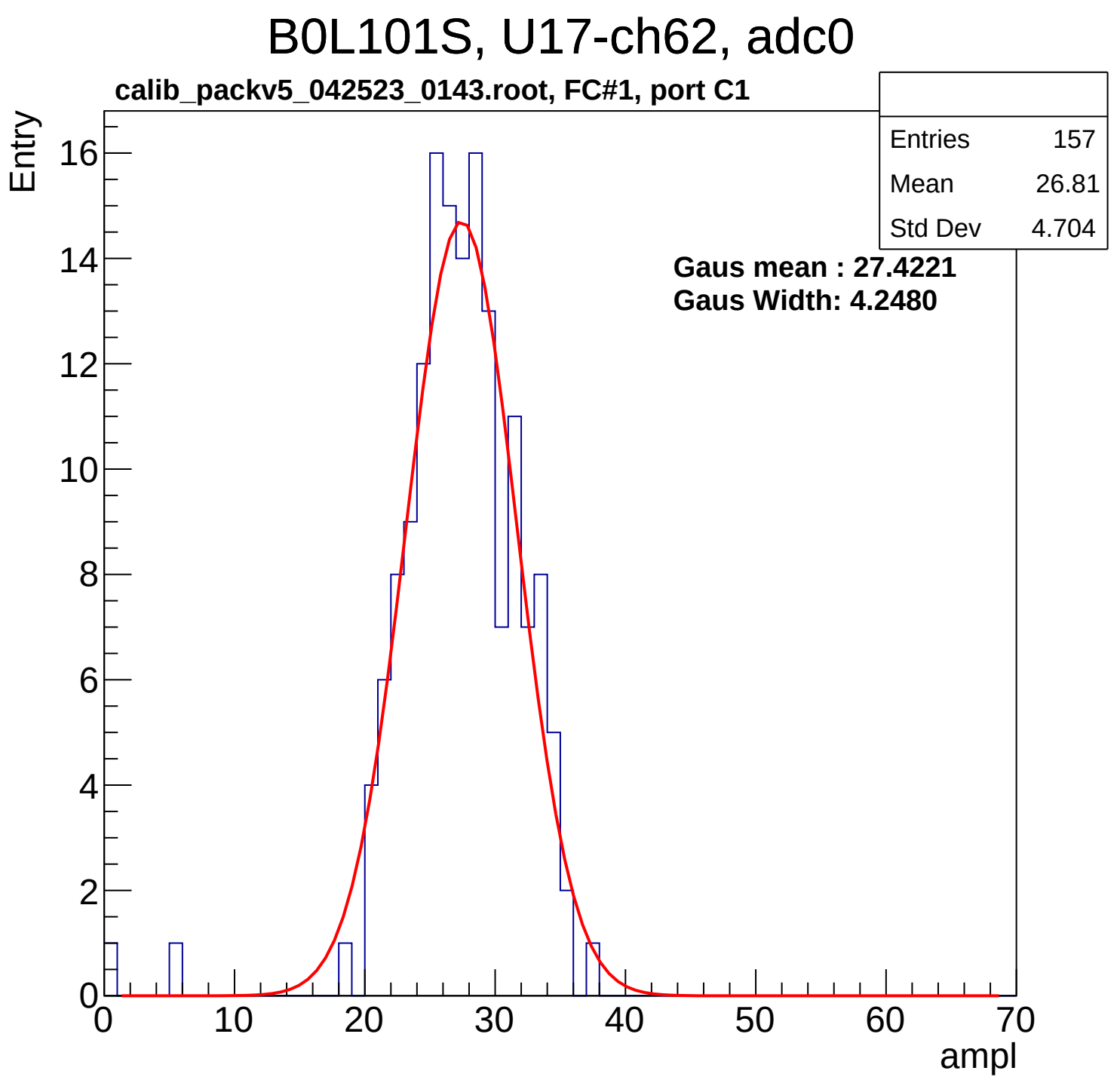

B0L101S, U17-ch61, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 49.47
Std Dev 3.403
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

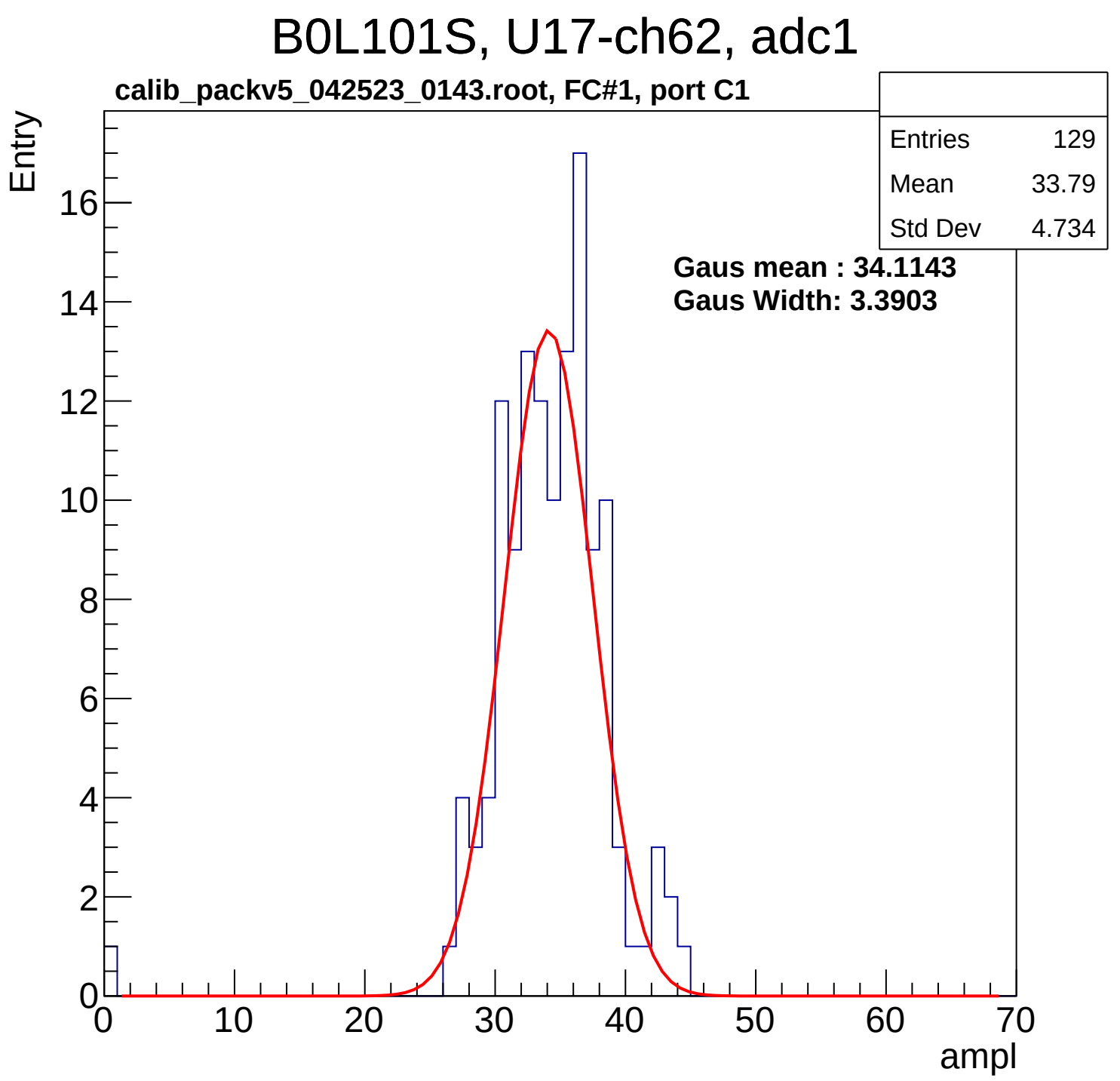

B0L101S, U17-ch61, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 55.33
Std Dev 3.241
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

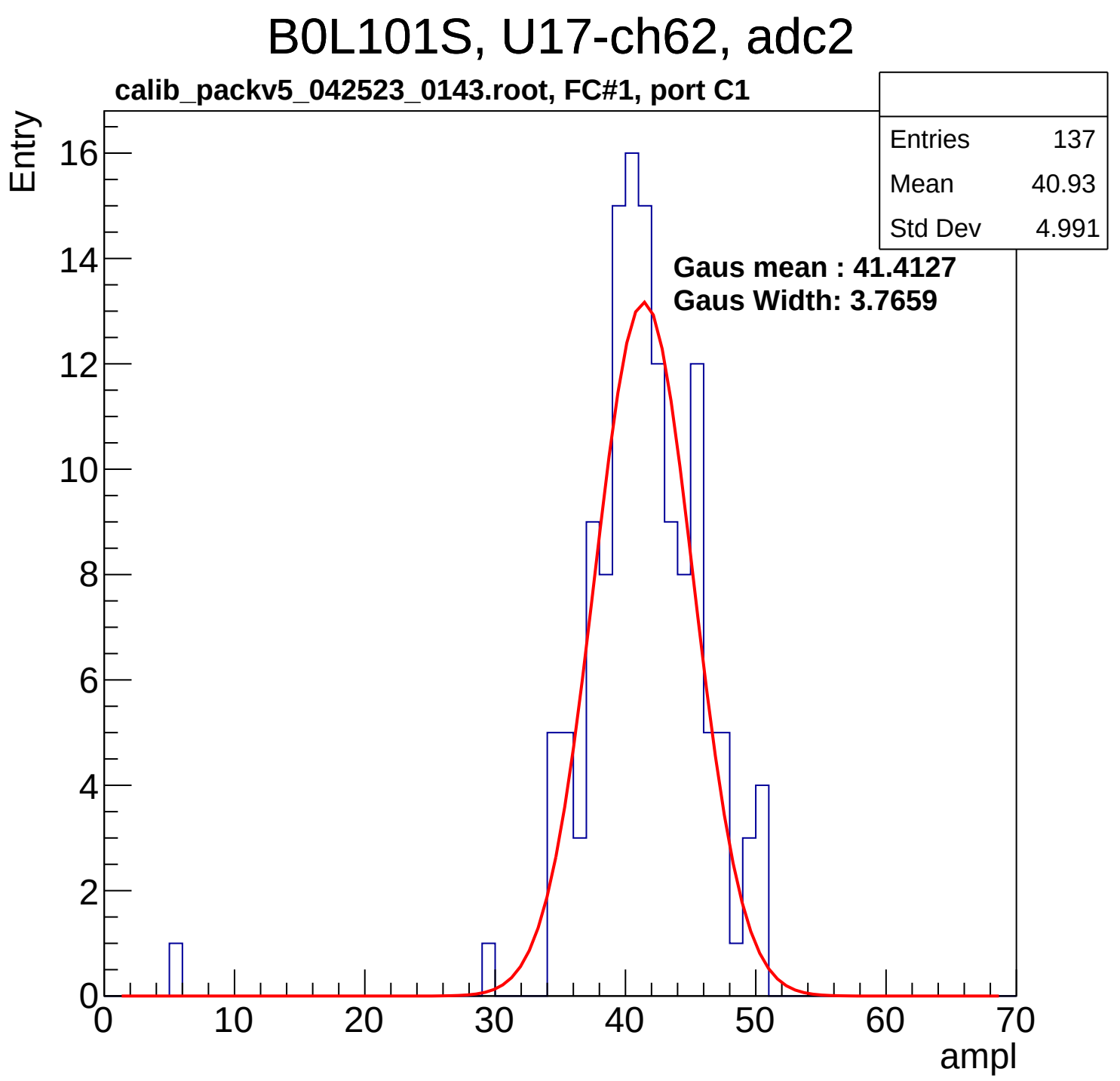

B0L101S, U17-ch61, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 88
Mean 58.86
Std Dev 9.233
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

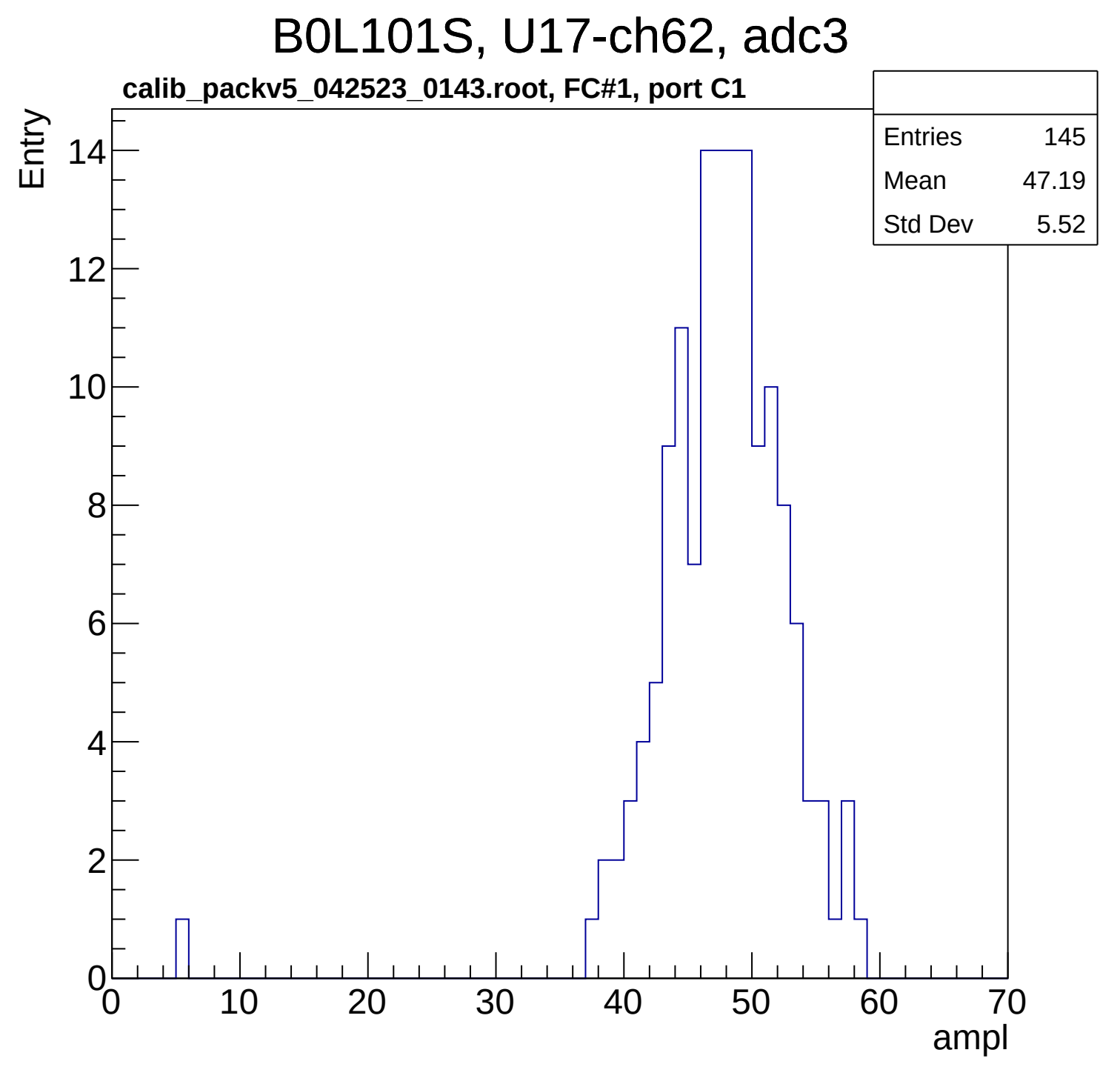

B0L101S, U17-ch61, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 8
Mean 62.12
Std Dev 1.615
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

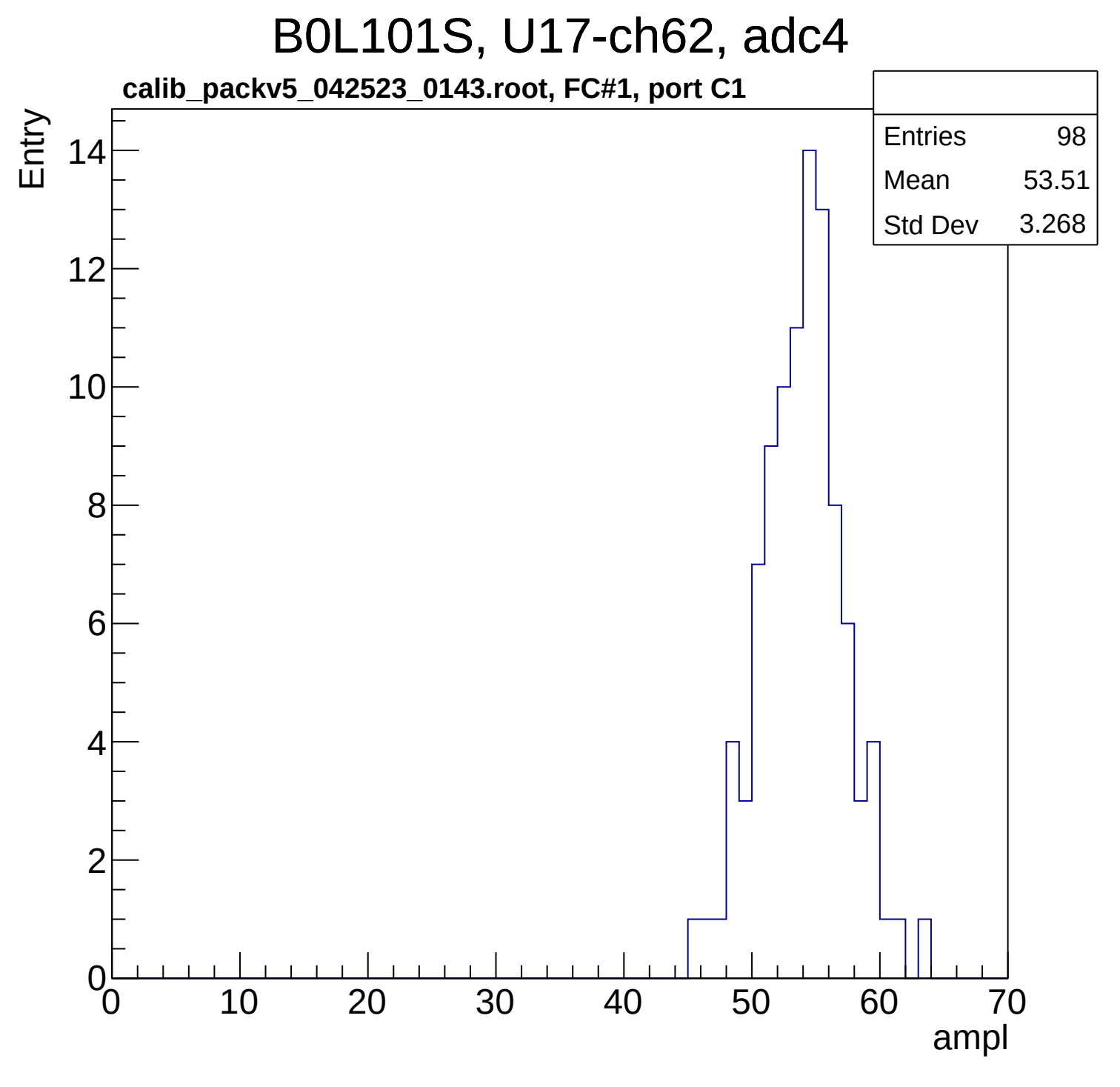

B0L101S, U17-ch61, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

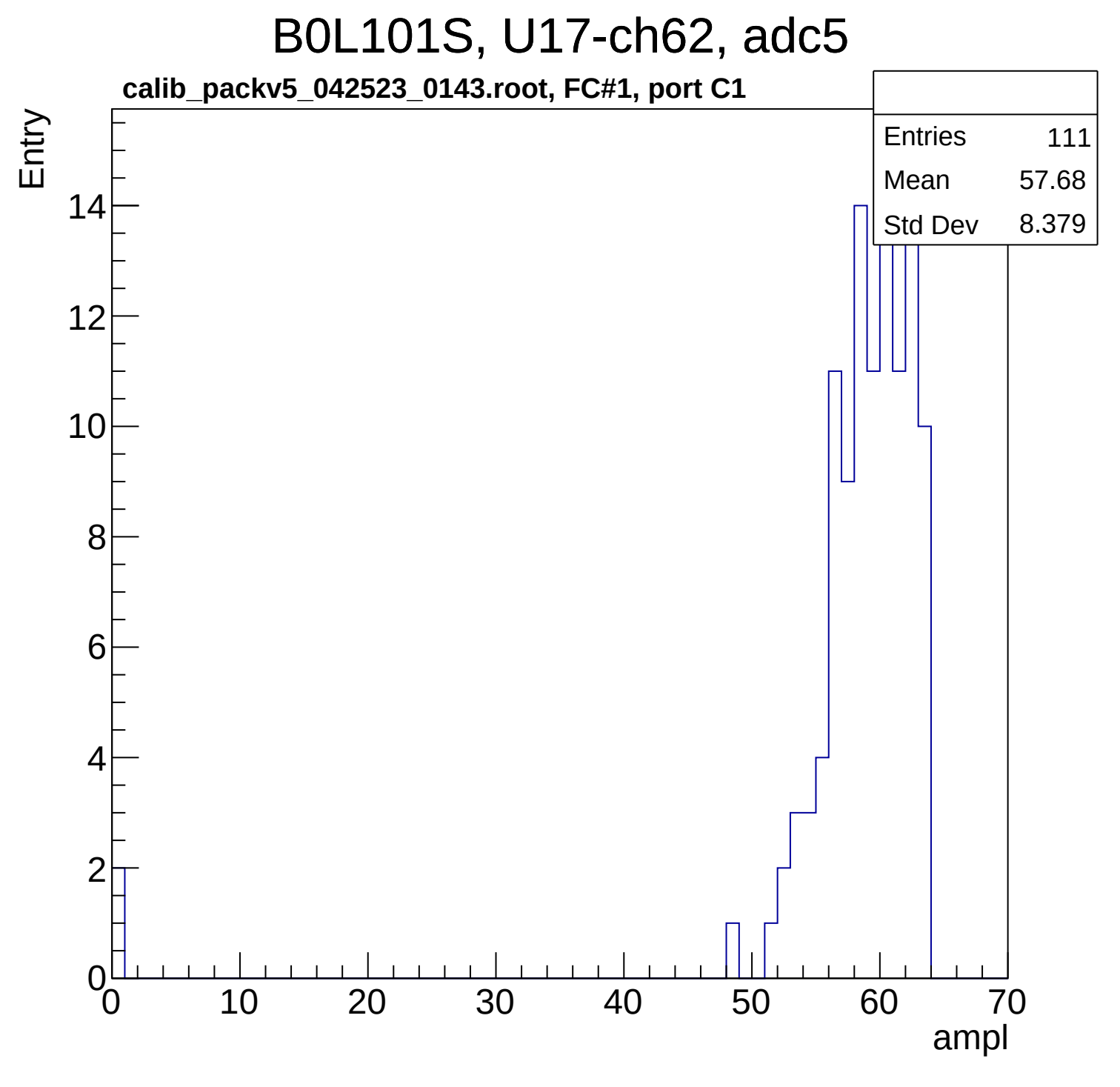

B0L101S, U17-ch62, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 26.81
Std Dev 4.704
Gaus mean : 27.4221
Gaus Width: 4.2480
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

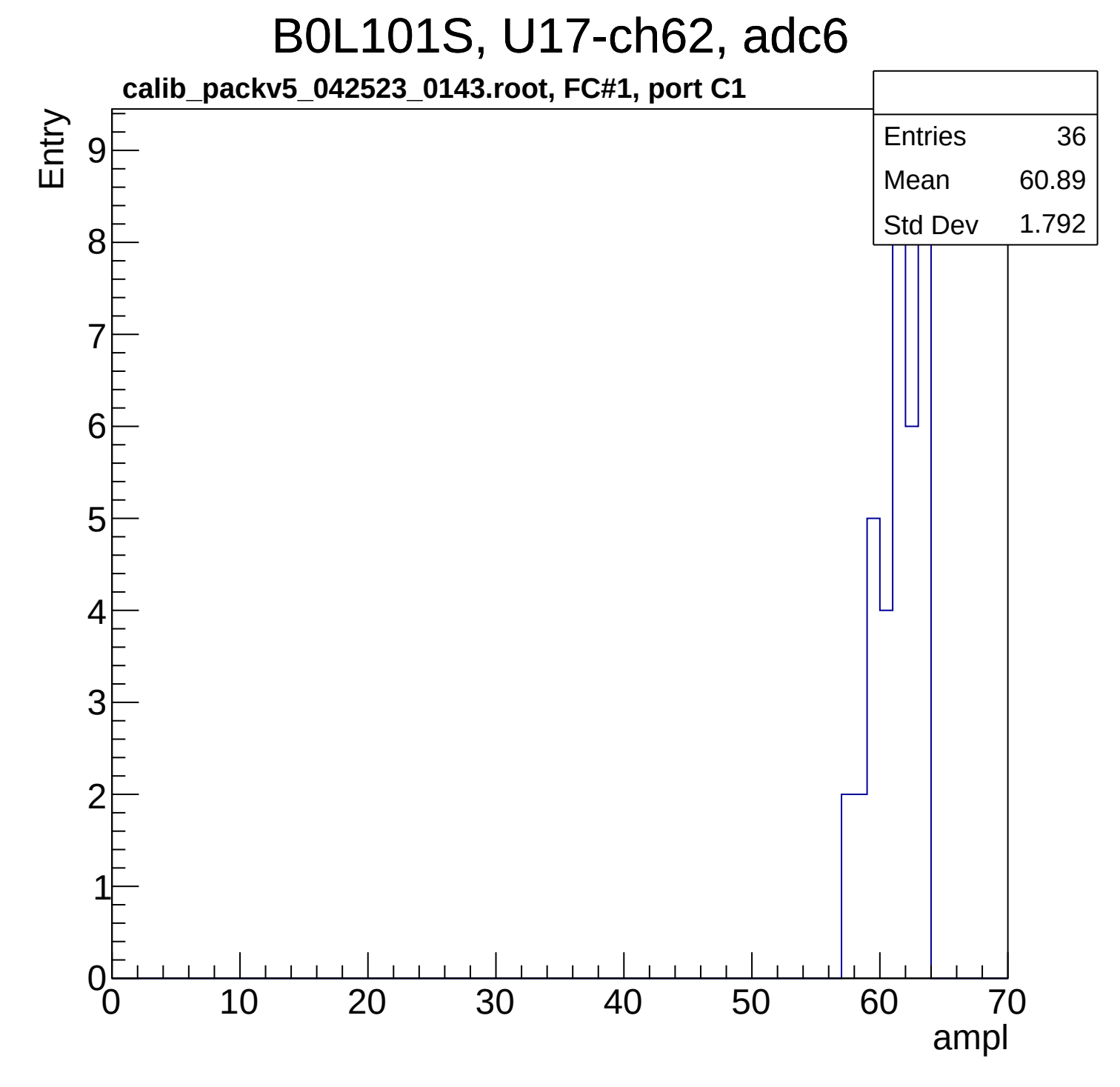

B0L101S, U17-ch62, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 33.79
Std Dev 4.734
Gaus mean : 34.1143
Gaus Width: 3.3903
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

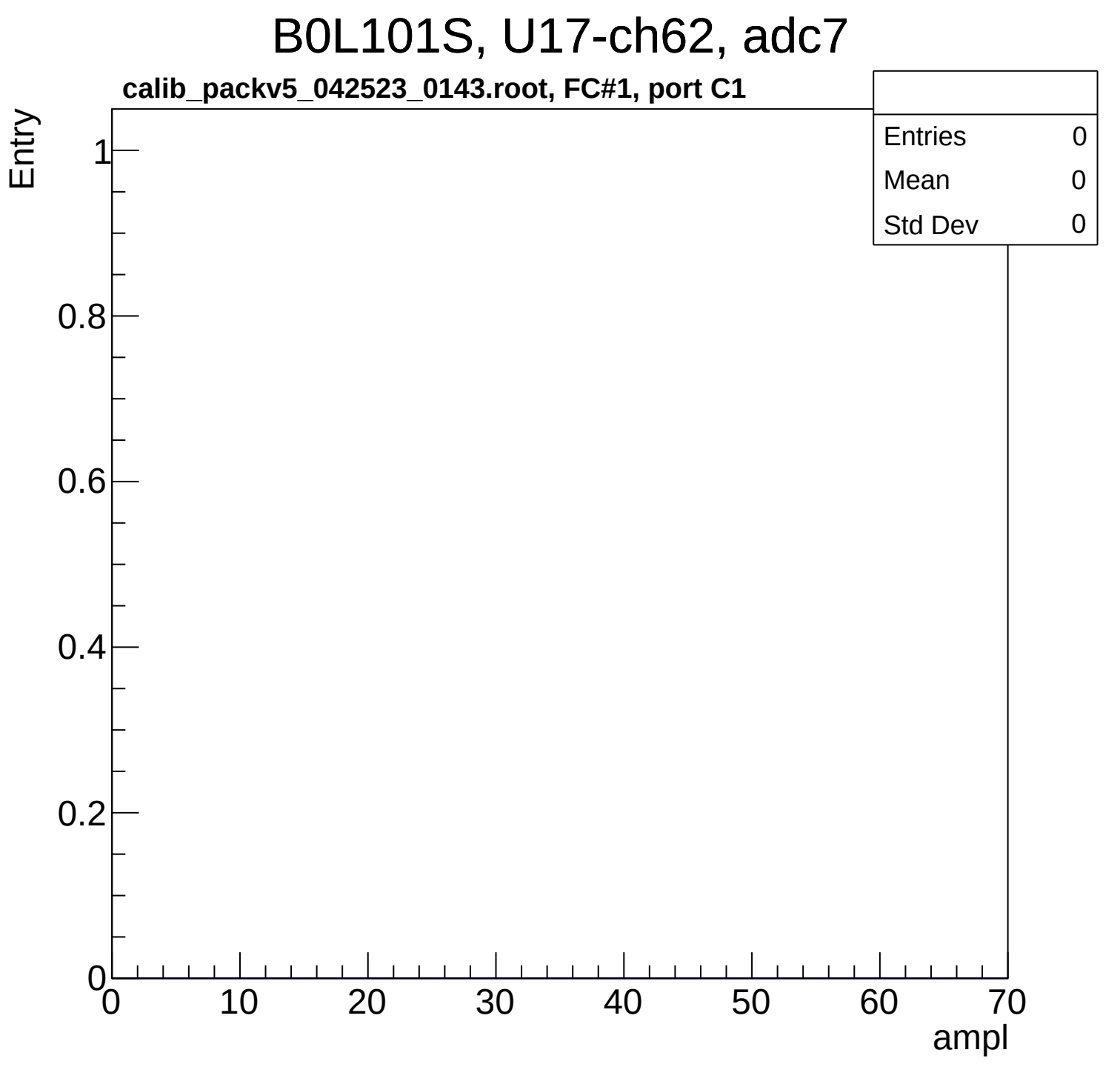

B0L101S, U17-ch62, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 40.93
Std Dev 4.991
Gaus mean : 41.4127
Gaus Width: 3.7659
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

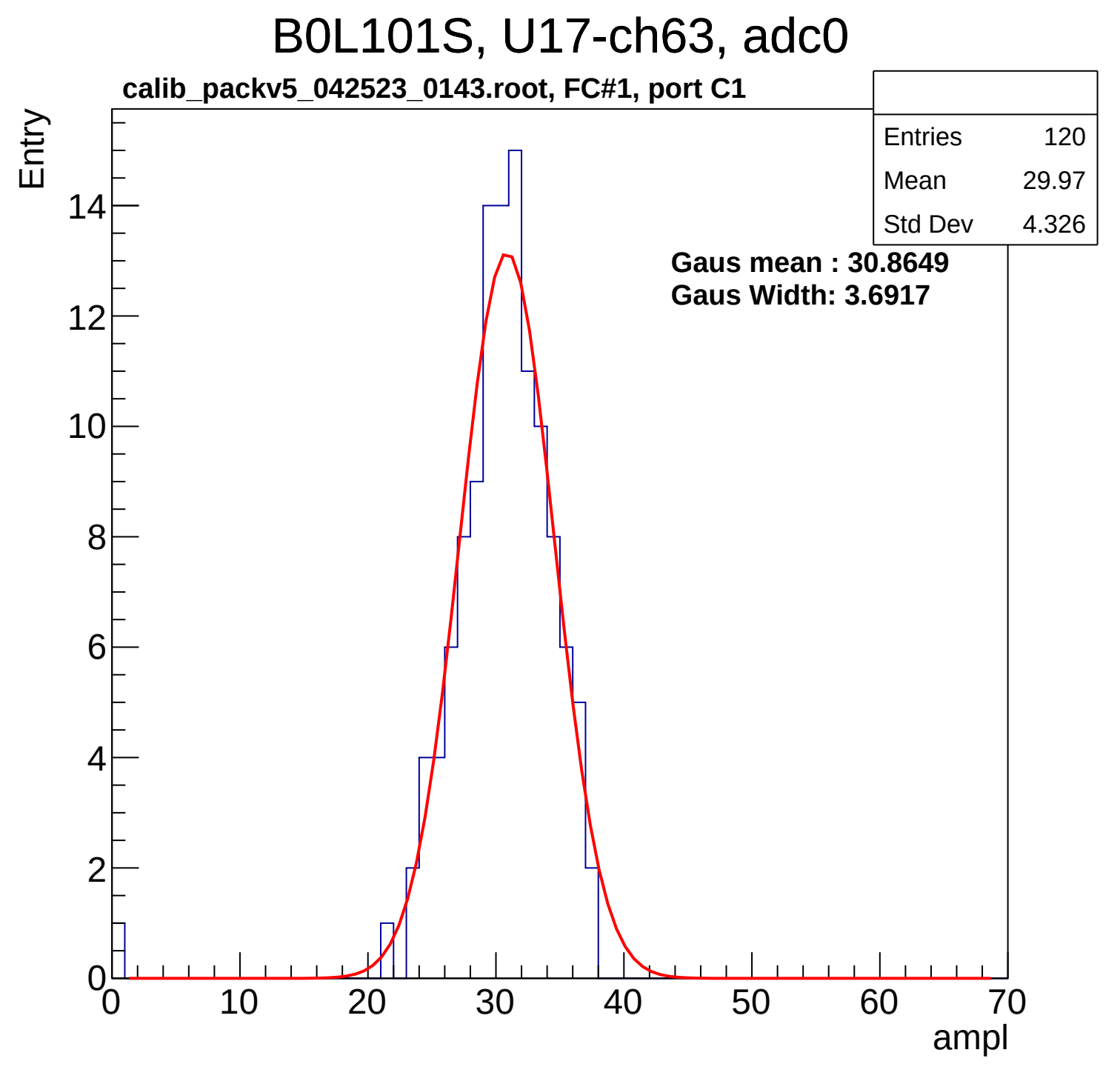

B0L101S, U17-ch62, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 47.19
Std Dev 5.52
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

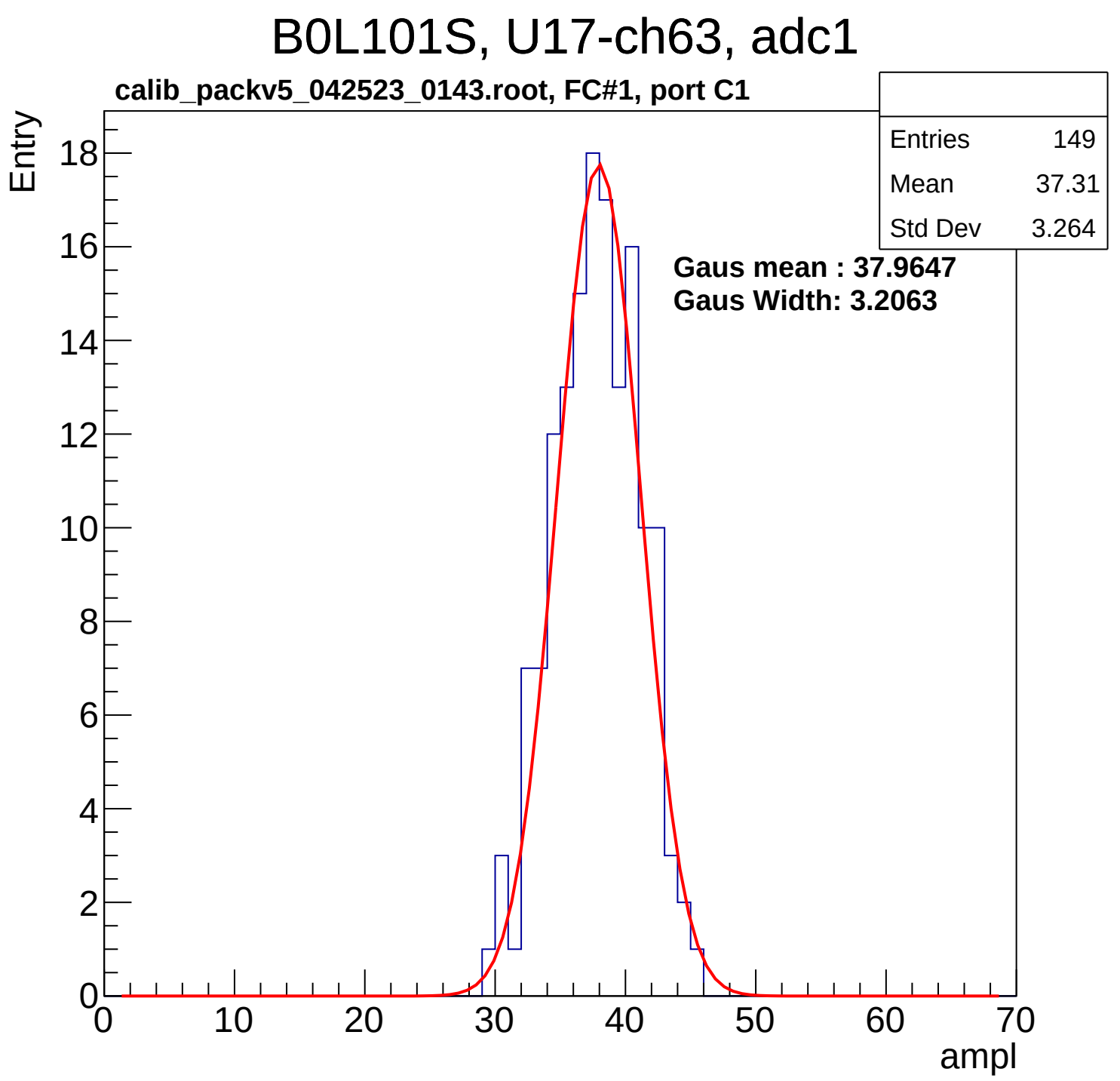

B0L101S, U17-ch62, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 98
Mean 53.51
Std Dev 3.268
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

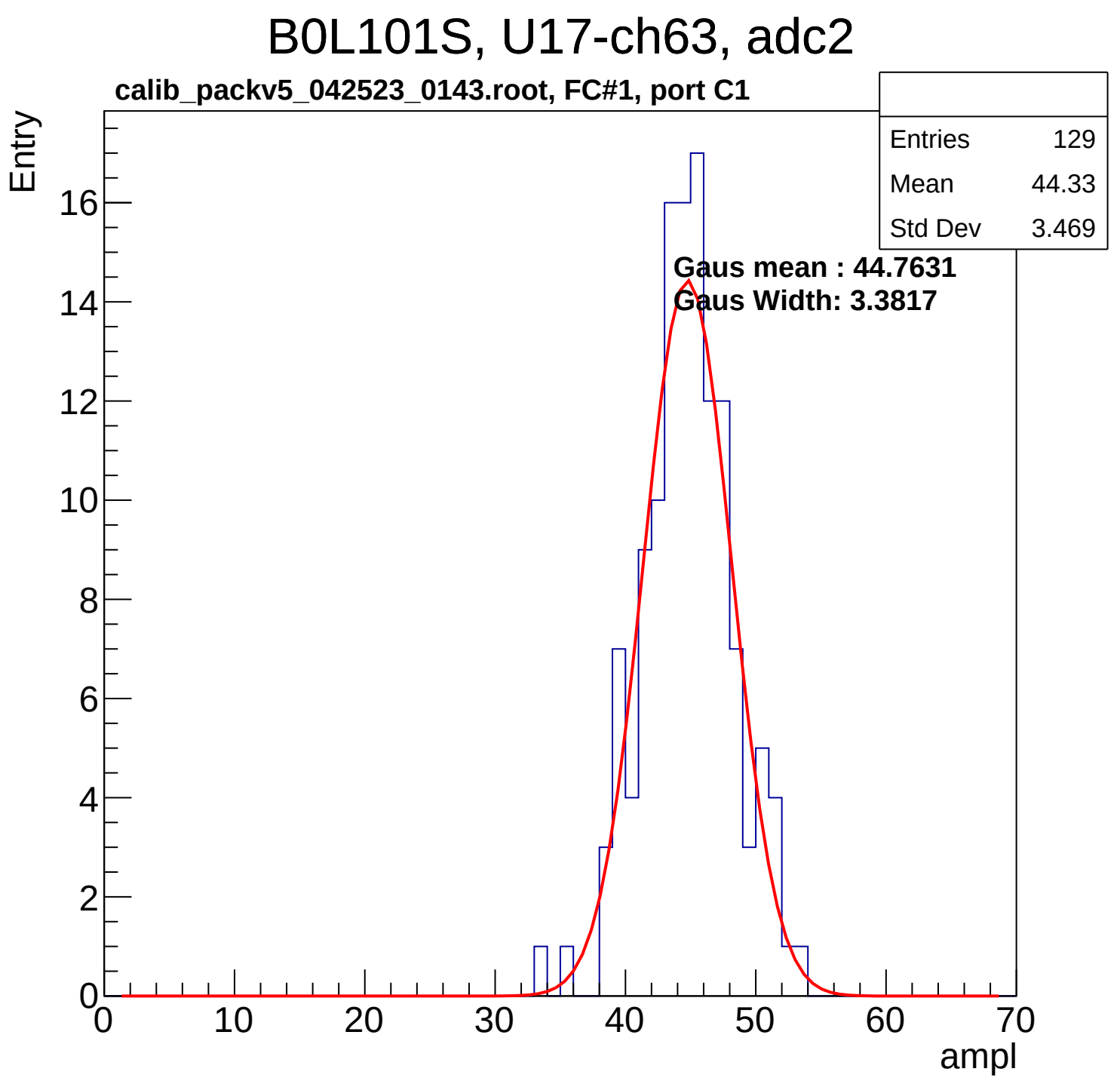

B0L101S, U17-ch62, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 57.68
Std Dev 8.379
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

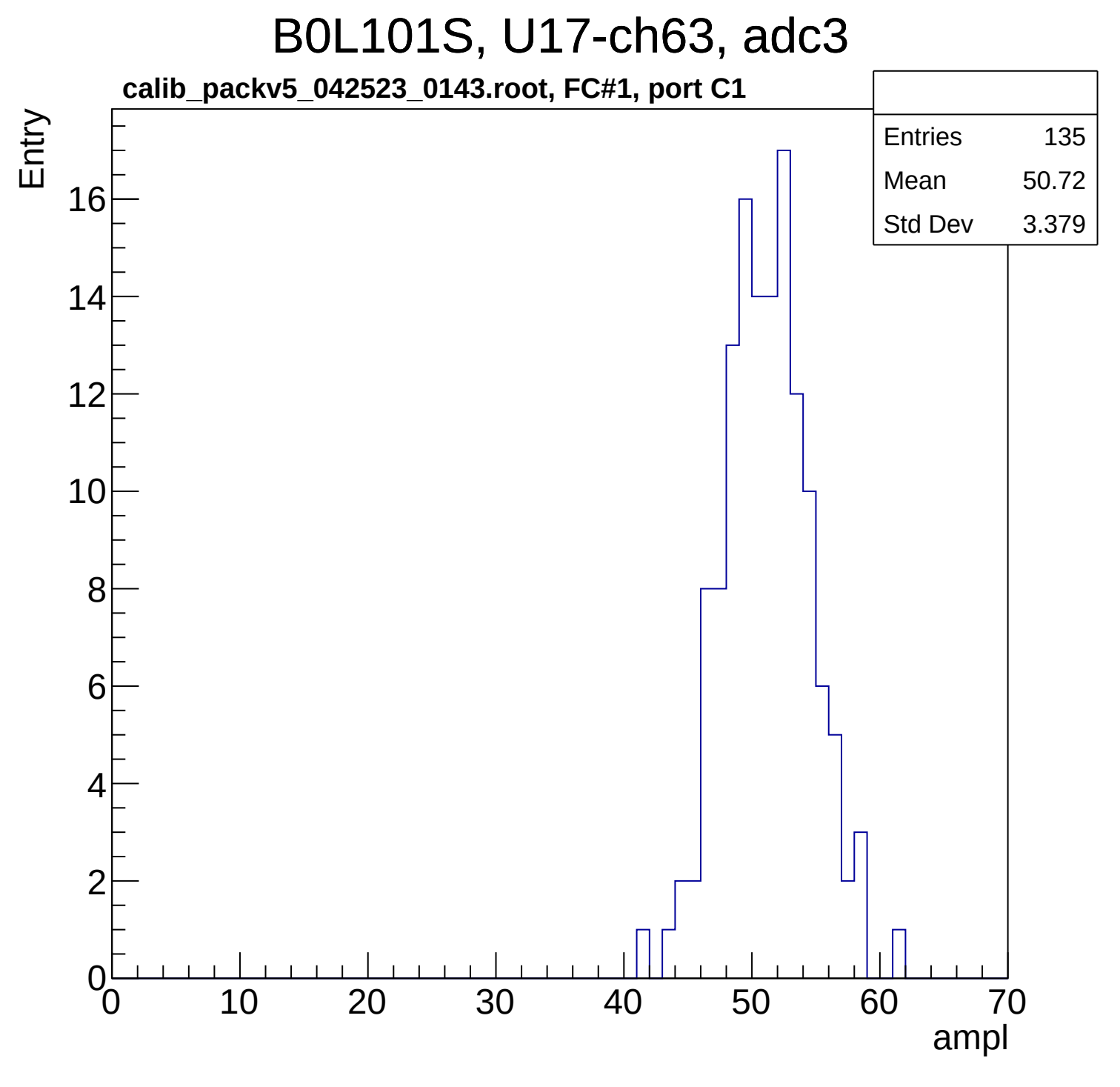

B0L101S, U17-ch62, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 36
Mean 60.89
Std Dev 1.792
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

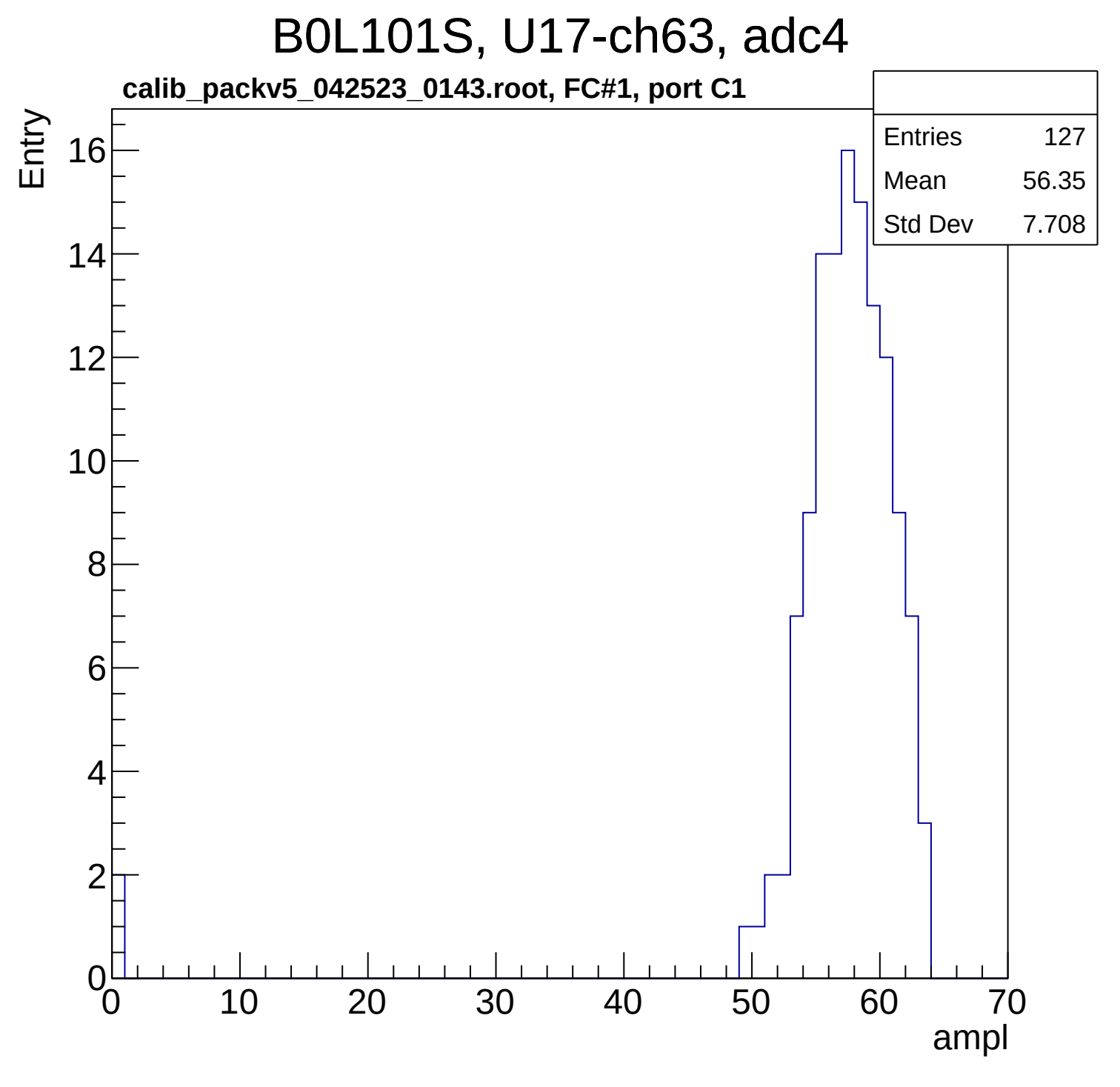

B0L101S, U17-ch62, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

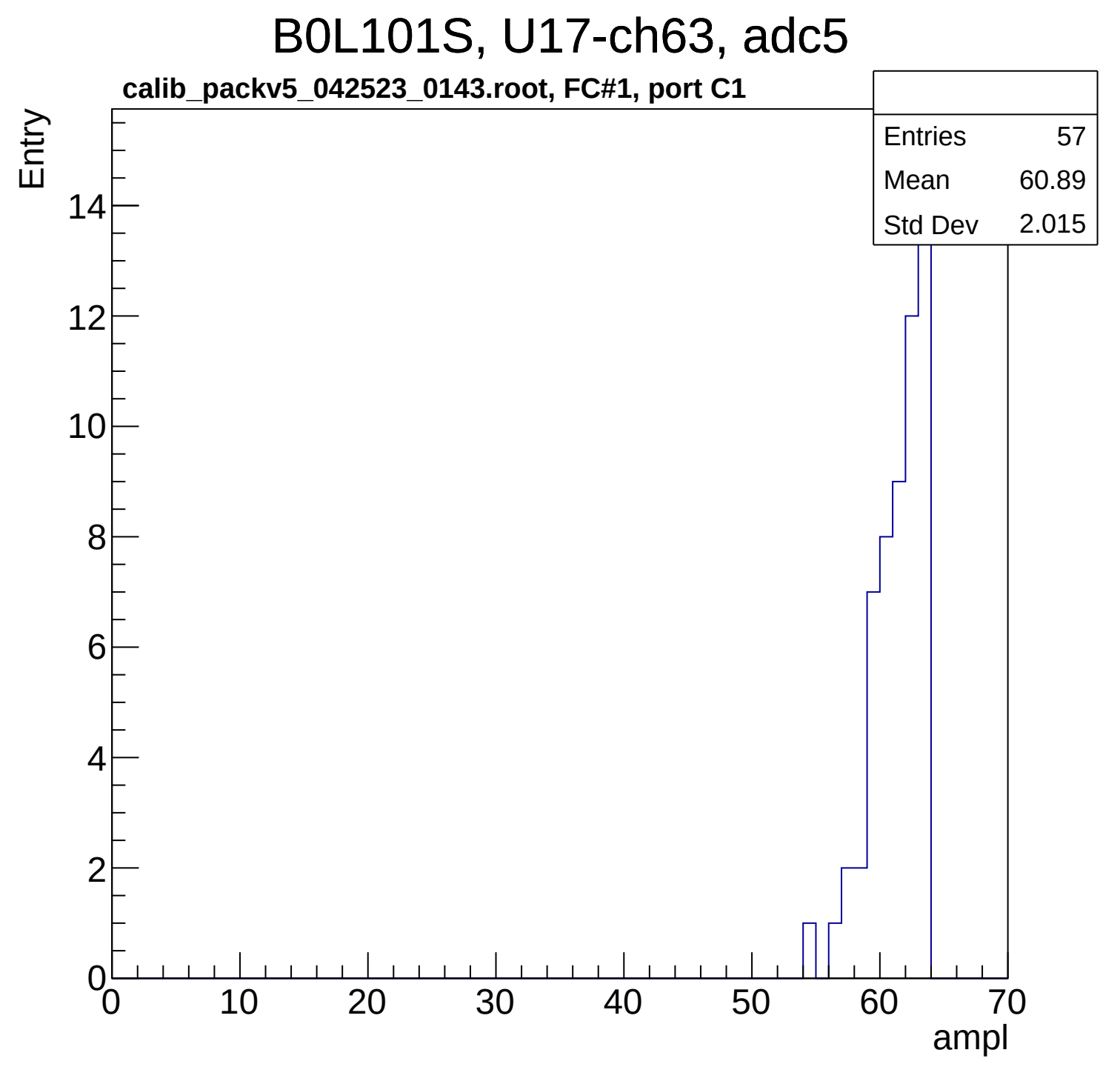

B0L101S, U17-ch63, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 29.97
Std Dev 4.326
Gaus mean : 30.8649
Gaus Width: 3.6917
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

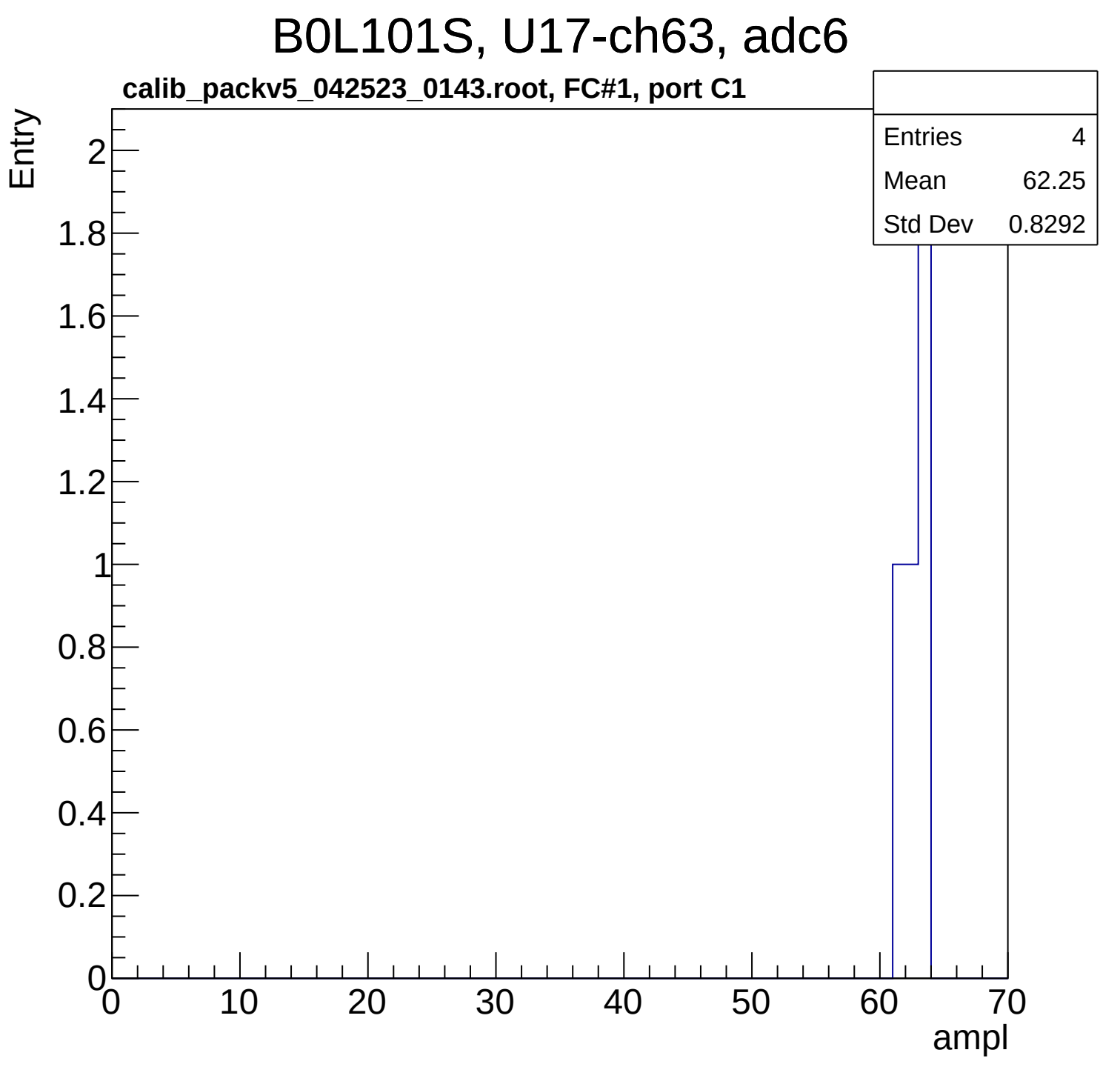

B0L101S, U17-ch63, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 37.31
Std Dev 3.264
Gaus mean : 37.9647
Gaus Width: 3.2063
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

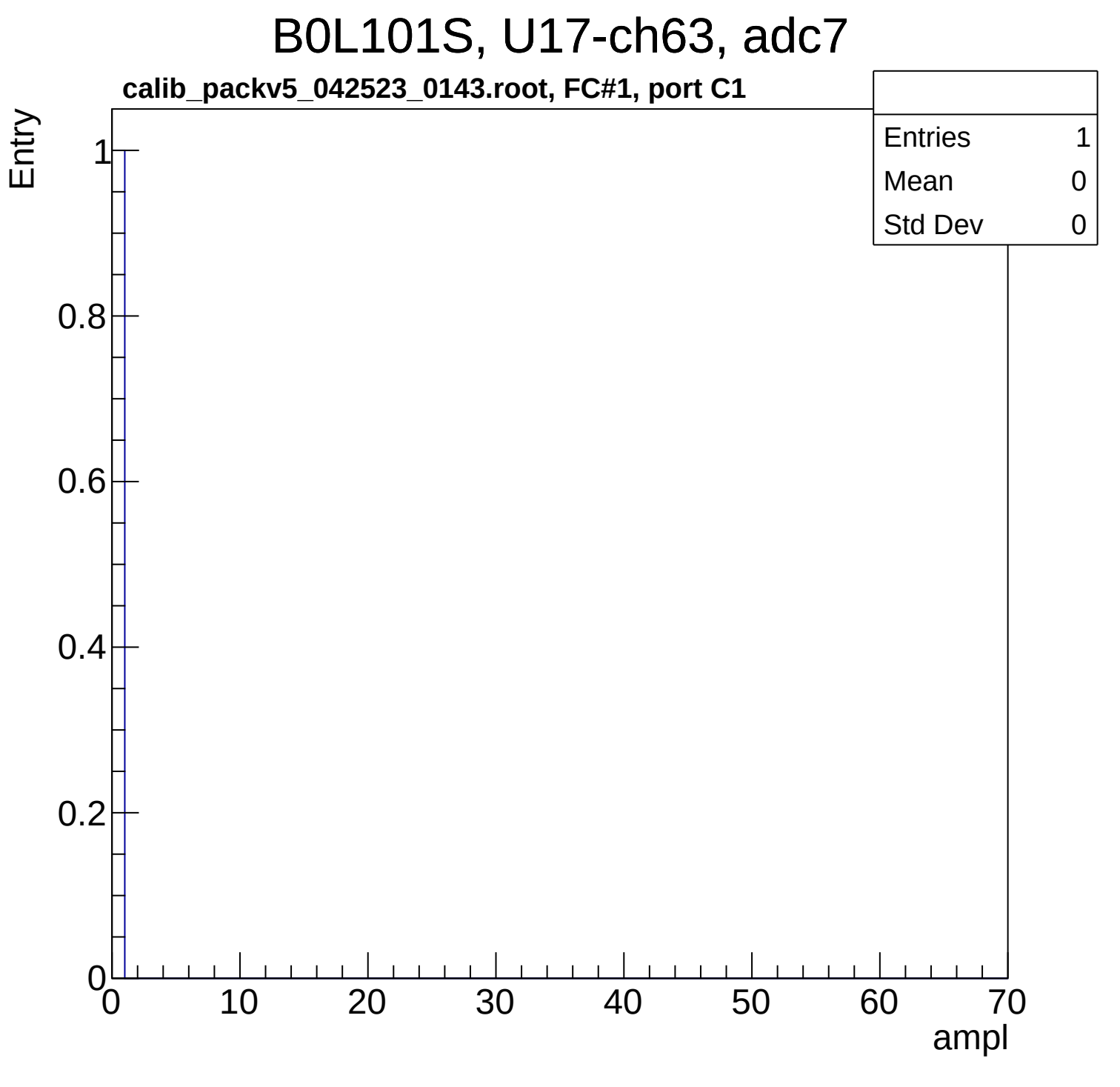

B0L101S, U17-ch63, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 44.33
Std Dev 3.469
Gaus mean : 44.7631
Gaus Width: 3.3817
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

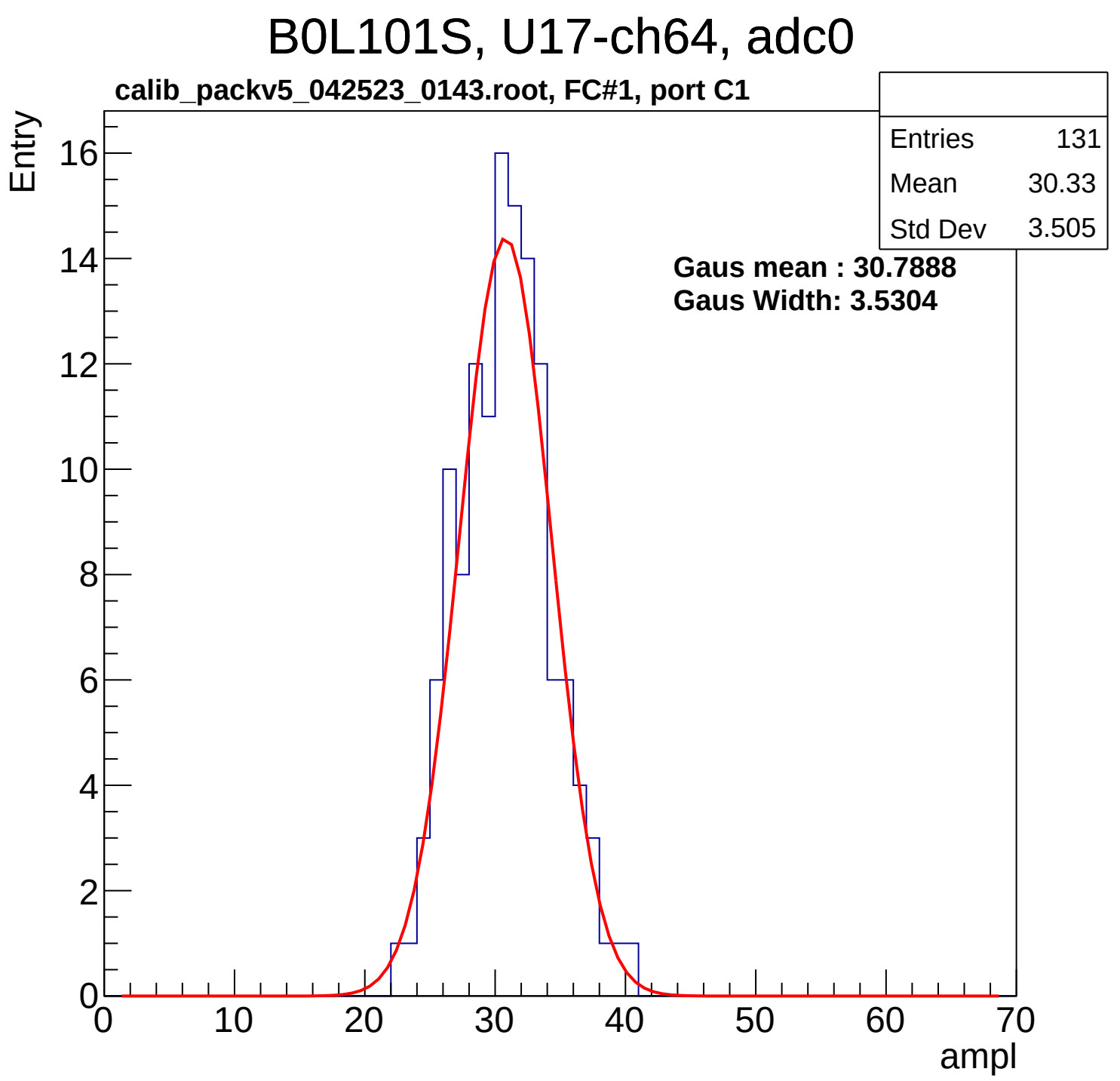

B0L101S, U17-ch63, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 50.72
Std Dev 3.379
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

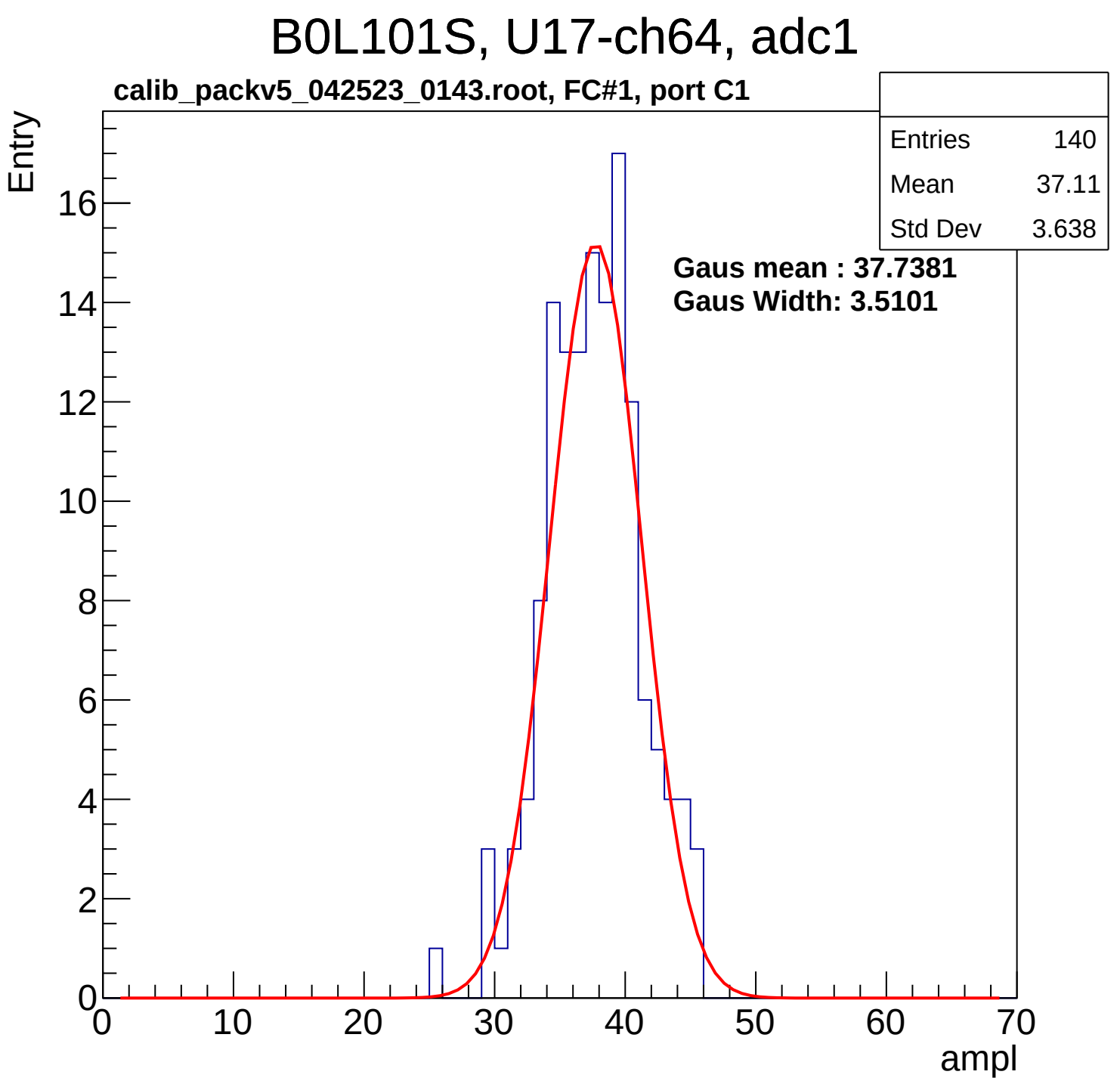

B0L101S, U17-ch63, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 56.35
Std Dev 7.708
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

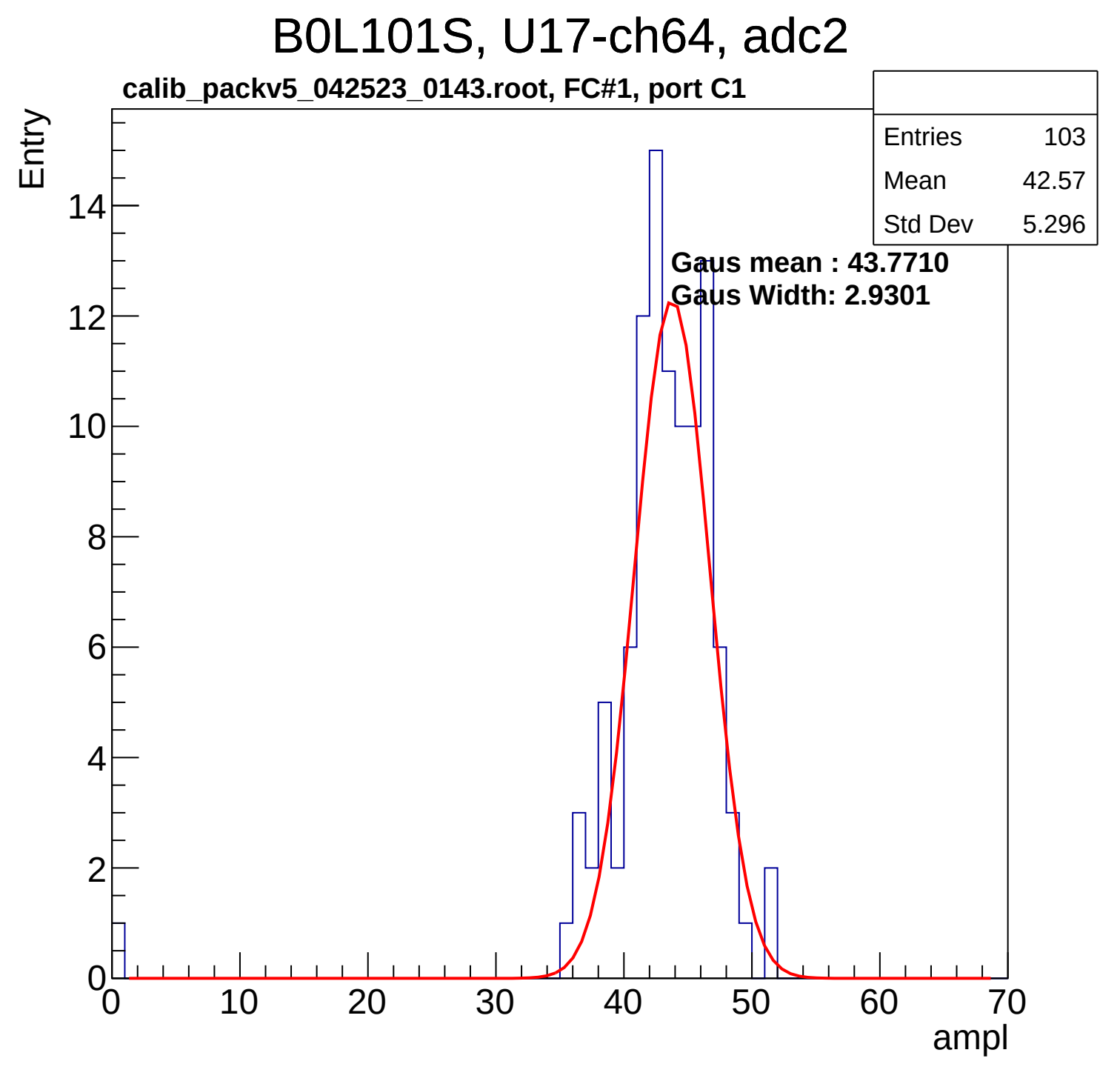

B0L101S, U17-ch63, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 57
Mean 60.89
Std Dev 2.015
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

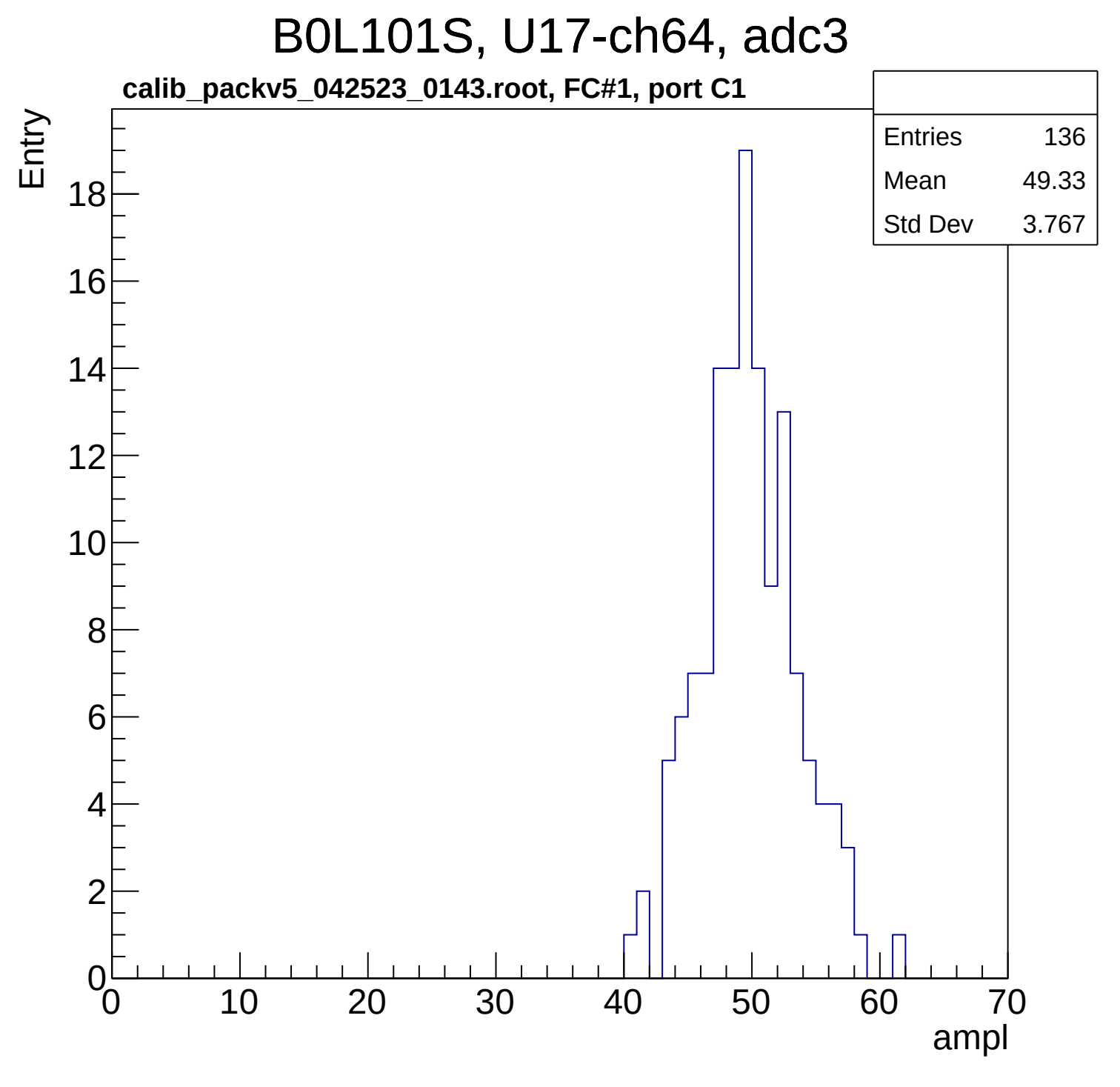

B0L101S, U17-ch63, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

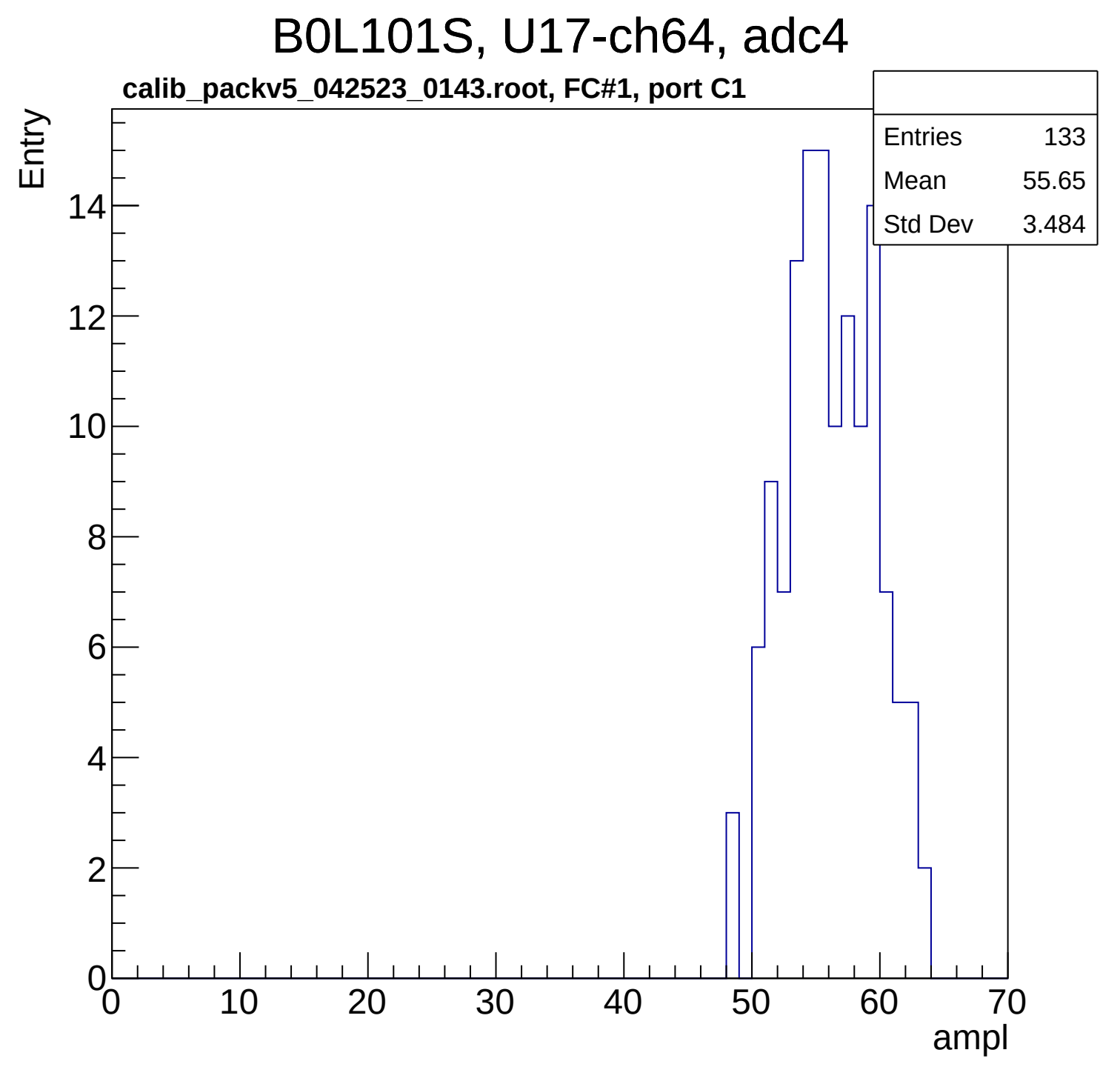

B0L101S, U17-ch63, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

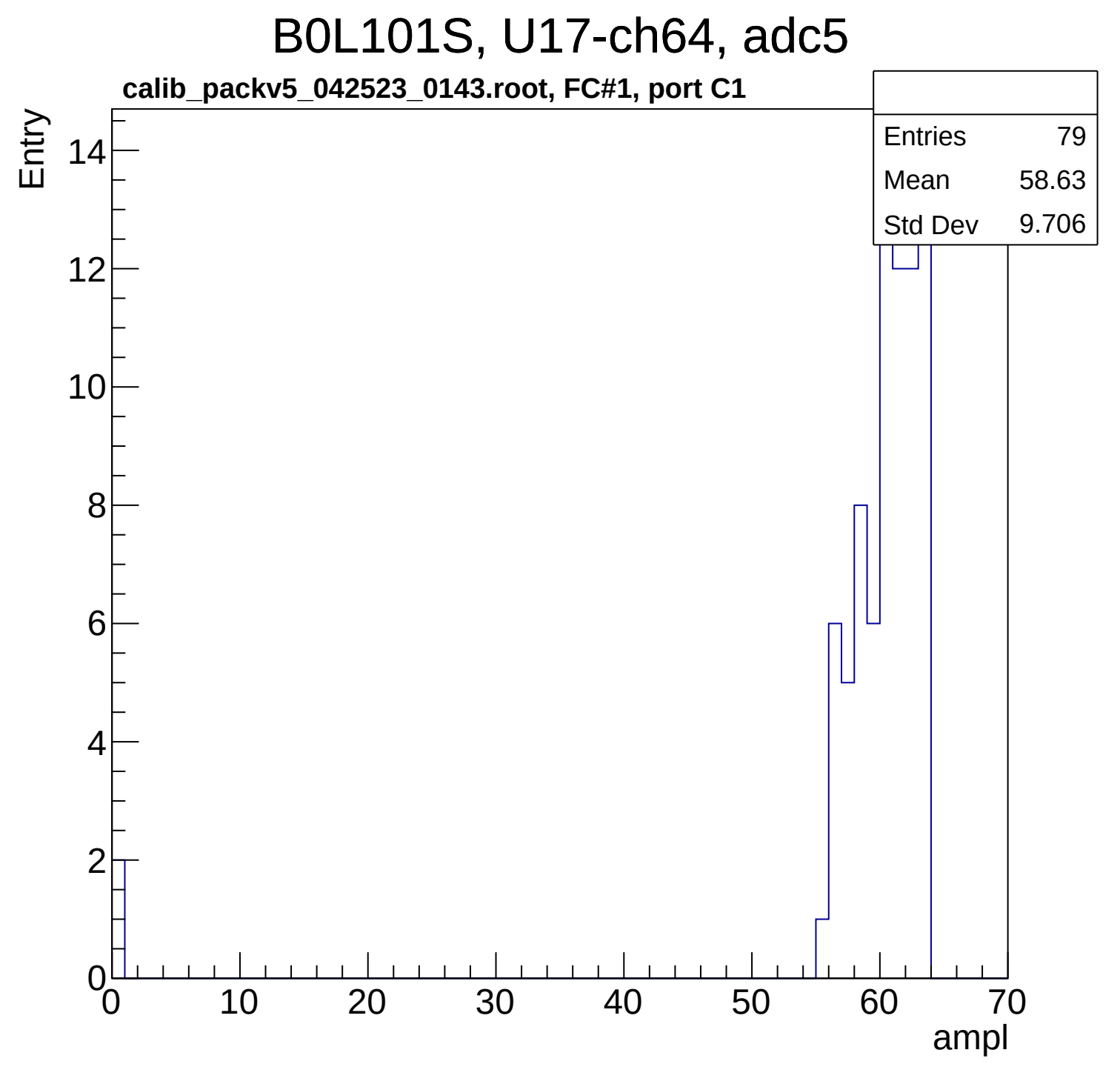

B0L101S, U17-ch64, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 30.33
Std Dev 3.505
Gaus mean : 30.7888
Gaus Width: 3.5304
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

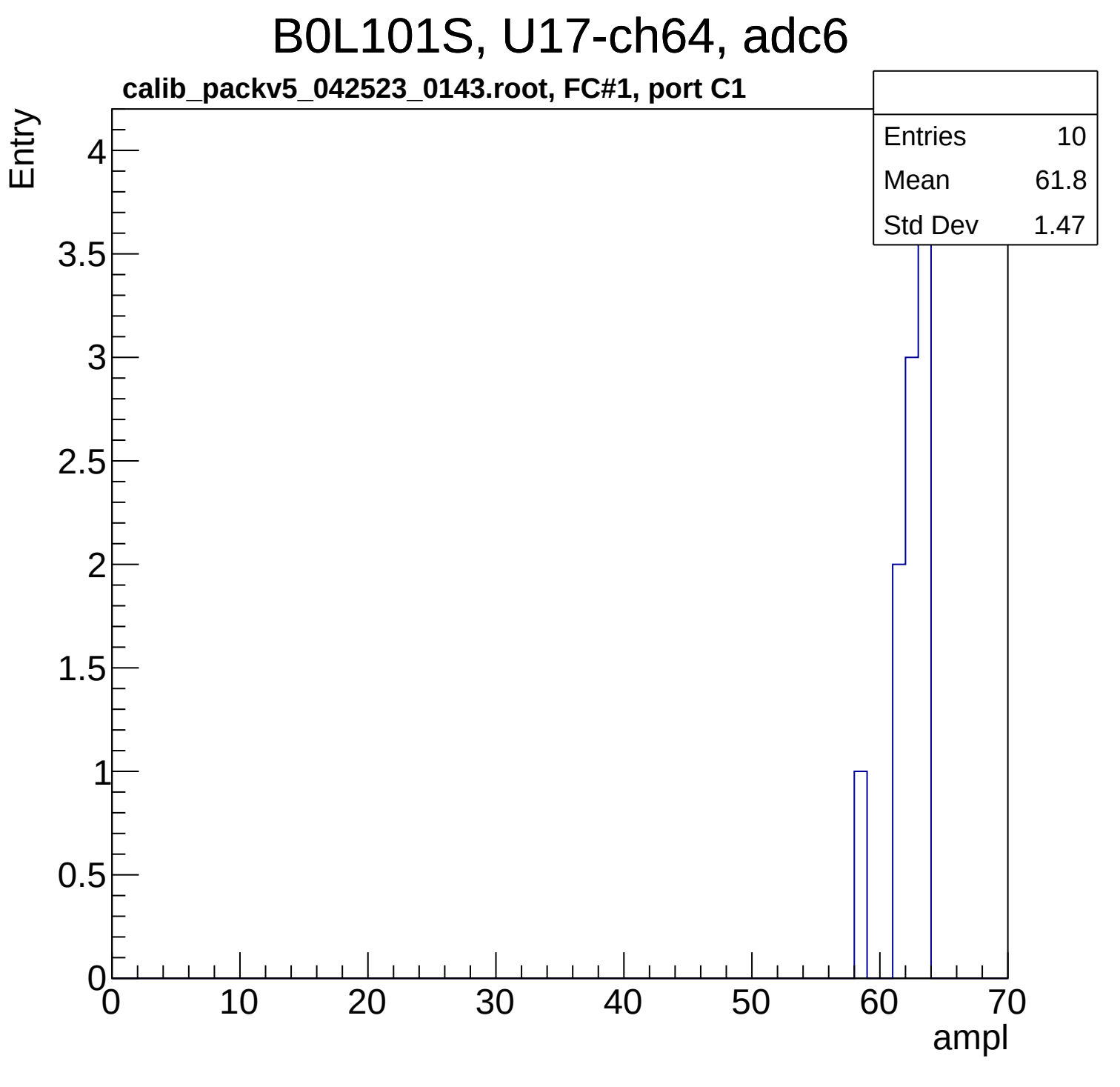

B0L101S, U17-ch64, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 37.11
Std Dev 3.638
Gaus mean : 37.7381
Gaus Width: 3.5101
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

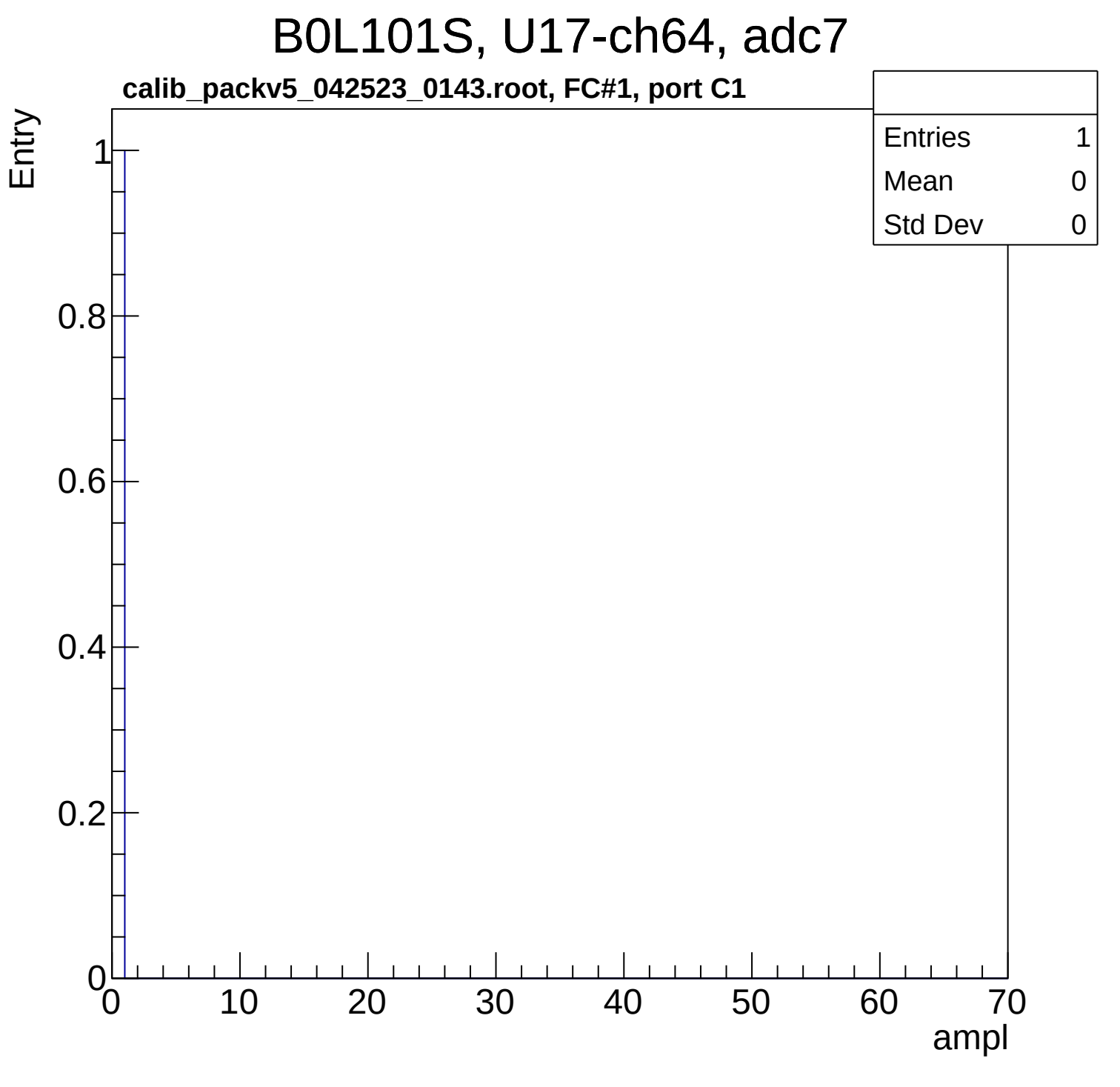

B0L101S, U17-ch64, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 42.57
Std Dev 5.296
Gaus mean : 43.7710
Gaus Width: 2.9301
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

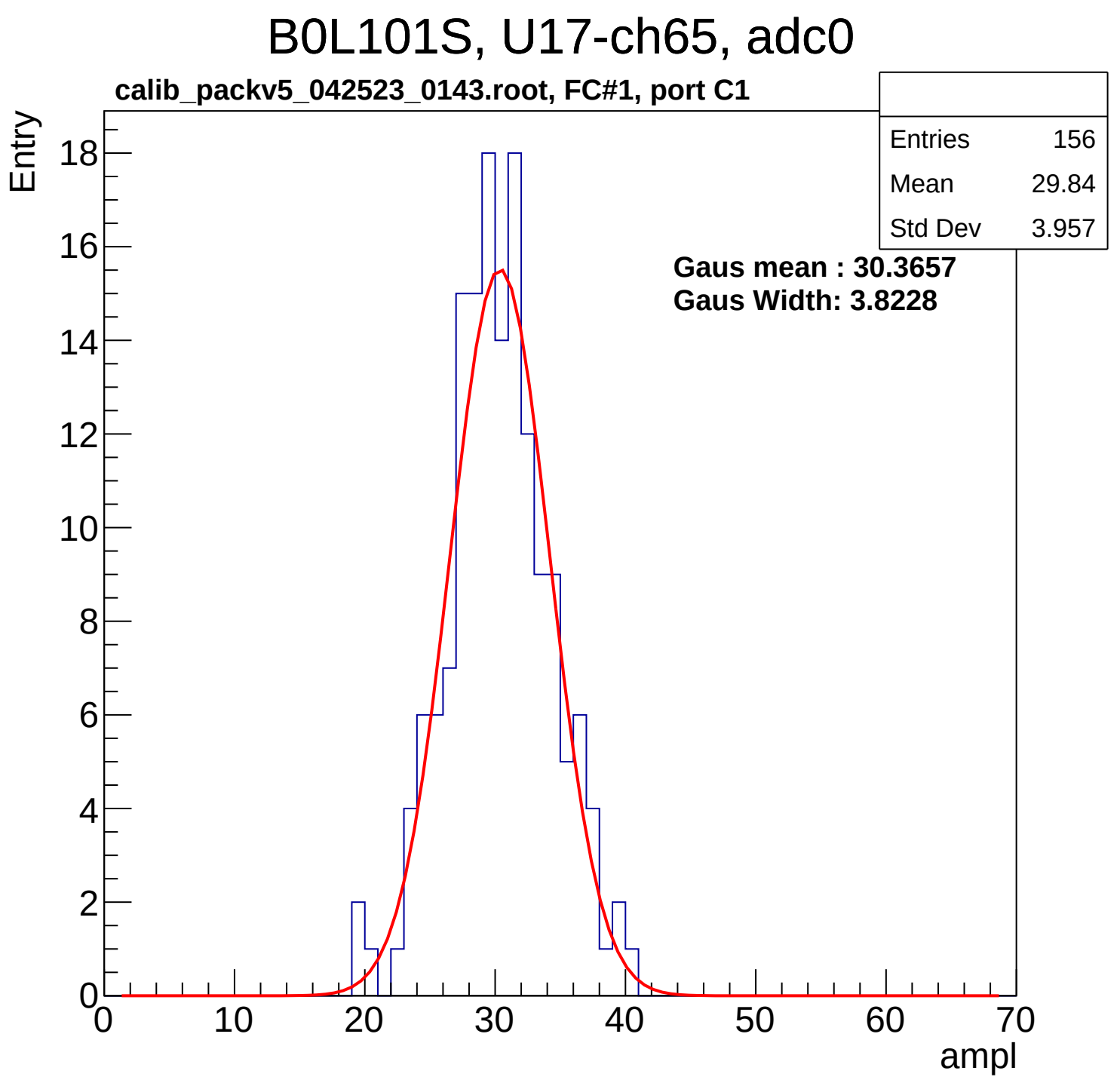

B0L101S, U17-ch64, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 49.33
Std Dev 3.767
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

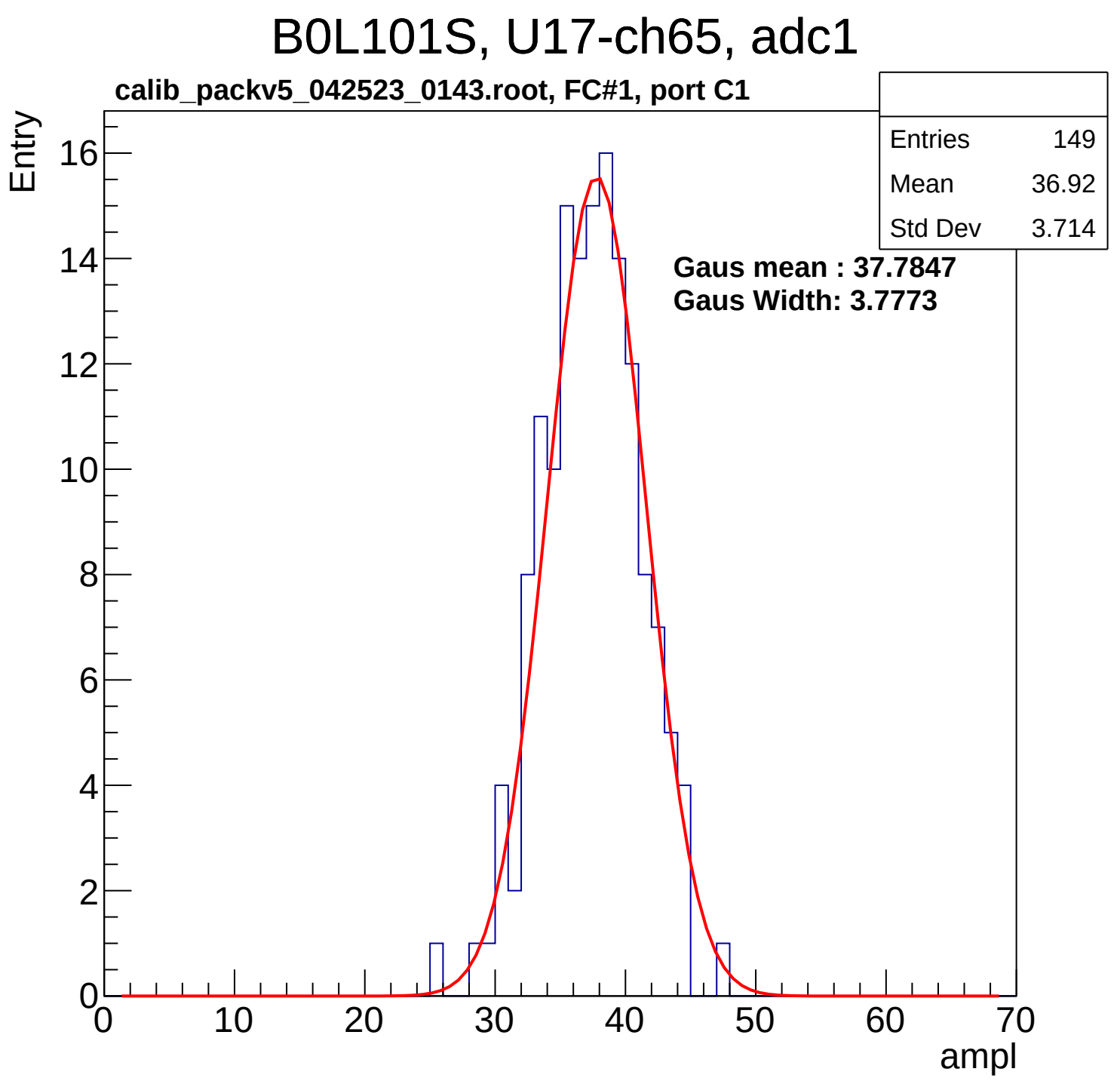

B0L101S, U17-ch64, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 55.65
Std Dev 3.484
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

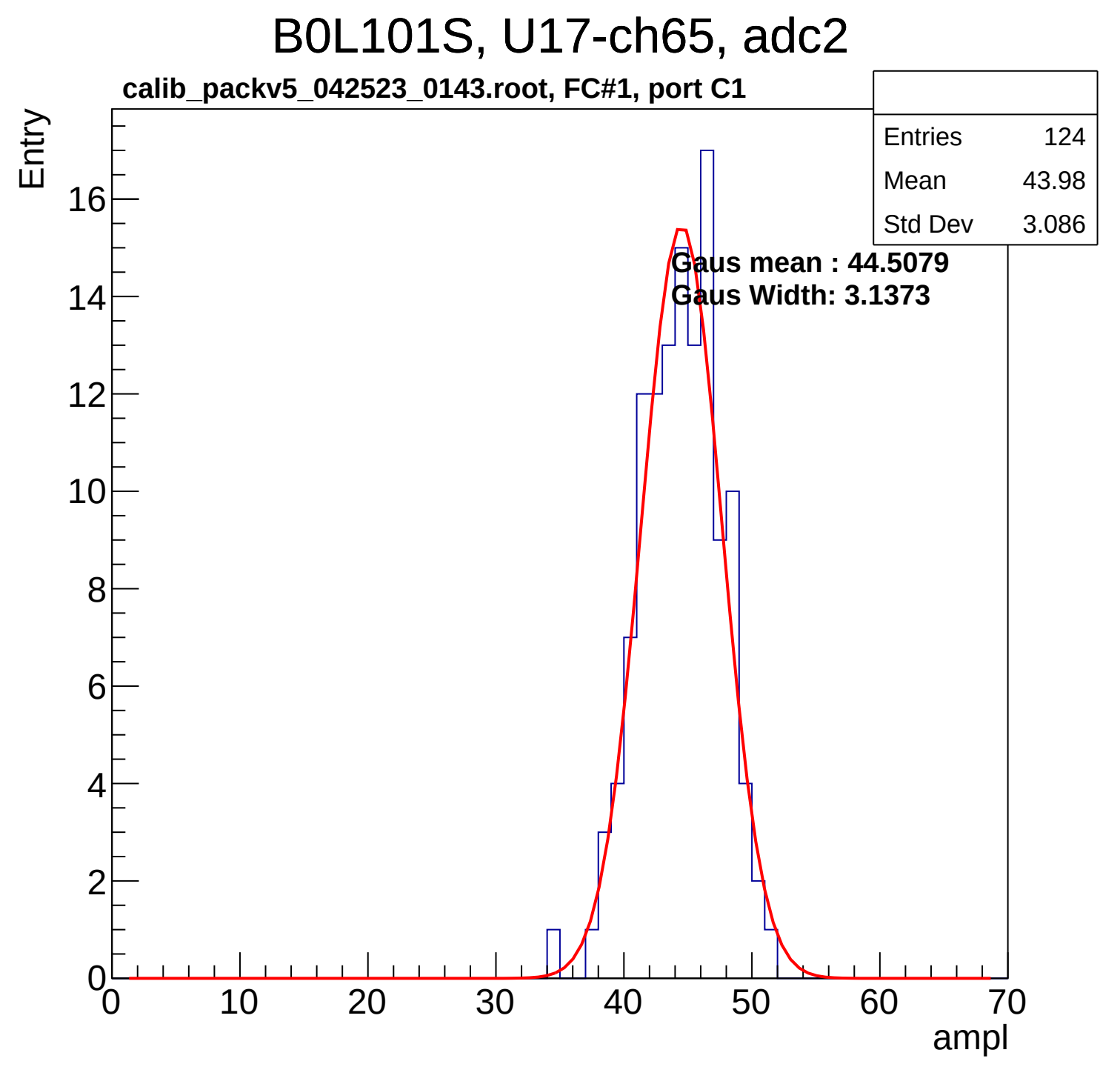

B0L101S, U17-ch64, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 79
Mean 58.63
Std Dev 9.706
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

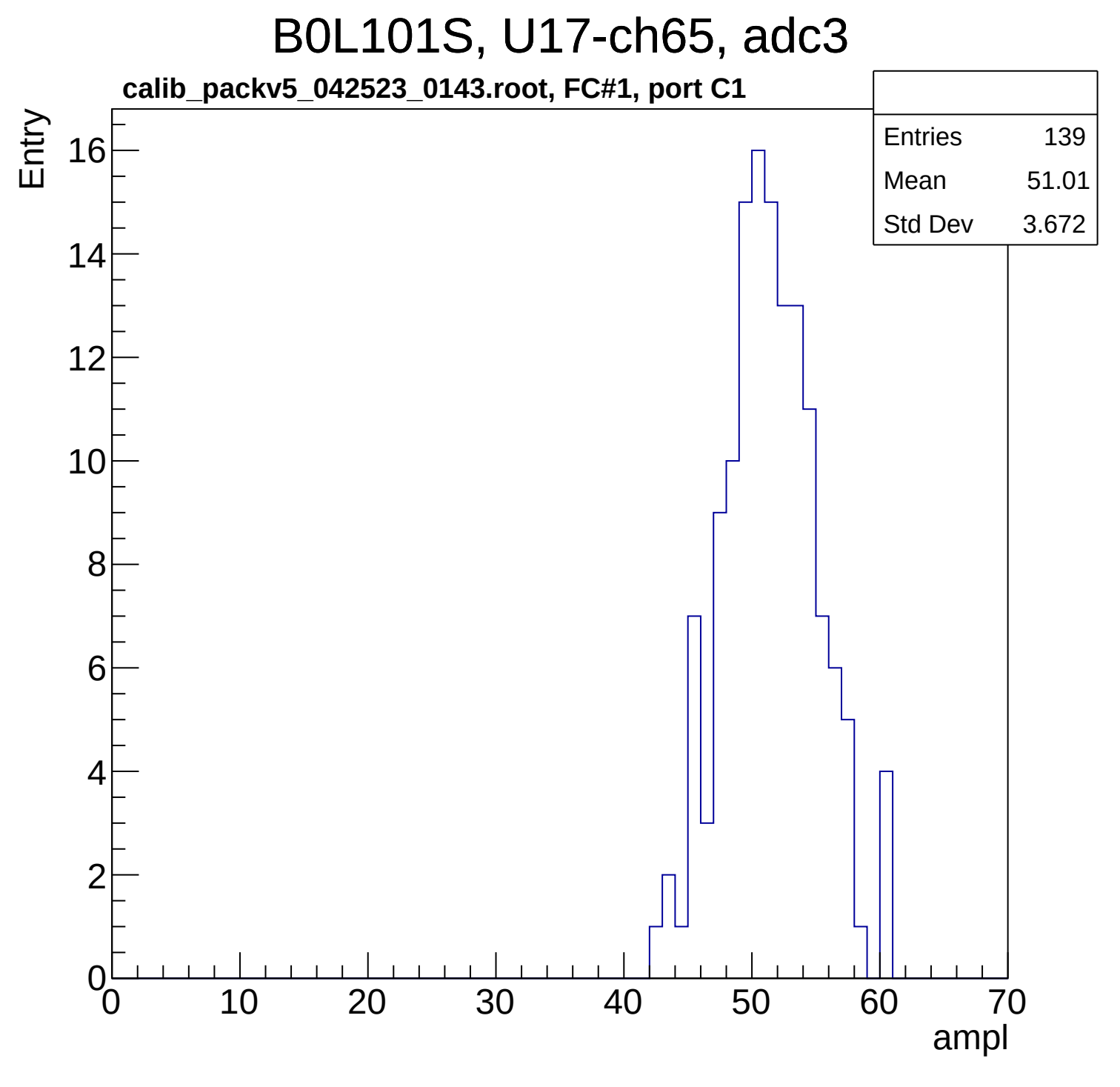

B0L101S, U17-ch64, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

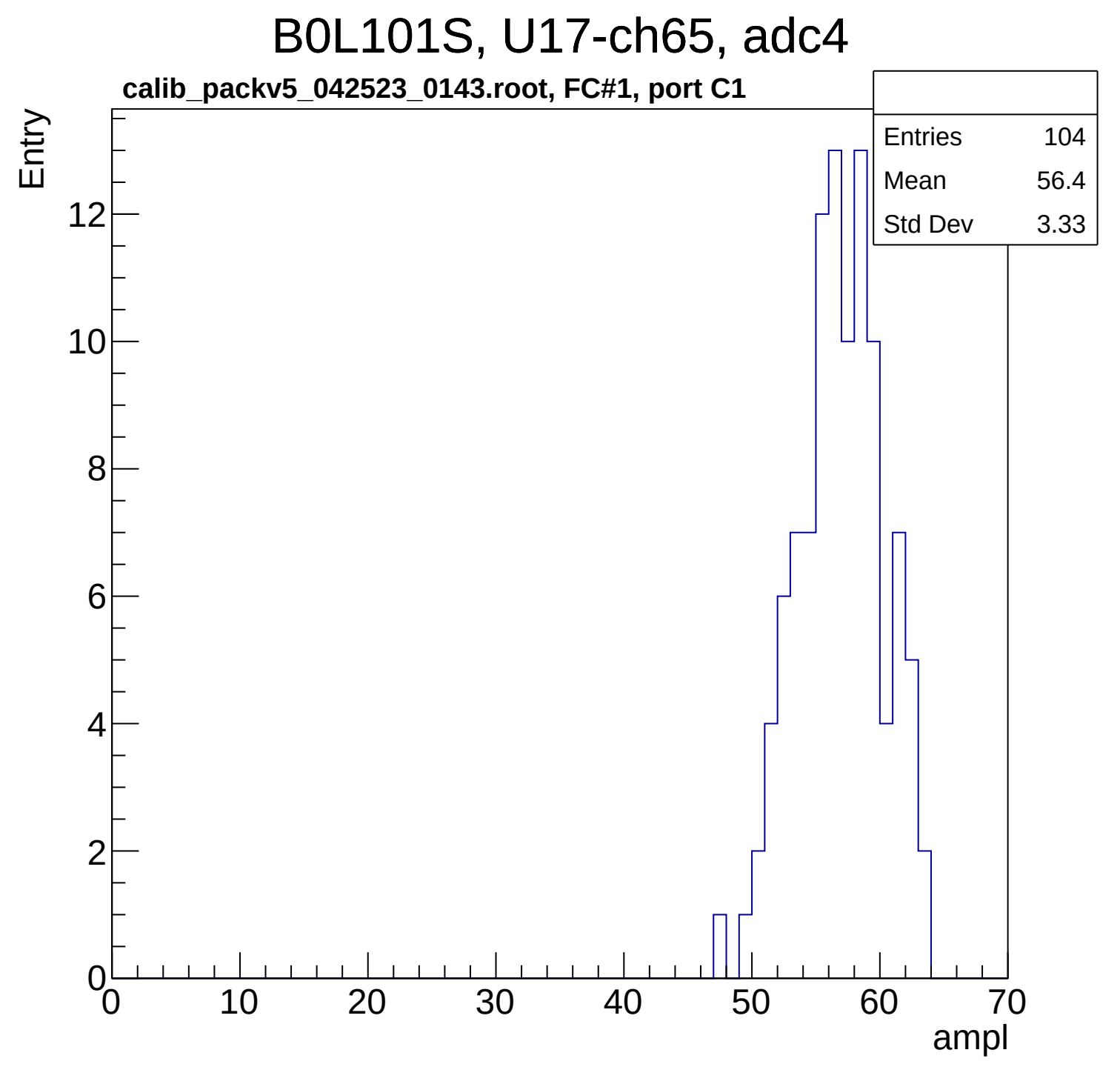

B0L101S, U17-ch64, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

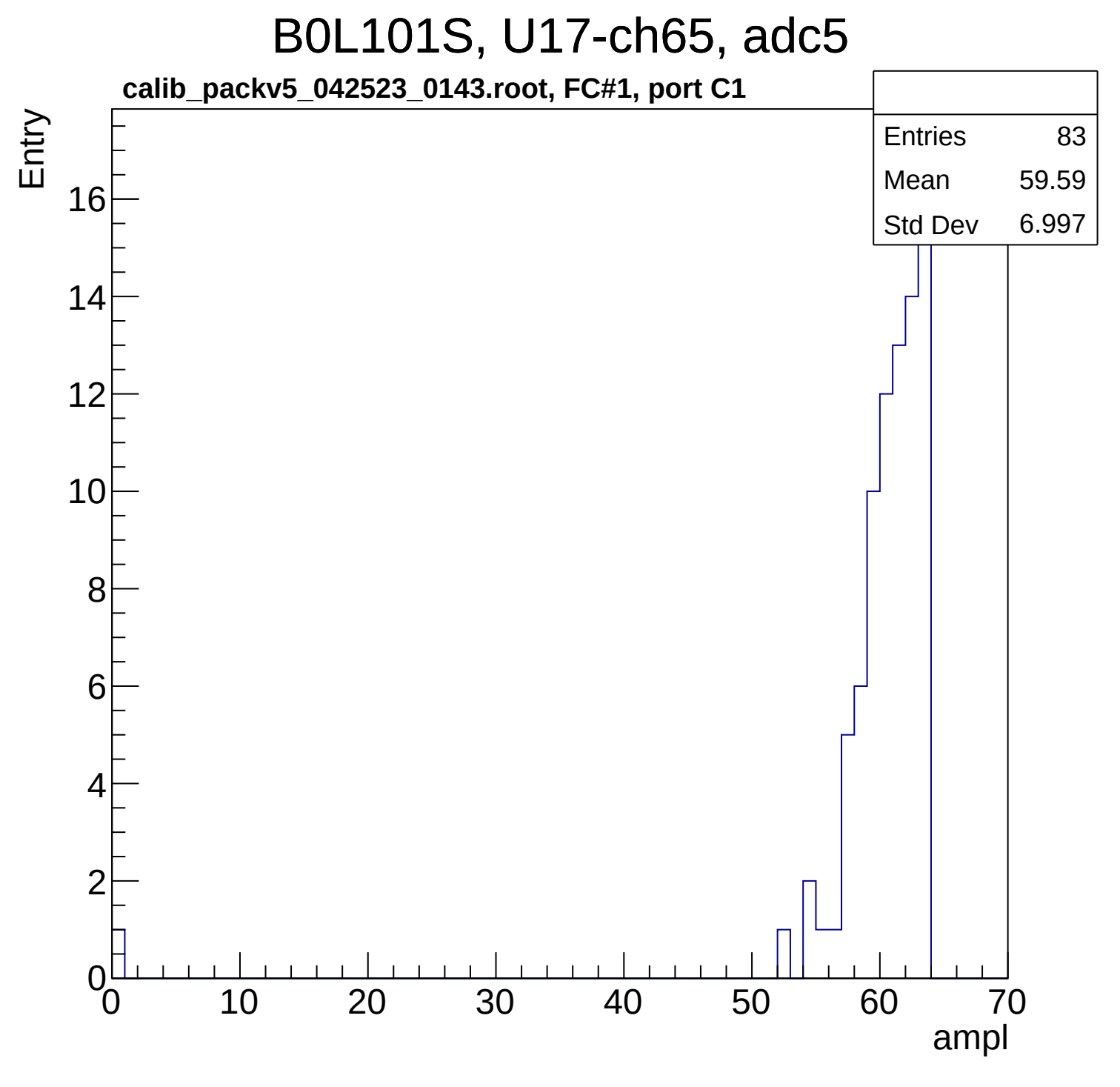

B0L101S, U17-ch65, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 29.84
Std Dev 3.957
Gaus mean : 30.3657
Gaus Width: 3.8228
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

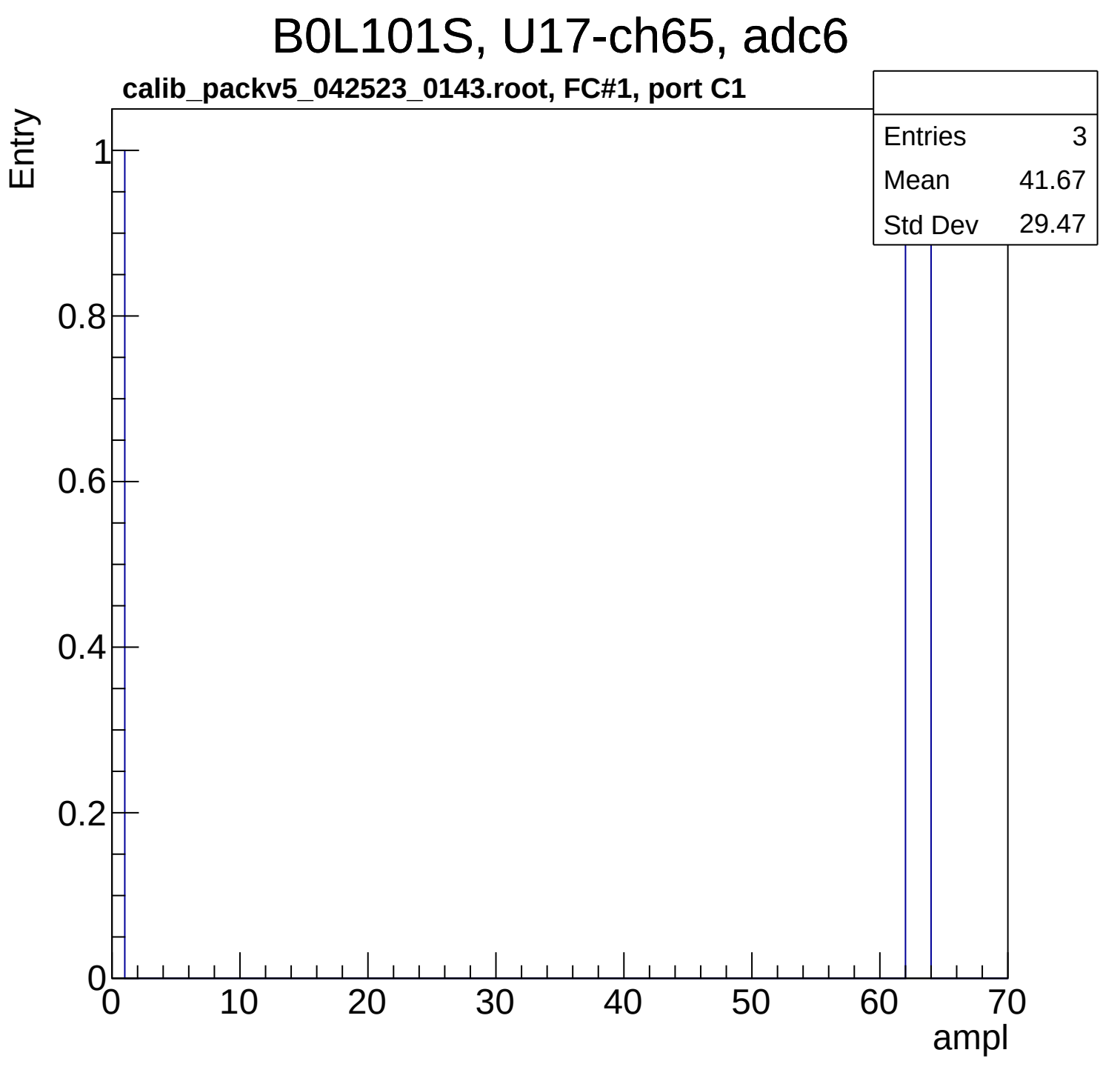

B0L101S, U17-ch65, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 36.92
Std Dev 3.714
Gaus mean : 37.7847
Gaus Width: 3.7773
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

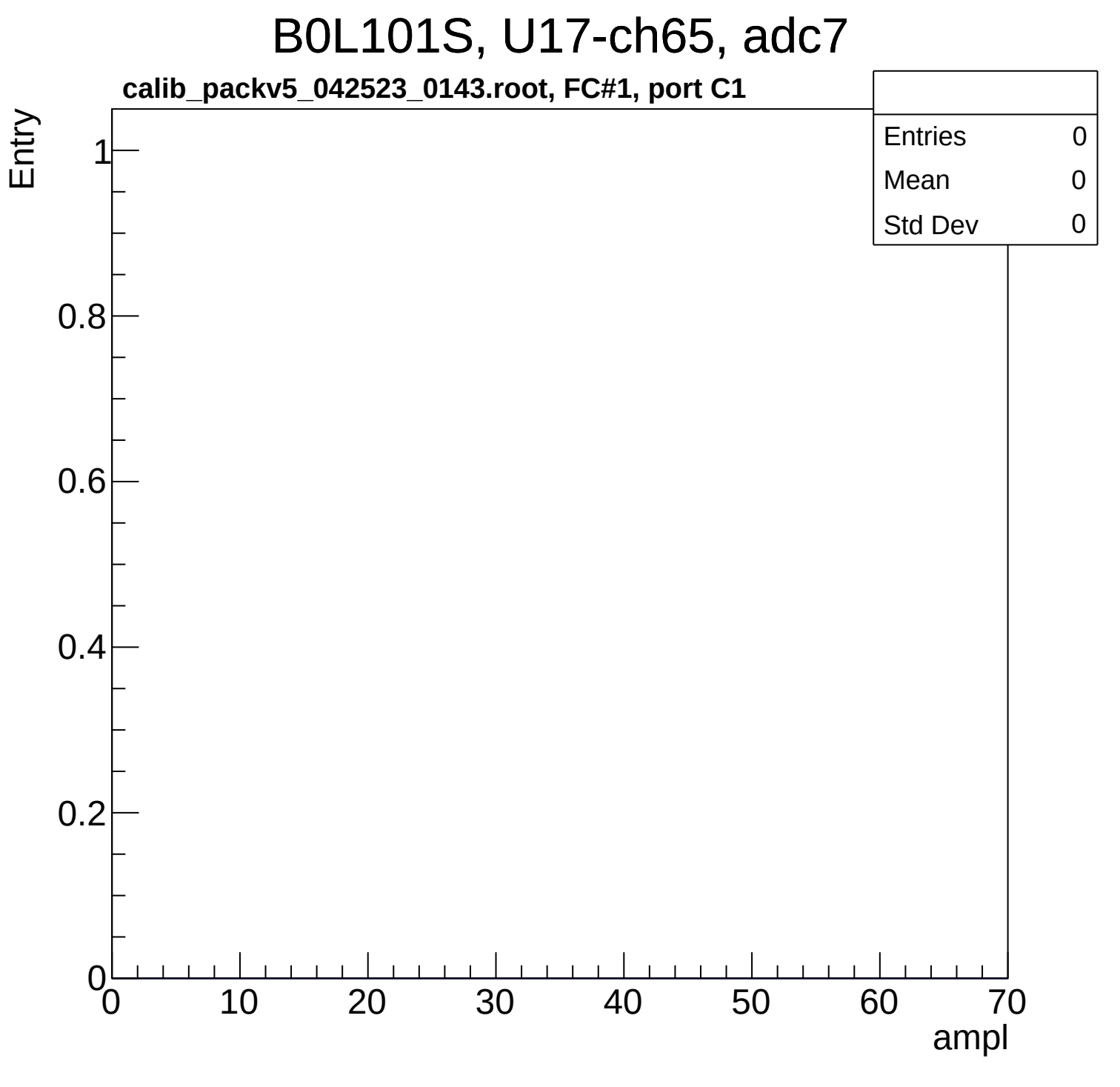

B0L101S, U17-ch65, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 43.98
Std Dev 3.086
Gaus mean : 44.5079
Gaus Width: 3.1373
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

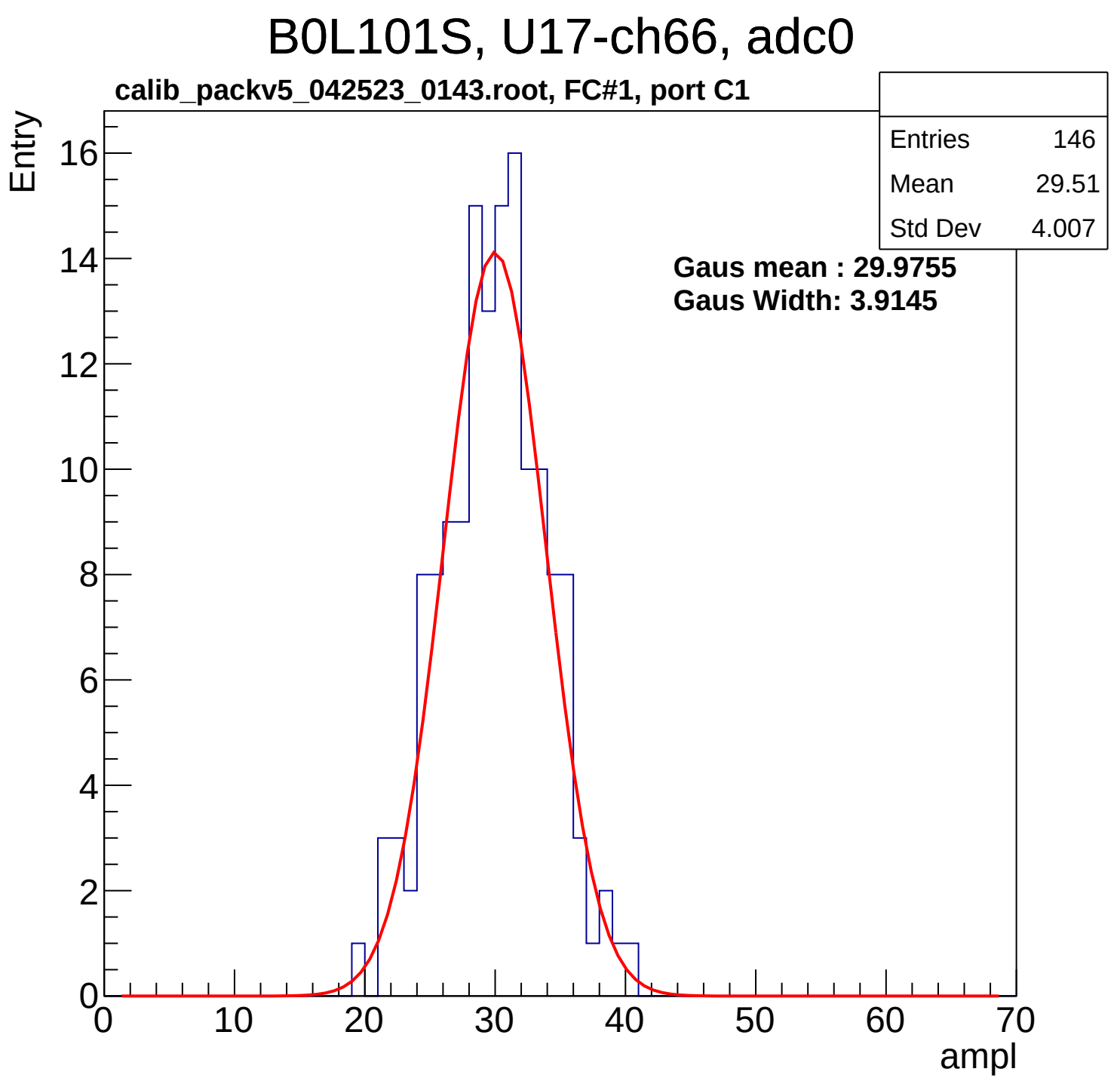

B0L101S, U17-ch65, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 51.01
Std Dev 3.672
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

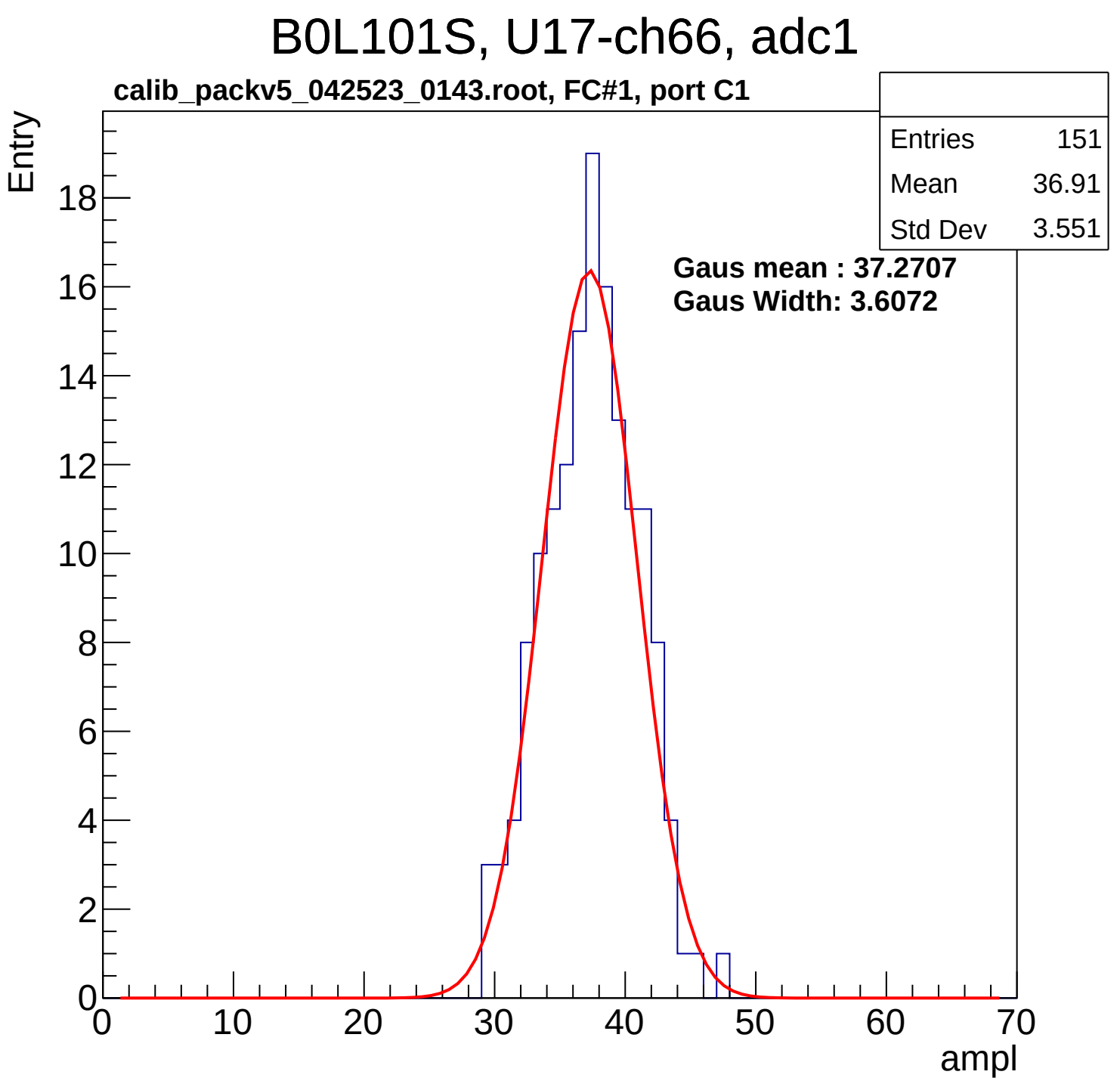

B0L101S, U17-ch65, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 56.4
Std Dev 3.33
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

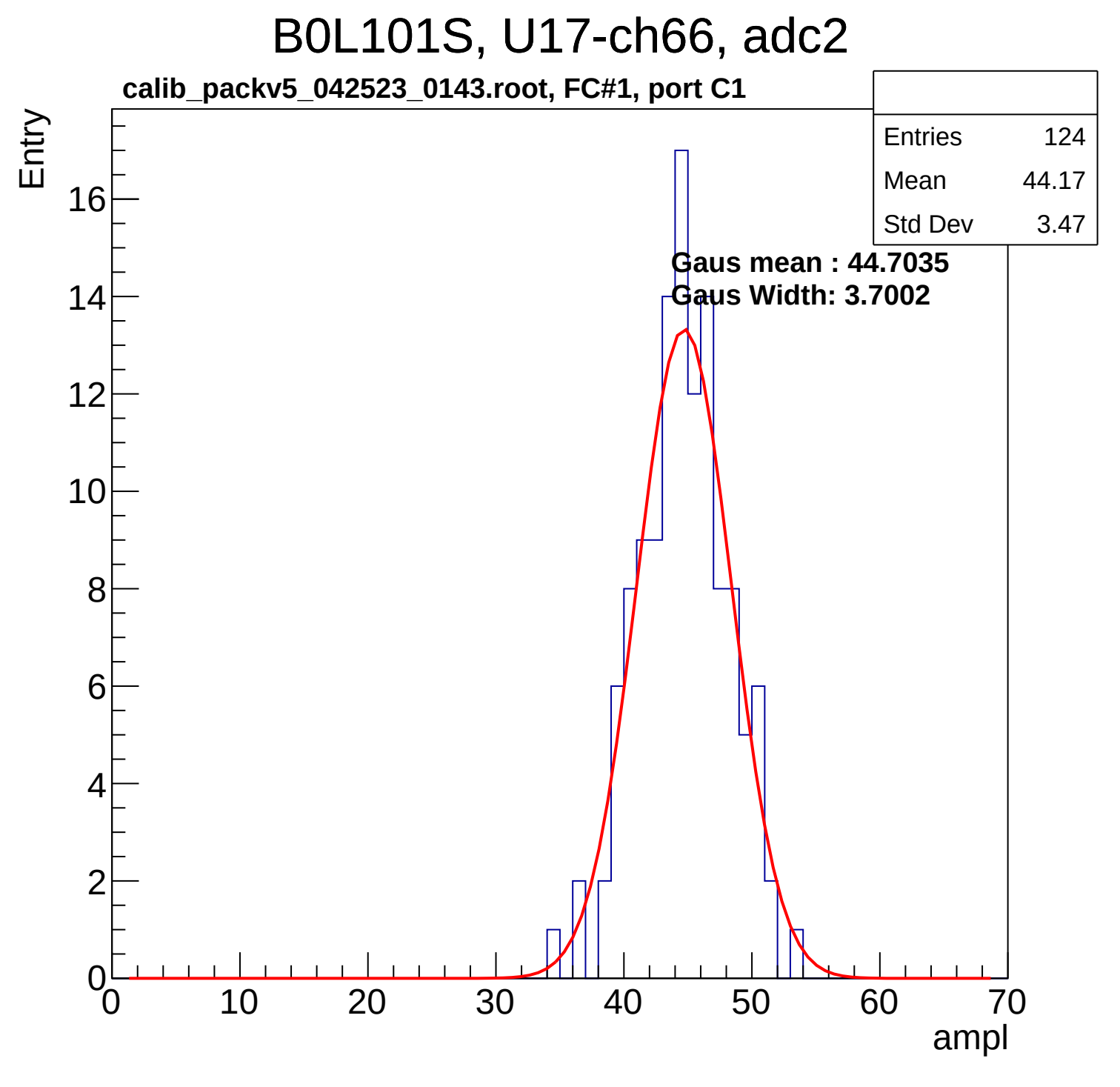

B0L101S, U17-ch65, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 83
Mean 59.59
Std Dev 6.997
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

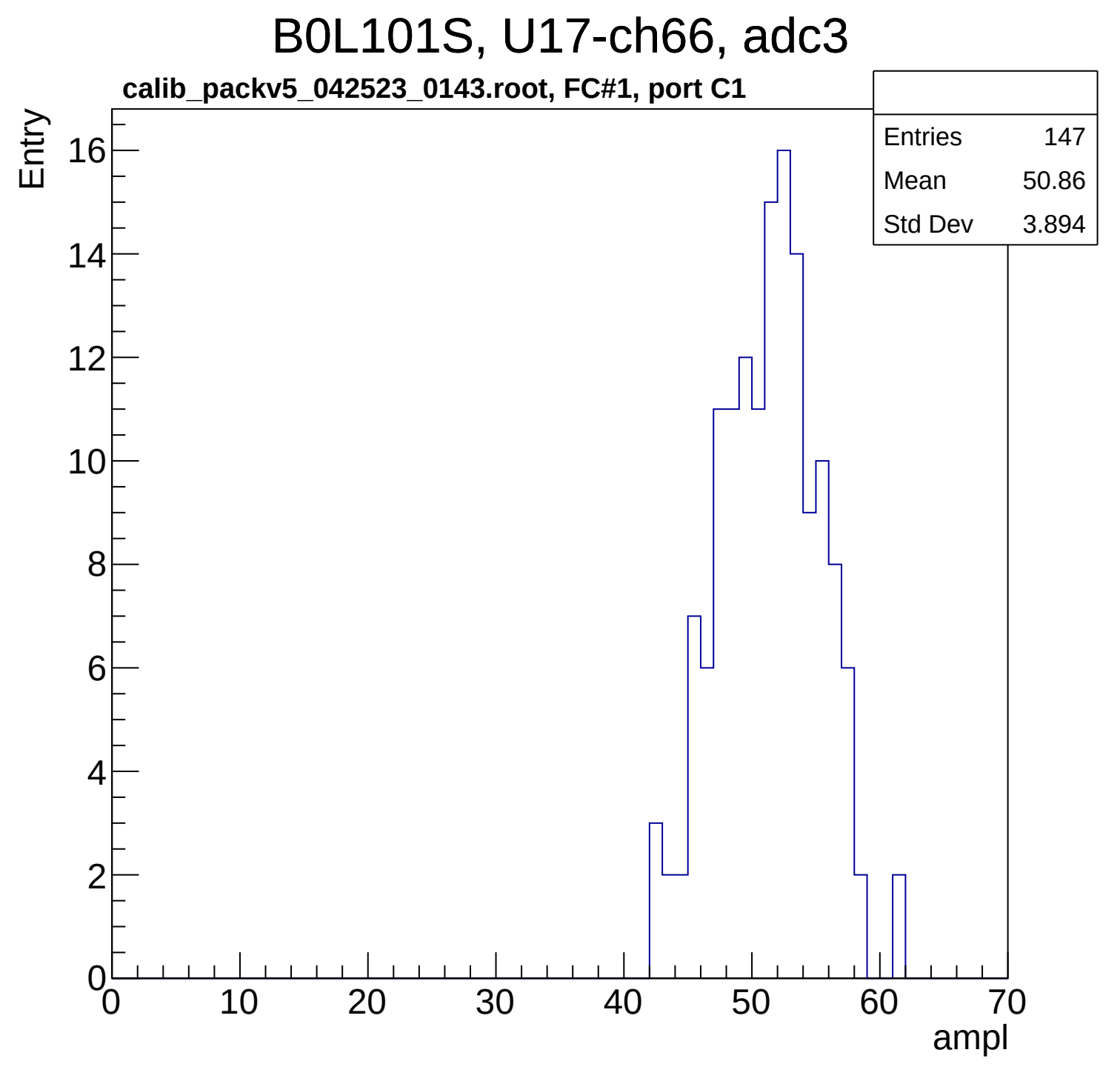

B0L101S, U17-ch65, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

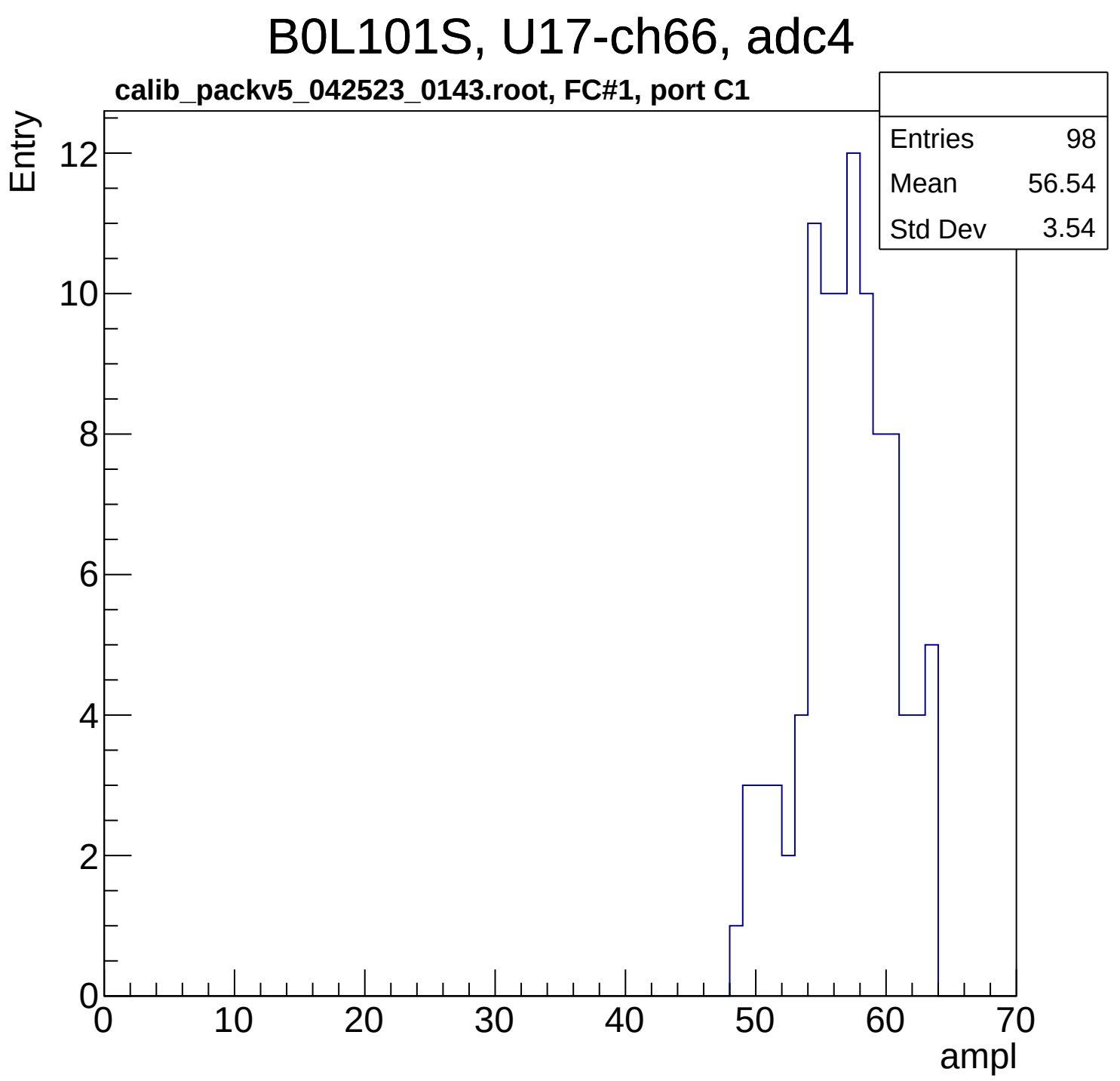

B0L101S, U17-ch65, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

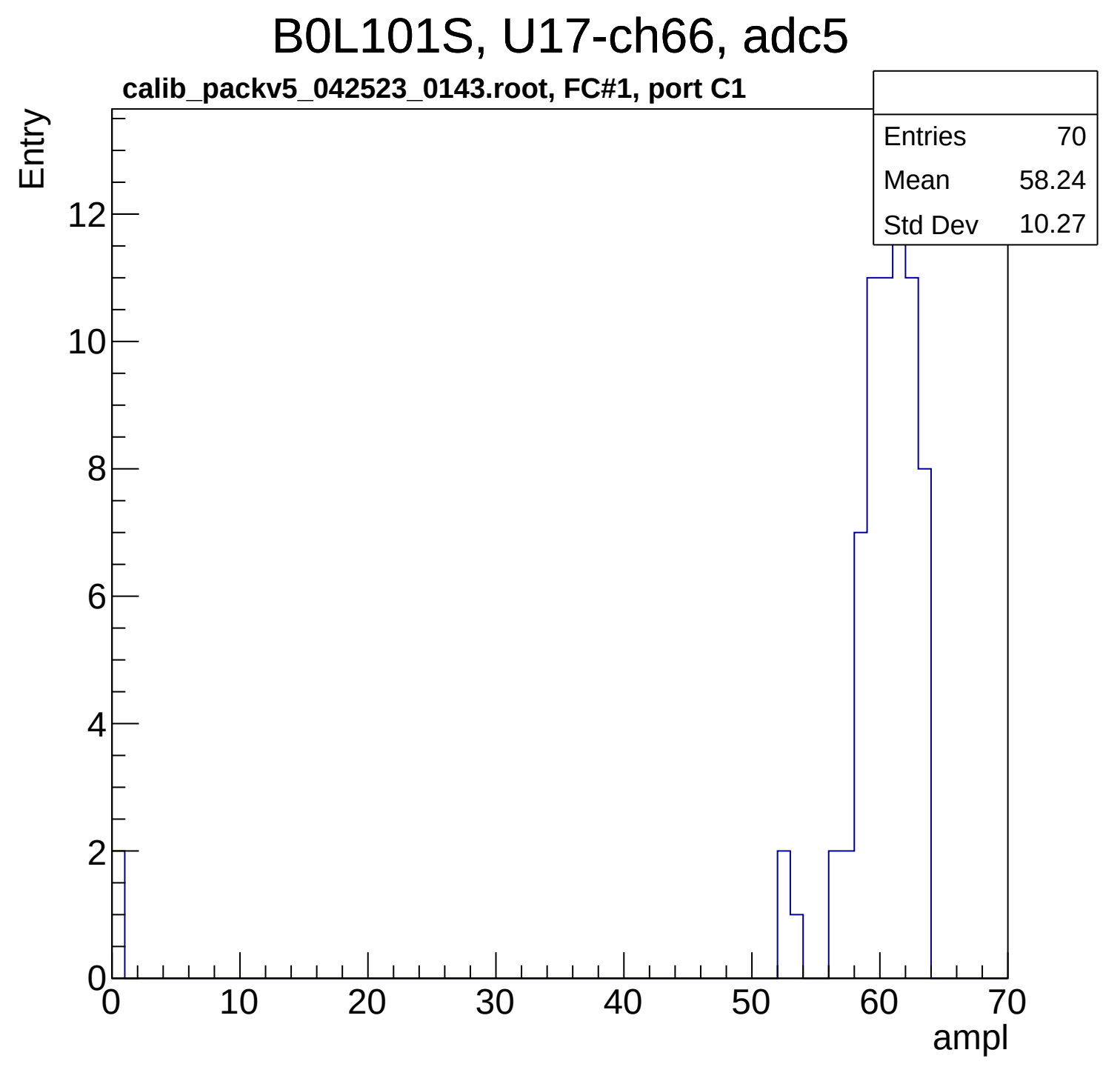

B0L101S, U17-ch66, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 29.51
Std Dev 4.007
Gaus mean : 29.9755
Gaus Width: 3.9145
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

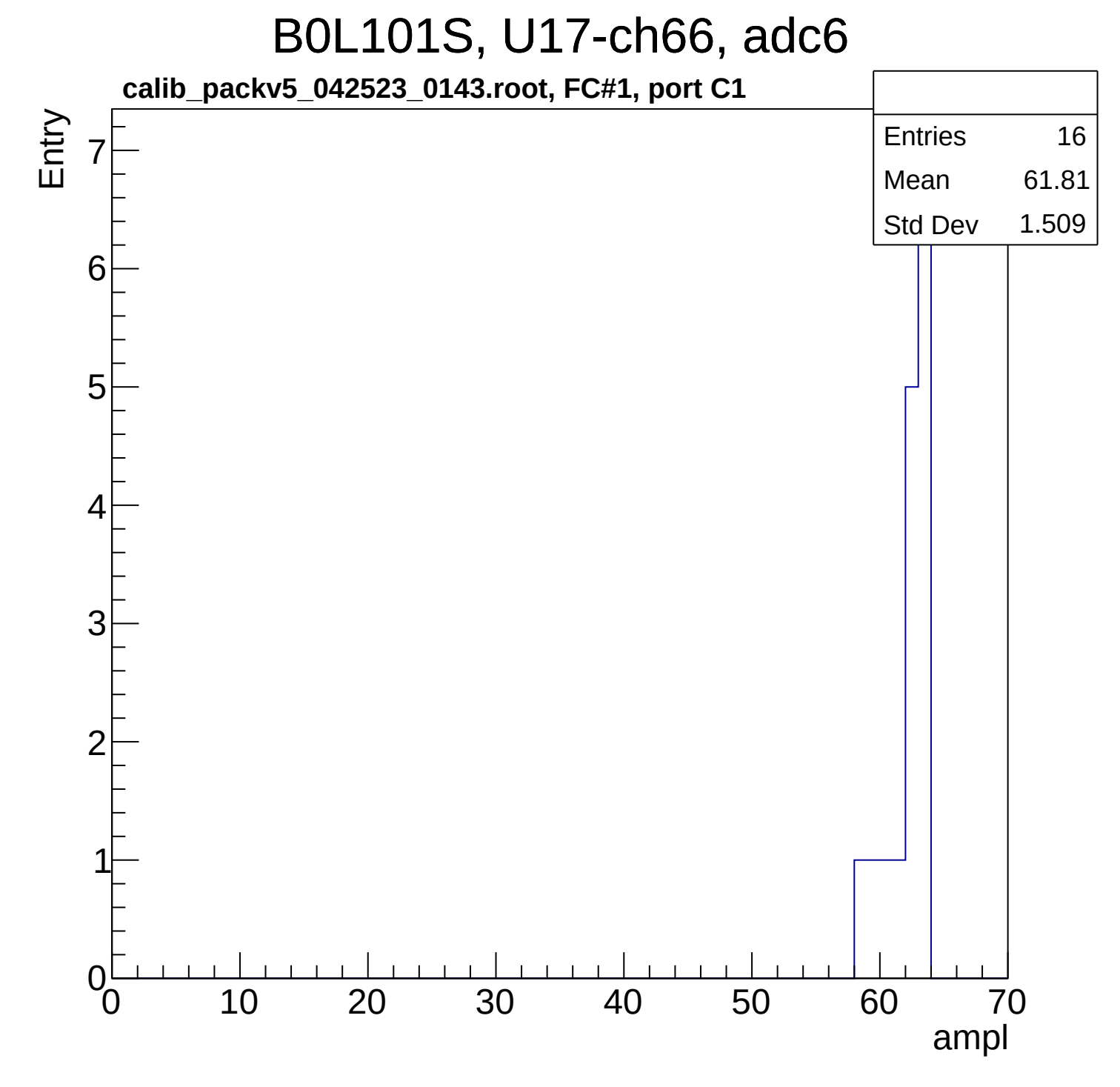

B0L101S, U17-ch66, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 36.91
Std Dev 3.551
Gaus mean : 37.2707
Gaus Width: 3.6072
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

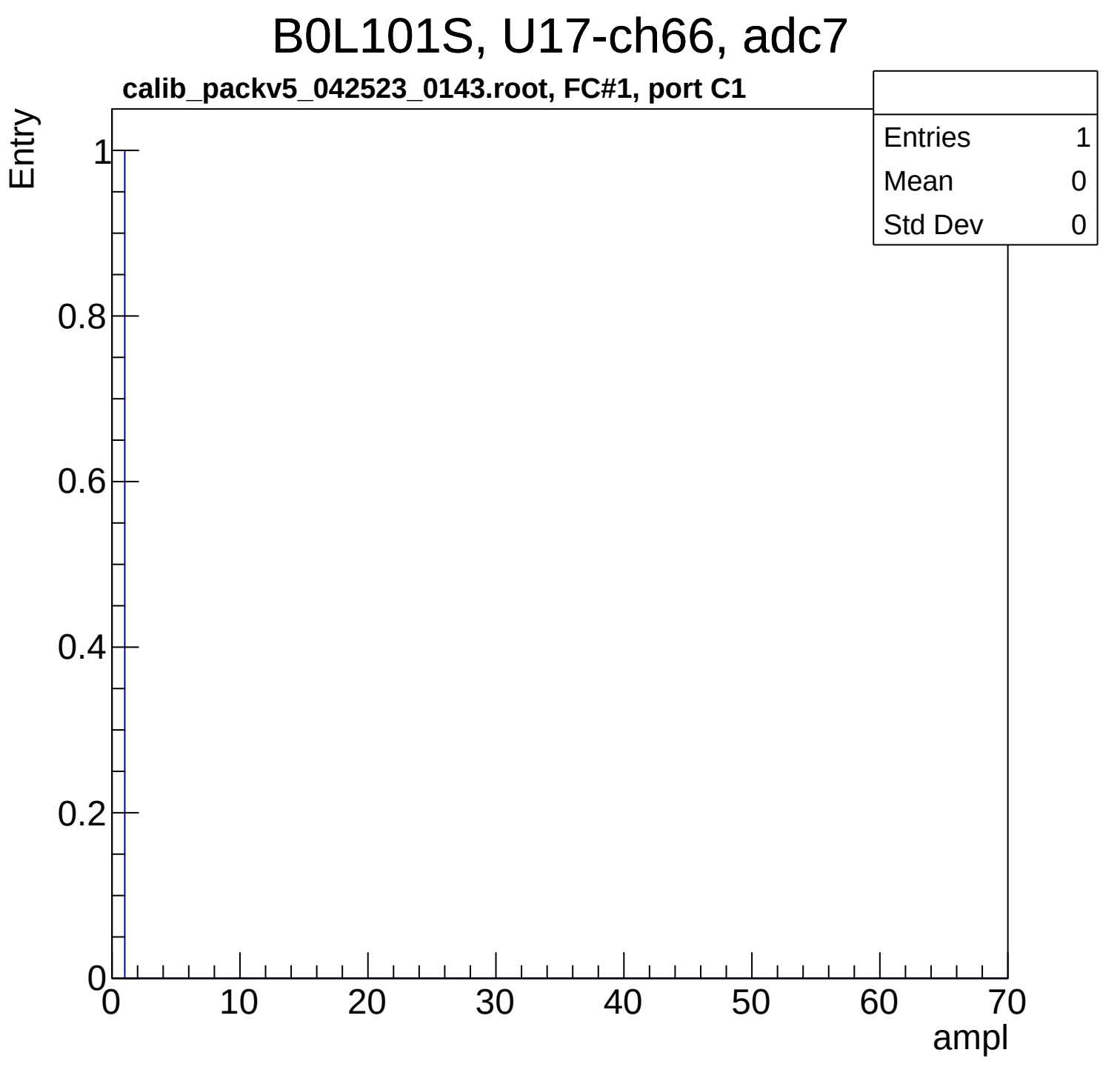

B0L101S, U17-ch66, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 44.17
Std Dev 3.47
Gaus mean : 44.7035
Gaus Width: 3.7002
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

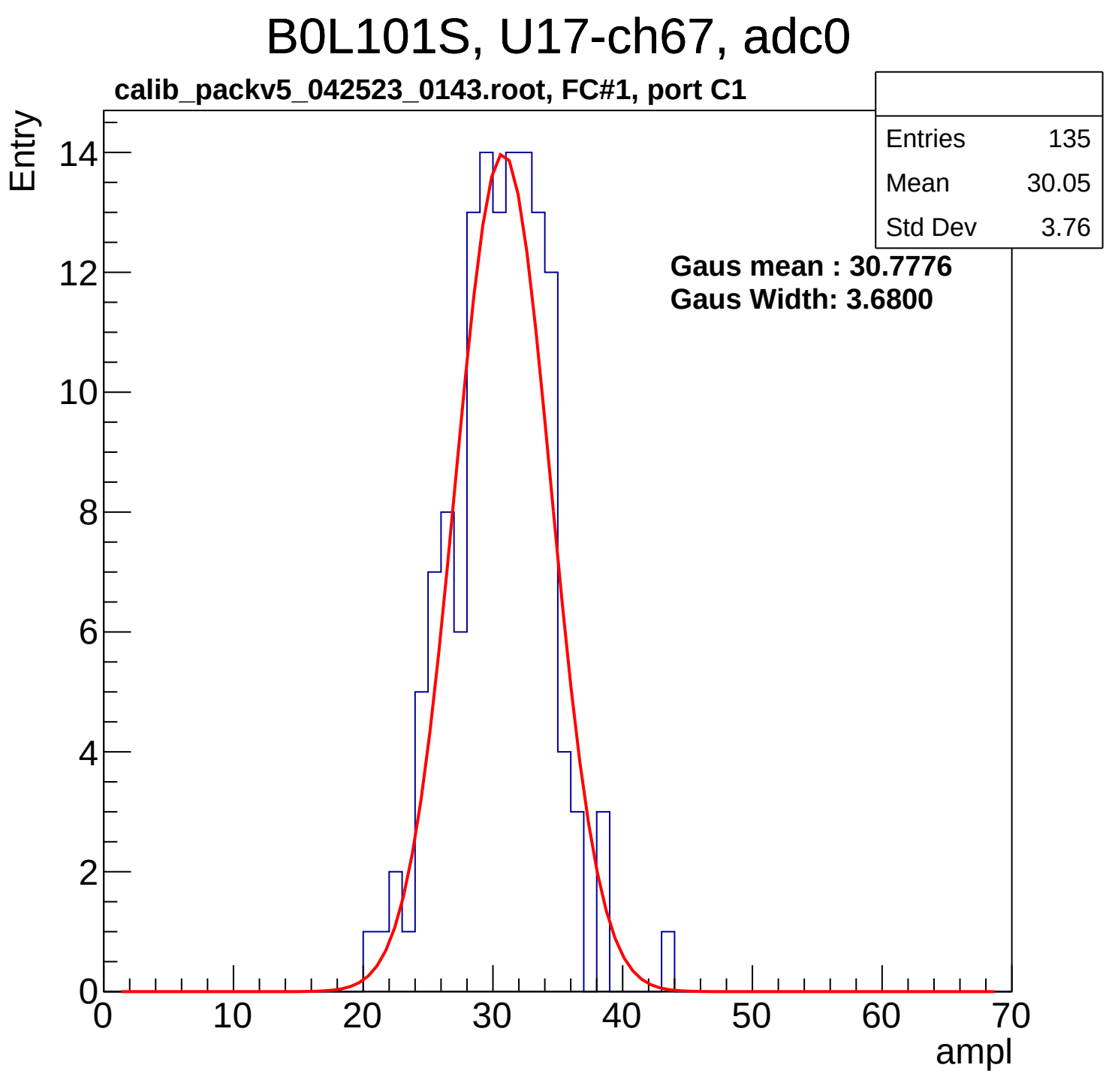

B0L101S, U17-ch66, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 50.86
Std Dev 3.894
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

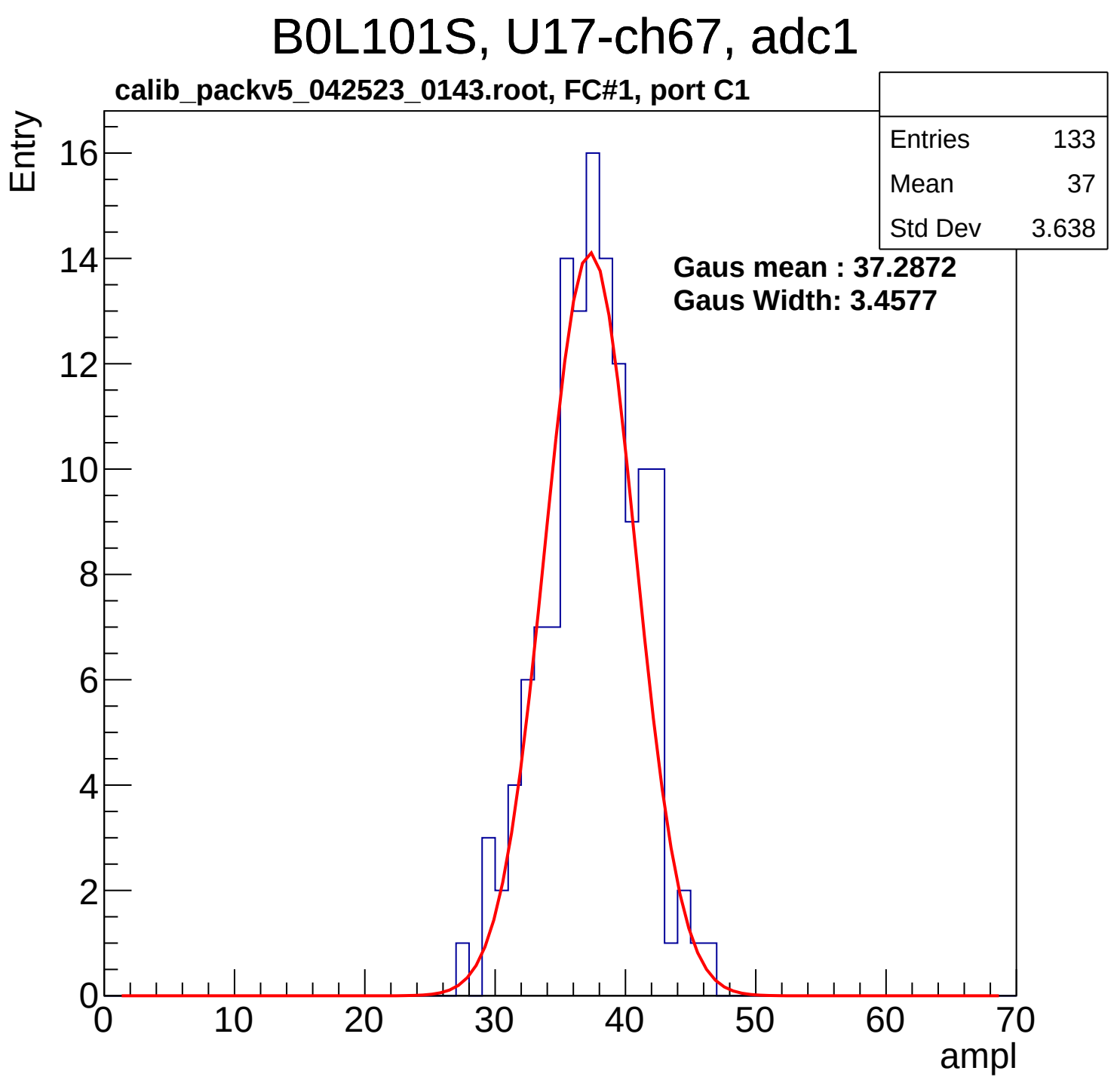

B0L101S, U17-ch66, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 98
Mean 56.54
Std Dev 3.54
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

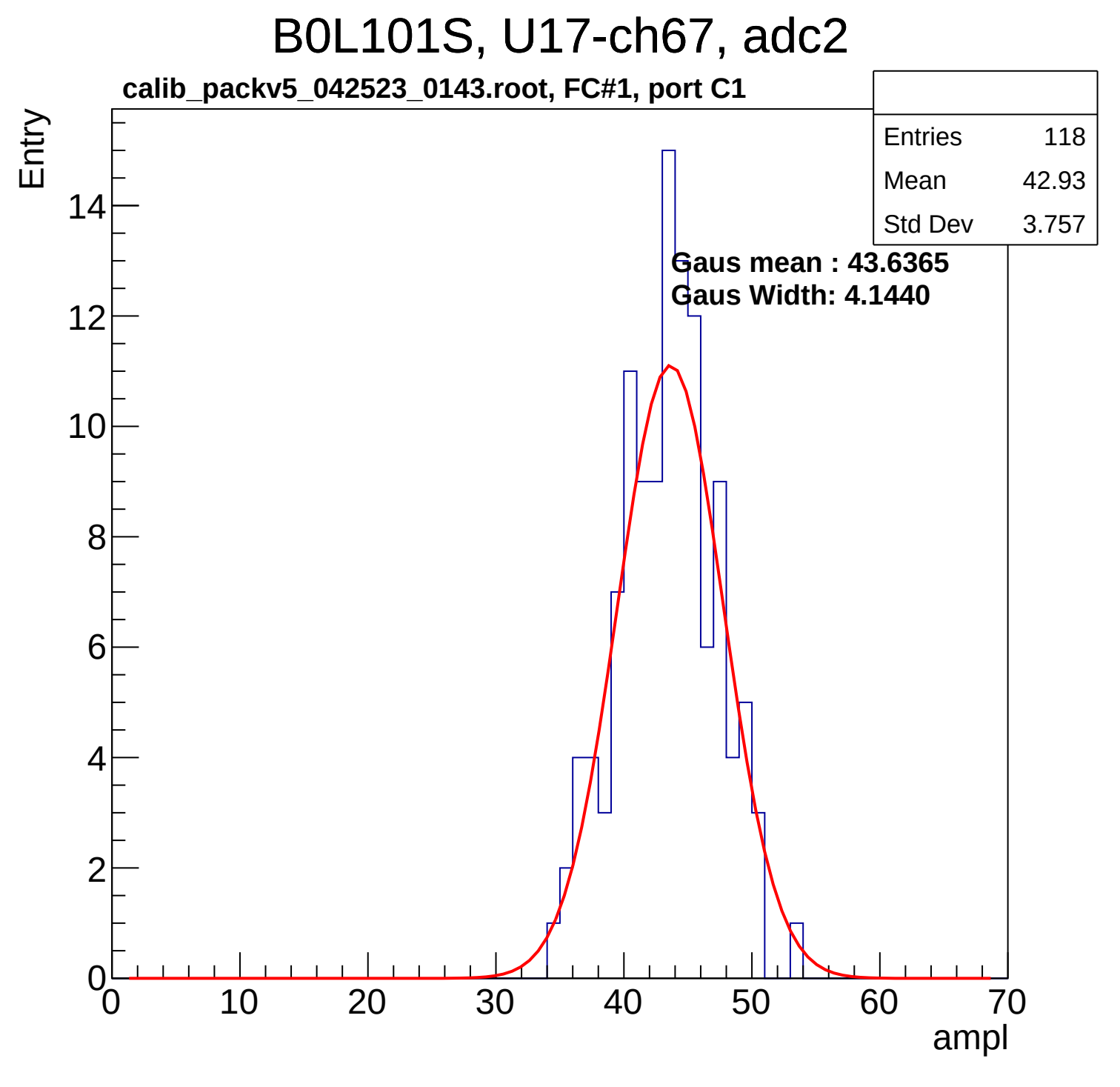

B0L101S, U17-ch66, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 70
Mean 58.24
Std Dev 10.27
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

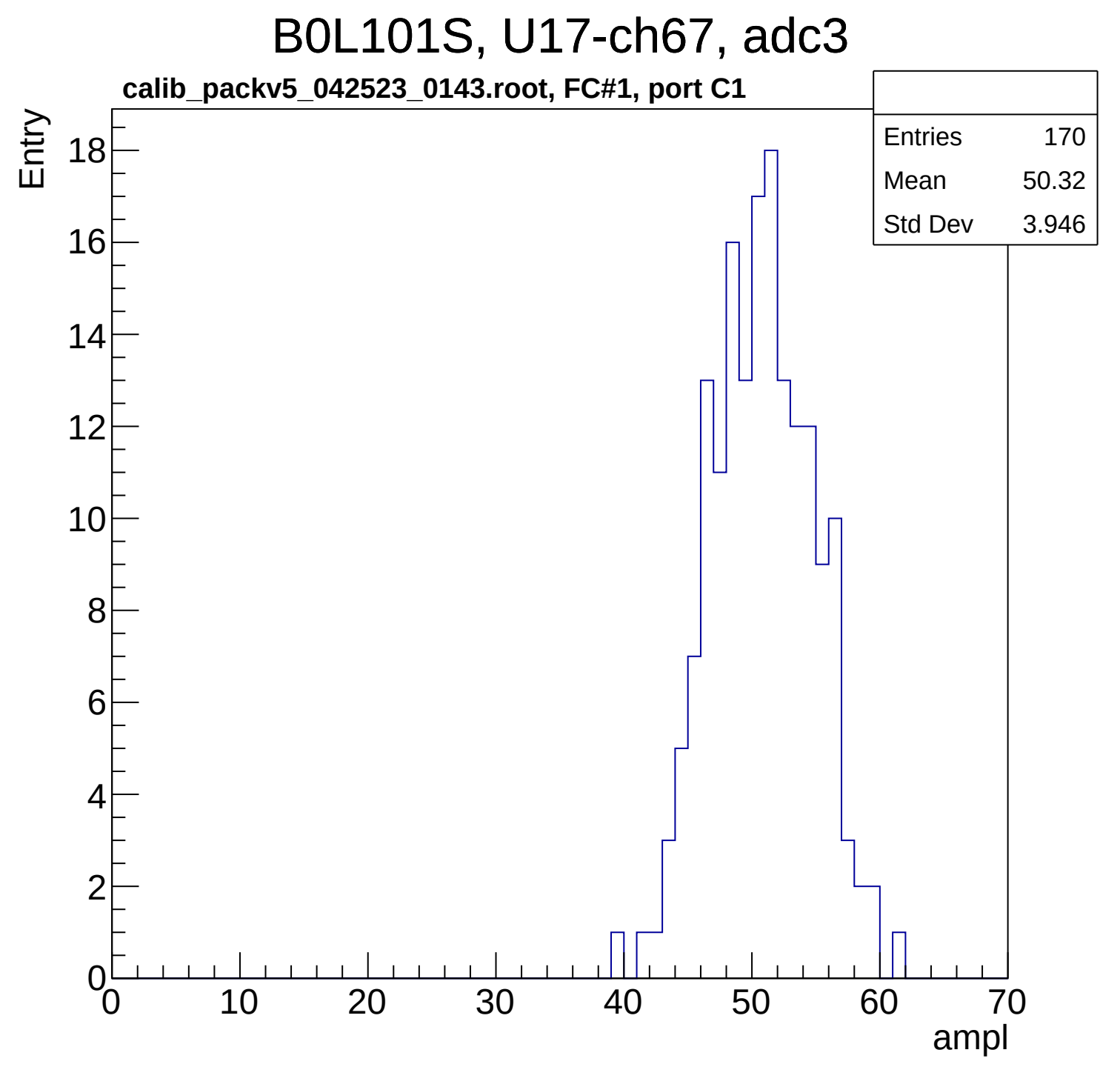

B0L101S, U17-ch66, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 61.81
Std Dev 1.509
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

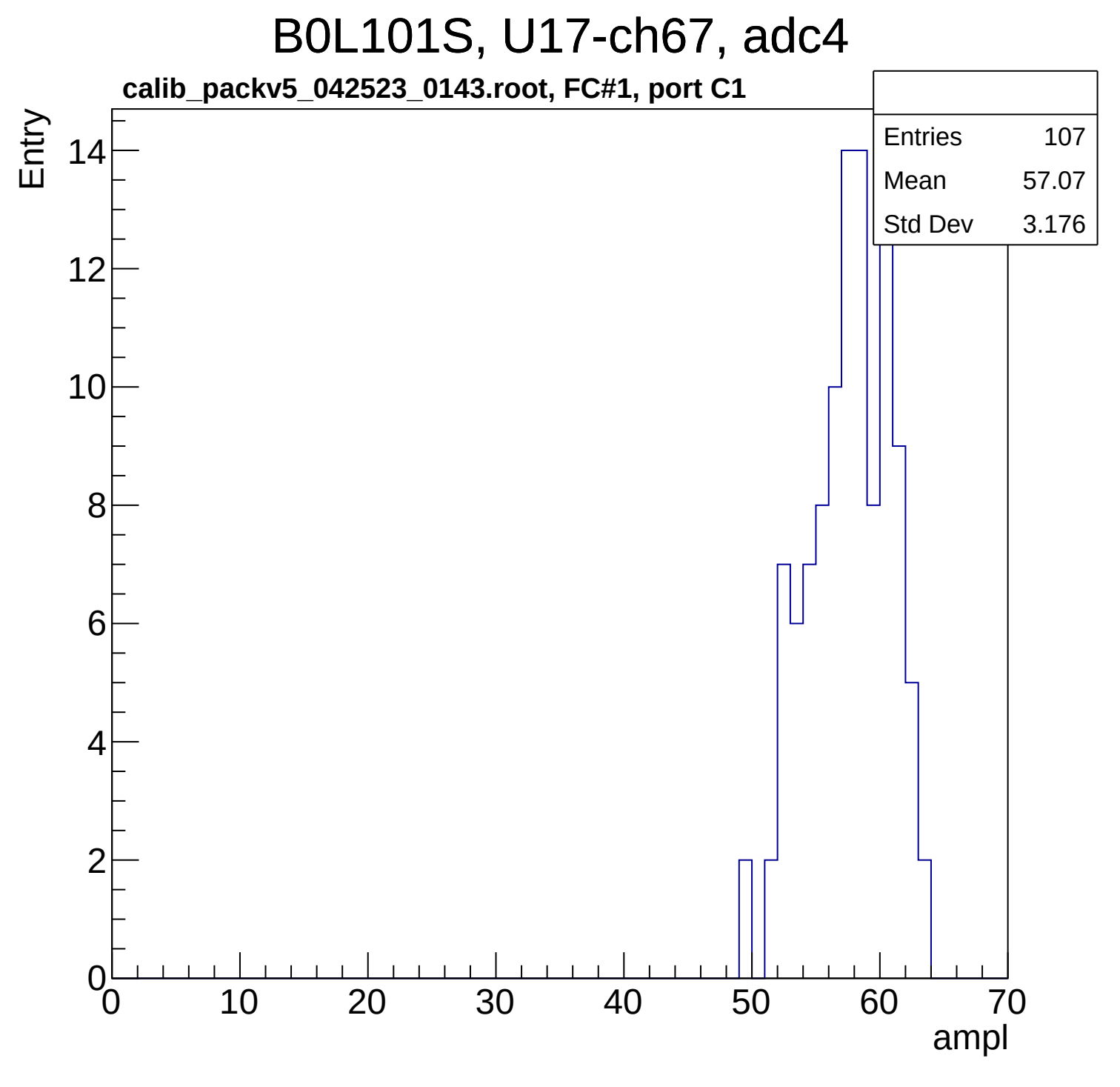

B0L101S, U17-ch66, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

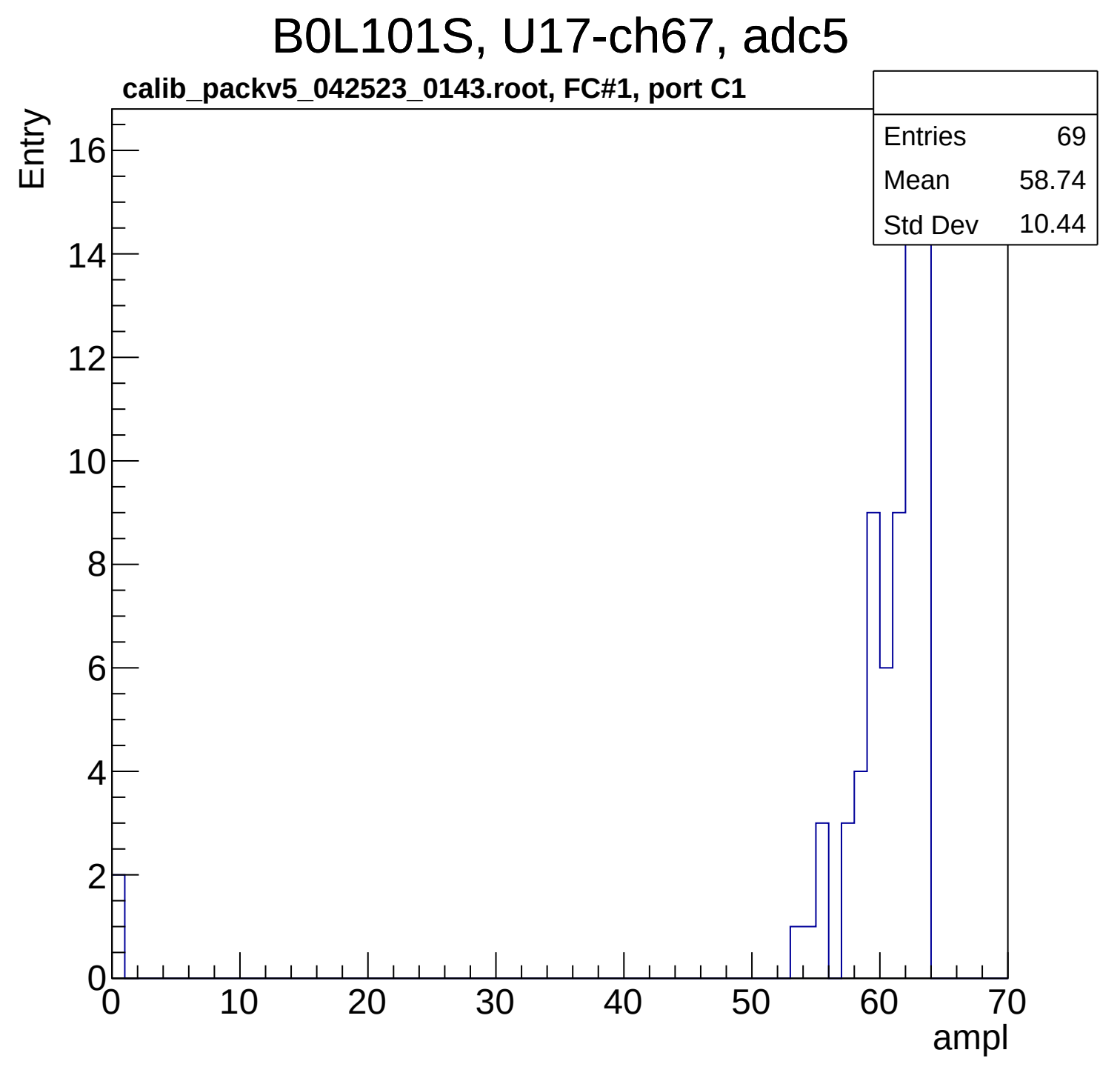

B0L101S, U17-ch67, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 30.05
Std Dev 3.76
Gaus mean : 30.7776
Gaus Width: 3.6800
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

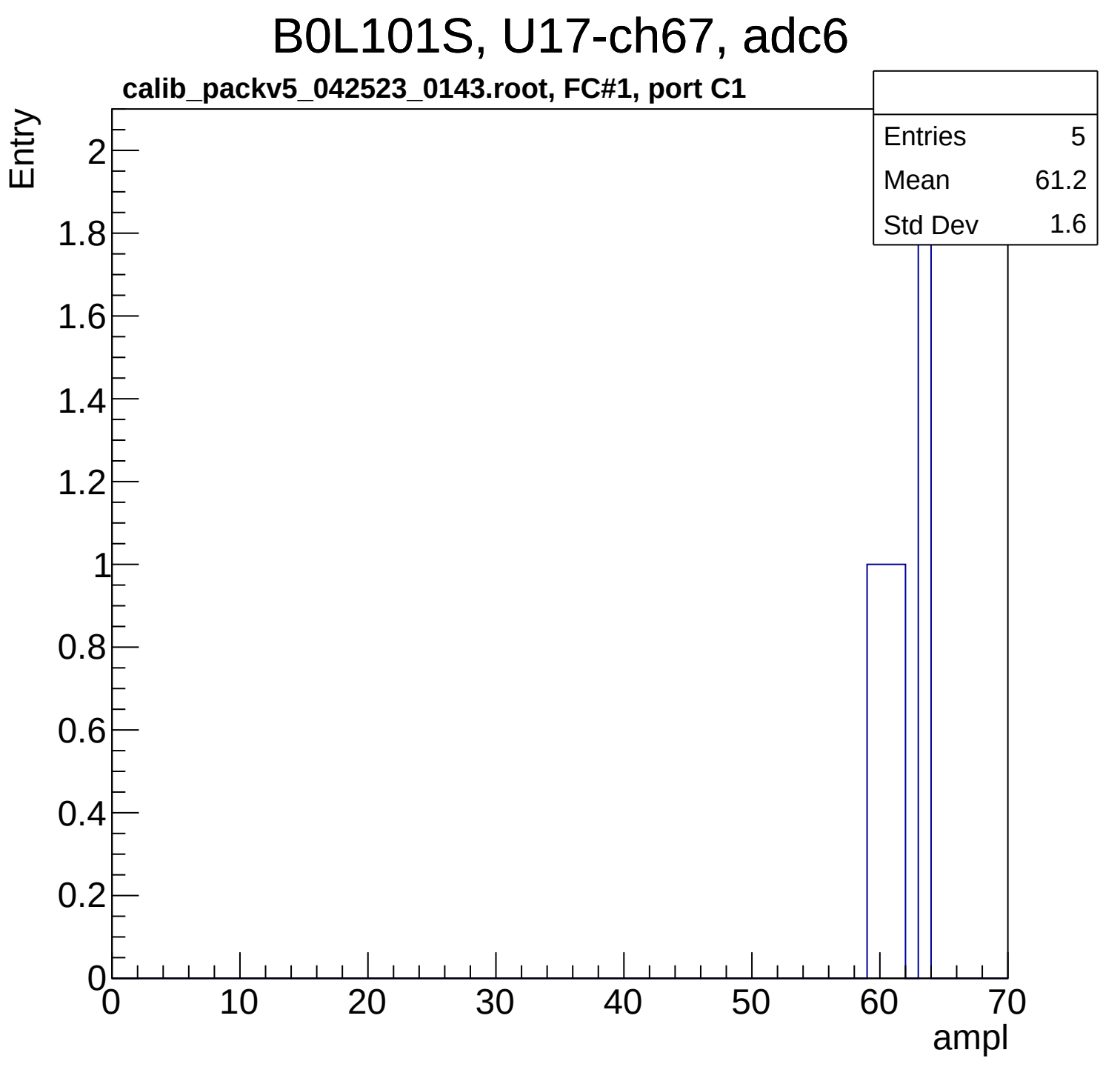

B0L101S, U17-ch67, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 37
Std Dev 3.638
Gaus mean : 37.2872
Gaus Width: 3.4577
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

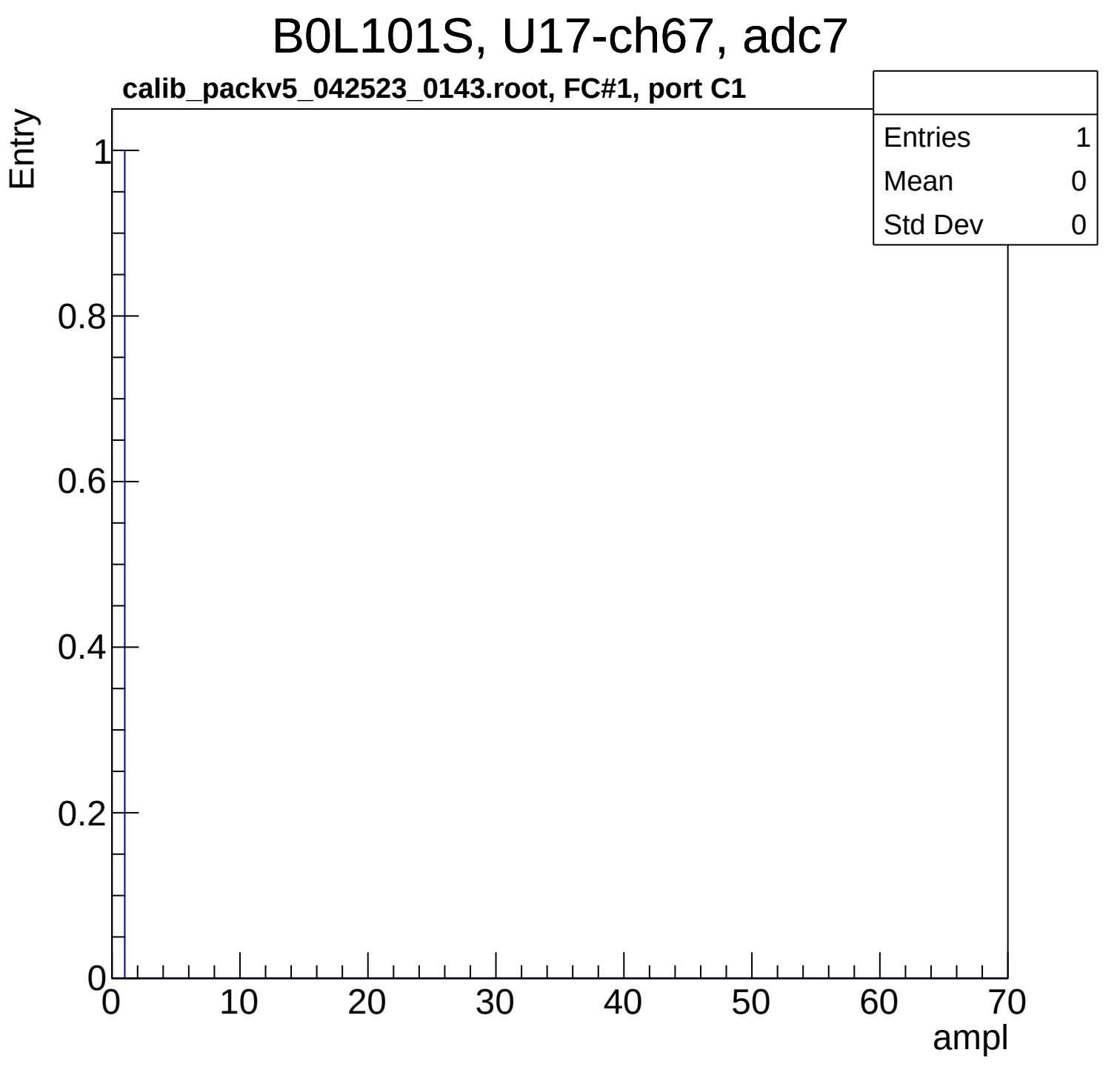

B0L101S, U17-ch67, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 42.93
Std Dev 3.757
Gaus mean : 43.6365
Gaus Width: 4.1440
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

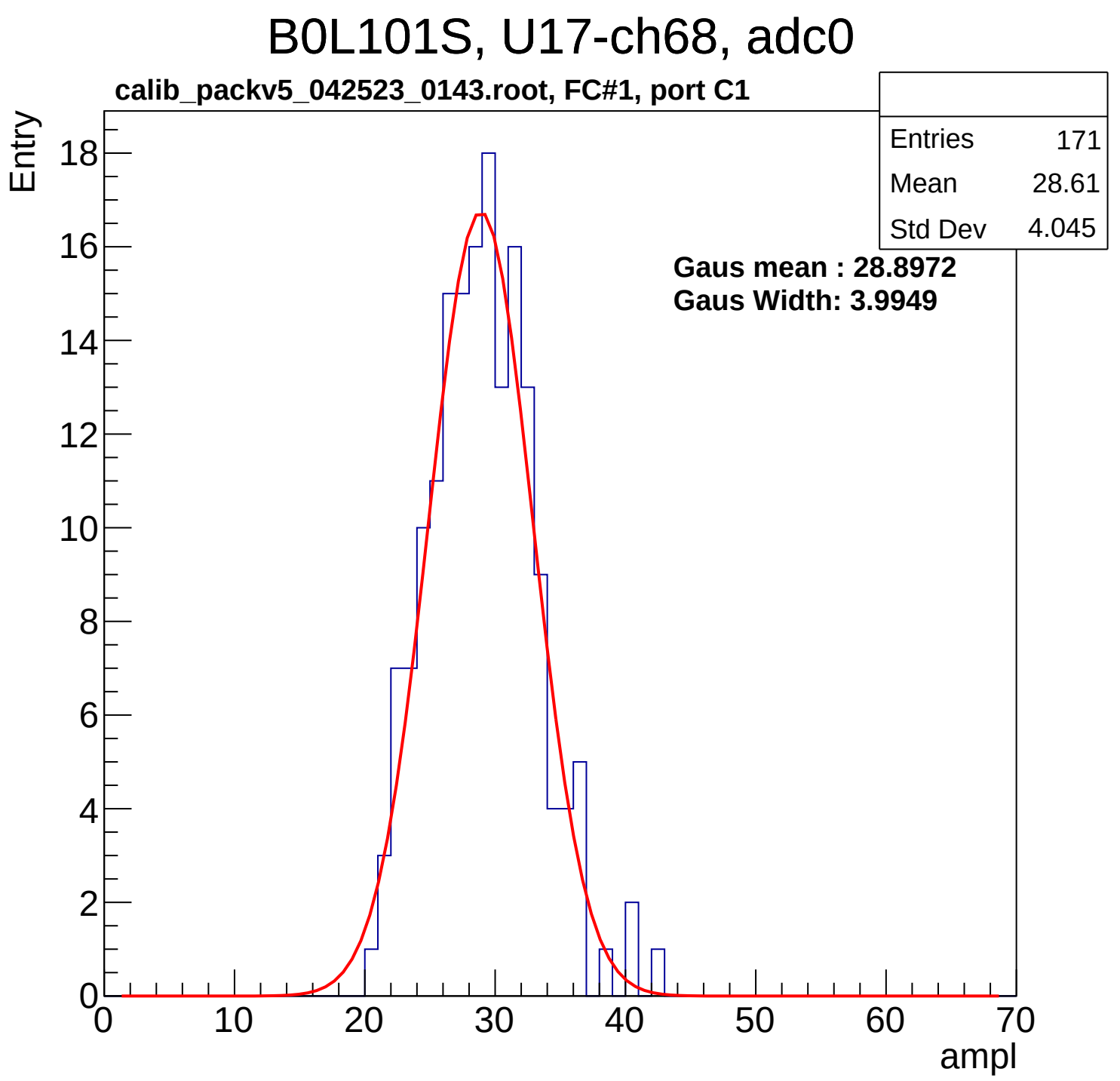

B0L101S, U17-ch67, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 170
Mean 50.32
Std Dev 3.946
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

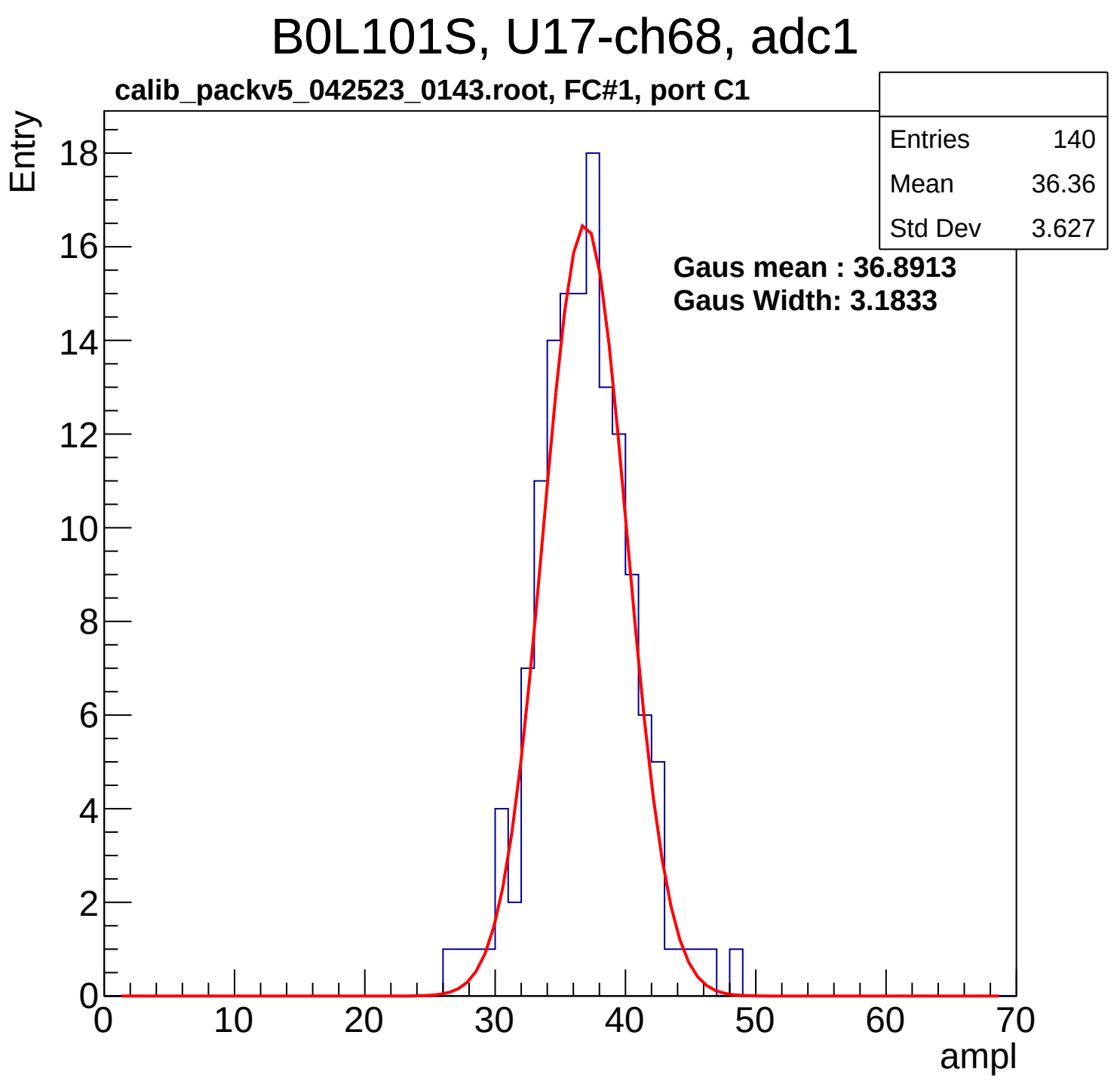

B0L101S, U17-ch67, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 107
Mean 57.07
Std Dev 3.176
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

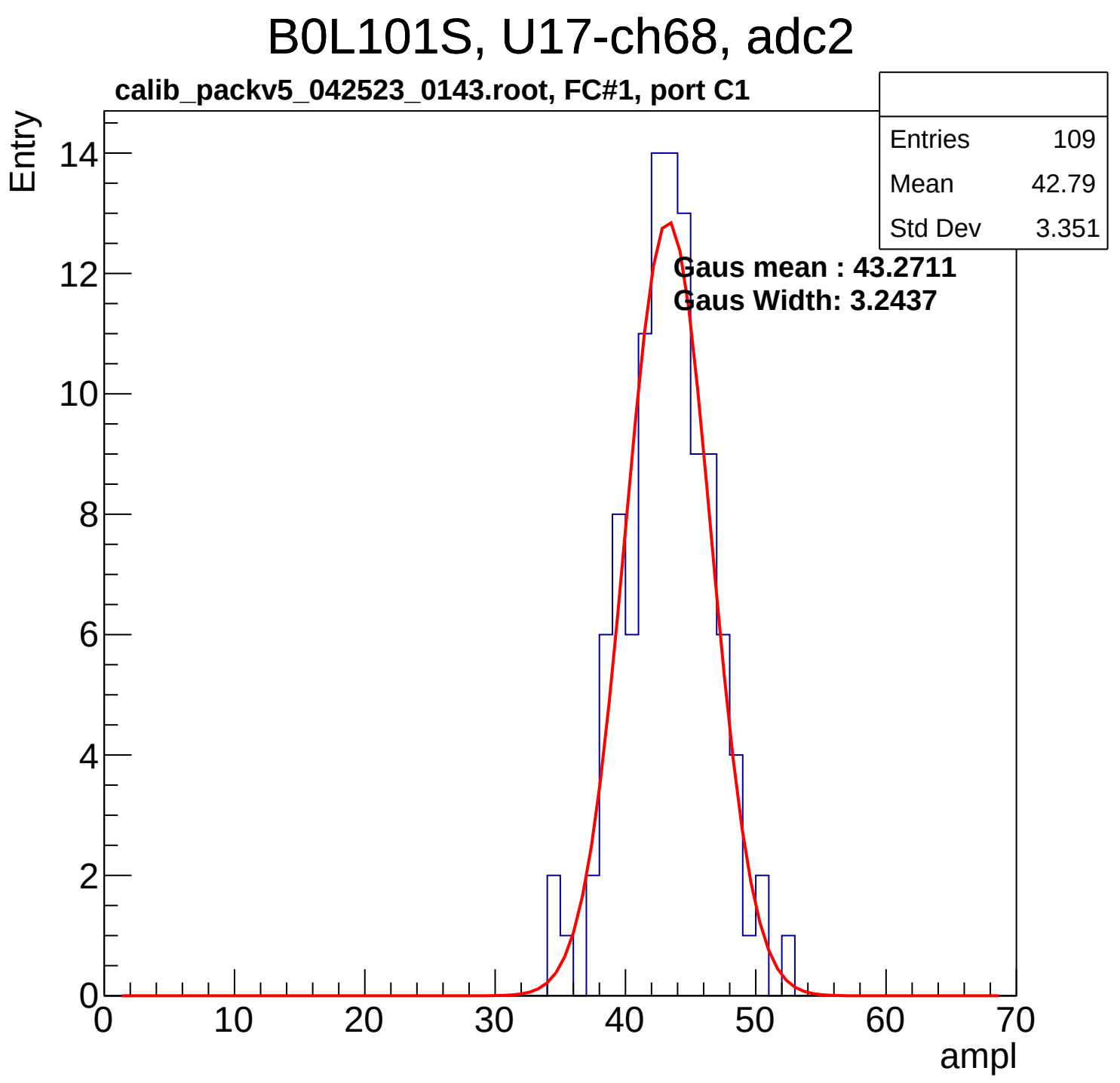

B0L101S, U17-ch67, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 69
Mean 58.74
Std Dev 10.44
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

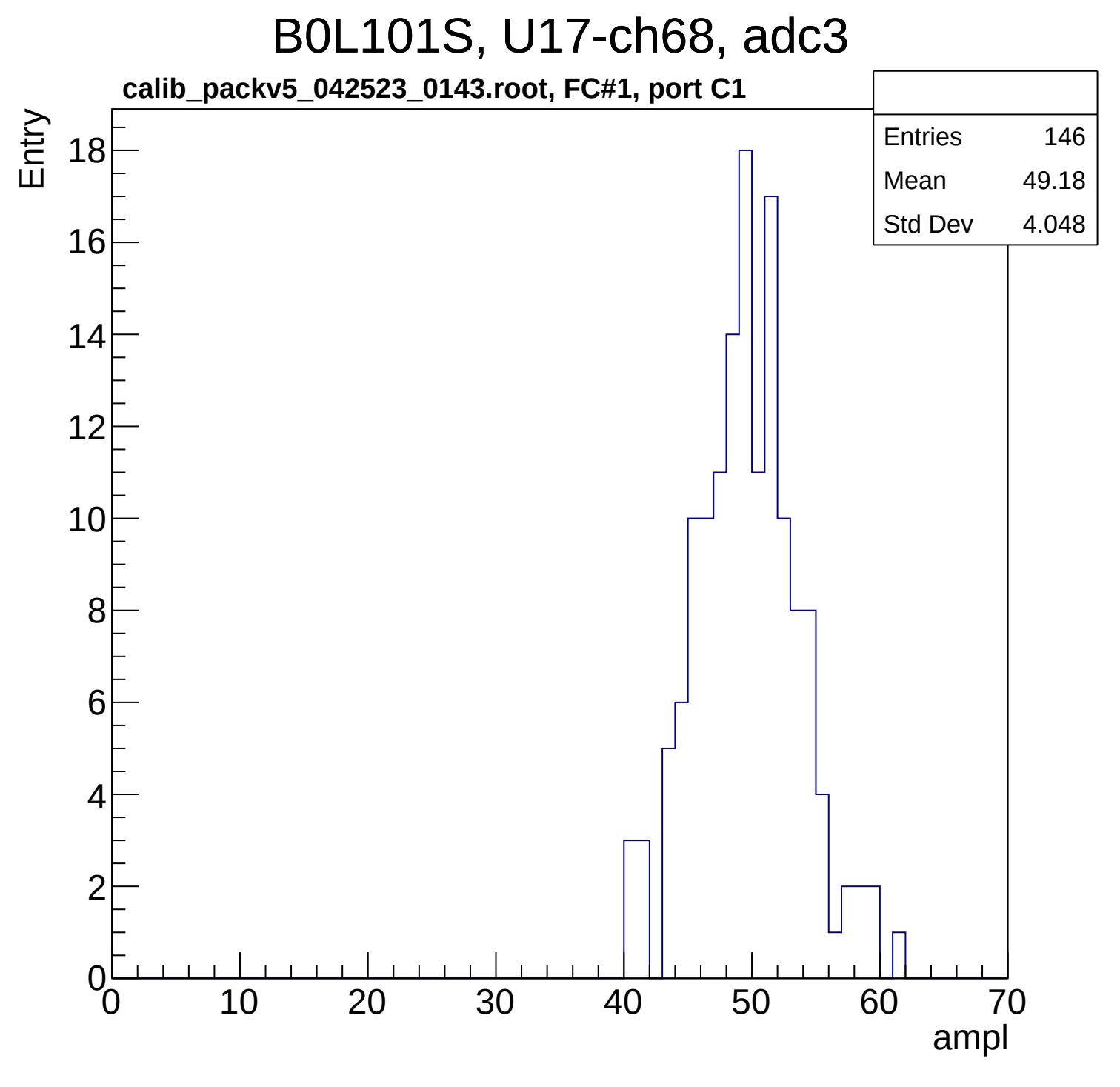

B0L101S, U17-ch67, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 61.2
Std Dev 1.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

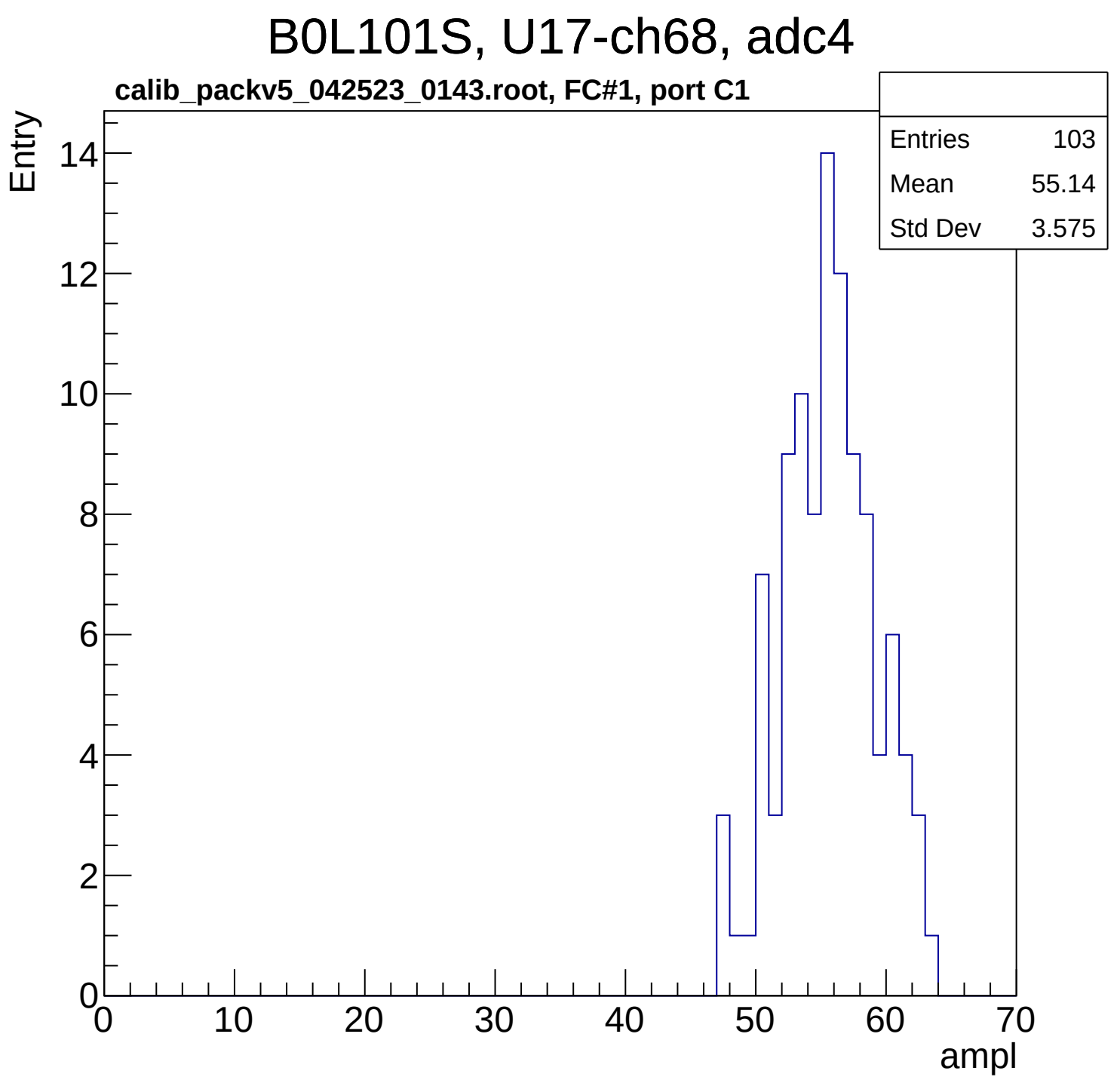

B0L101S, U17-ch67, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

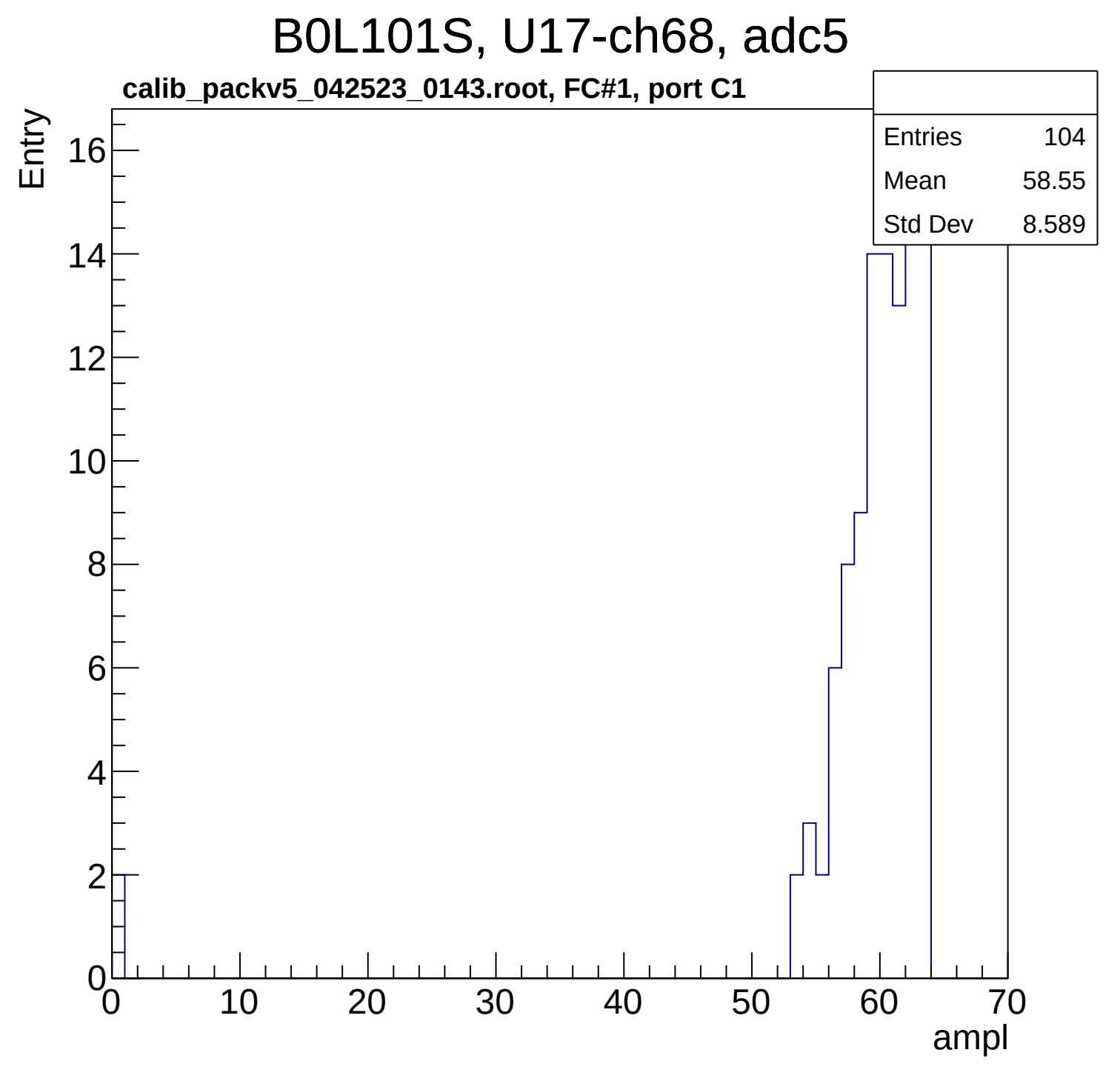

B0L101S, U17-ch68, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 171
Mean 28.61
Std Dev 4.045
Gaus mean : 28.8972
Gaus Width: 3.9949
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

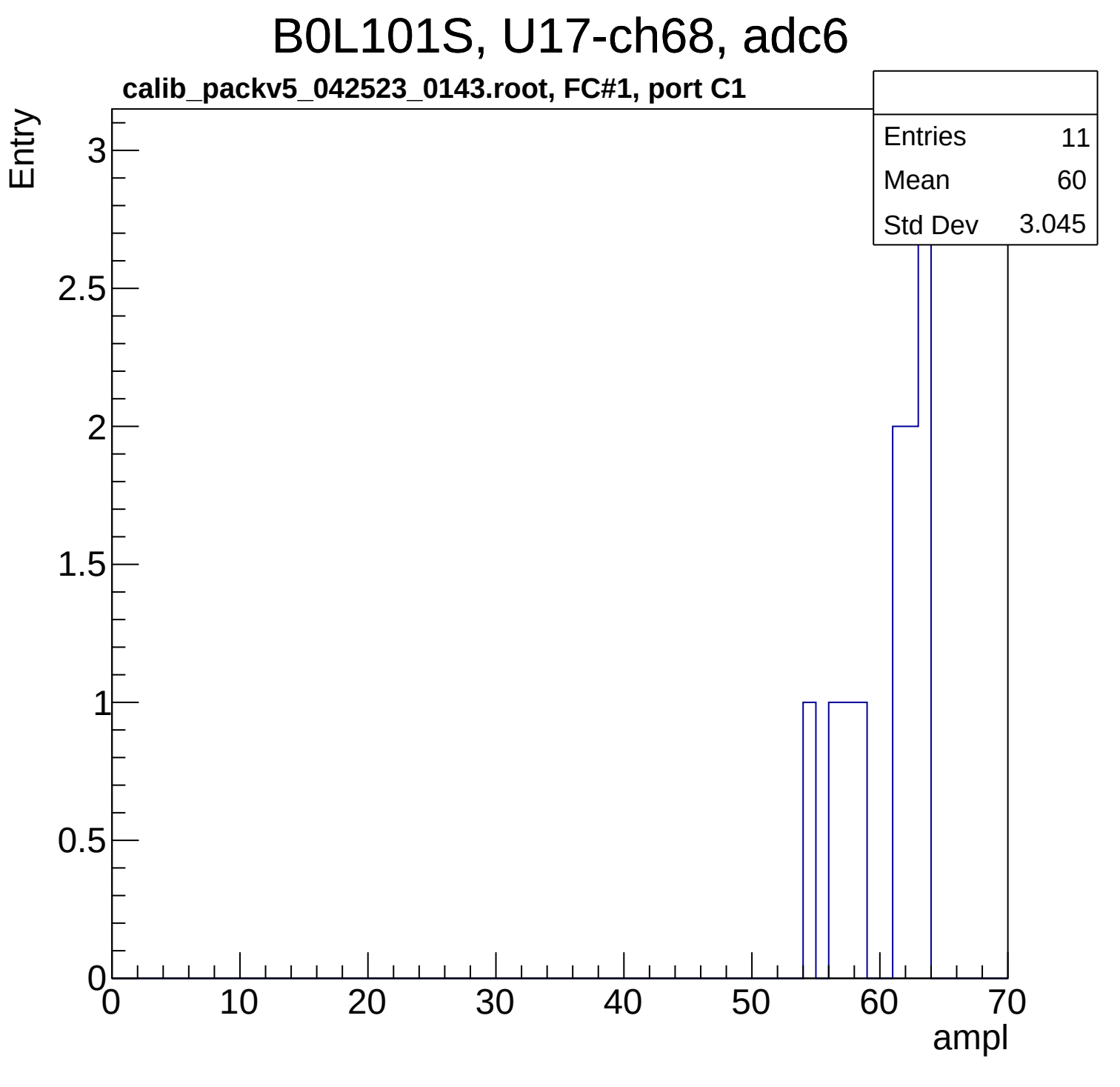

B0L101S, U17-ch68, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 36.36
Std Dev 3.627
Gaus mean : 36.8913
Gaus Width: 3.1833
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

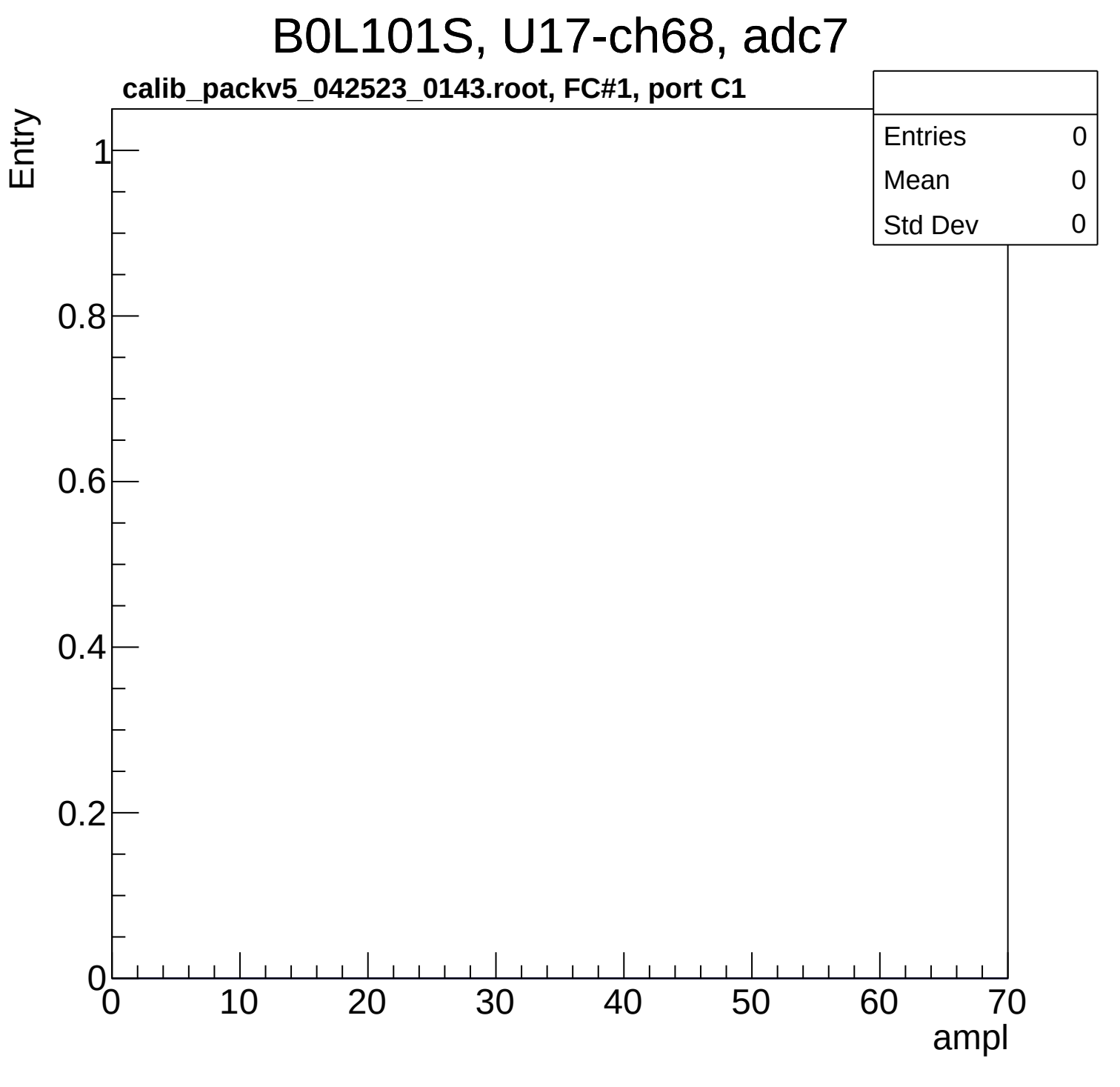

B0L101S, U17-ch68, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 42.79
Std Dev 3.351
Gaus mean : 43.2711
Gaus Width: 3.2437
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

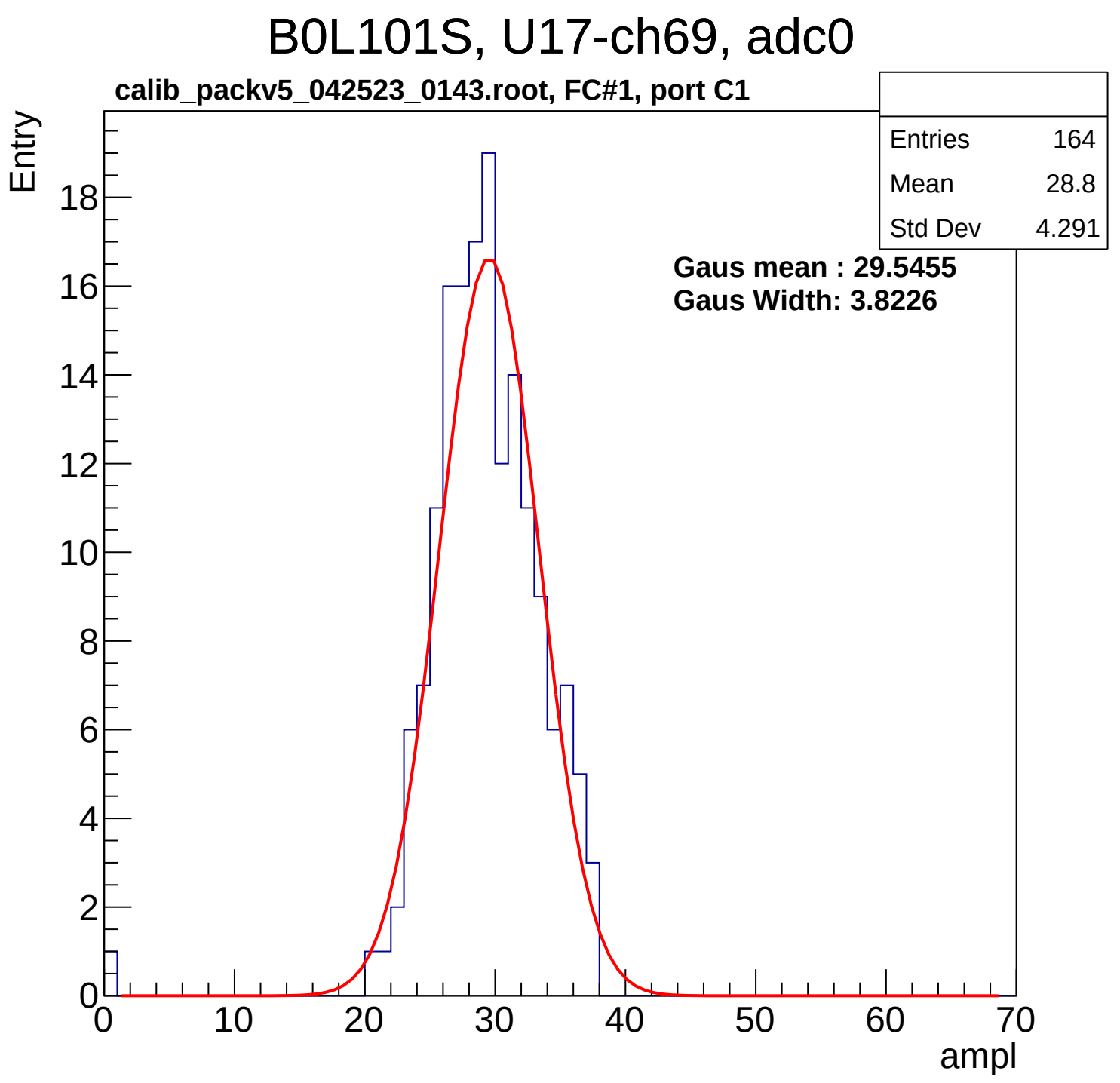

B0L101S, U17-ch68, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 49.18
Std Dev 4.048
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

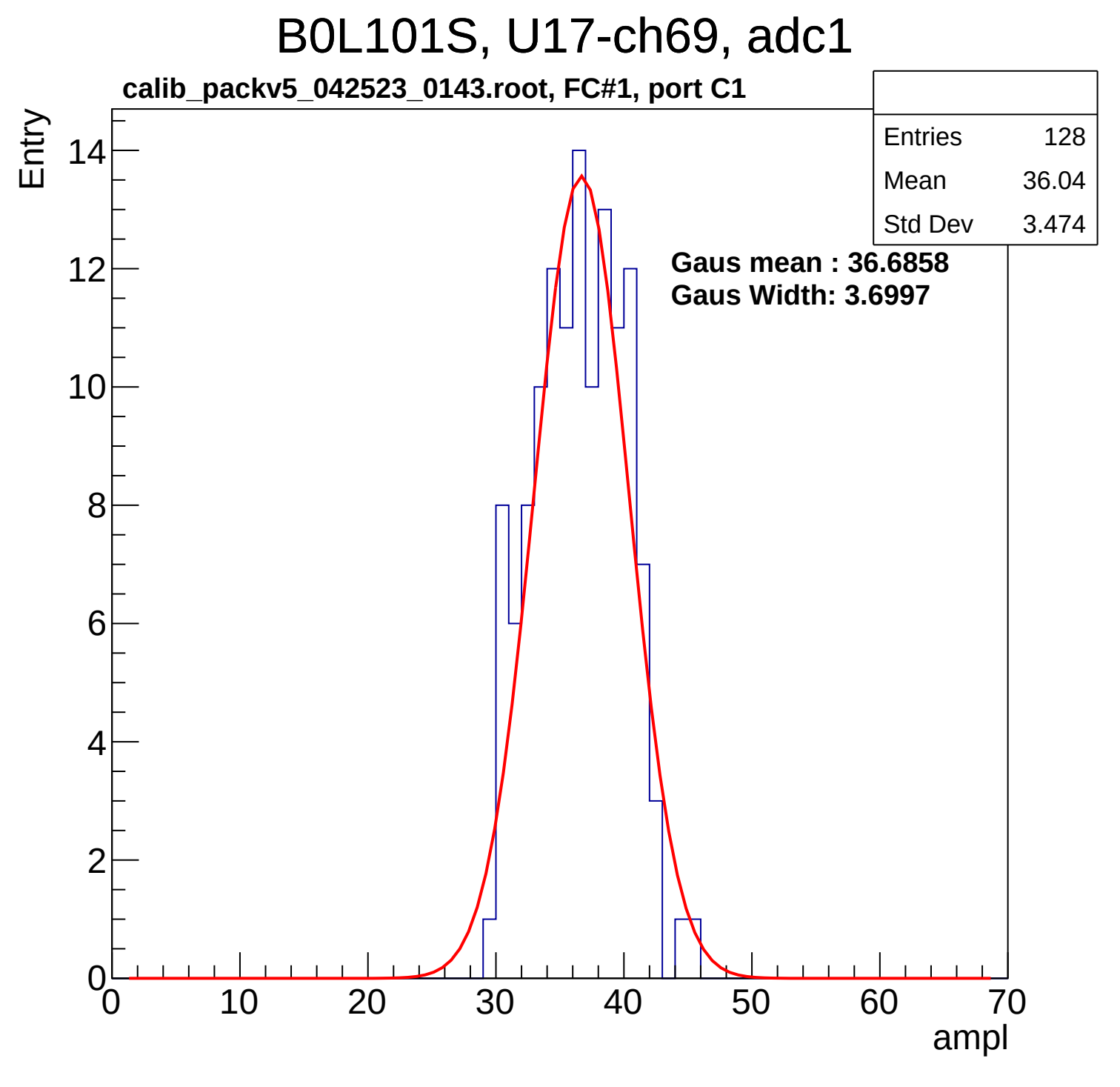

B0L101S, U17-ch68, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 55.14
Std Dev 3.575
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

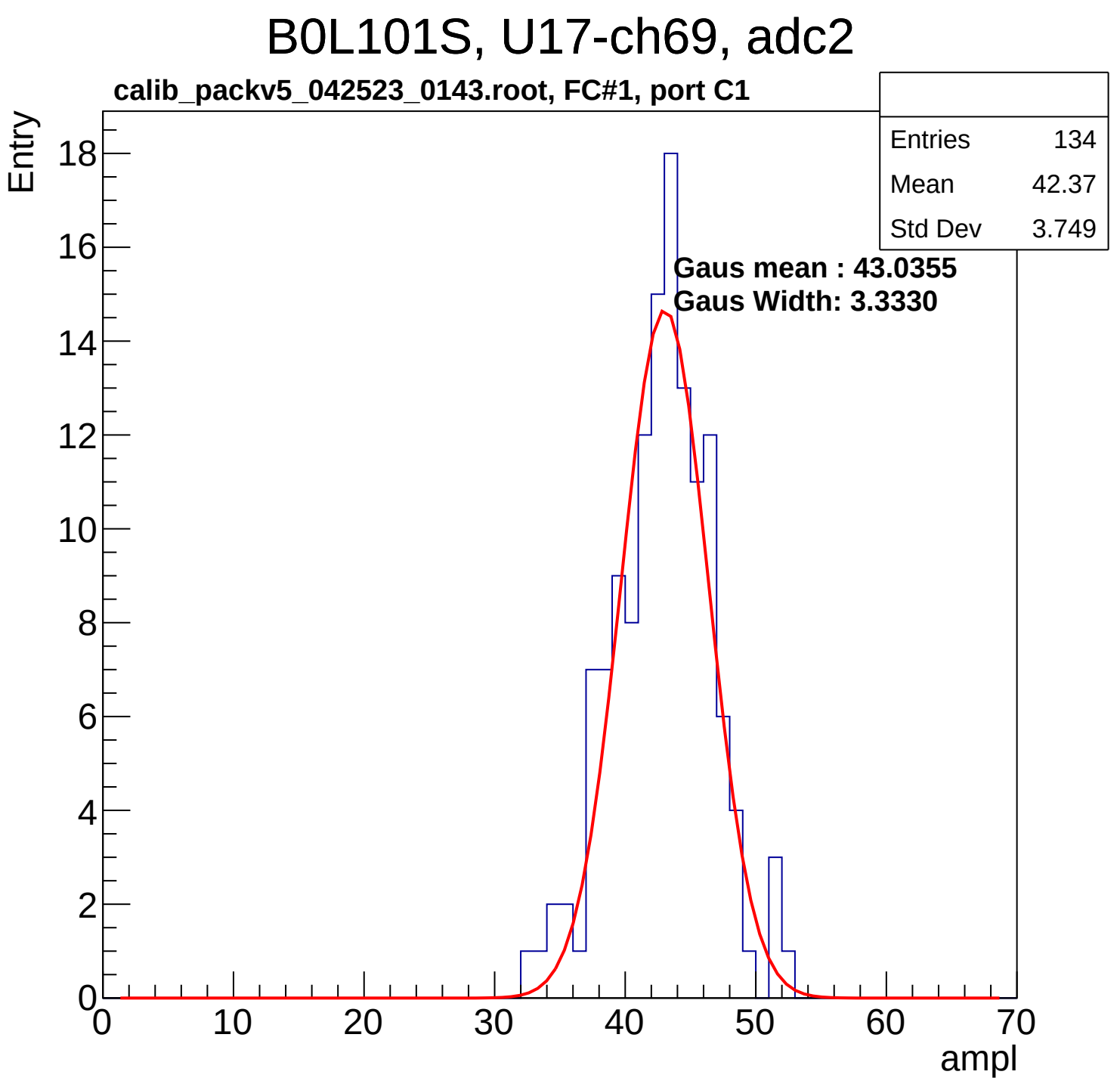

B0L101S, U17-ch68, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.55
Std Dev 8.589
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

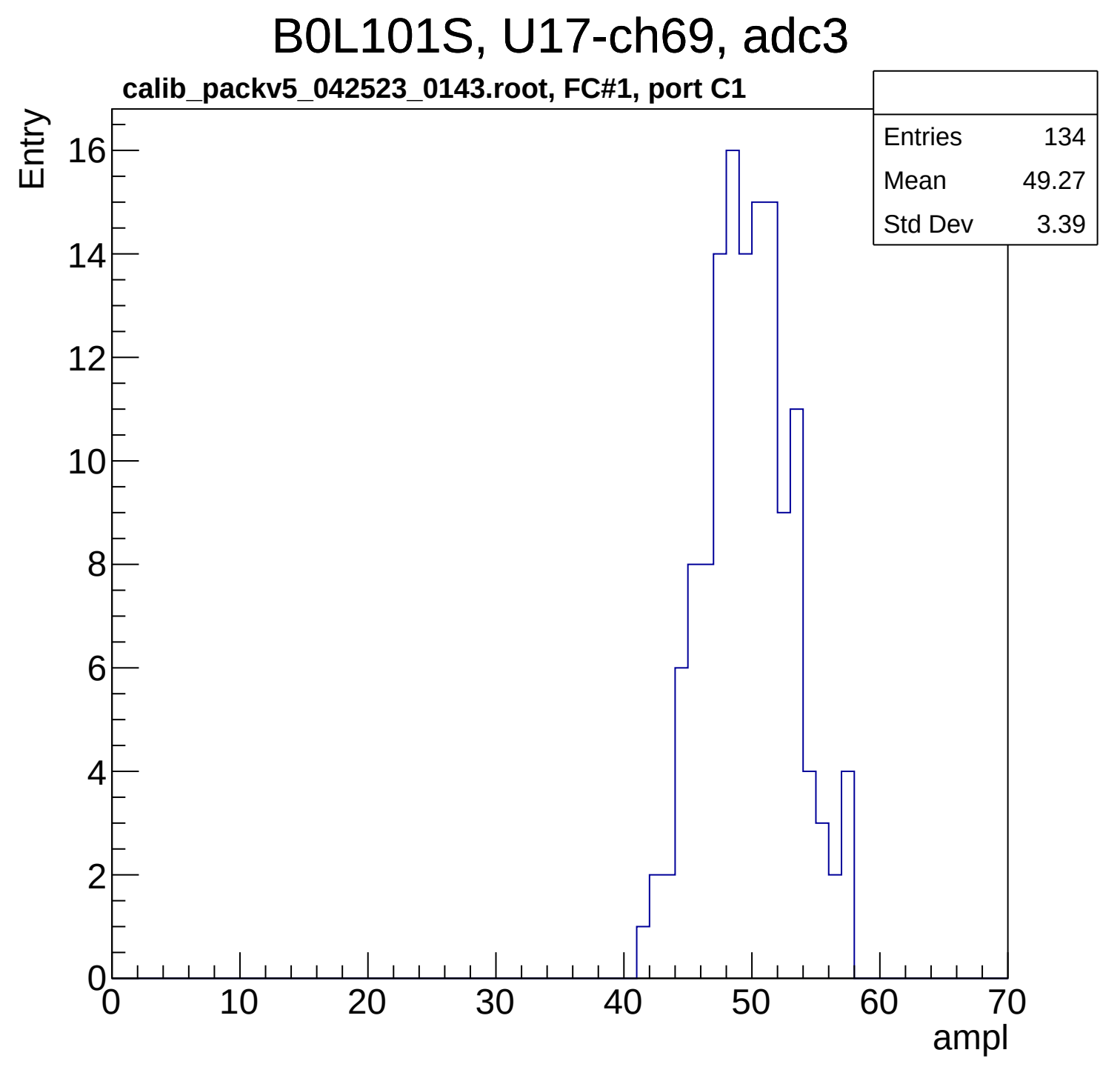

B0L101S, U17-ch68, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 60
Std Dev 3.045
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

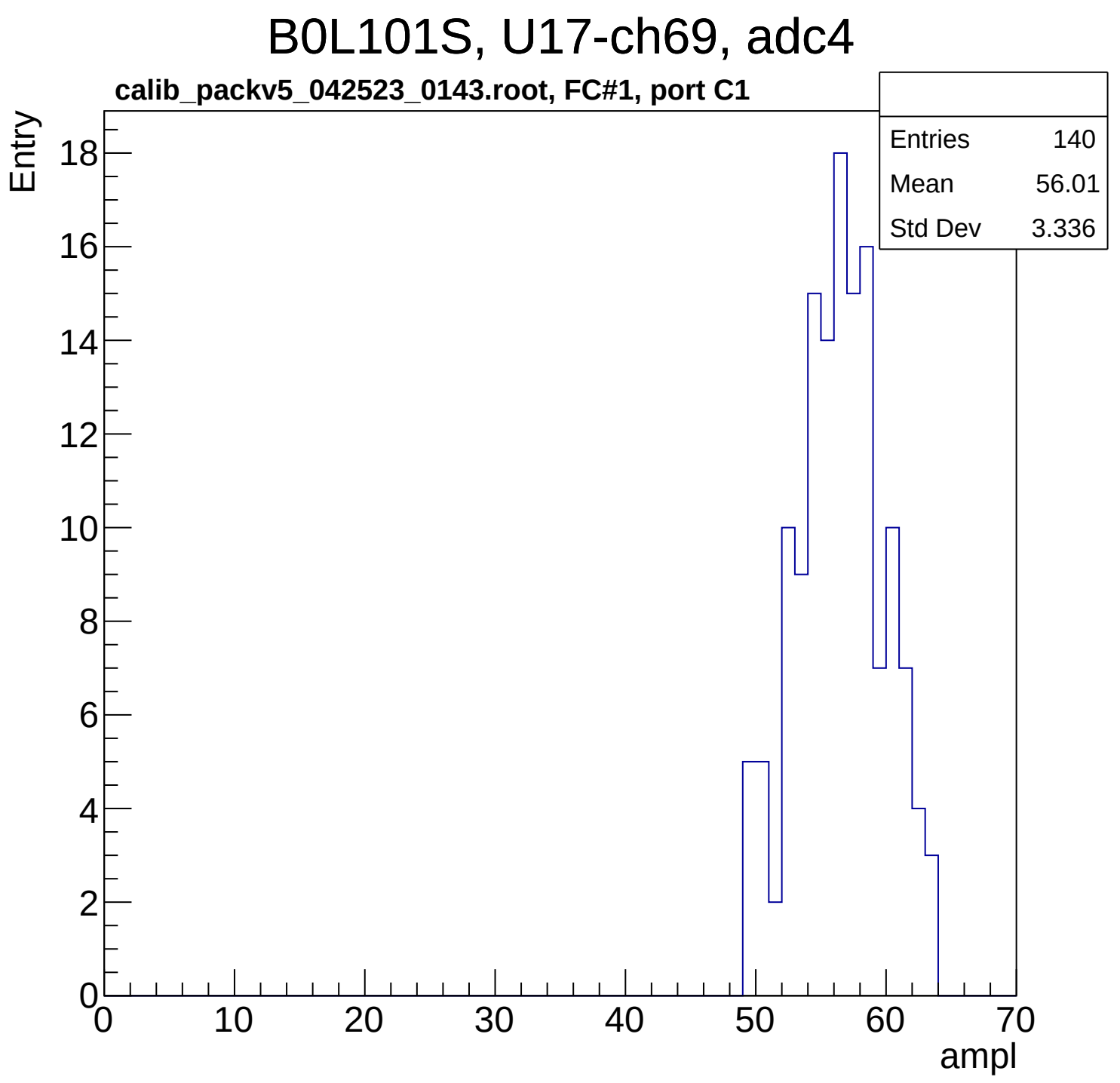

B0L101S, U17-ch68, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

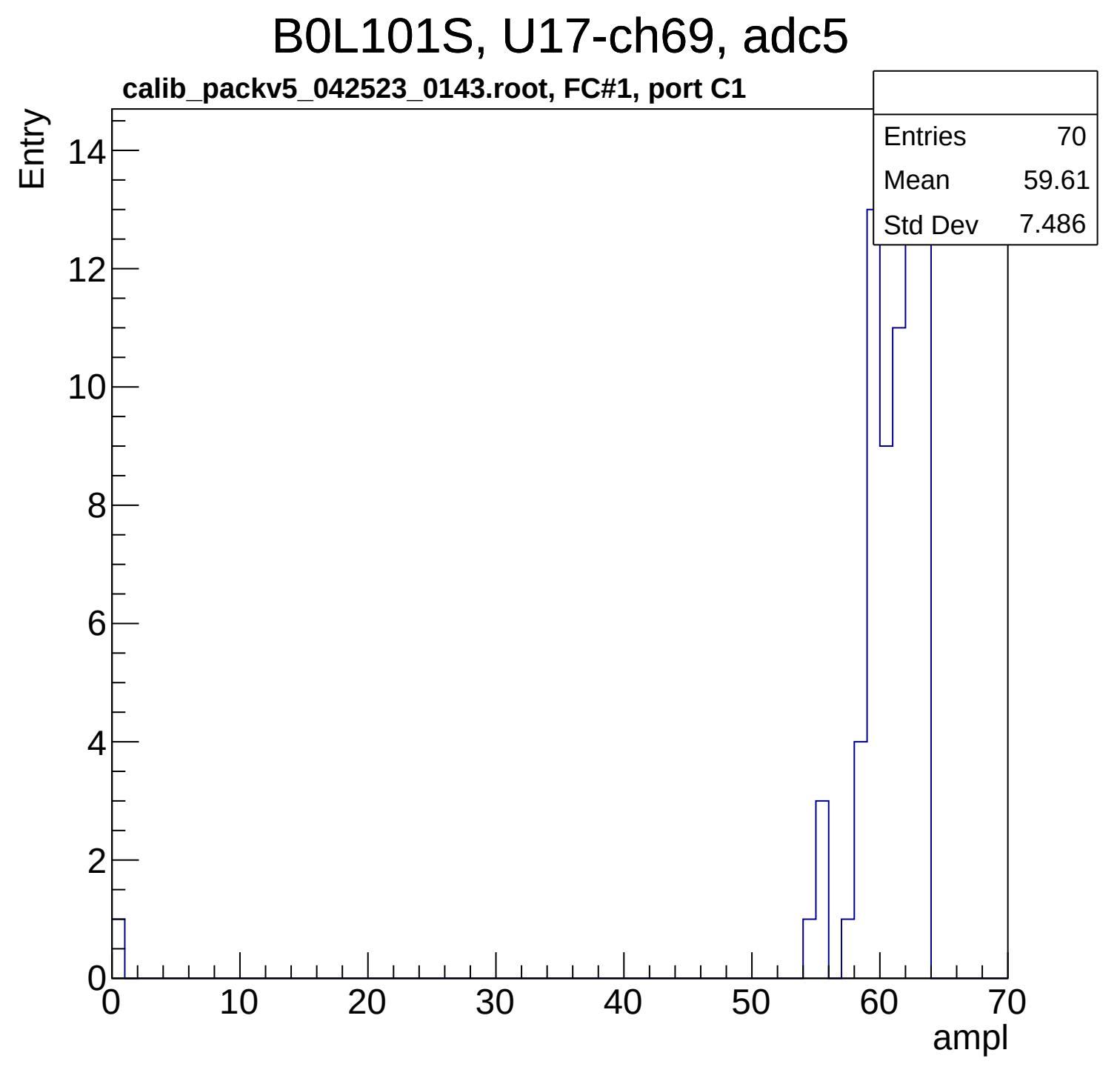

B0L101S, U17-ch69, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 164
Mean 28.8
Std Dev 4.291
Gaus mean : 29.5455
Gaus Width: 3.8226
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

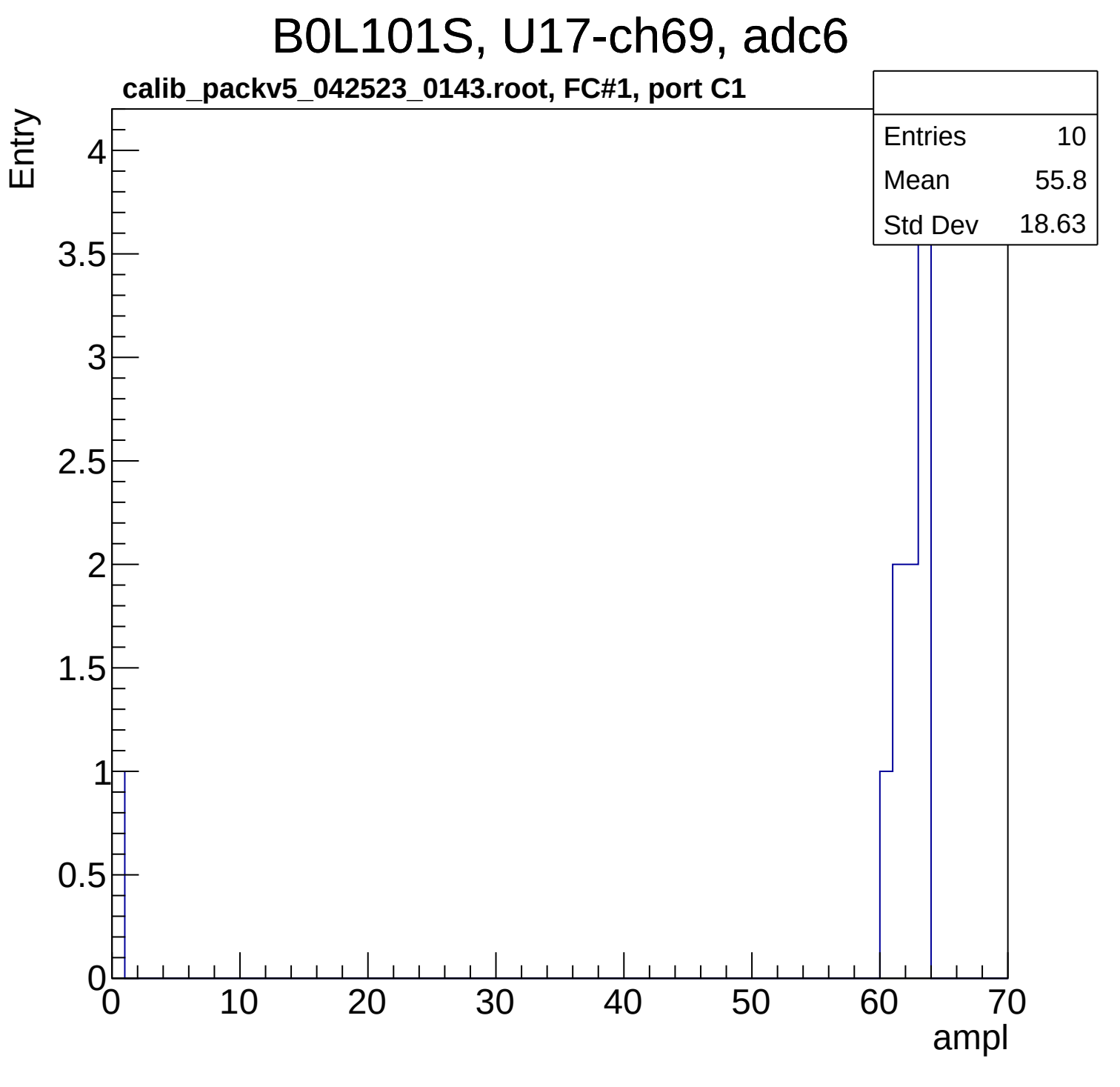

B0L101S, U17-ch69, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 36.04
Std Dev 3.474
Gaus mean : 36.6858
Gaus Width: 3.6997
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

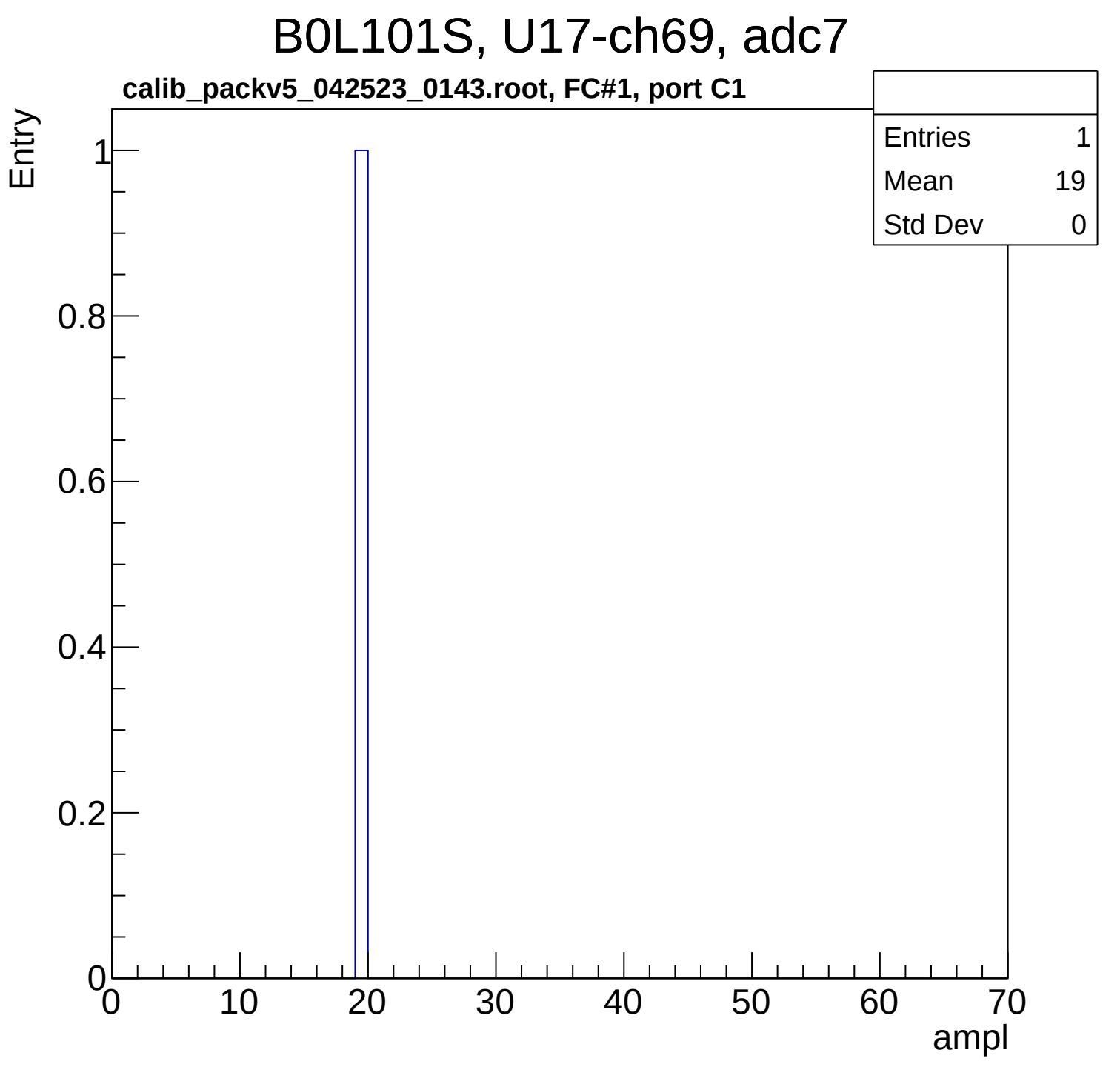

B0L101S, U17-ch69, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 42.37
Std Dev 3.749
Gaus mean : 43.0355
Gaus Width: 3.3330
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

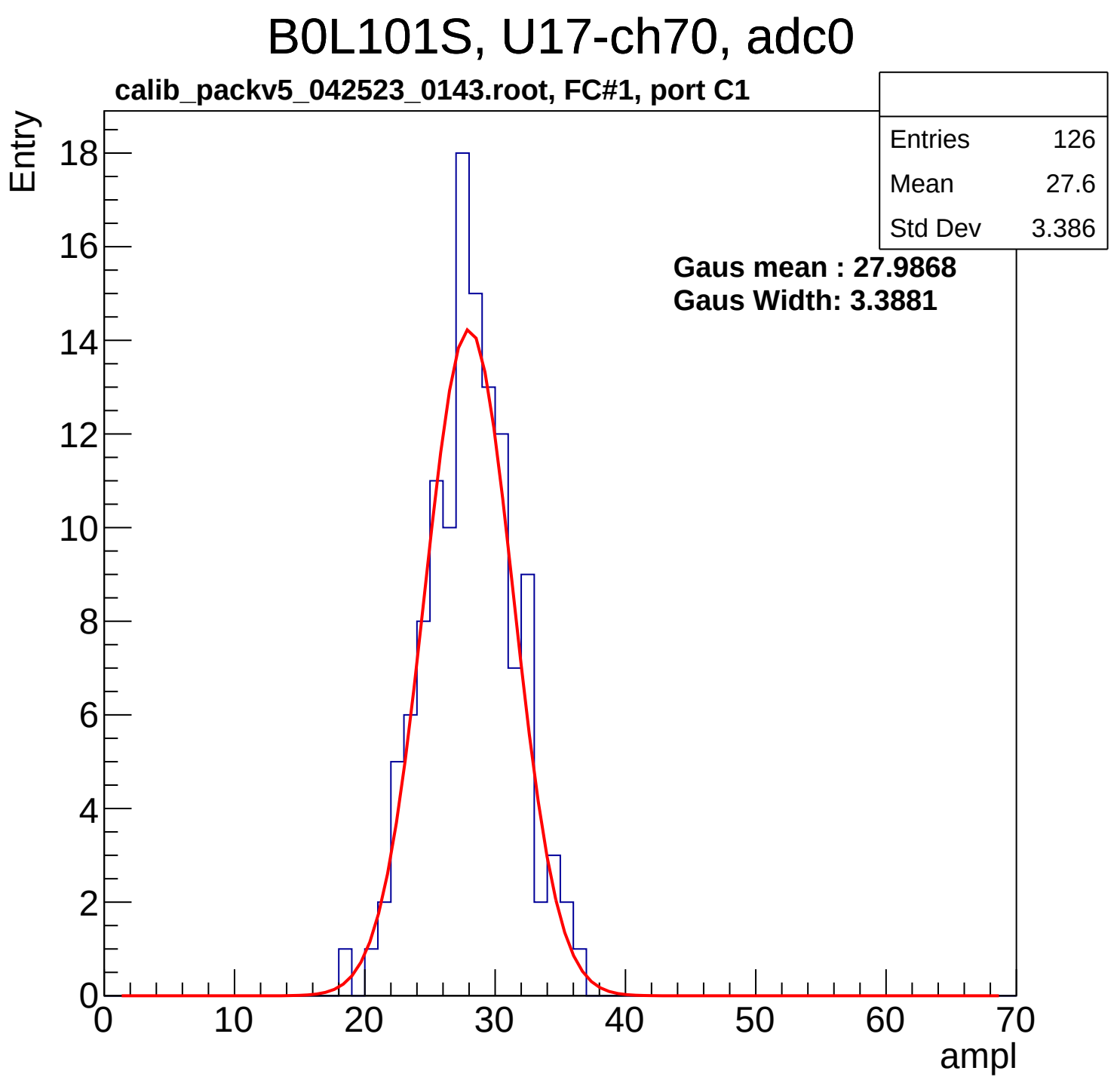

B0L101S, U17-ch69, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 134
Mean 49.27
Std Dev 3.39
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

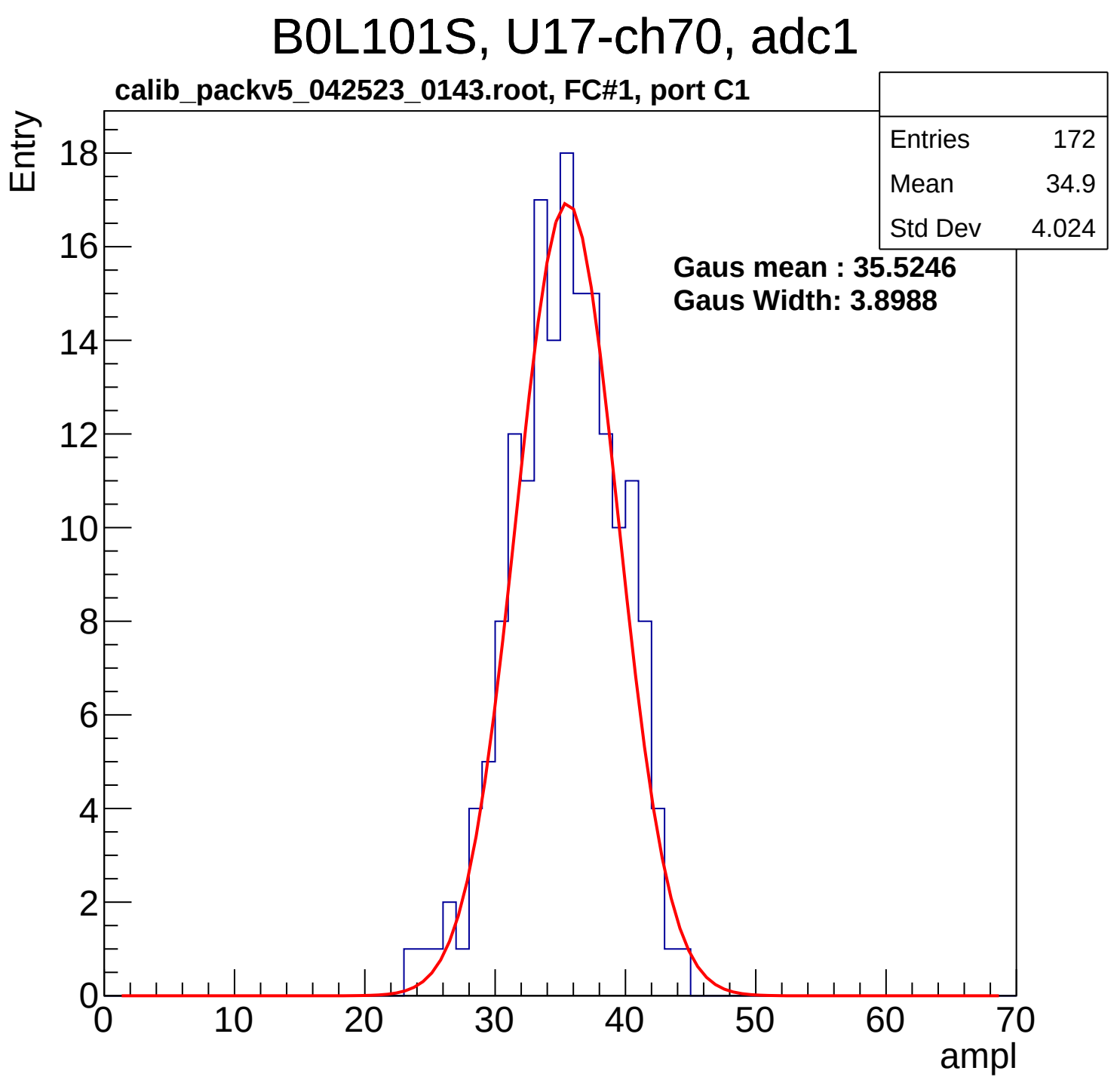

B0L101S, U17-ch69, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 56.01
Std Dev 3.336
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl

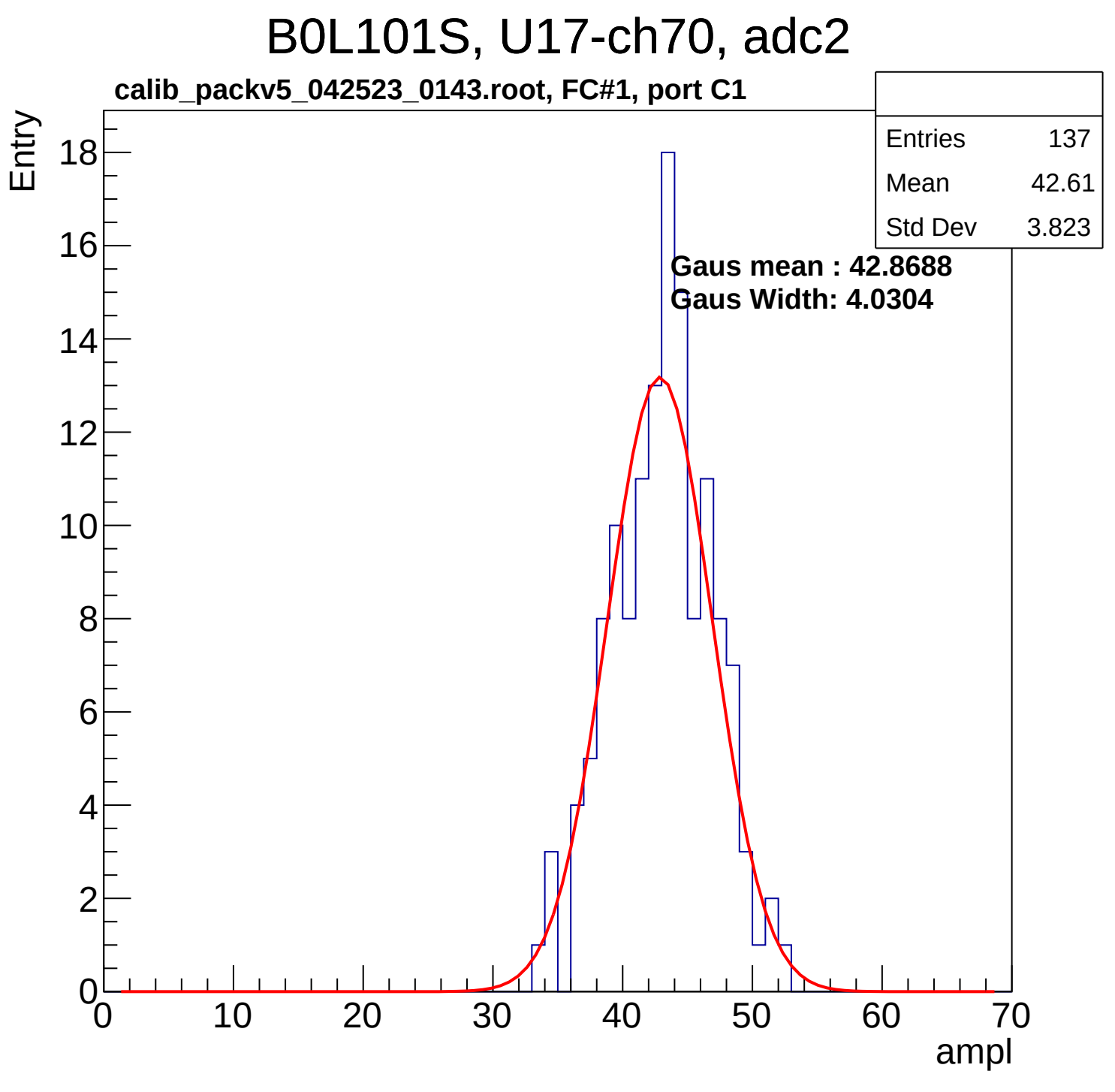

B0L101S, U17-ch69, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 70
Mean 59.61
Std Dev 7.486
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

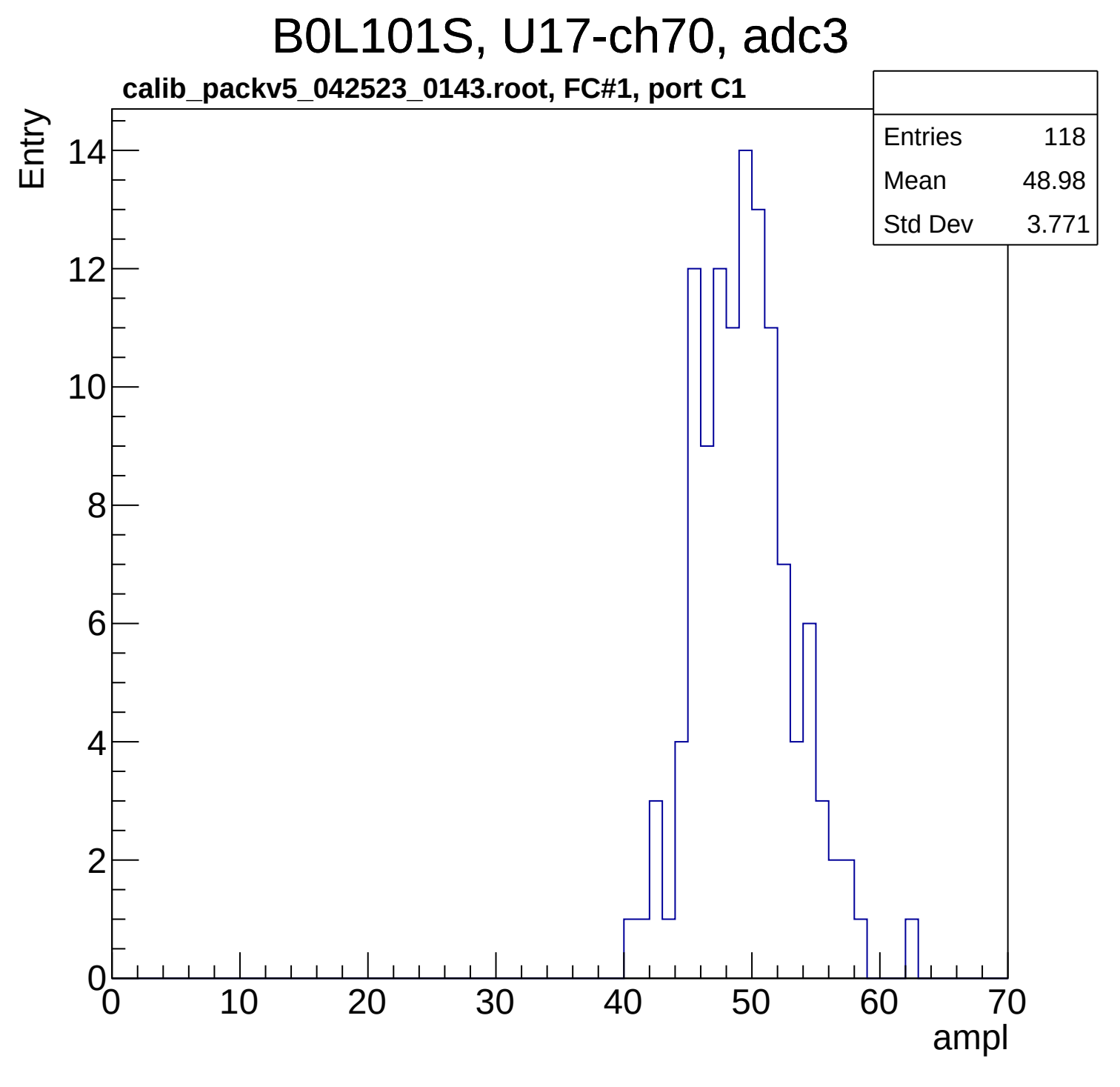

B0L101S, U17-ch69, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

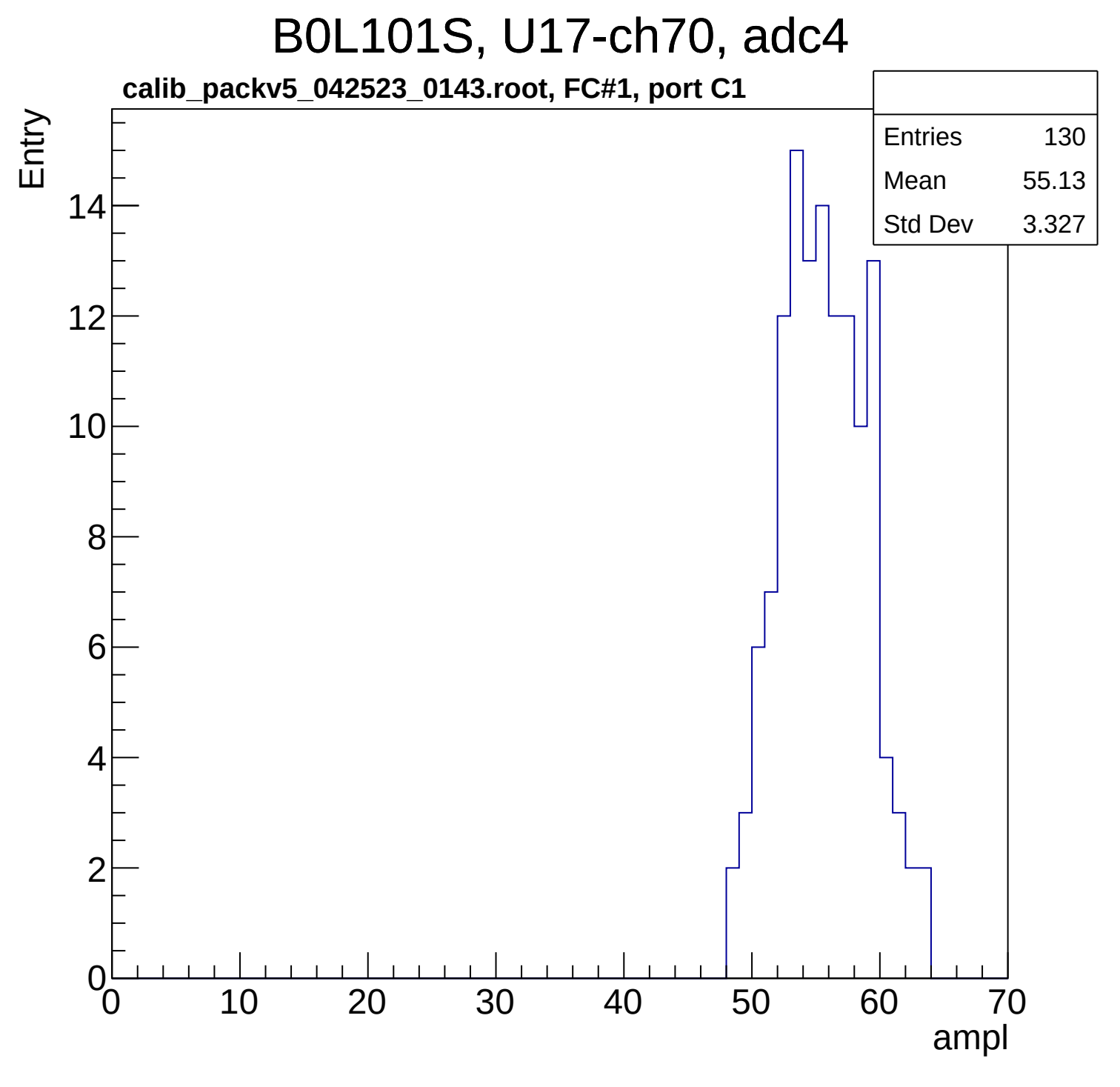

B0L101S, U17-ch69, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

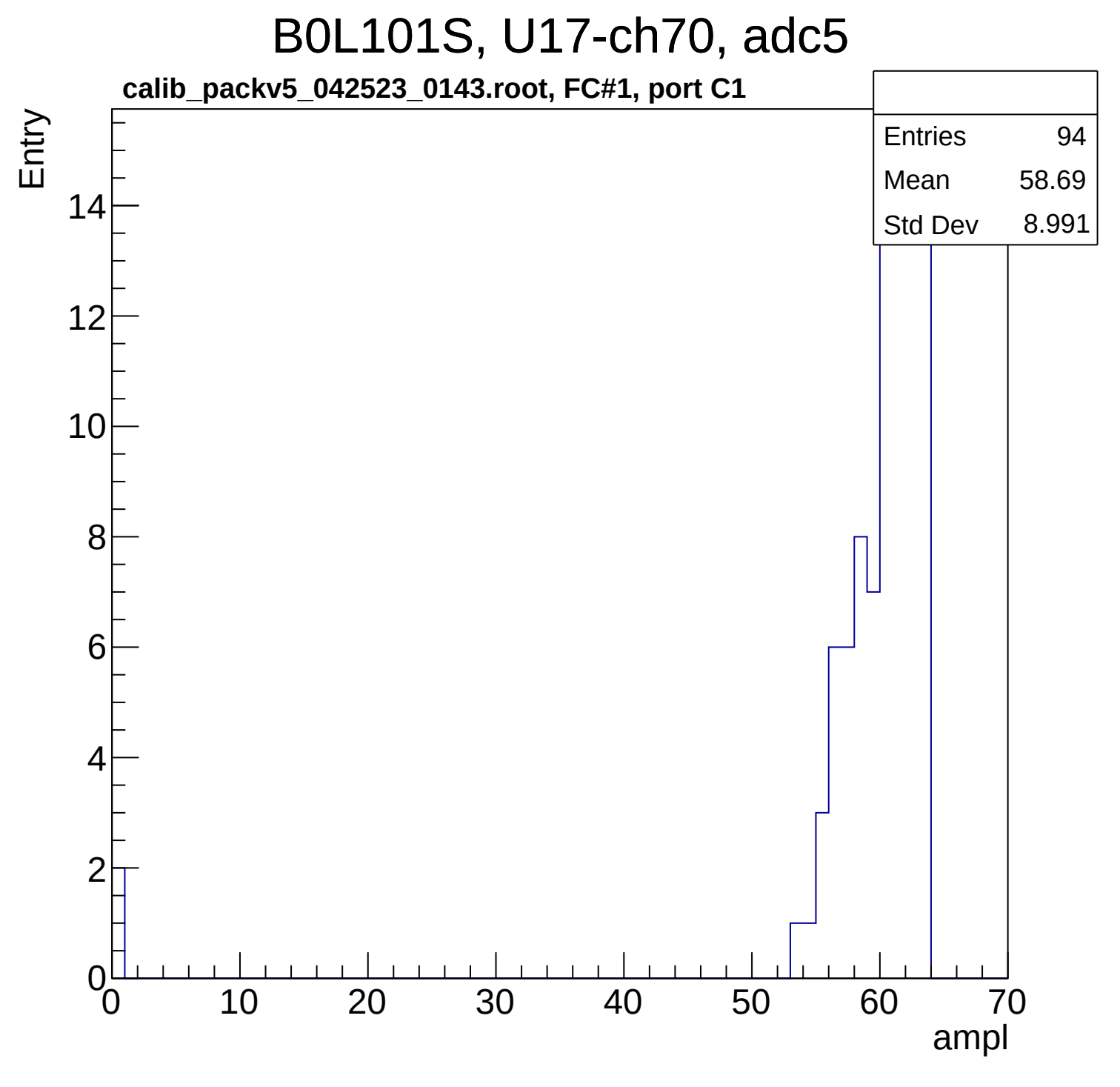

B0L101S, U17-ch70, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 27.6
Std Dev 3.386
Gaus mean : 27.9868
Gaus Width: 3.3881
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

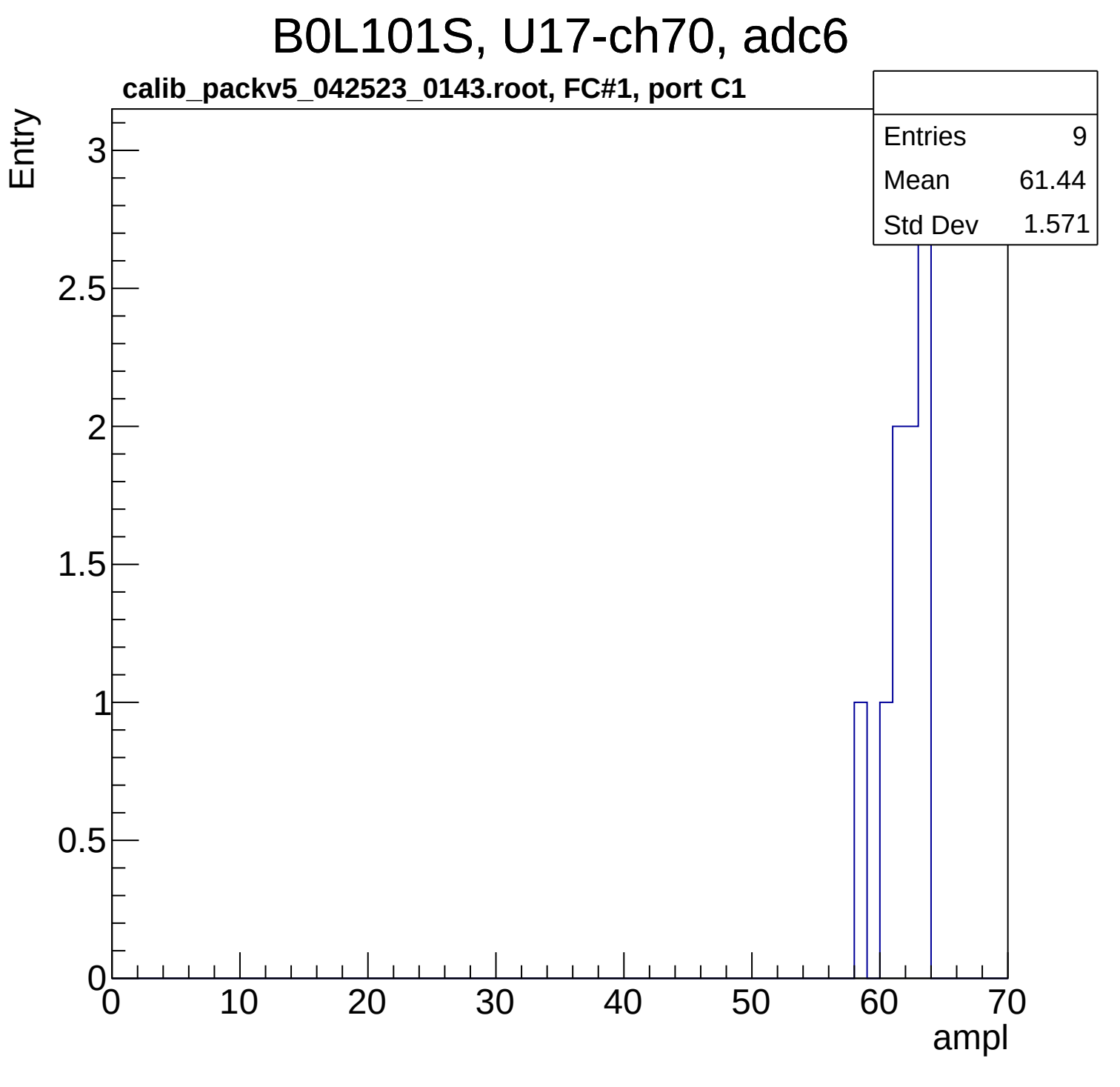

B0L101S, U17-ch70, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 172
Mean 34.9
Std Dev 4.024
Gaus mean : 35.5246
Gaus Width: 3.8988
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

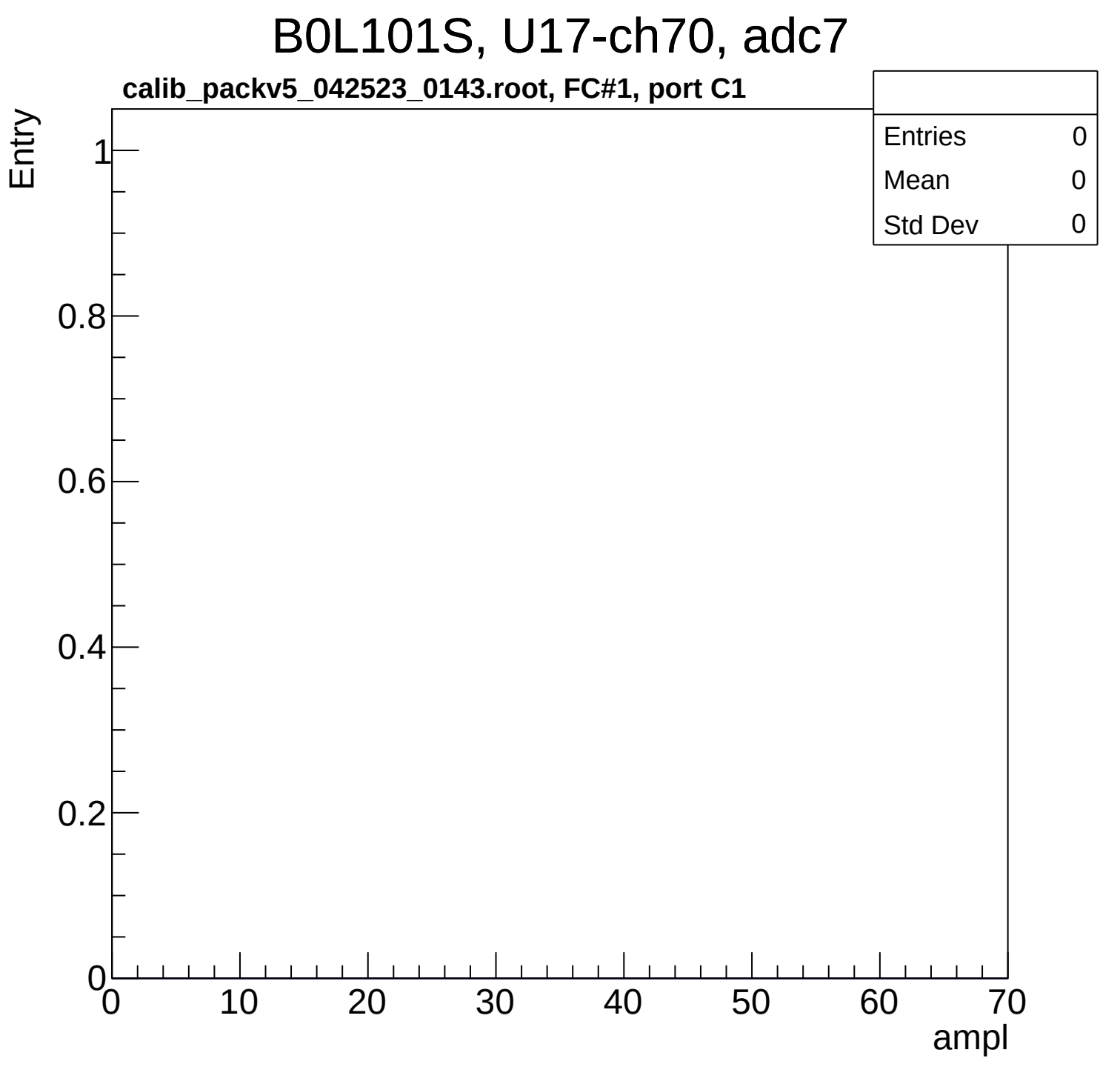

B0L101S, U17-ch70, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 42.61
Std Dev 3.823
Gaus mean : 42.8688
Gaus Width: 4.0304
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

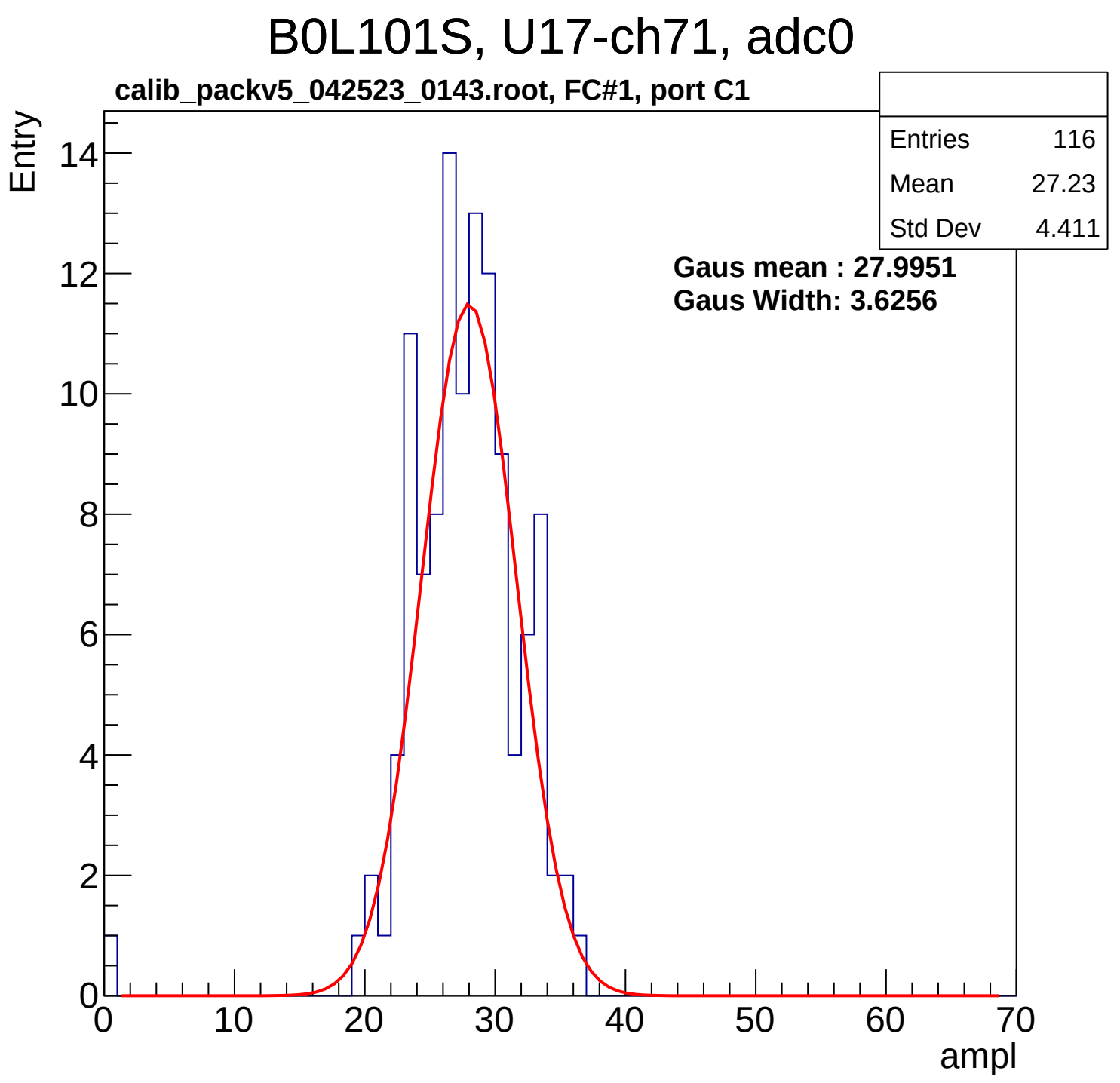

B0L101S, U17-ch70, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 48.98
Std Dev 3.771
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

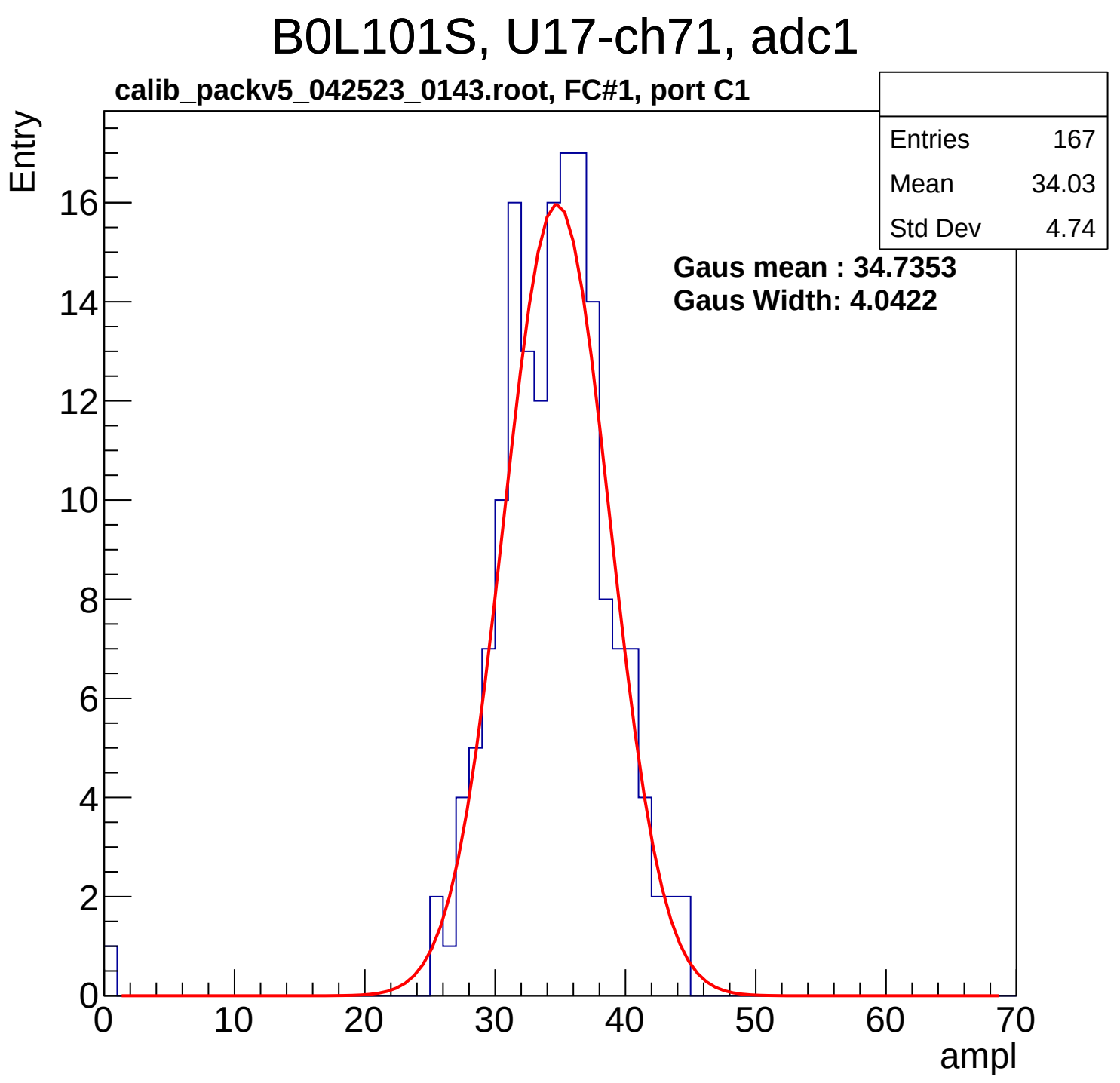

B0L101S, U17-ch70, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 55.13
Std Dev 3.327
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

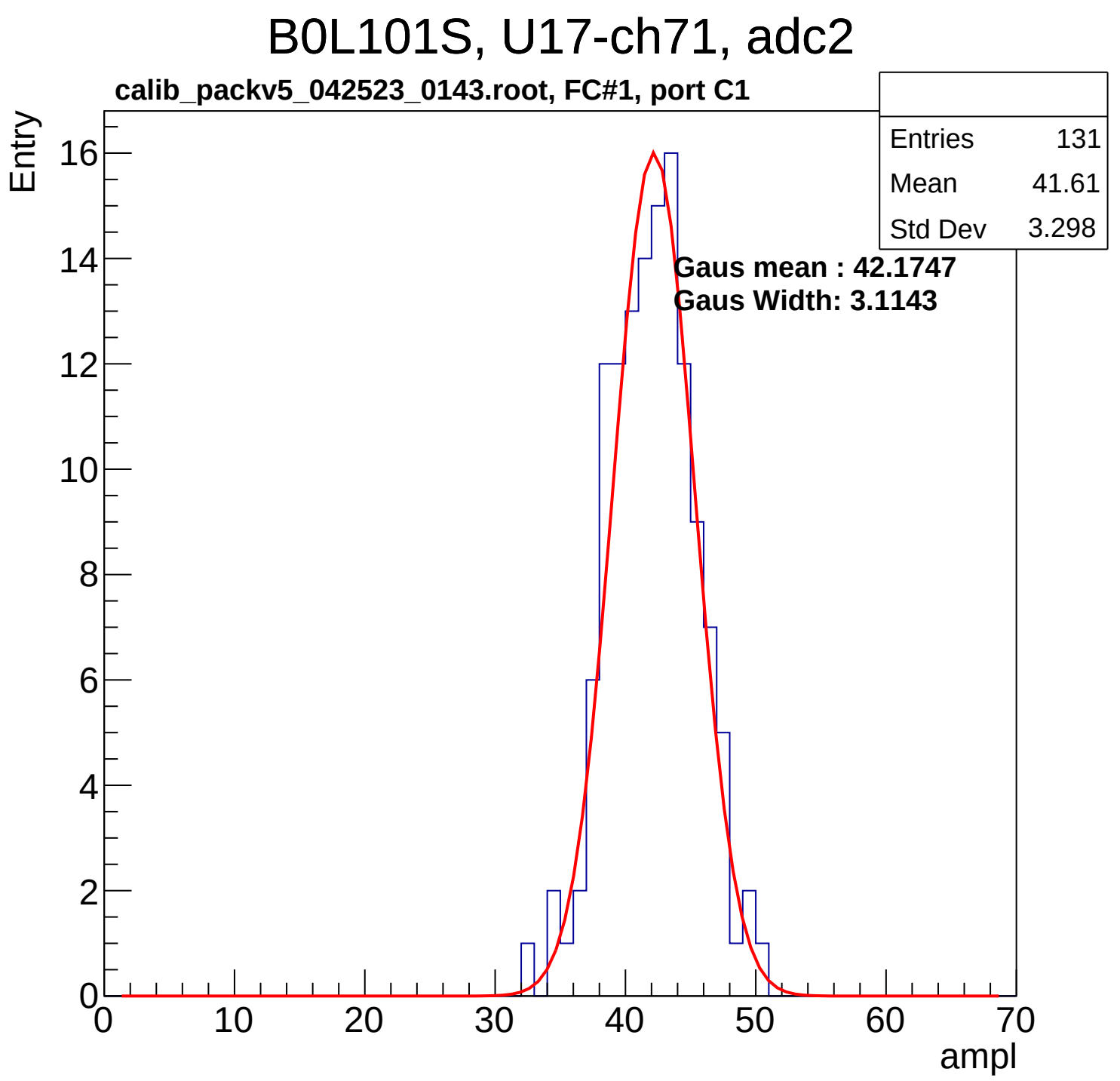

B0L101S, U17-ch70, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 94
Mean 58.69
Std Dev 8.991
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

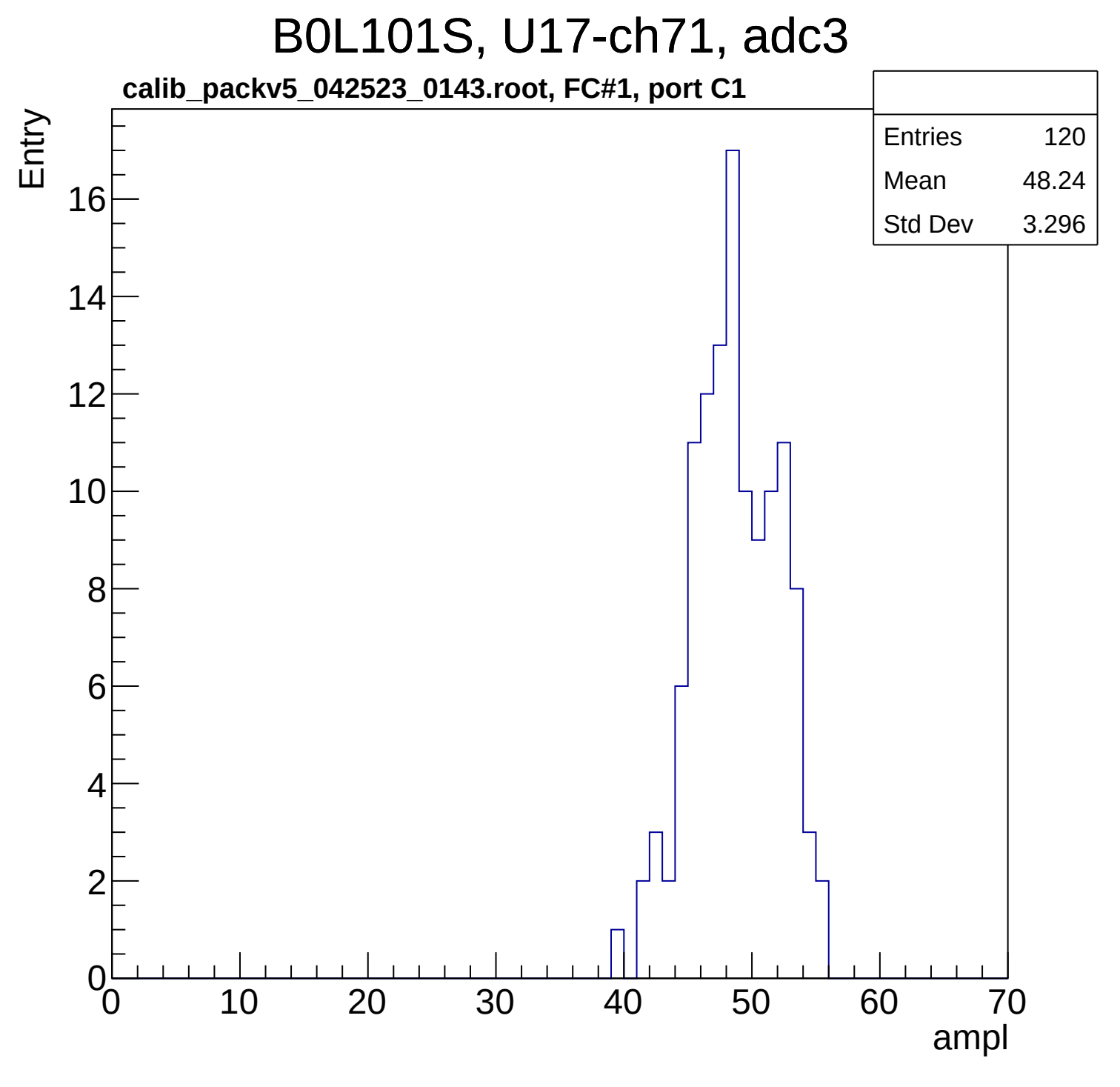

B0L101S, U17-ch70, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 9
Mean 61.44
Std Dev 1.571
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

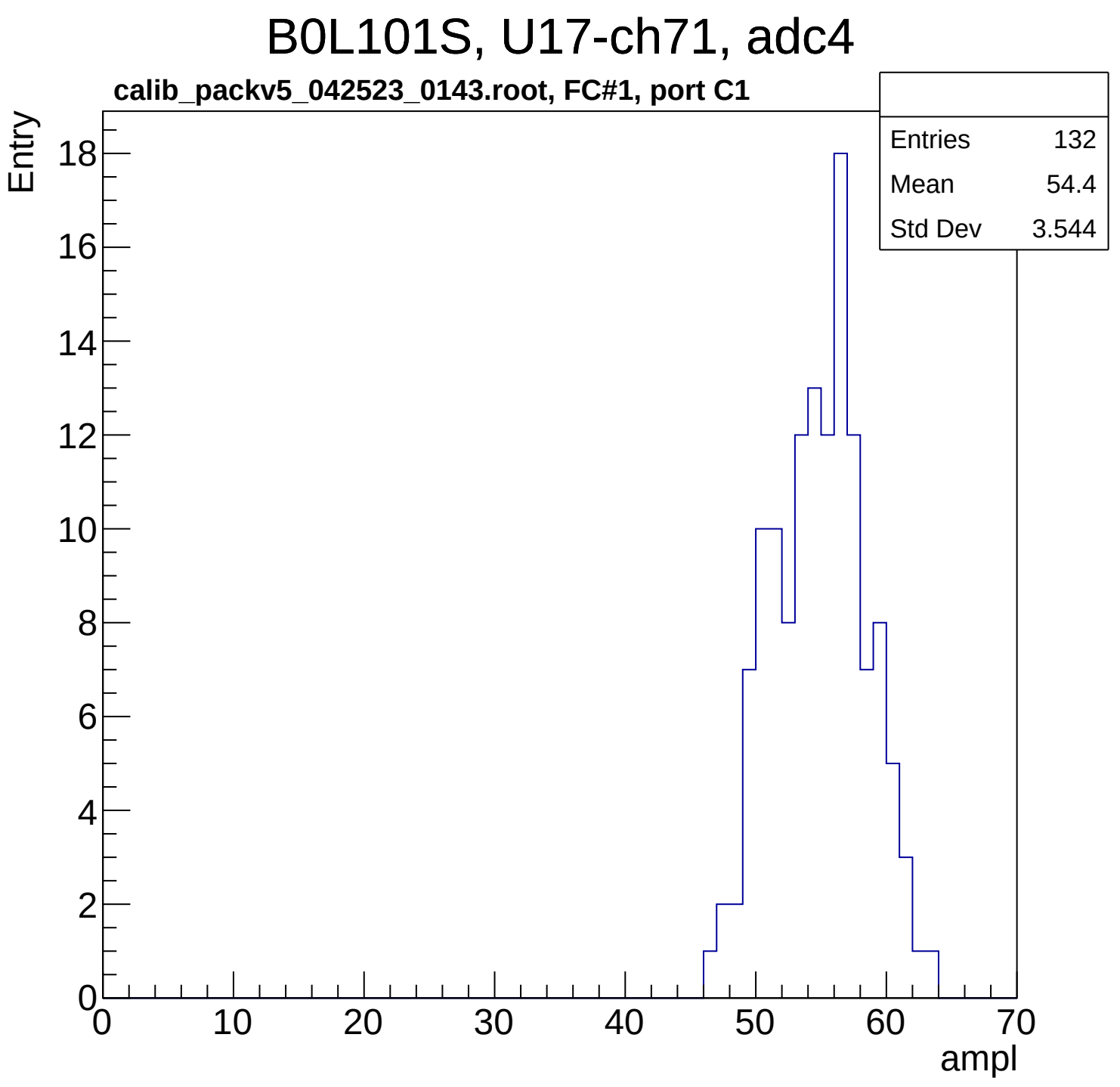

B0L101S, U17-ch70, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

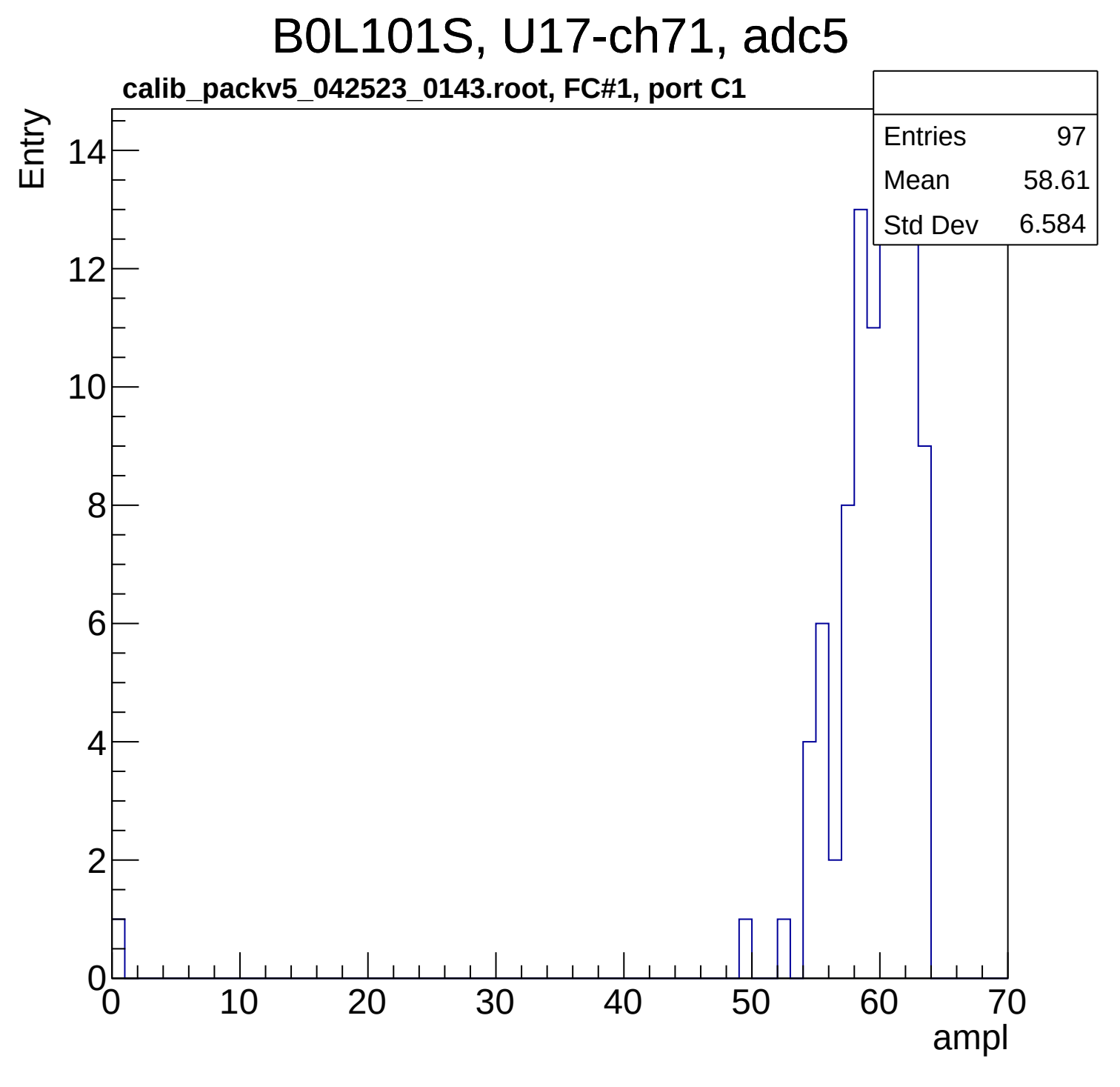

B0L101S, U17-ch71, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 27.23
Std Dev 4.411
Gaus mean : 27.9951
Gaus Width: 3.6256
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

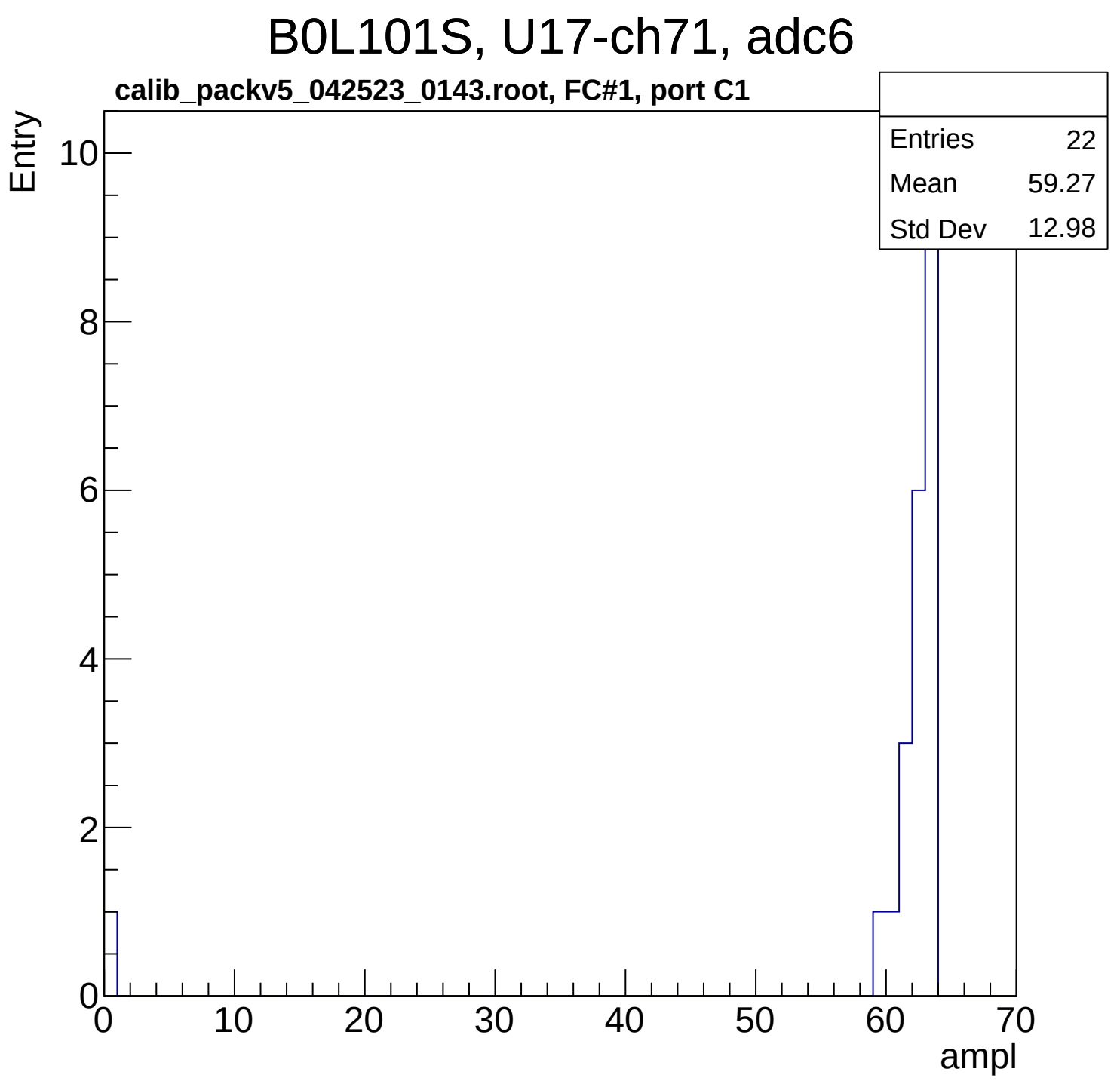

B0L101S, U17-ch71, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 167
Mean 34.03
Std Dev 4.74
Gaus mean : 34.7353
Gaus Width: 4.0422
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

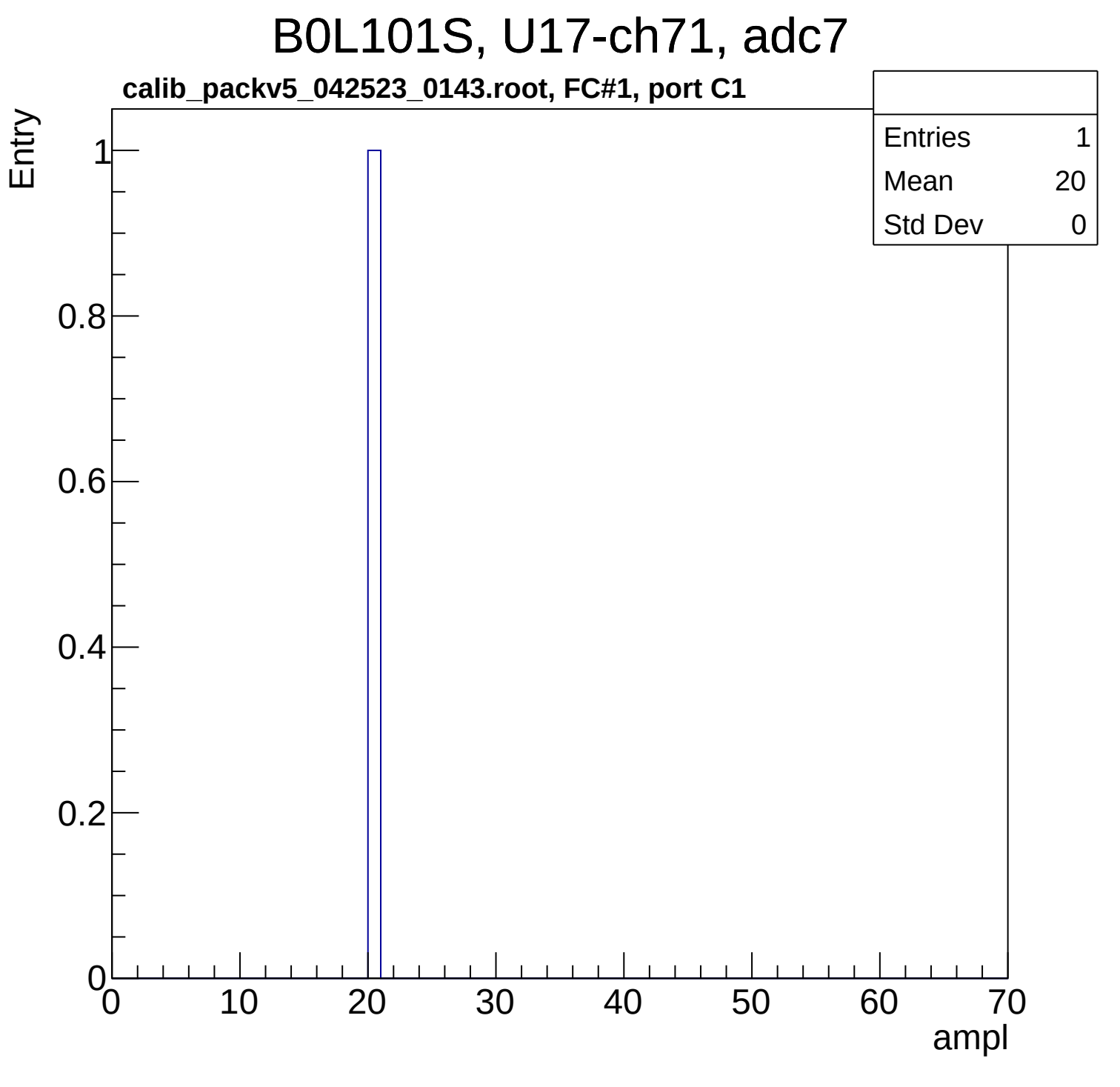

B0L101S, U17-ch71, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 41.61
Std Dev 3.298
Gaus mean : 42.1747
Gaus Width: 3.1143
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

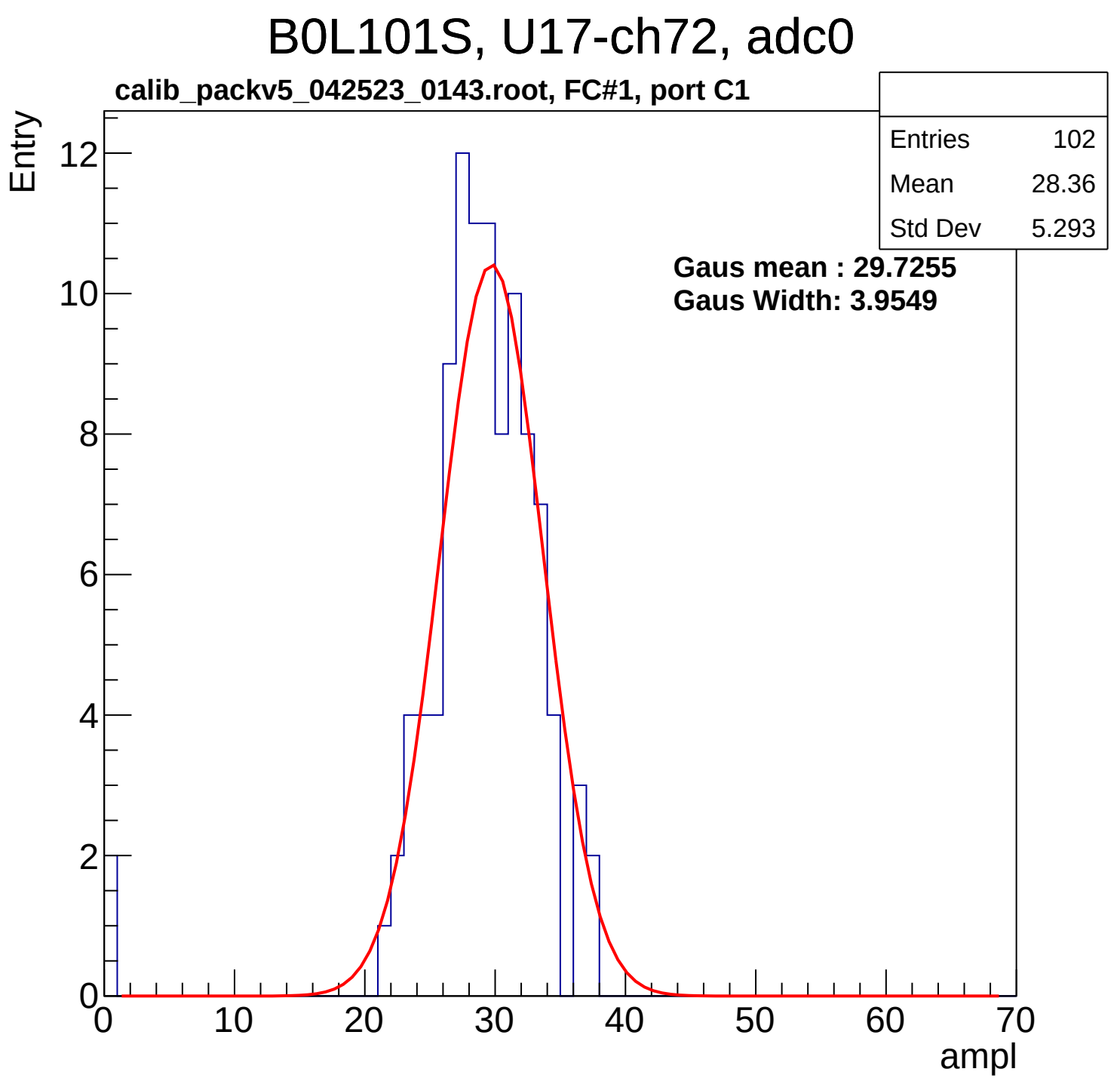

B0L101S, U17-ch71, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 48.24
Std Dev 3.296
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

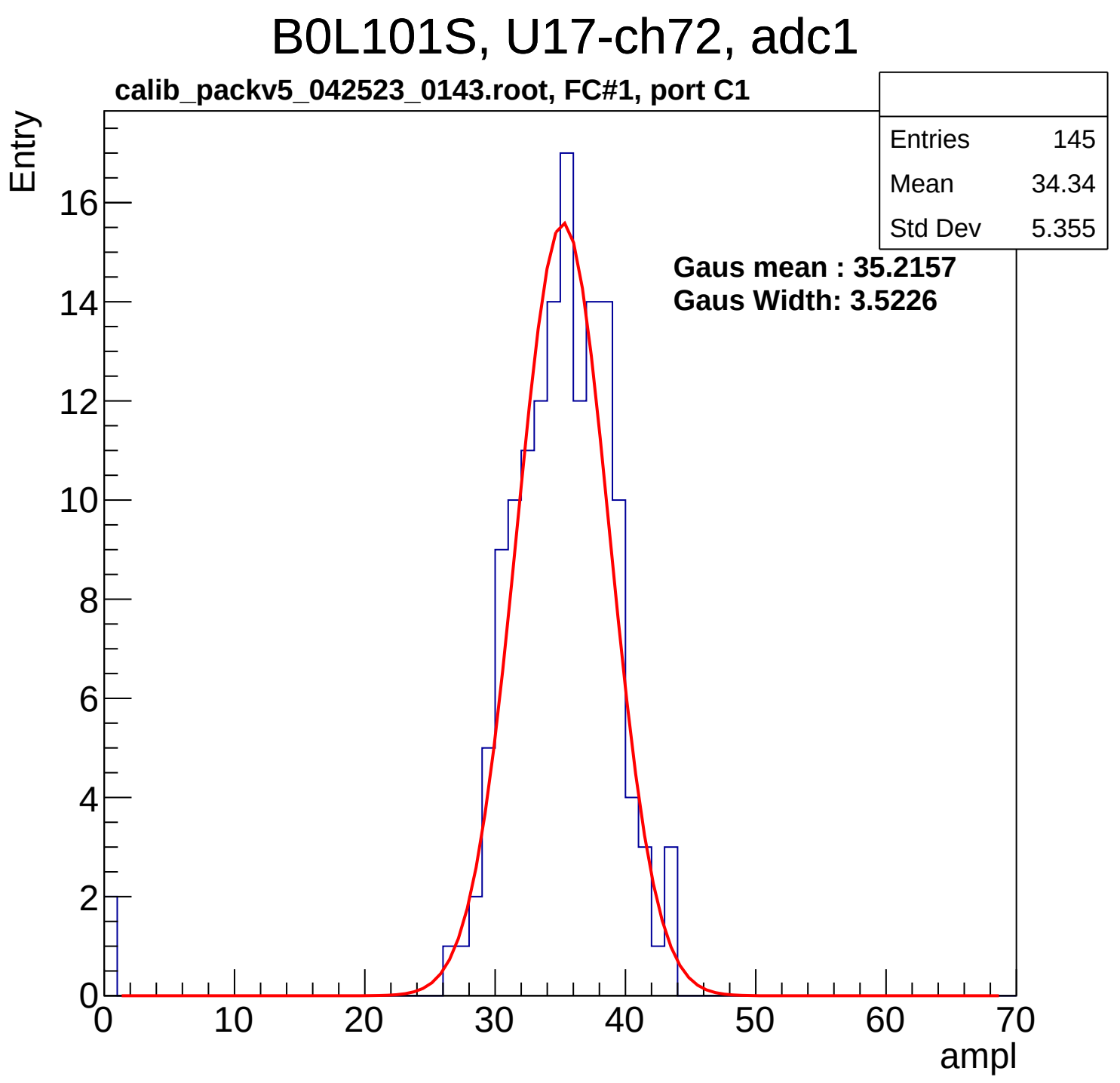

B0L101S, U17-ch71, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 54.4
Std Dev 3.544
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

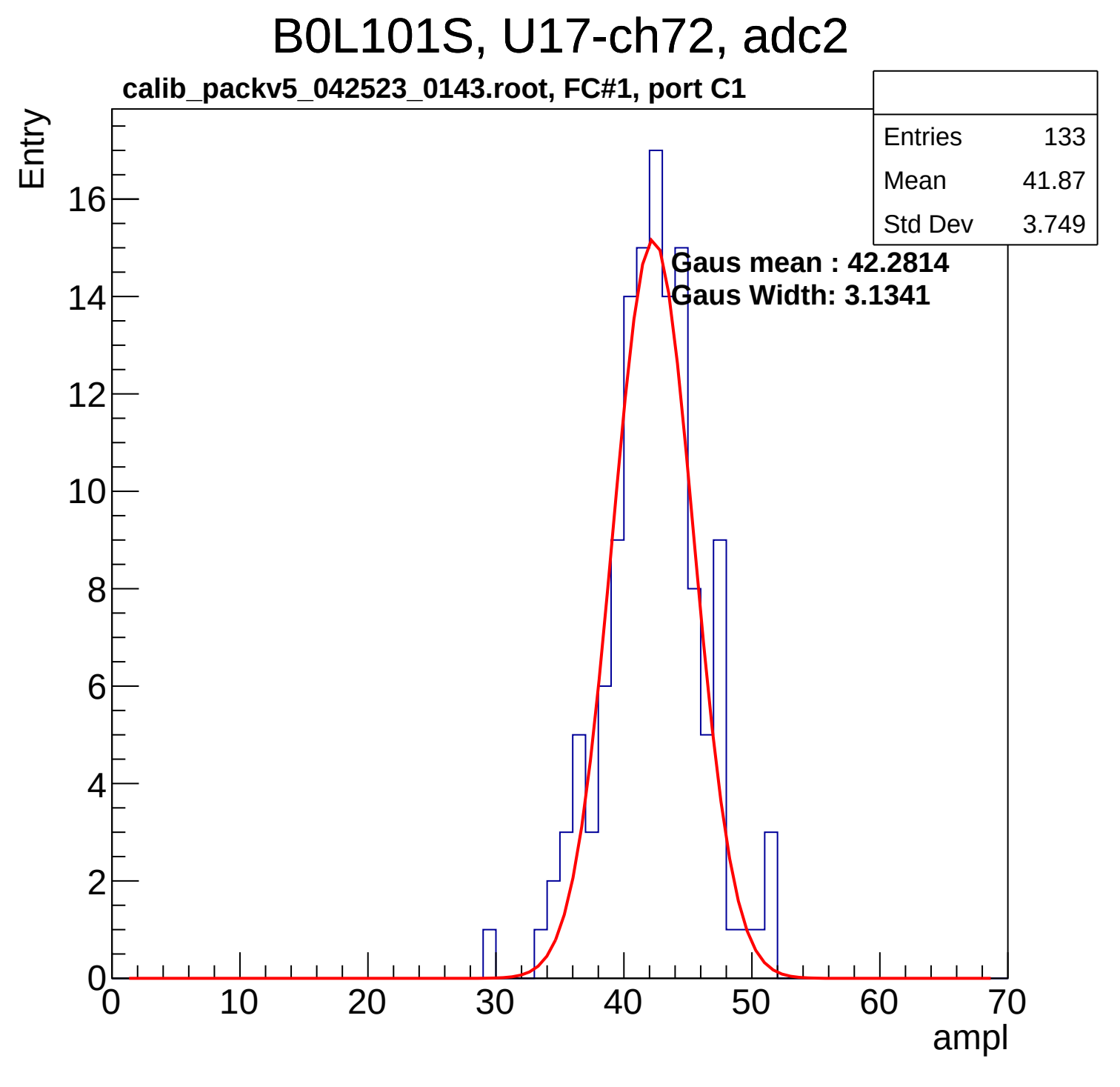

B0L101S, U17-ch71, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 97
Mean 58.61
Std Dev 6.584
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

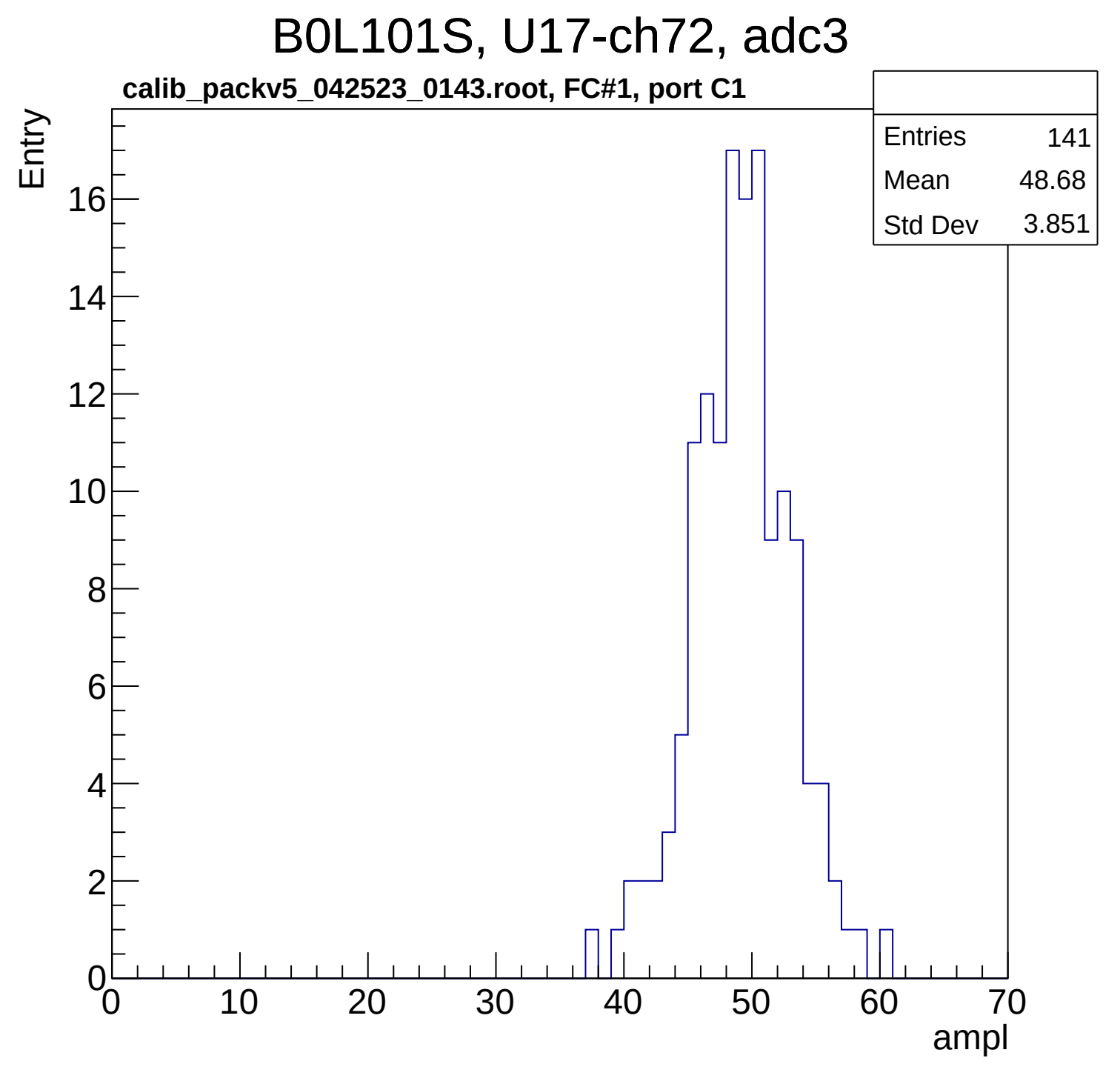

B0L101S, U17-ch71, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 22
Mean 59.27
Std Dev 12.98
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

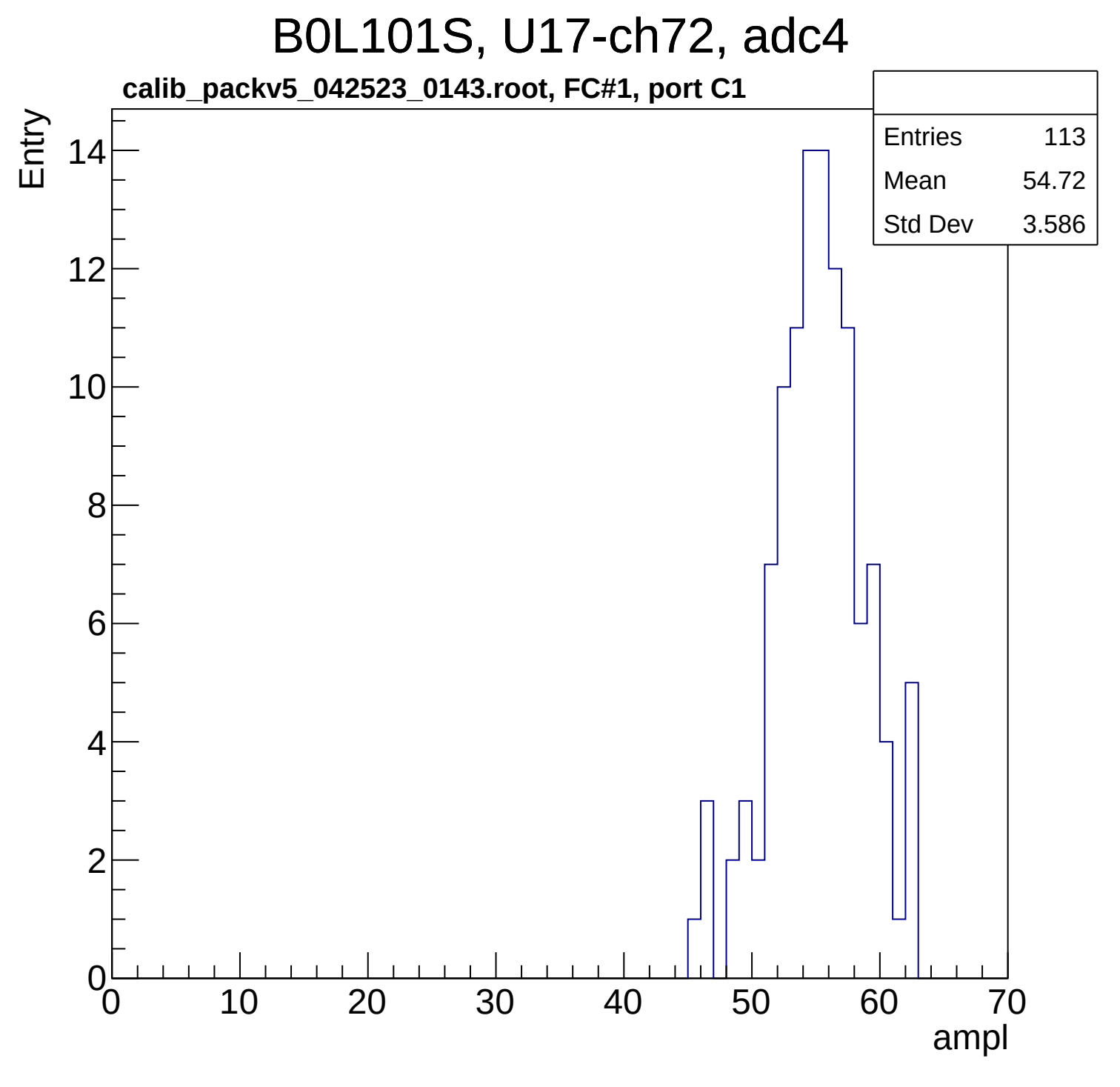

B0L101S, U17-ch71, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

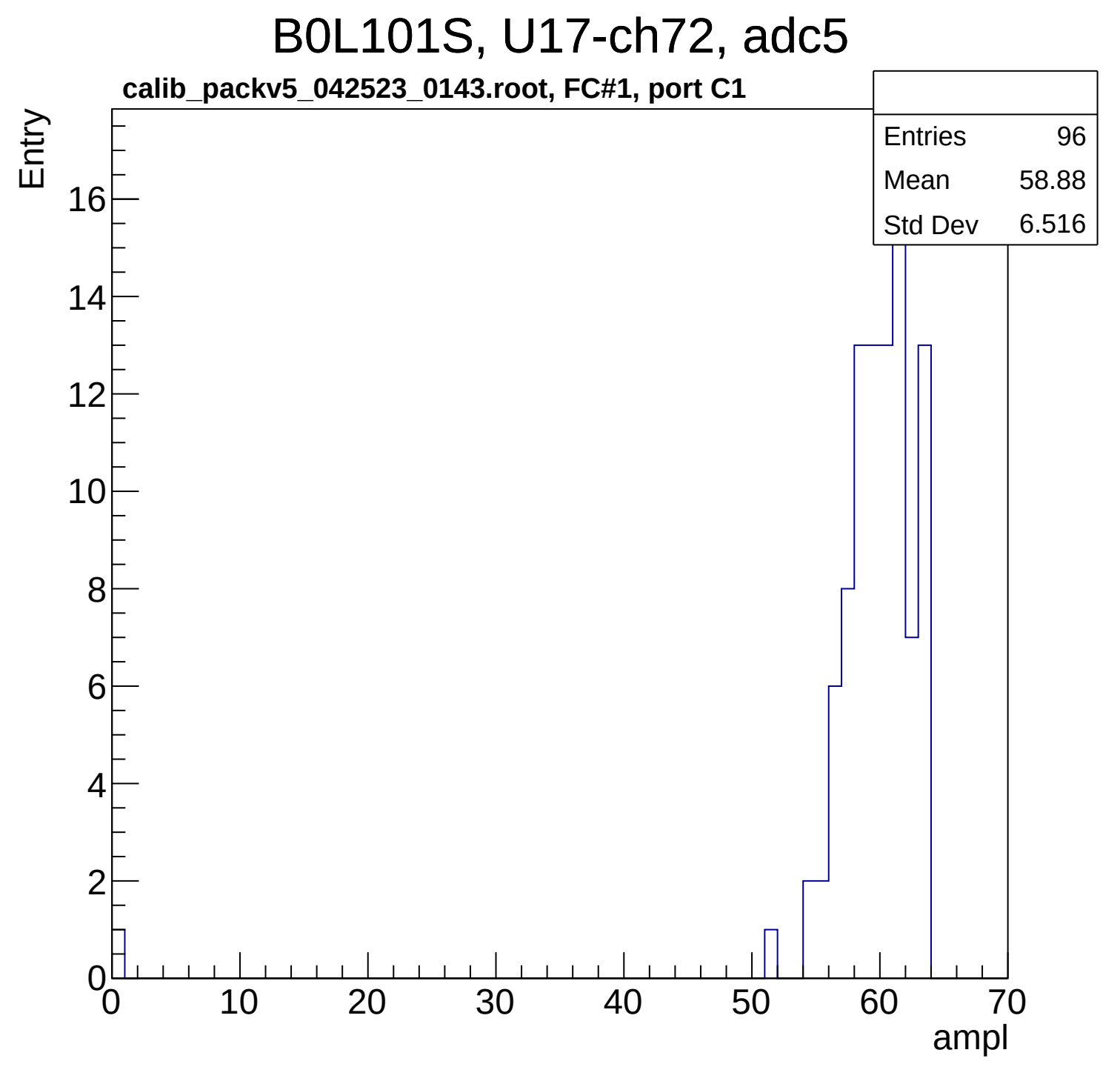

B0L101S, U17-ch72, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 28.36
Std Dev 5.293
Gaus mean : 29.7255
Gaus Width: 3.9549
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

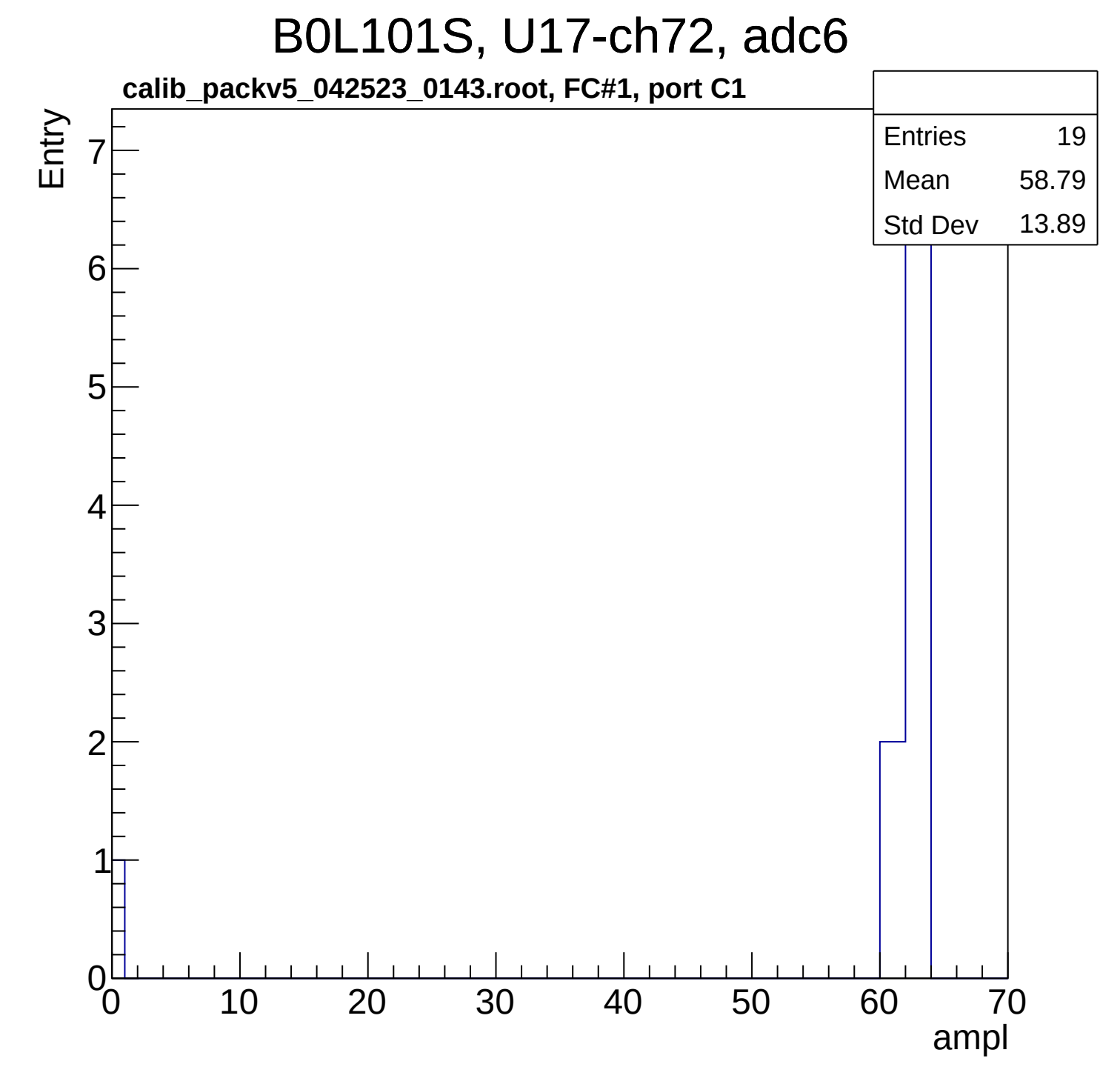

B0L101S, U17-ch72, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 34.34
Std Dev 5.355
Gaus mean : 35.2157
Gaus Width: 3.5226
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

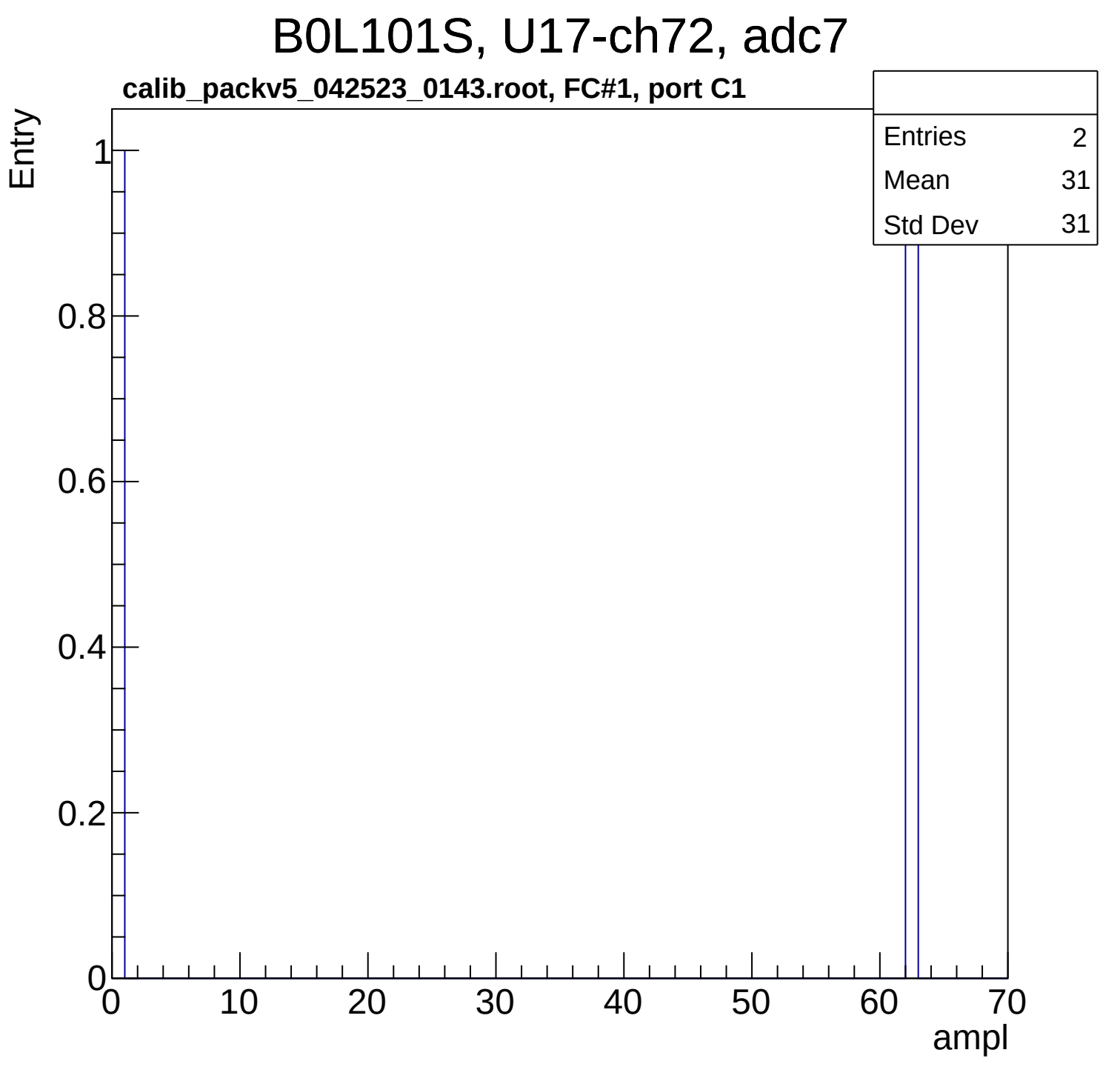

B0L101S, U17-ch72, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 41.87
Std Dev 3.749
Gaus mean : 42.2814
Gaus Width: 3.1341
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

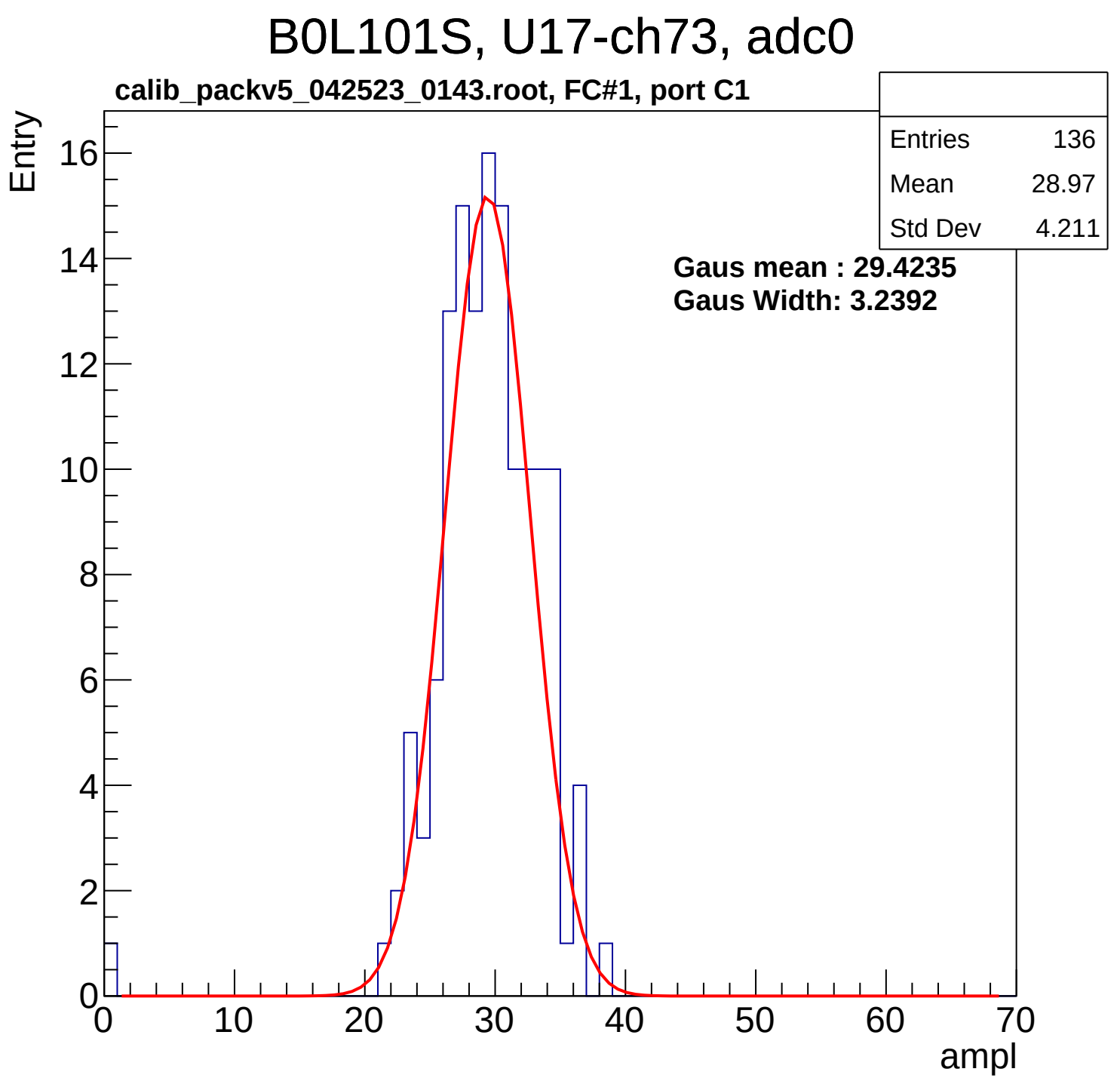

B0L101S, U17-ch72, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 48.68
Std Dev 3.851
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

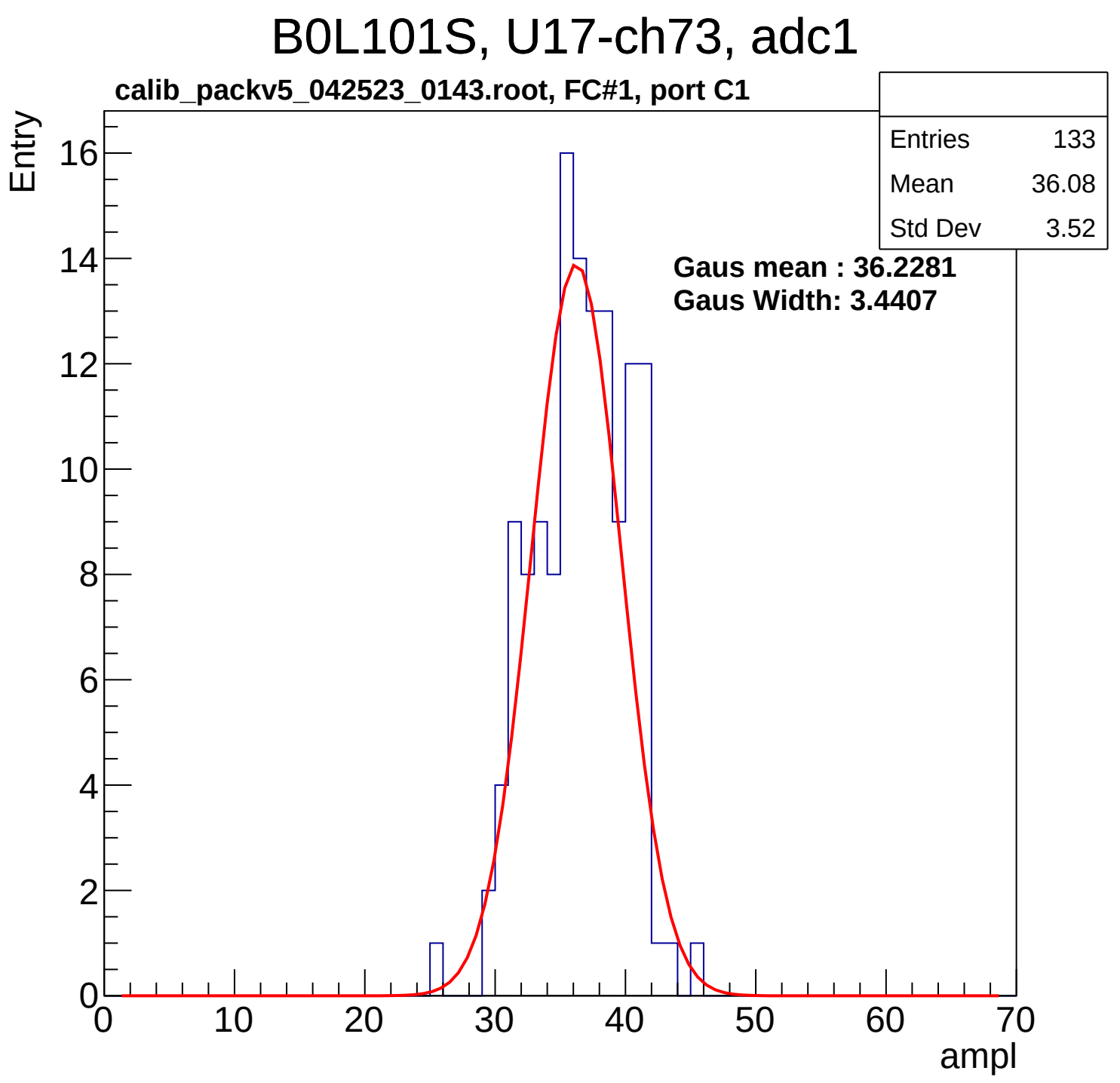

B0L101S, U17-ch72, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 54.72
Std Dev 3.586
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

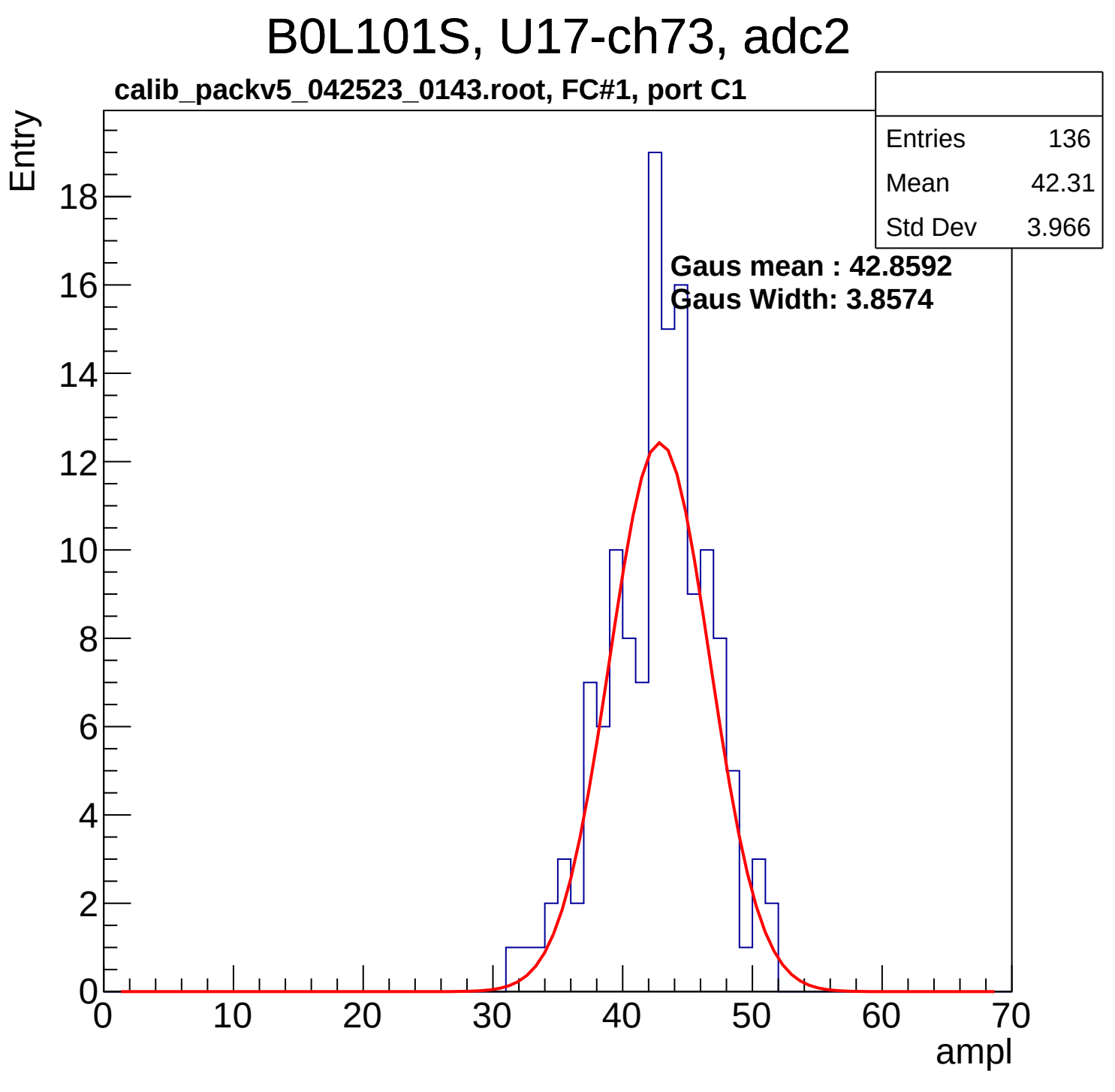

B0L101S, U17-ch72, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 58.88
Std Dev 6.516
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

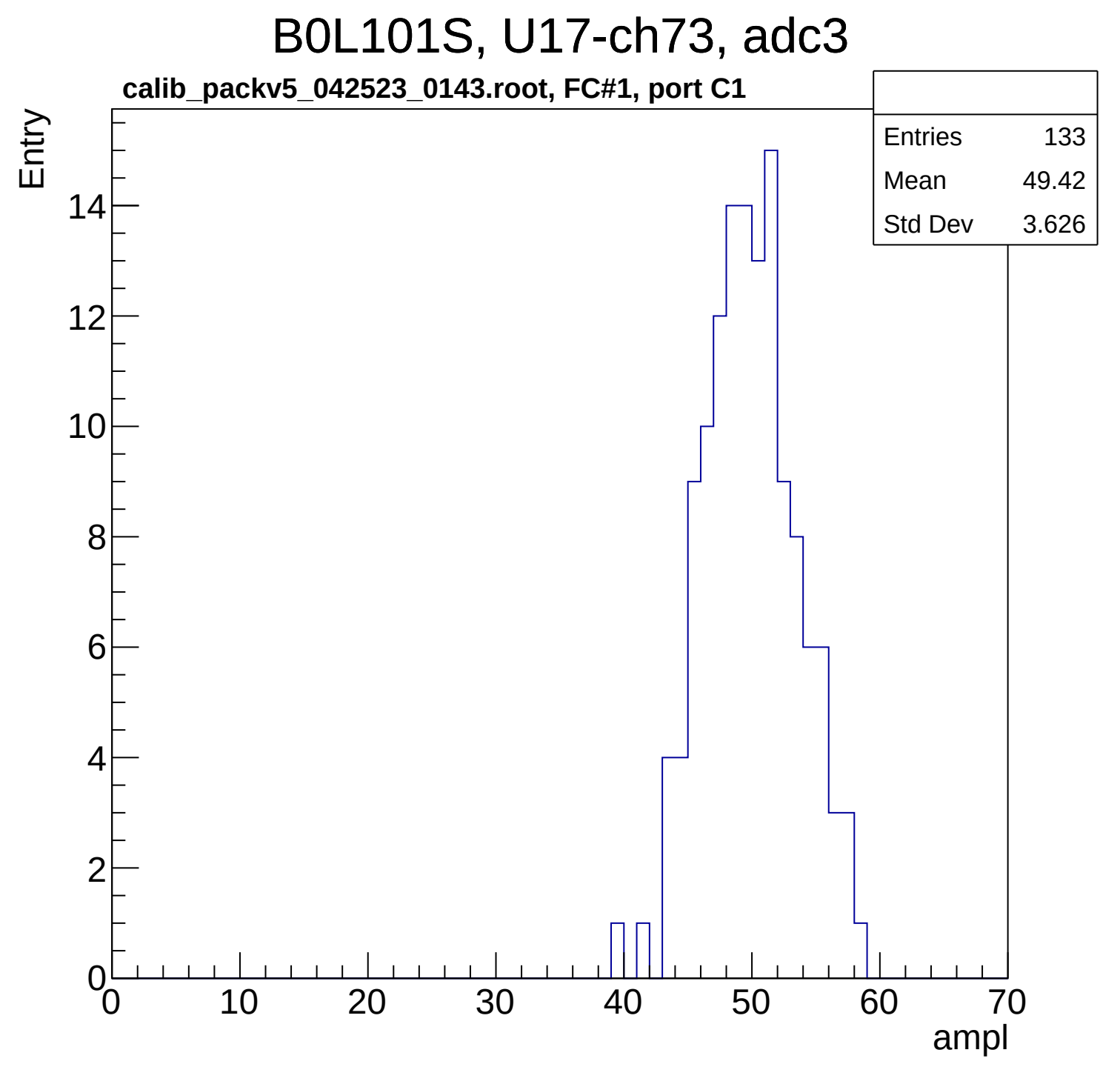

B0L101S, U17-ch72, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 19
Mean 58.79
Std Dev 13.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

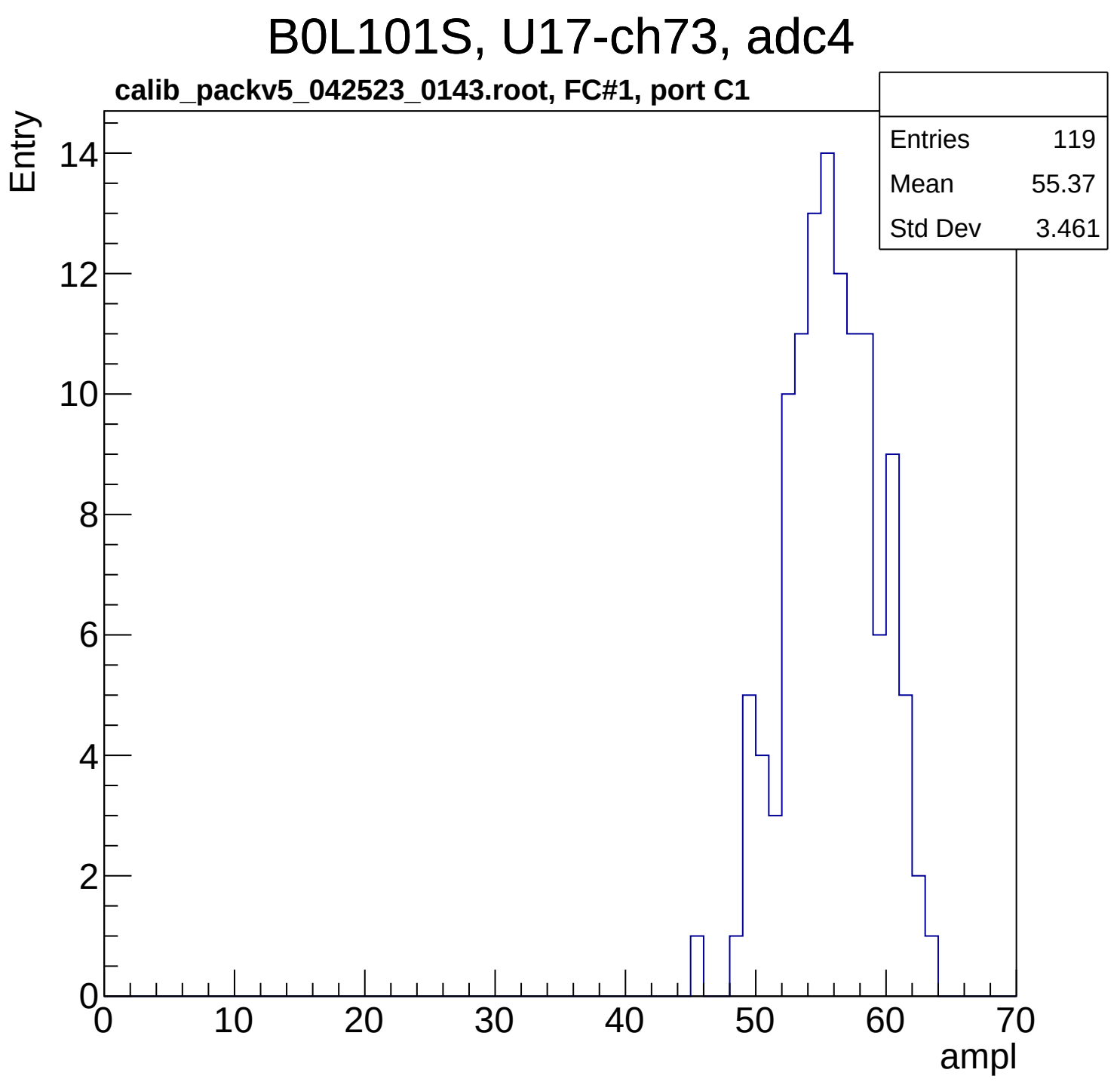

B0L101S, U17-ch72, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

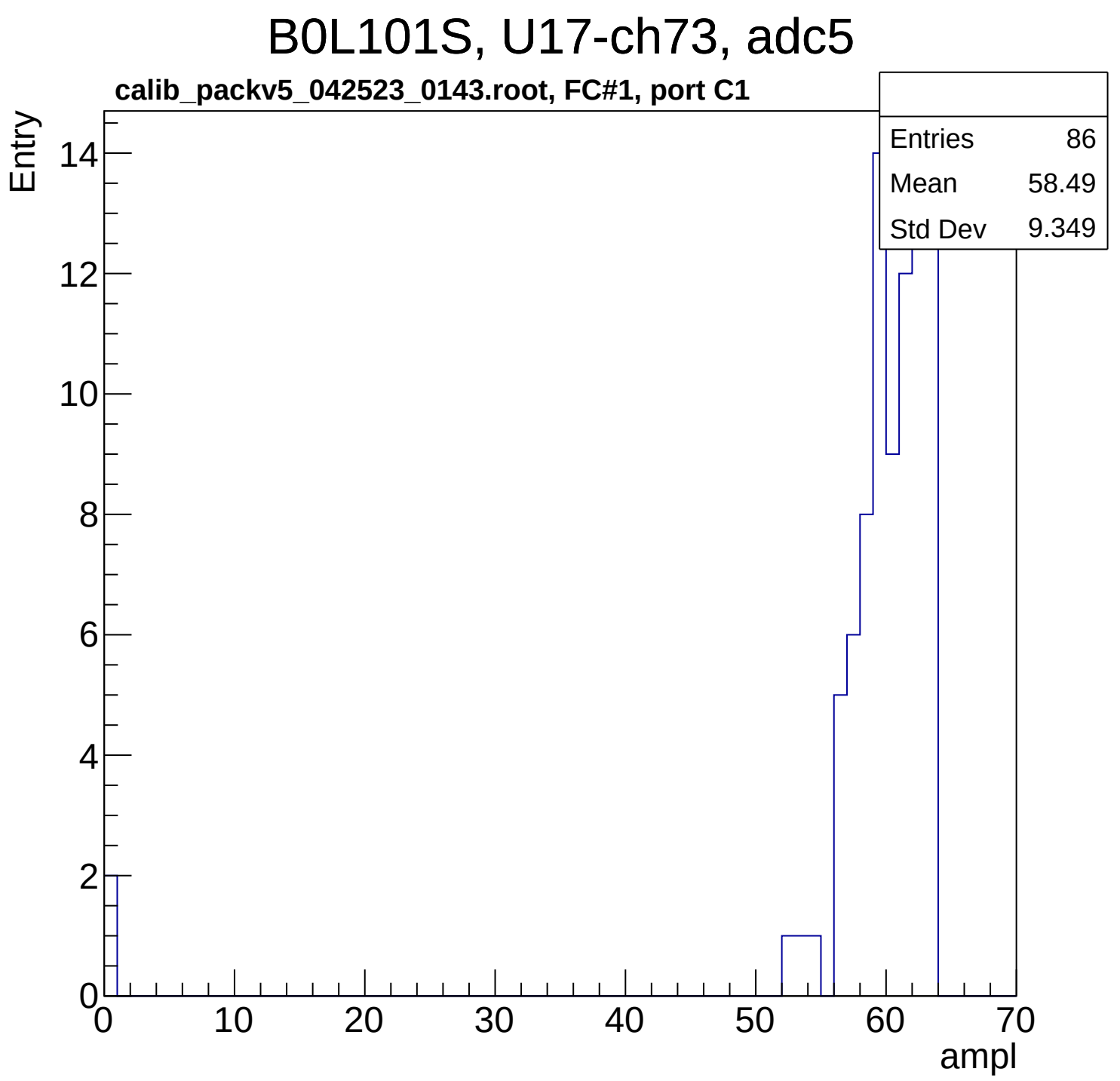

B0L101S, U17-ch73, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 28.97
Std Dev 4.211
Gaus mean : 29.4235
Gaus Width: 3.2392
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

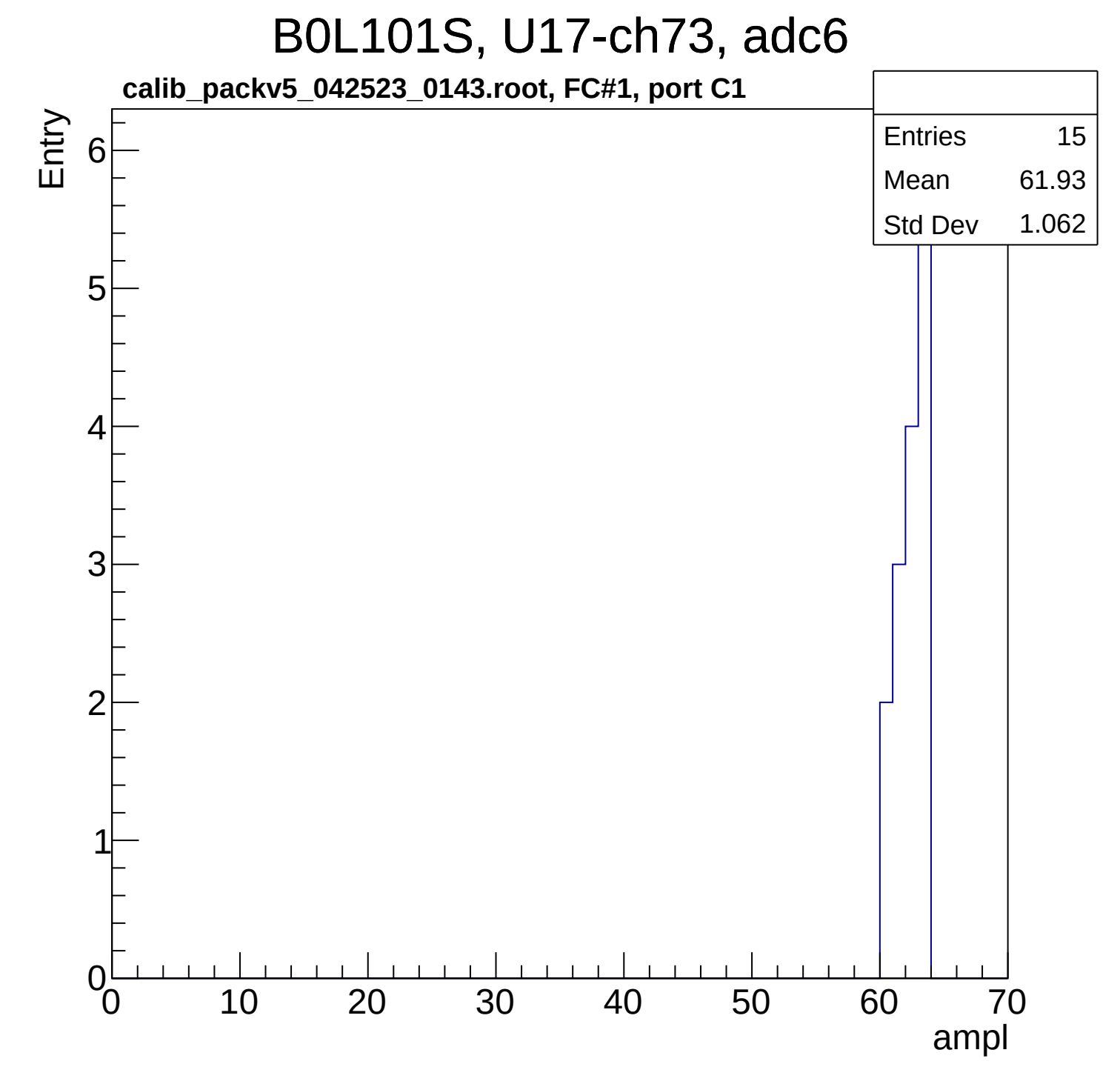

B0L101S, U17-ch73, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 36.08
Std Dev 3.52
Gaus mean : 36.2281
Gaus Width: 3.4407
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

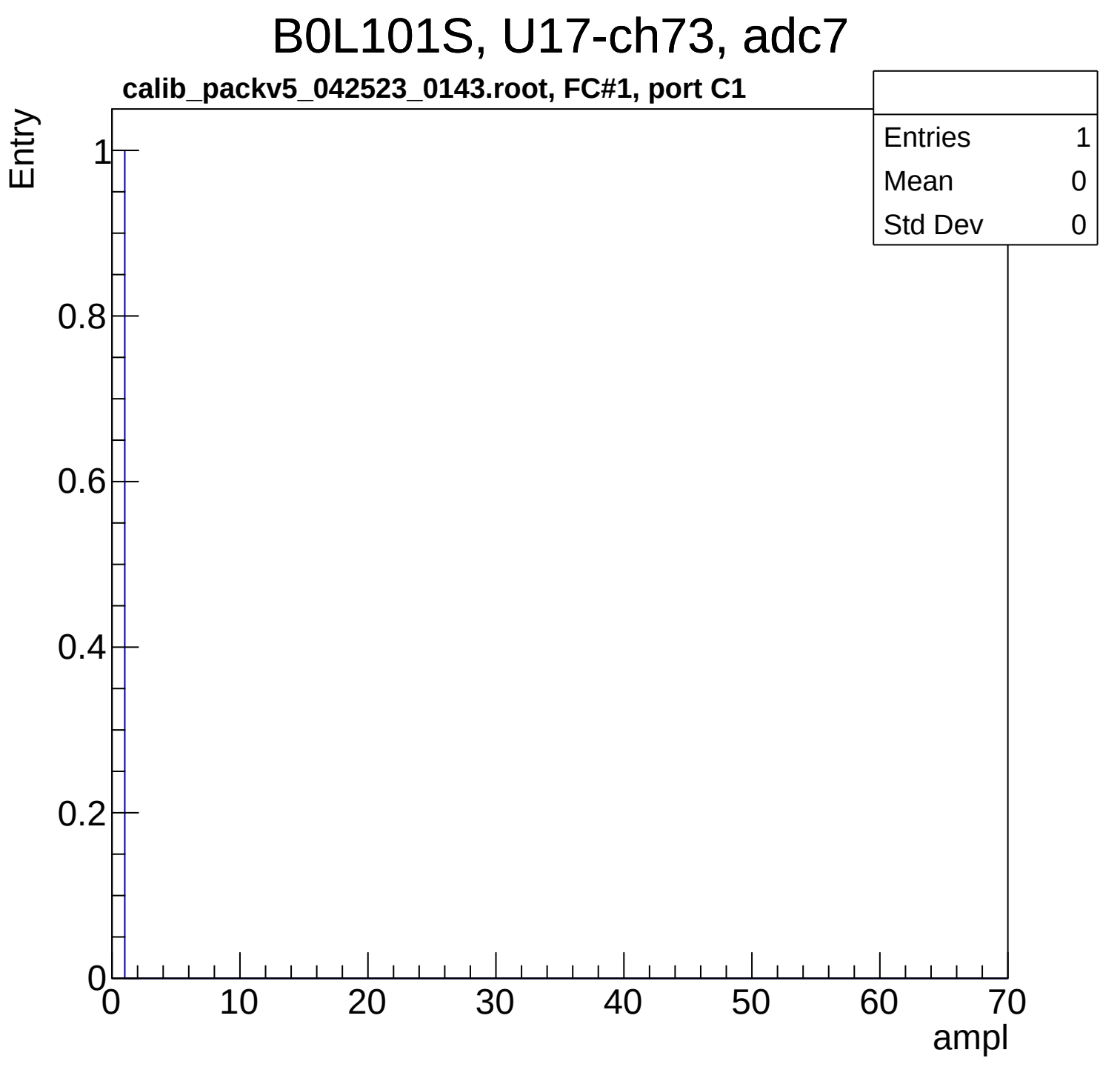

B0L101S, U17-ch73, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 42.31
Std Dev 3.966
Gaus mean : 42.8592
Gaus Width: 3.8574
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

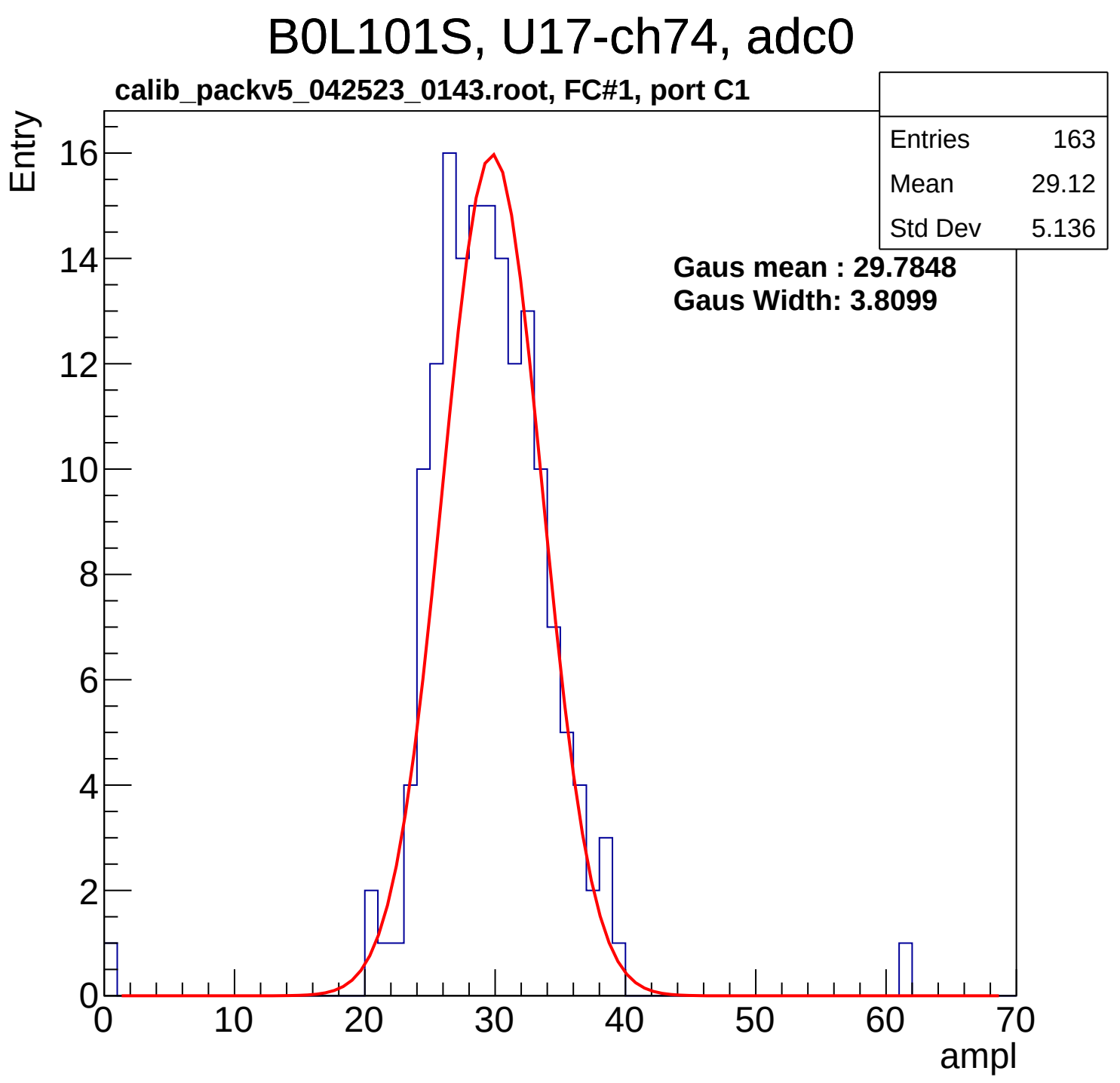

B0L101S, U17-ch73, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 49.42
Std Dev 3.626
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

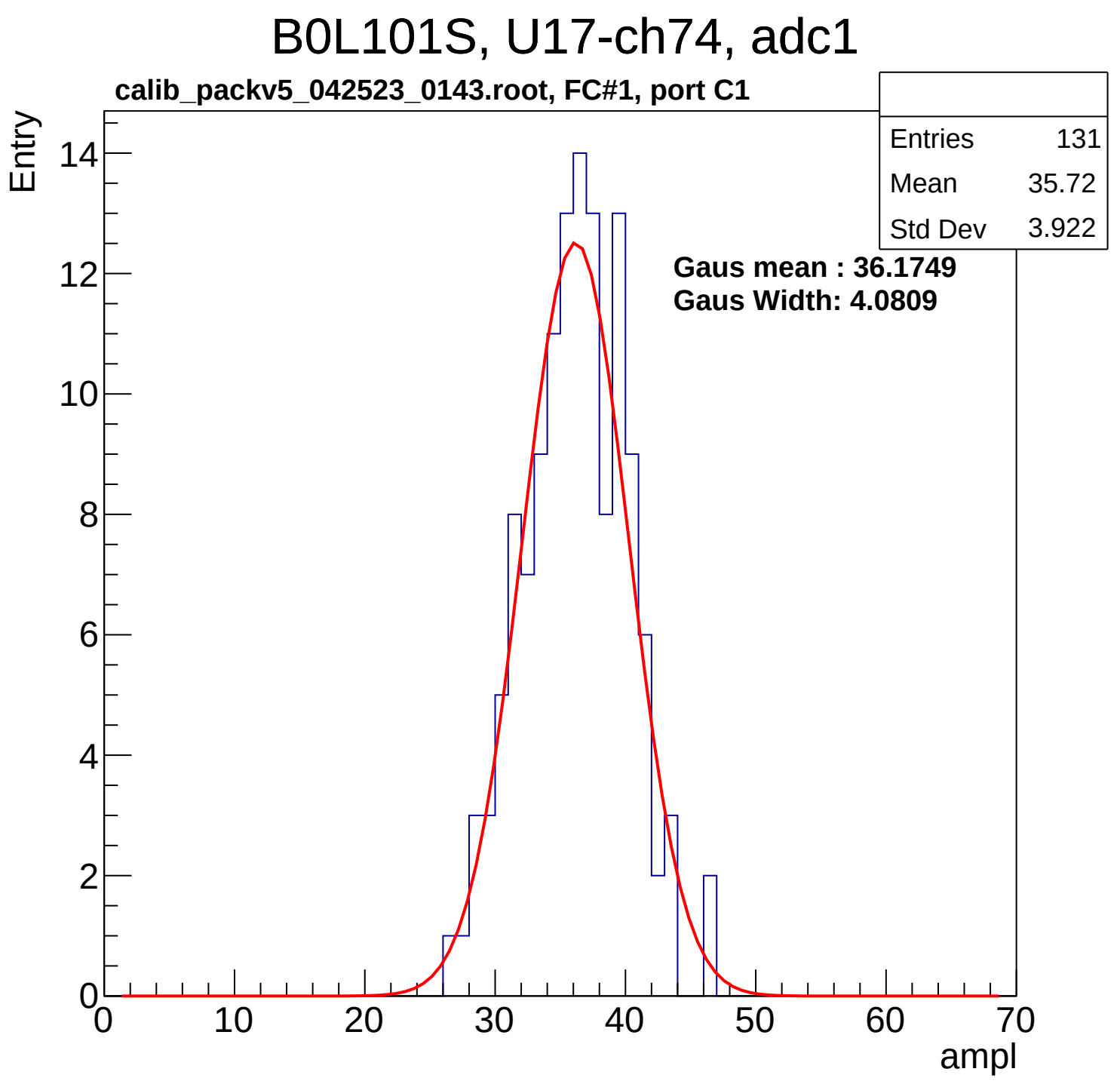

B0L101S, U17-ch73, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 55.37
Std Dev 3.461
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

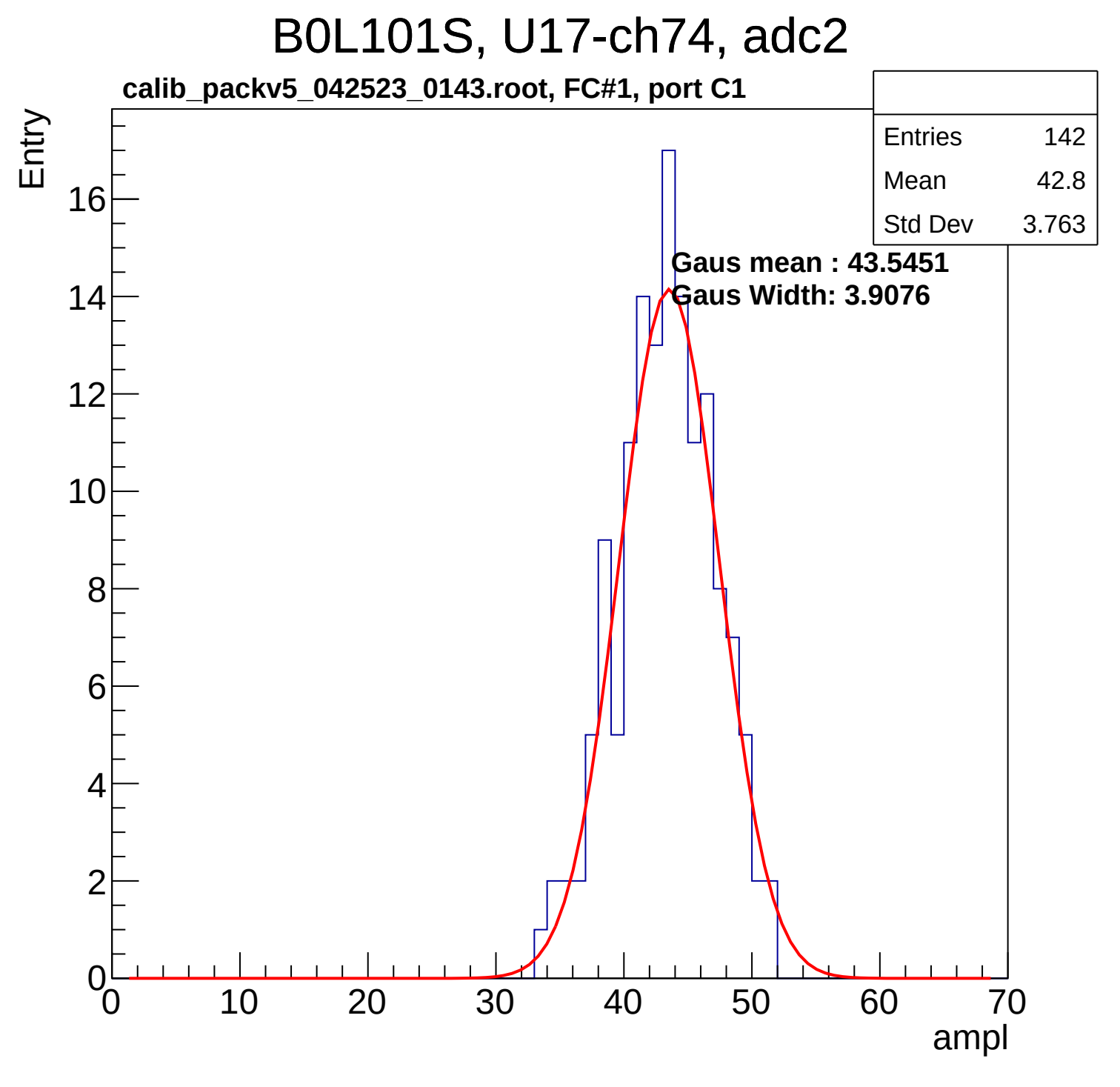

B0L101S, U17-ch73, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 86
Mean 58.49
Std Dev 9.349
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

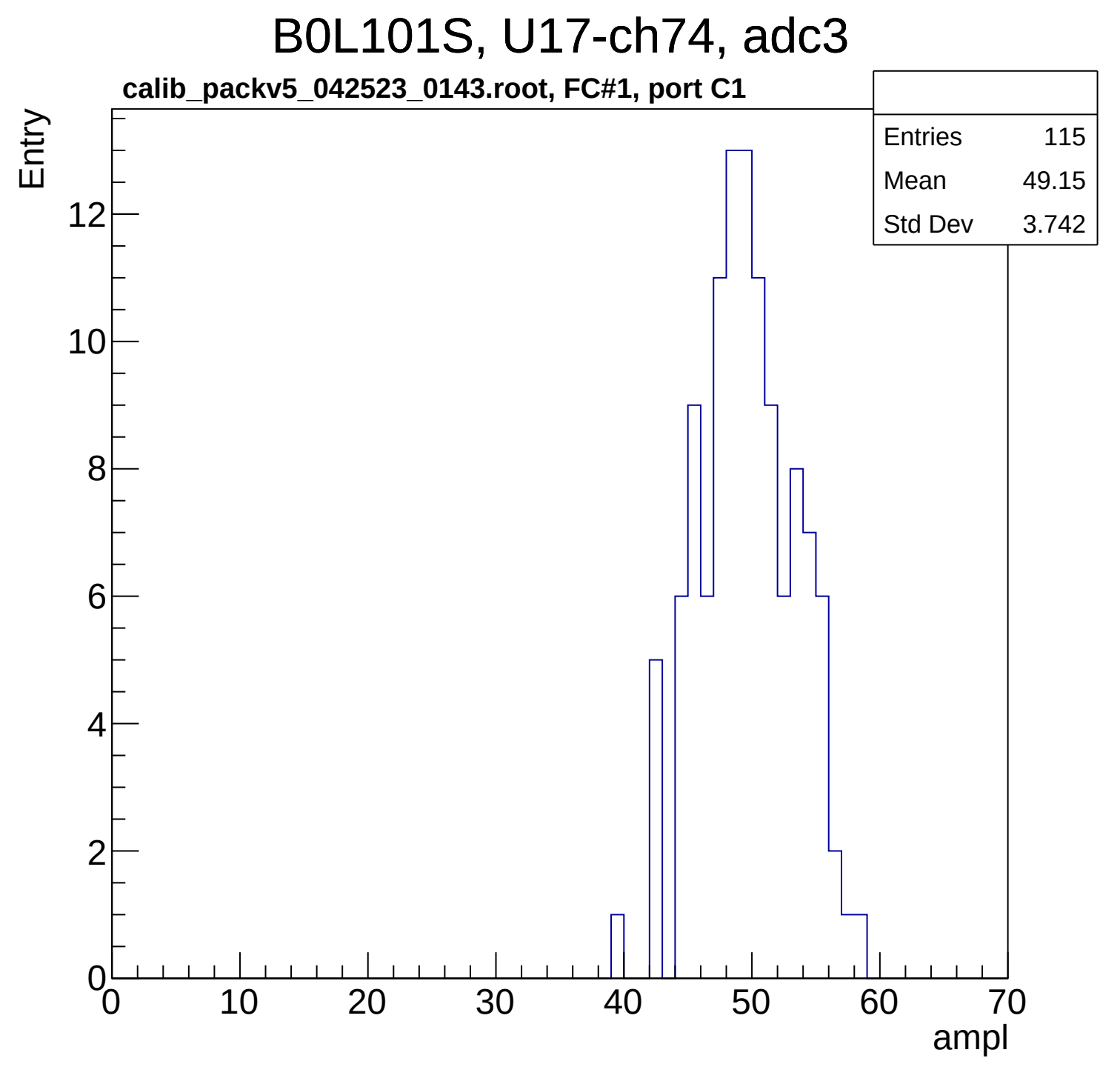

B0L101S, U17-ch73, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 15
Mean 61.93
Std Dev 1.062
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

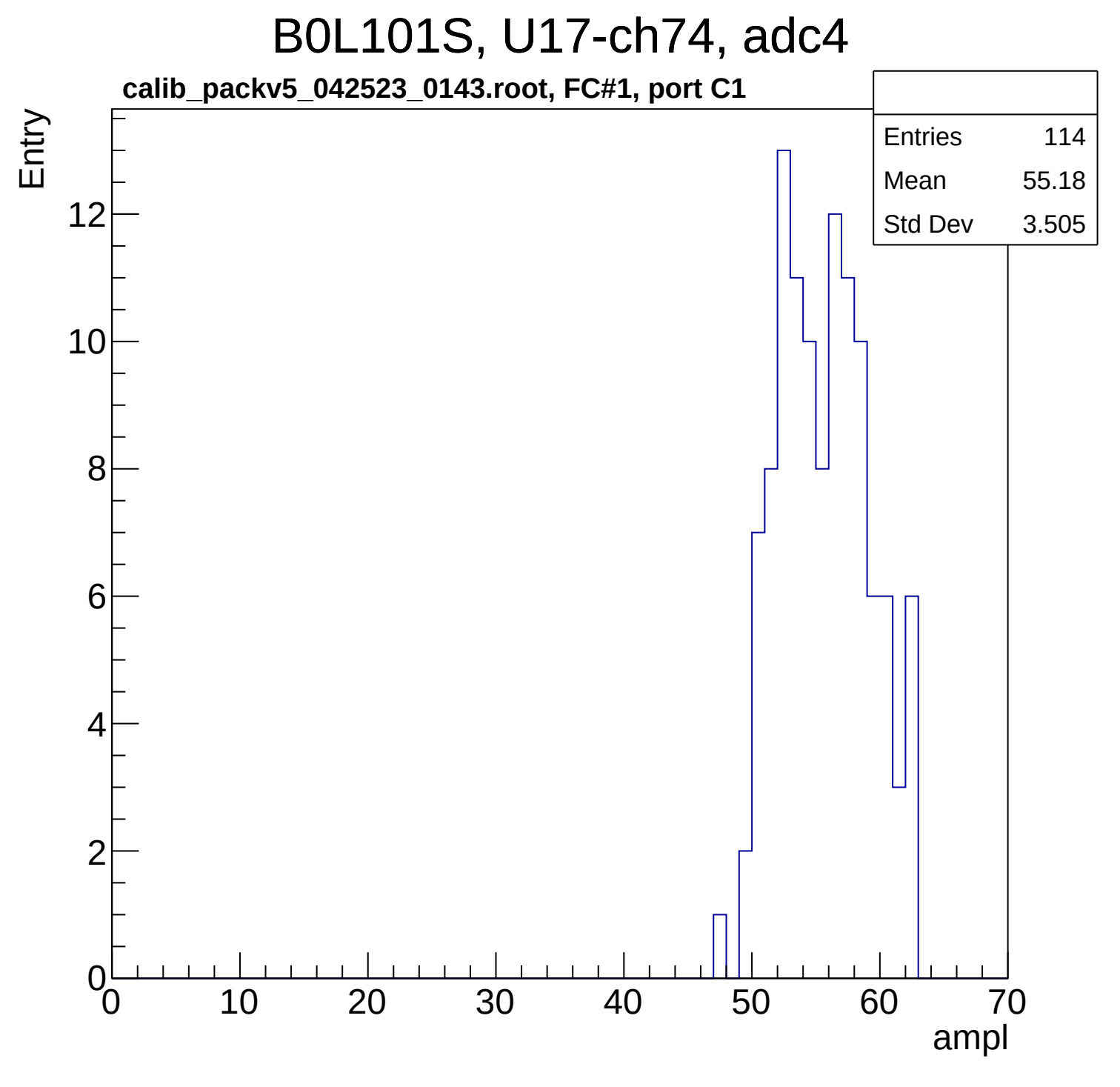

B0L101S, U17-ch73, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

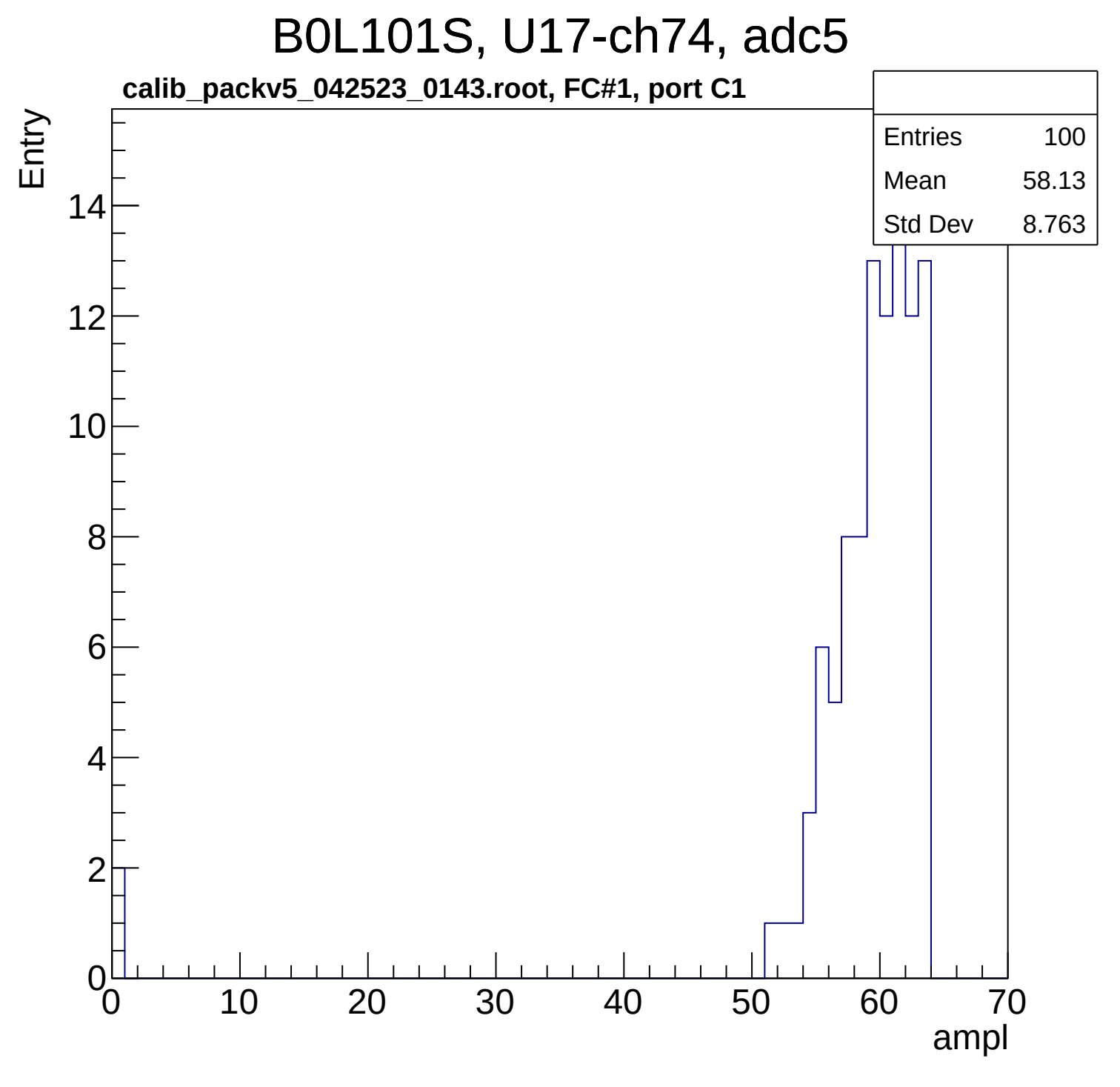

B0L101S, U17-ch74, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 29.12
Std Dev 5.136
Gaus mean : 29.7848
Gaus Width: 3.8099
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

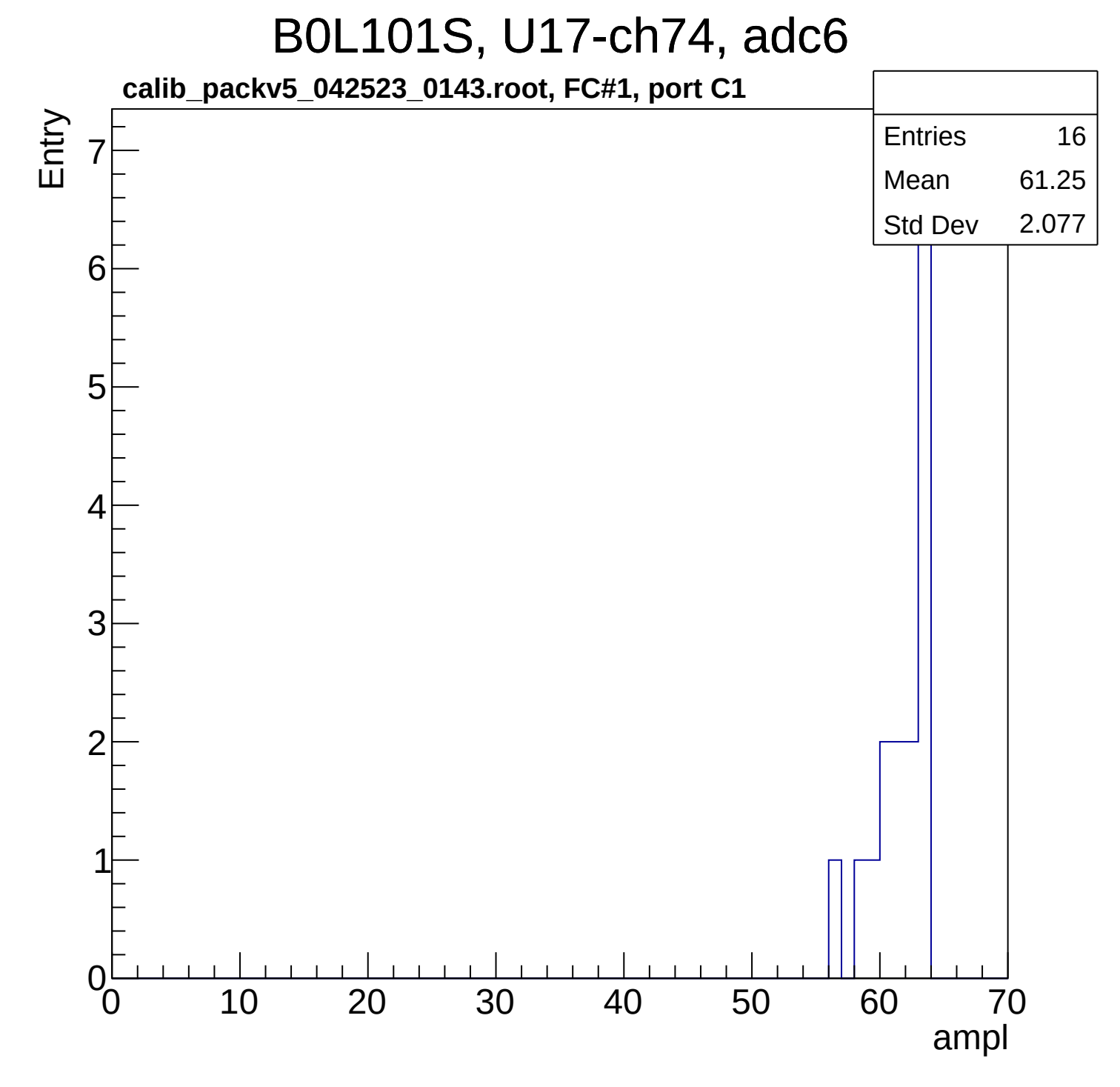

B0L101S, U17-ch74, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 35.72
Std Dev 3.922
Gaus mean : 36.1749
Gaus Width: 4.0809
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

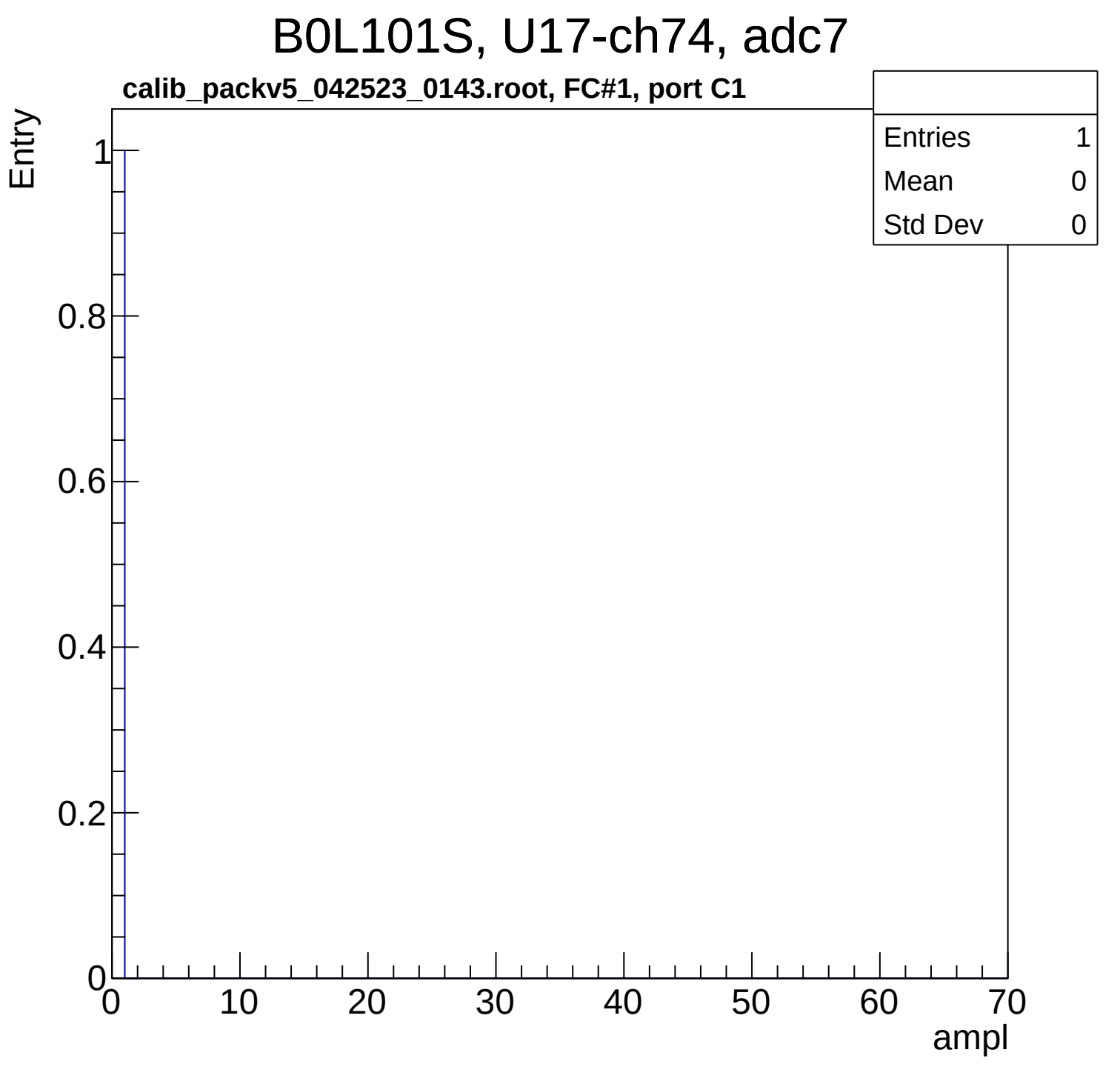

B0L101S, U17-ch74, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 42.8
Std Dev 3.763
Gaus mean : 43.5451
Gaus Width: 3.9076
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

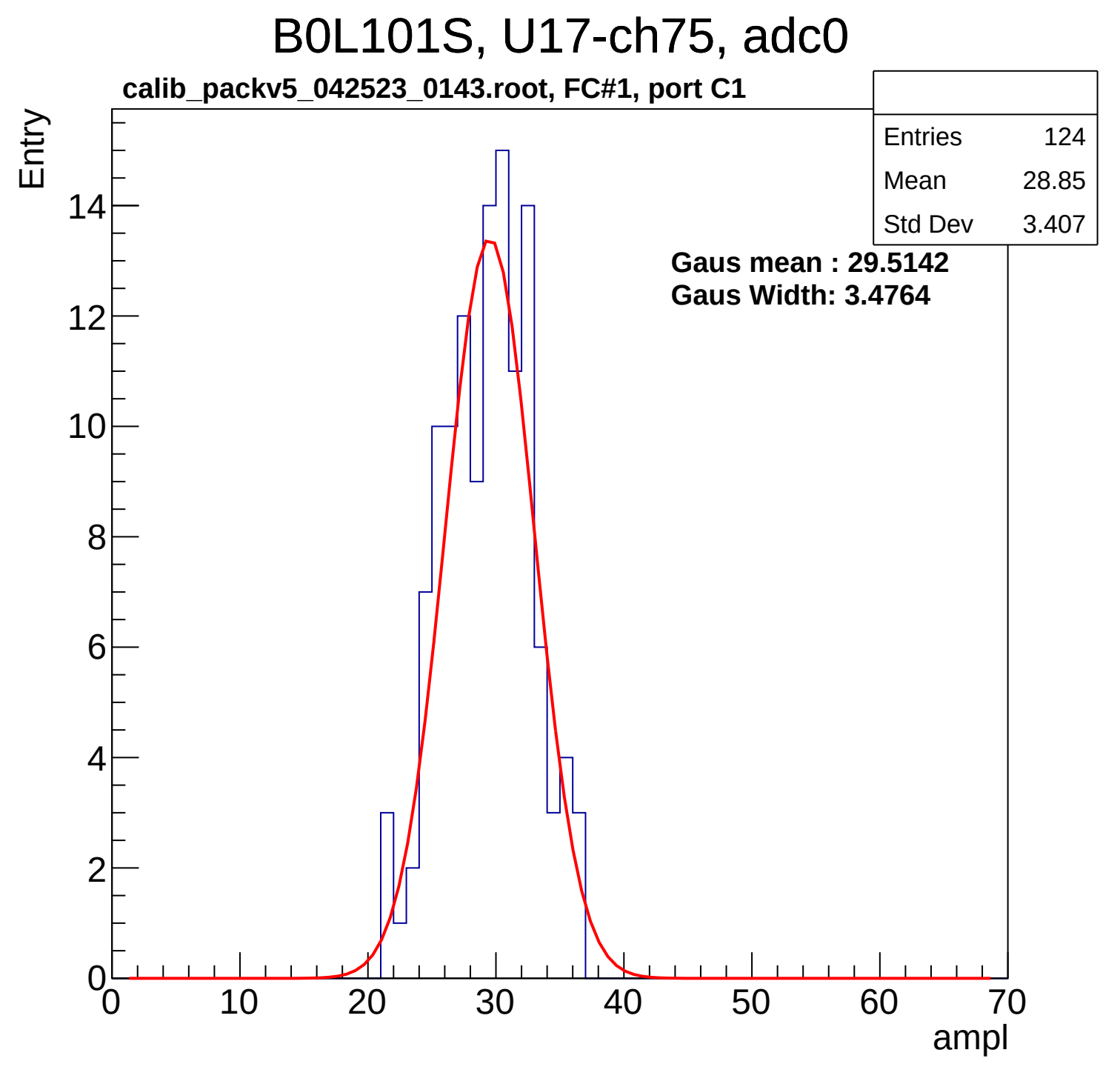

B0L101S, U17-ch74, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 49.15
Std Dev 3.742
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

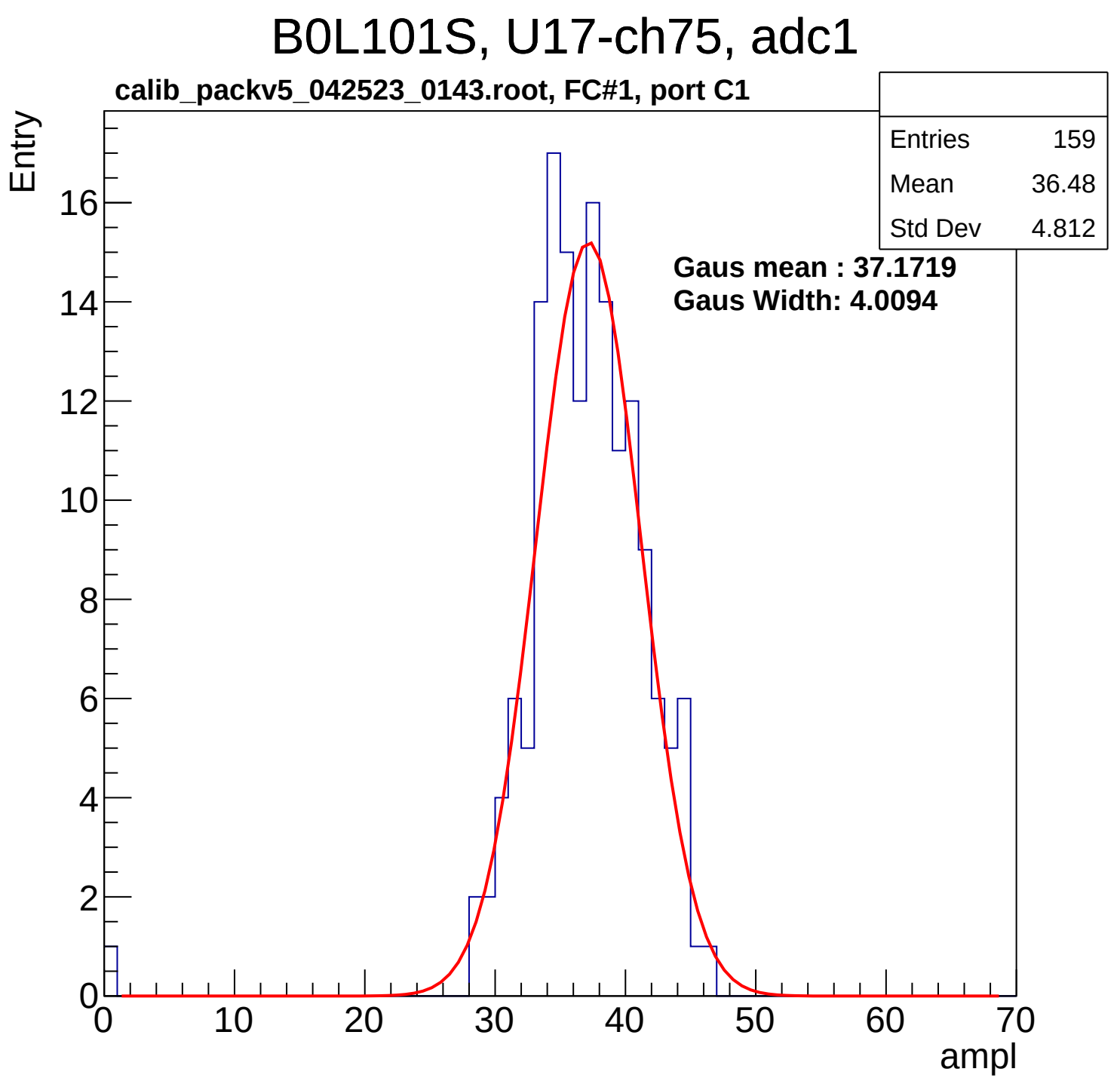

B0L101S, U17-ch74, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 55.18
Std Dev 3.505
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

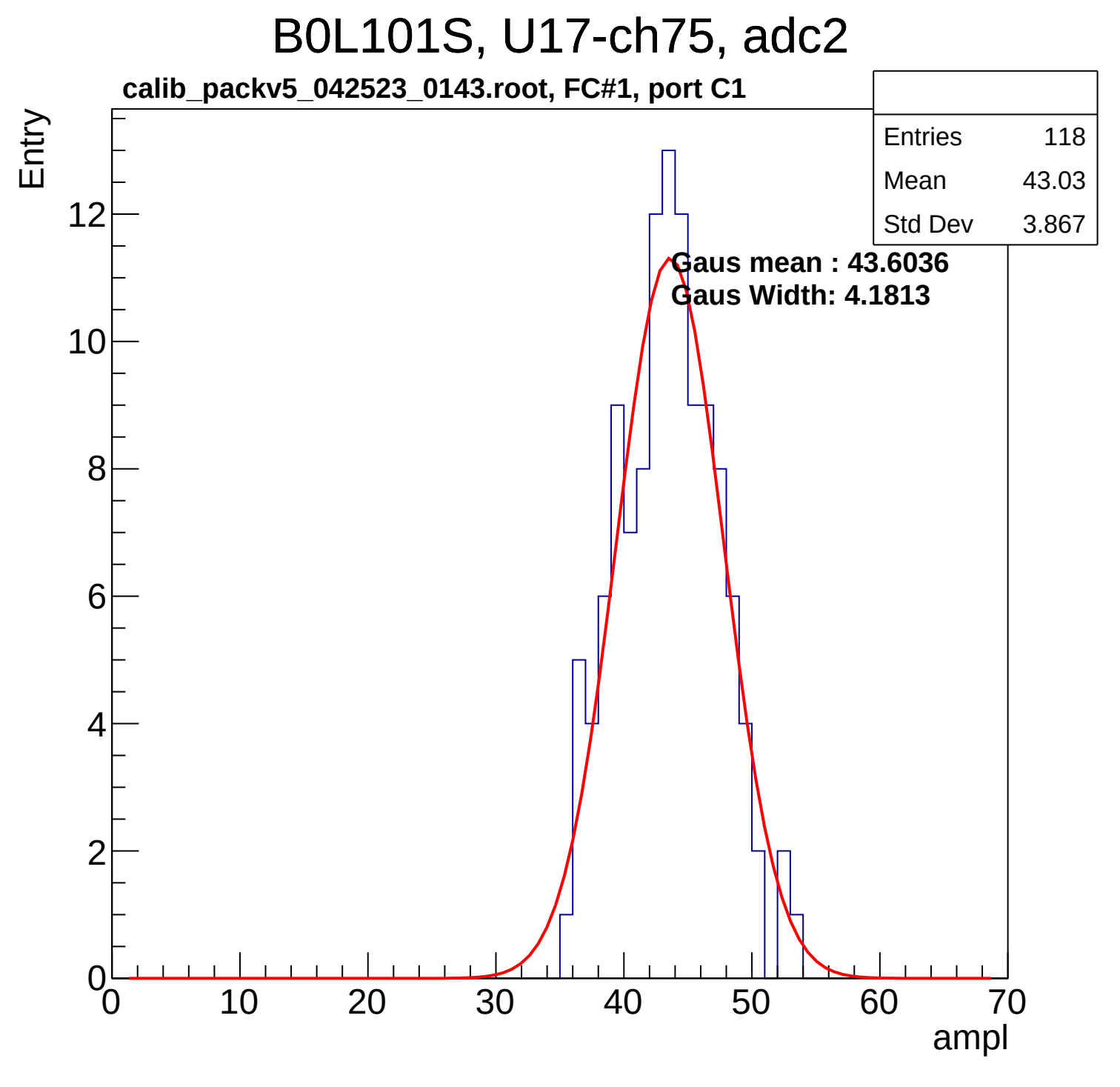

B0L101S, U17-ch74, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 58.13
Std Dev 8.763
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

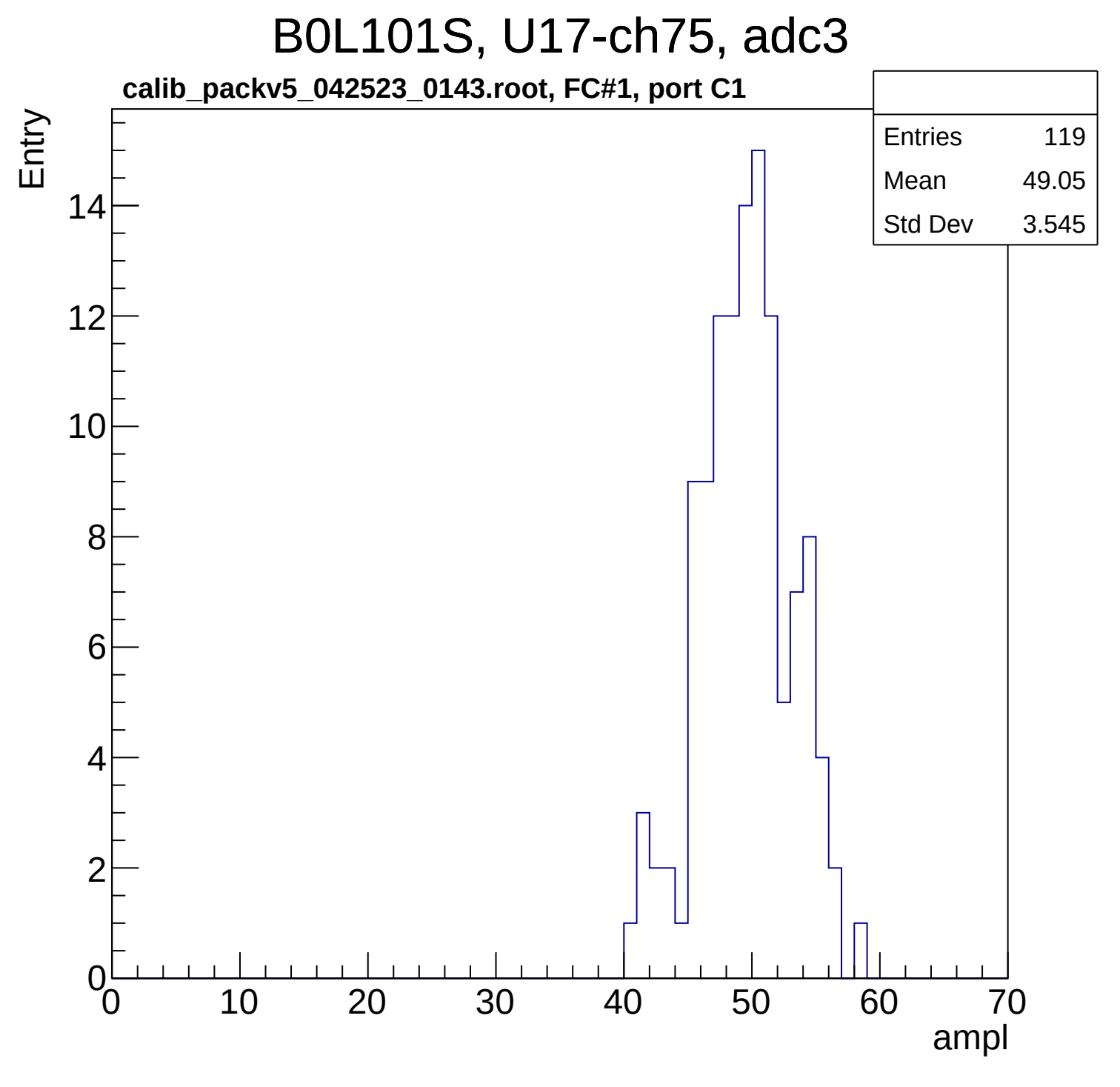

B0L101S, U17-ch74, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 61.25
Std Dev 2.077
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

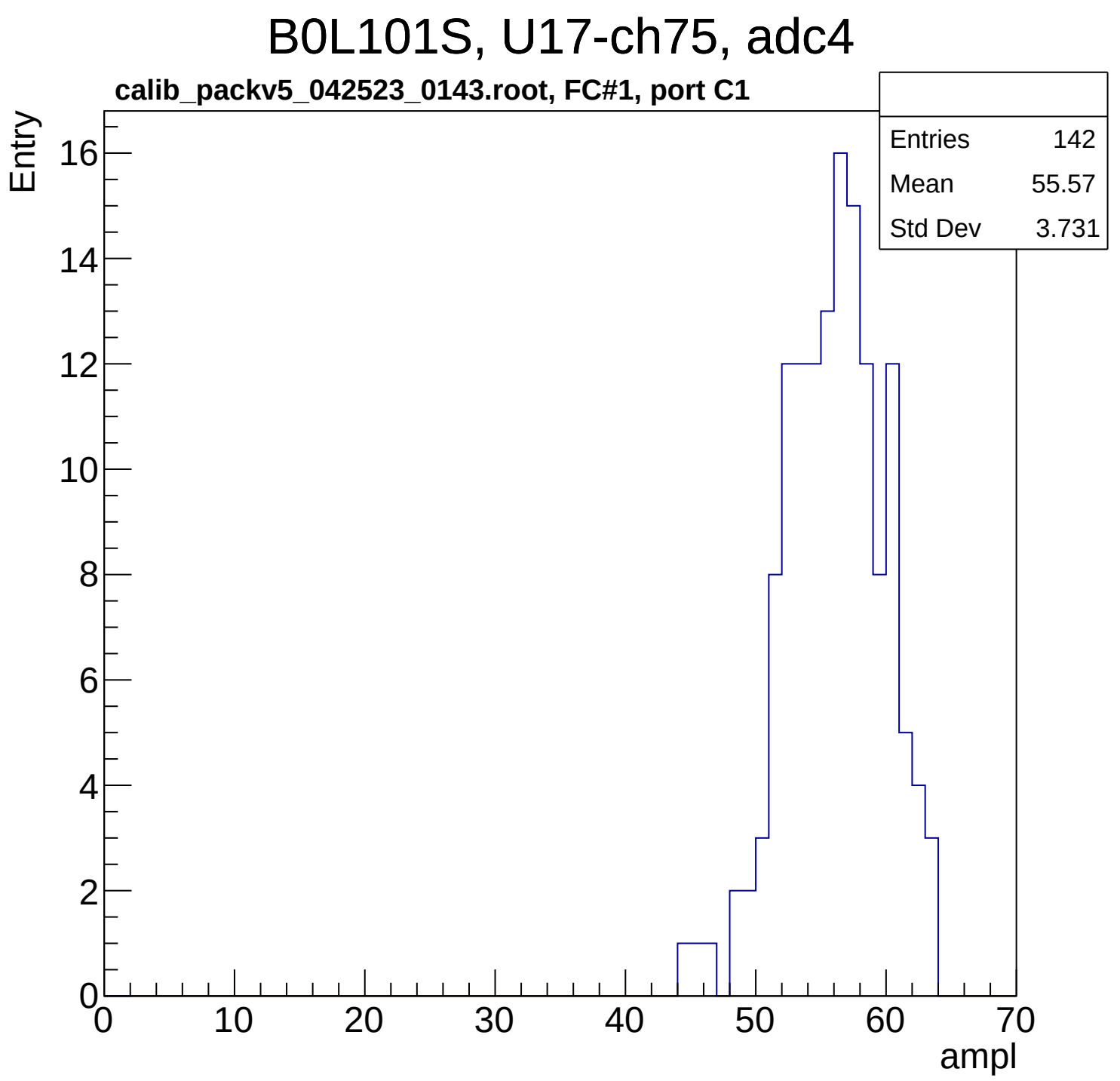

B0L101S, U17-ch74, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

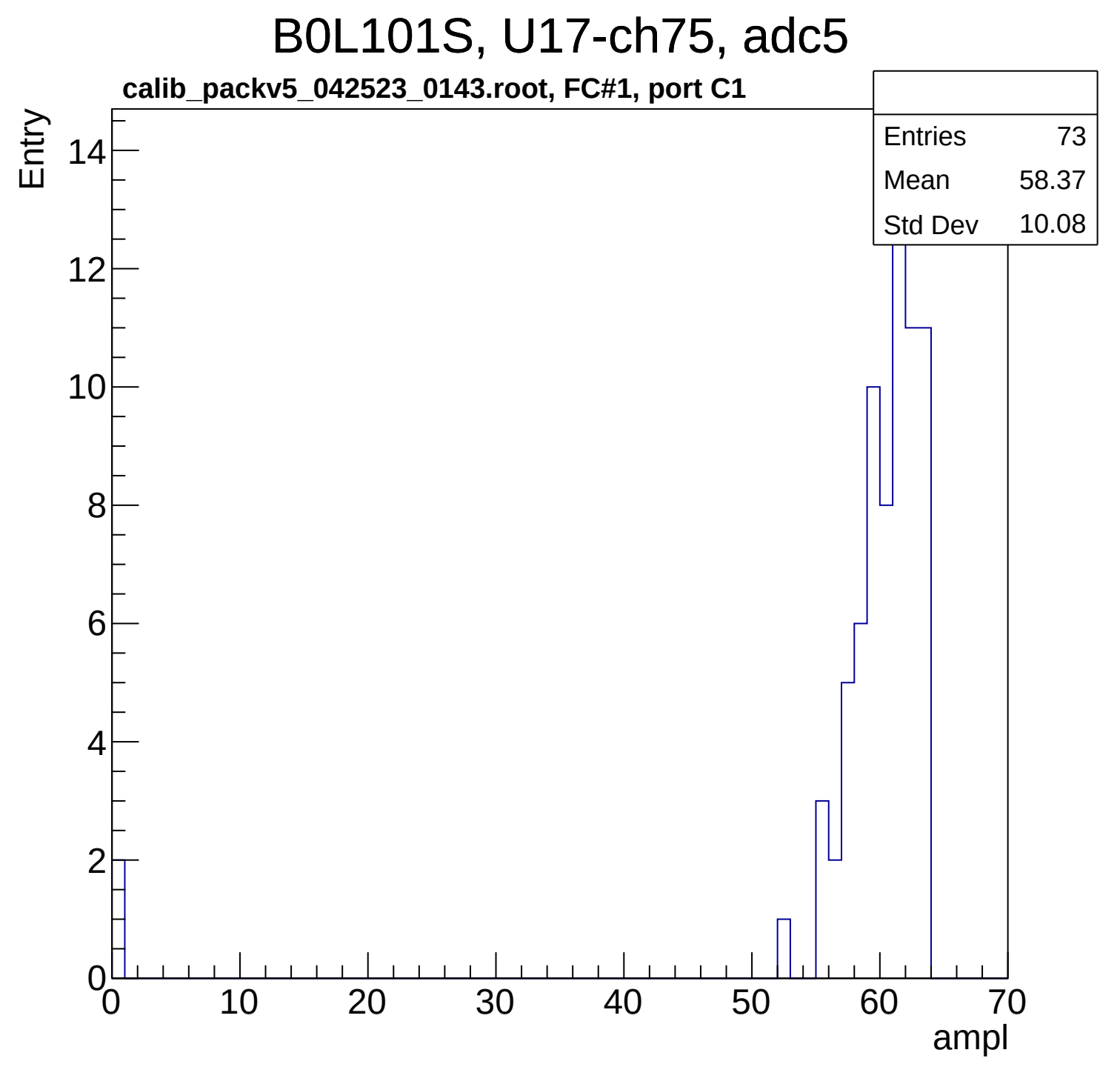

B0L101S, U17-ch75, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 28.85
Std Dev 3.407
Gaus mean : 29.5142
Gaus Width: 3.4764
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

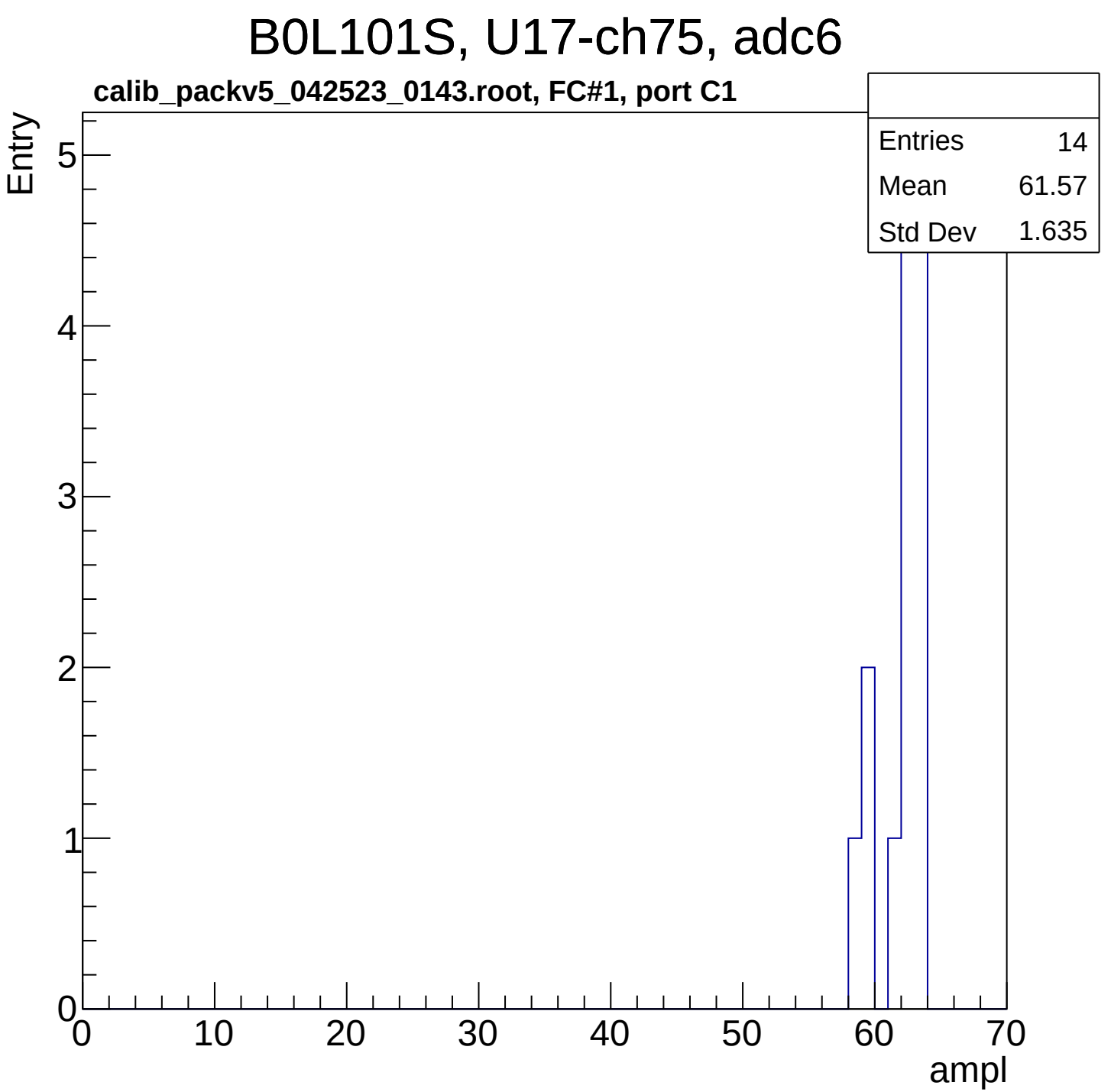

B0L101S, U17-ch75, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 159
Mean 36.48
Std Dev 4.812
Gaus mean : 37.1719
Gaus Width: 4.0094
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

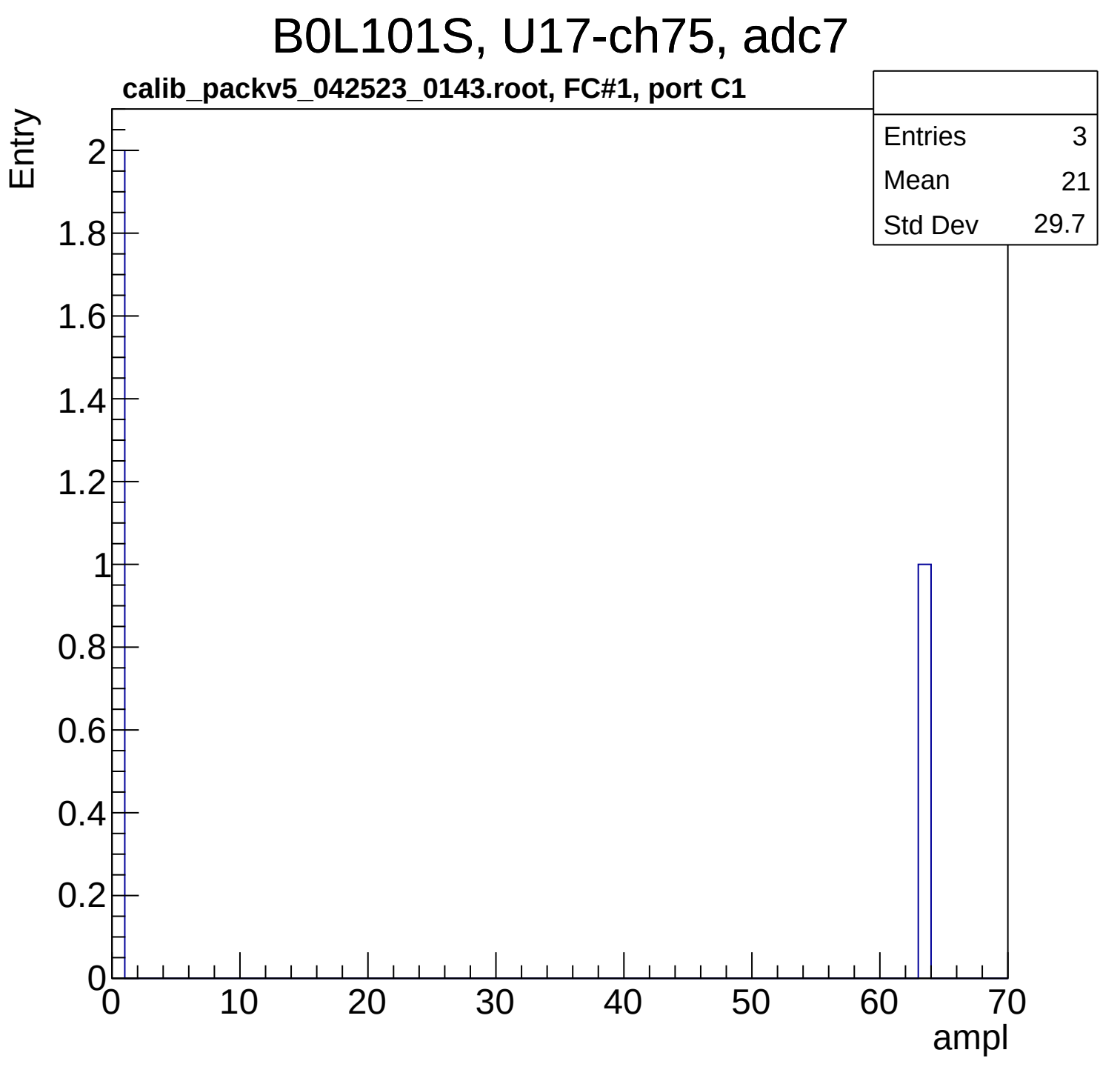

B0L101S, U17-ch75, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 43.03
Std Dev 3.867
Gaus mean : 43.6036
Gaus Width: 4.1813
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

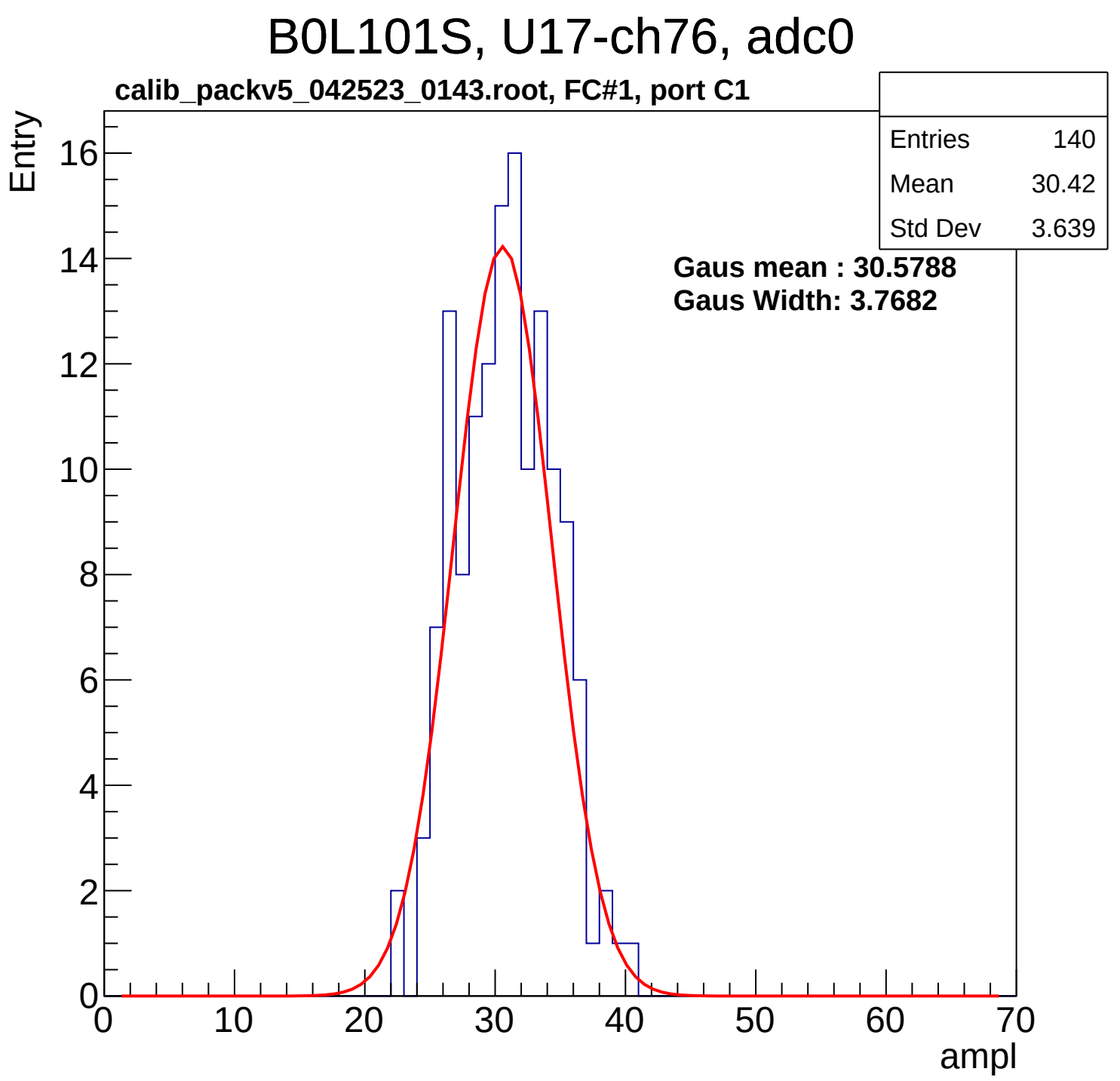

B0L101S, U17-ch75, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 49.05
Std Dev 3.545
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

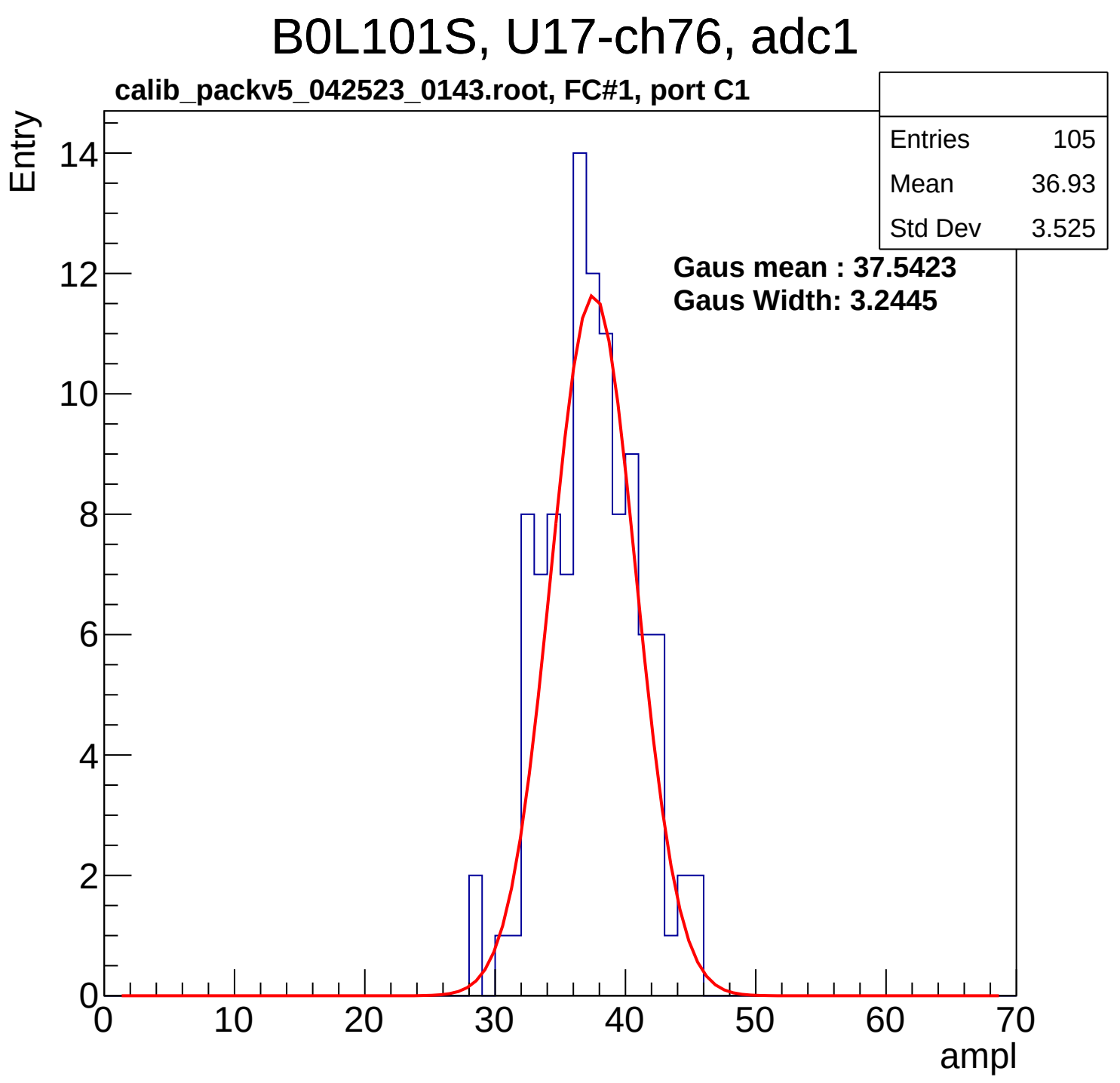

B0L101S, U17-ch75, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 55.57
Std Dev 3.731
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

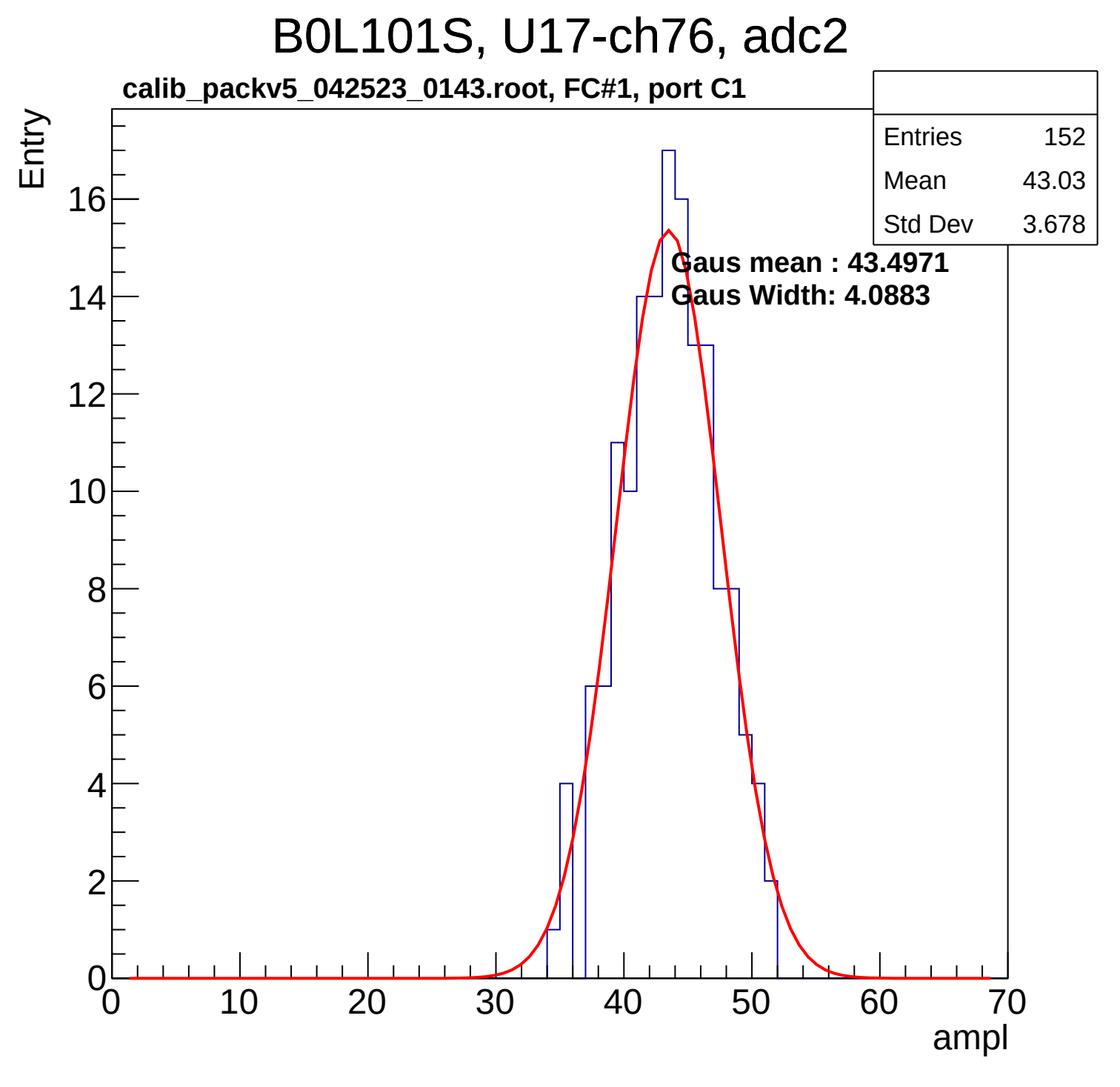

B0L101S, U17-ch75, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 73
Mean 58.37
Std Dev 10.08
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

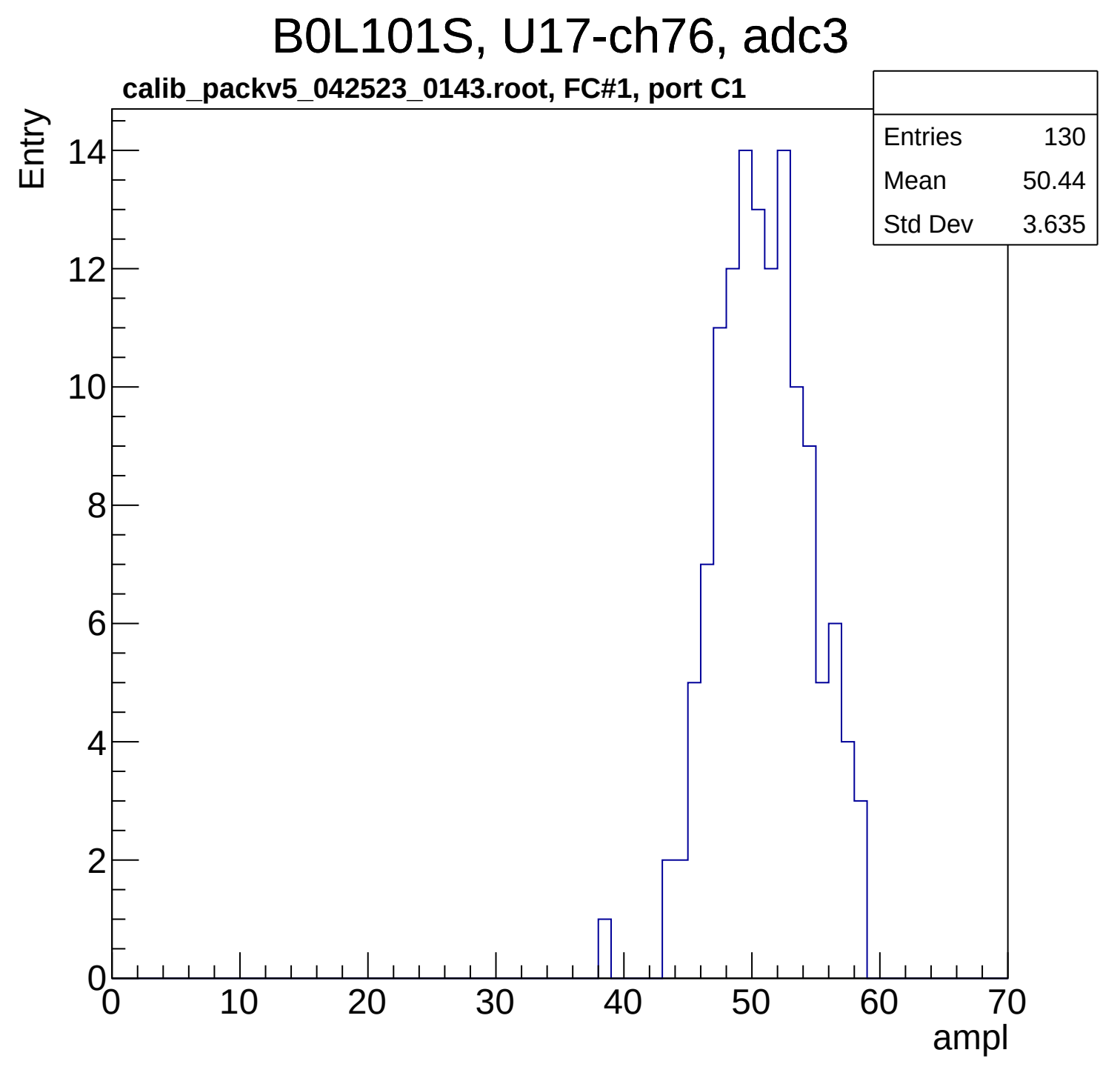

B0L101S, U17-ch75, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 61.57
Std Dev 1.635
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

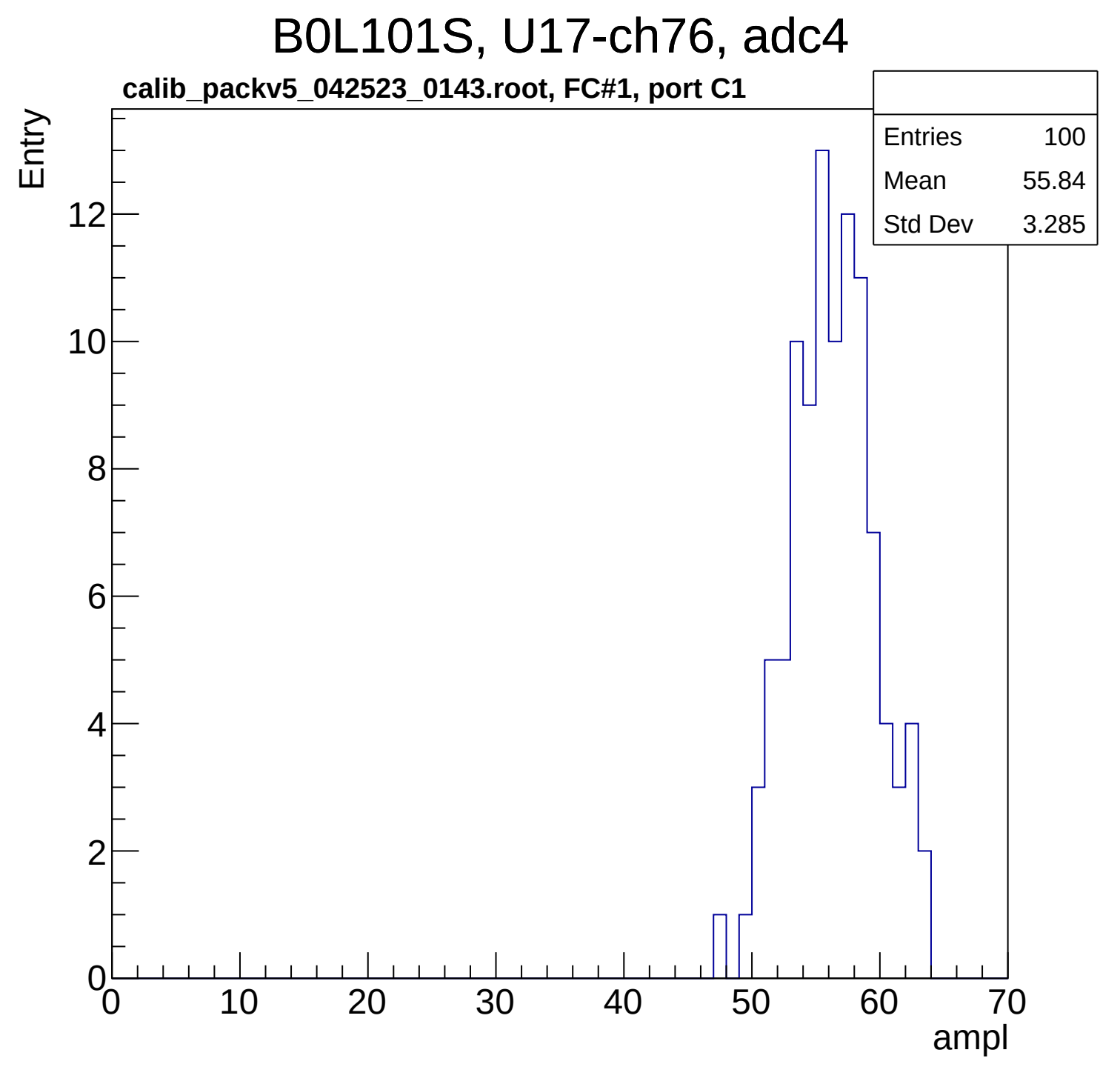

B0L101S, U17-ch75, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

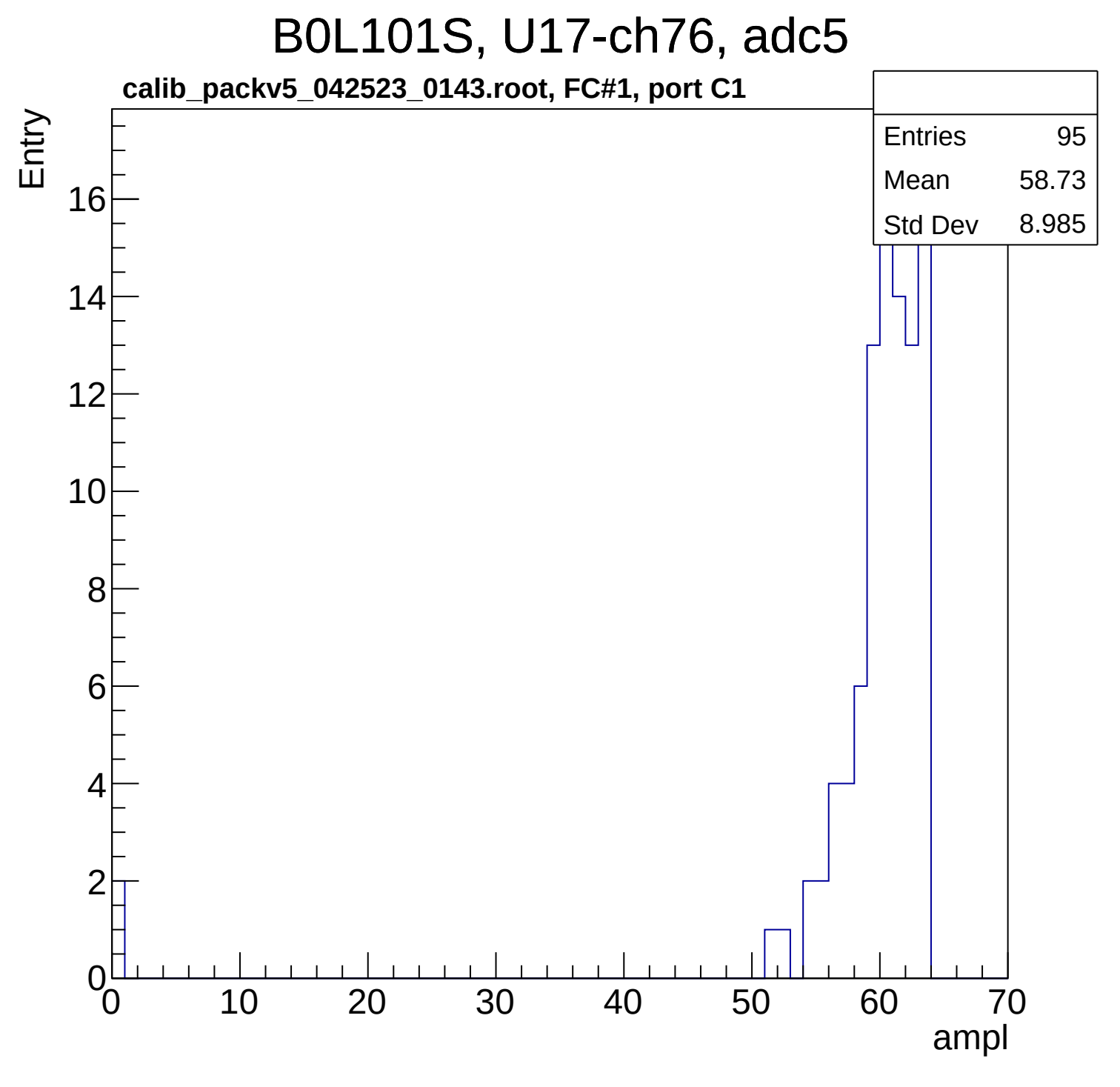

B0L101S, U17-ch76, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 30.42
Std Dev 3.639
Gaus mean : 30.5788
Gaus Width: 3.7682
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

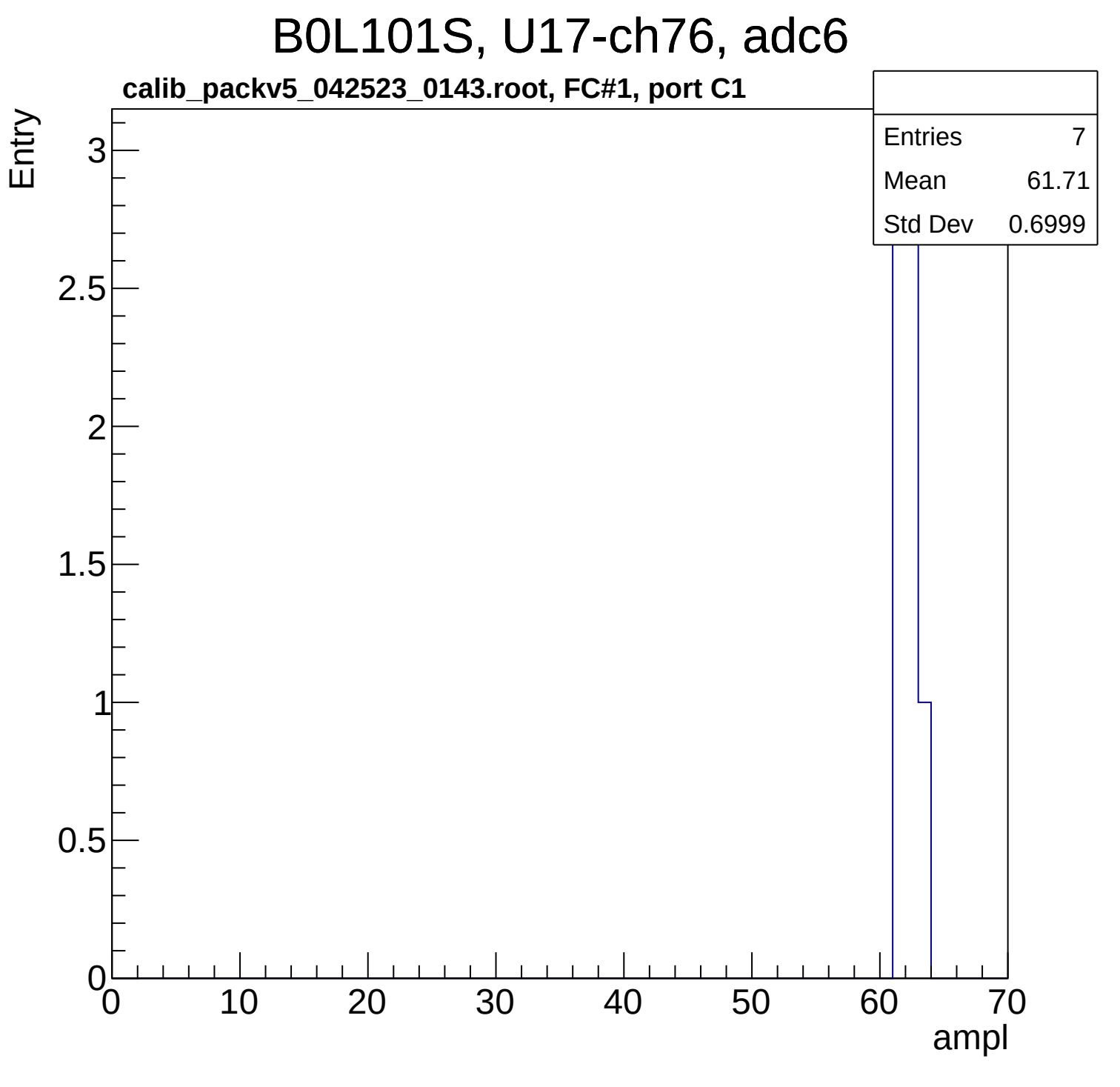

B0L101S, U17-ch76, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 36.93
Std Dev 3.525
Gaus mean : 37.5423
Gaus Width: 3.2445
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

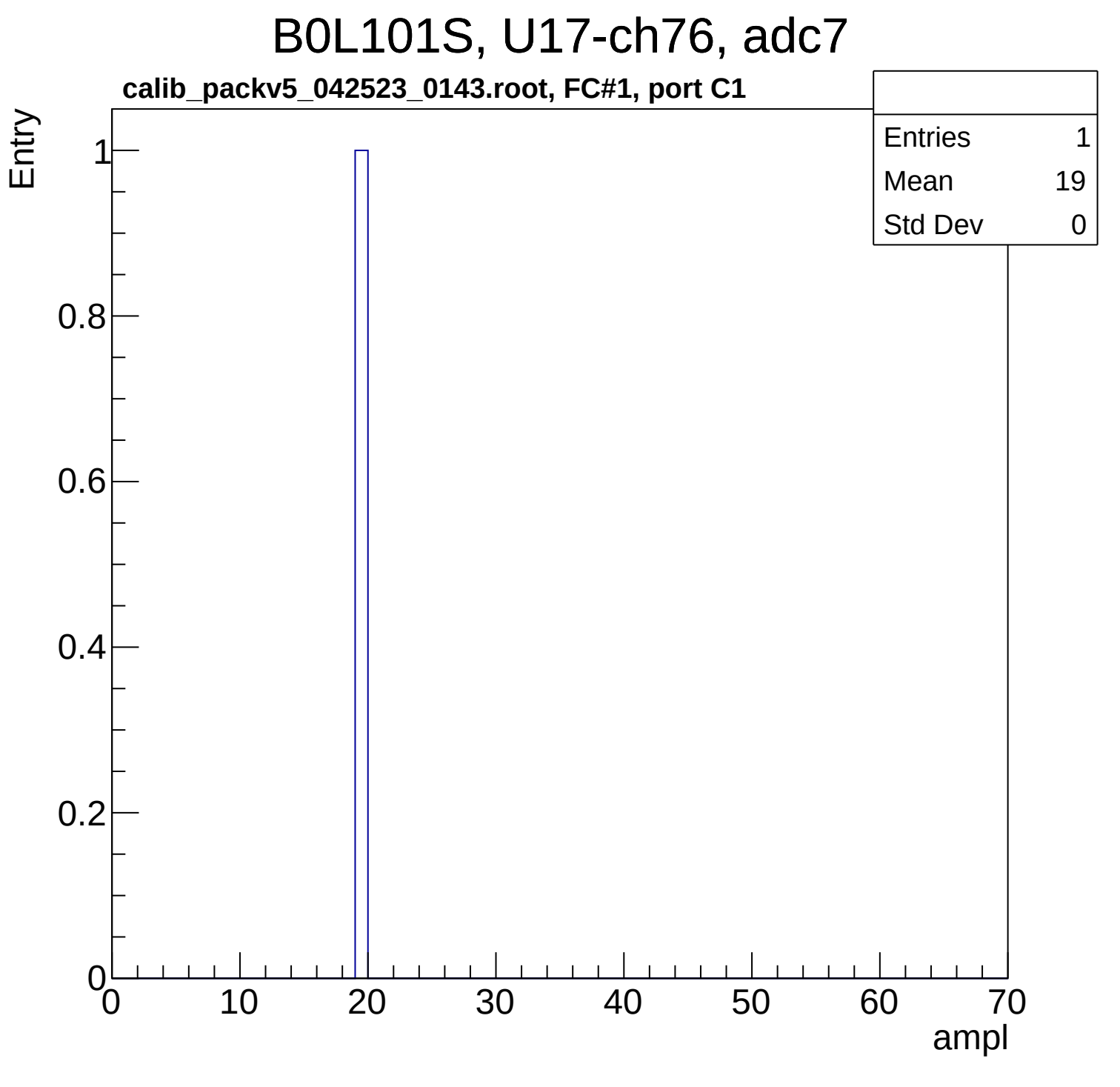

B0L101S, U17-ch76, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 152
Mean 43.03
Std Dev 3.678
Gaus mean : 43.4971
Gaus Width: 4.0883
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

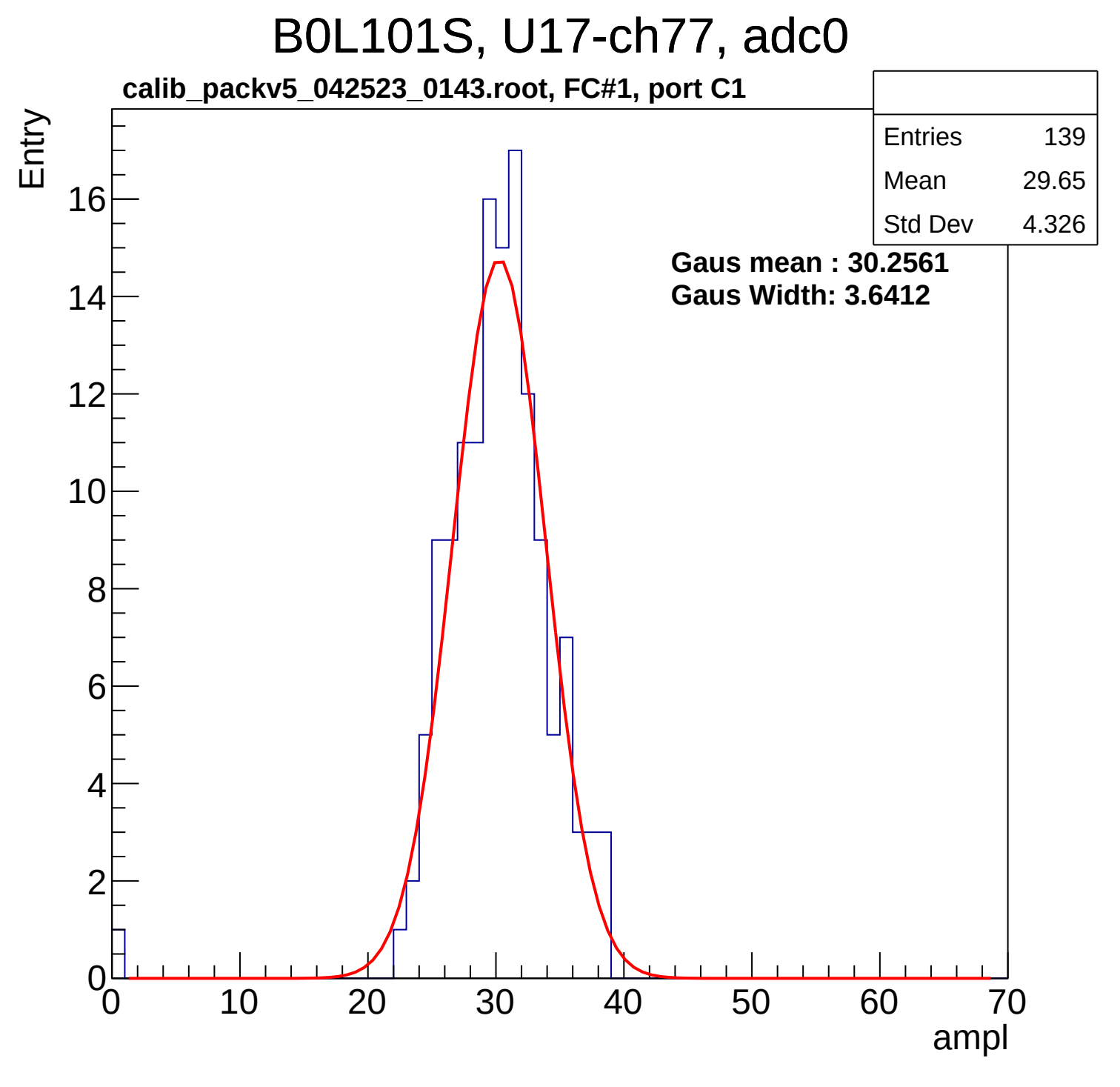

B0L101S, U17-ch76, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 50.44
Std Dev 3.635
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

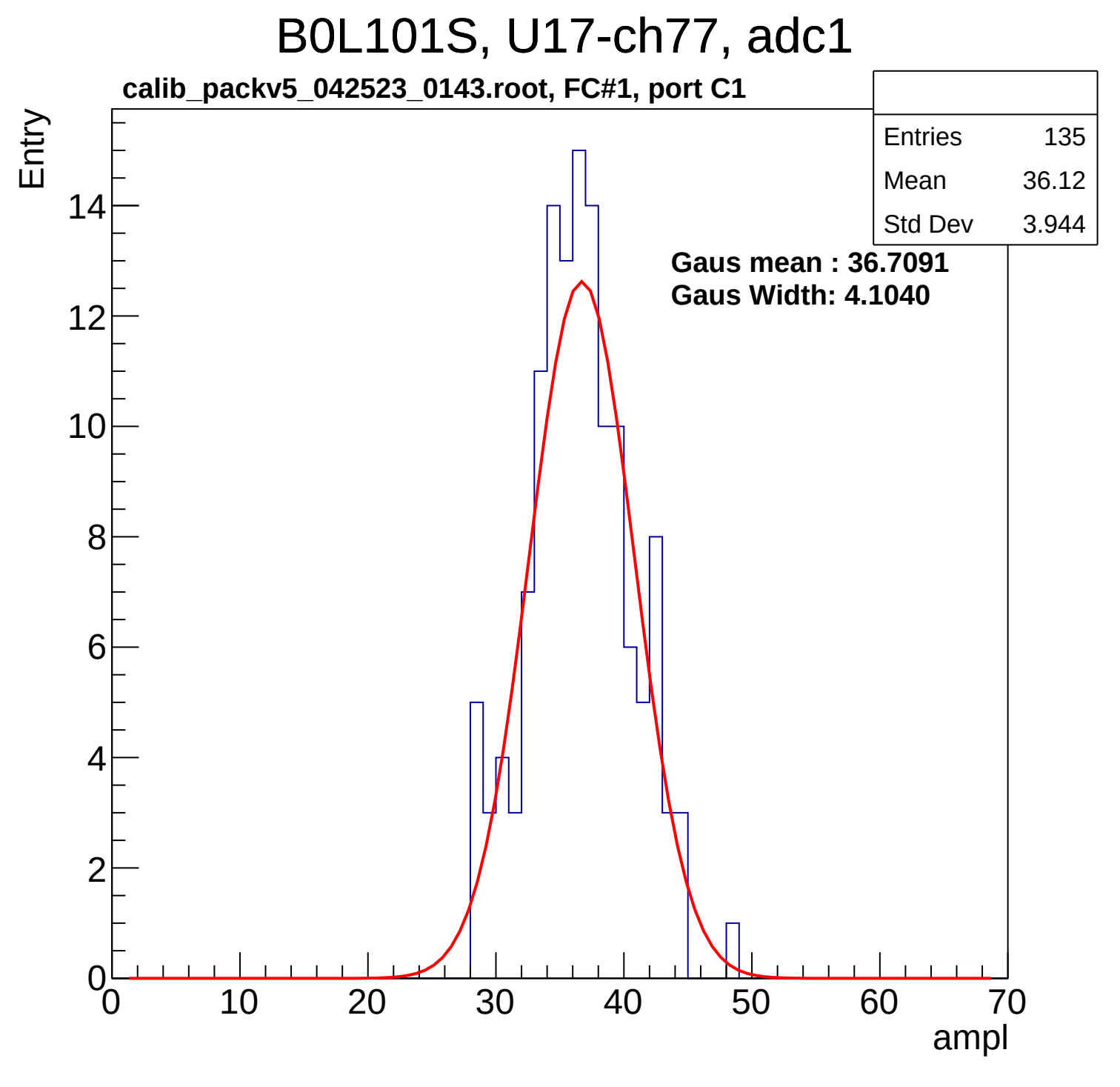

B0L101S, U17-ch76, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 55.84
Std Dev 3.285
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

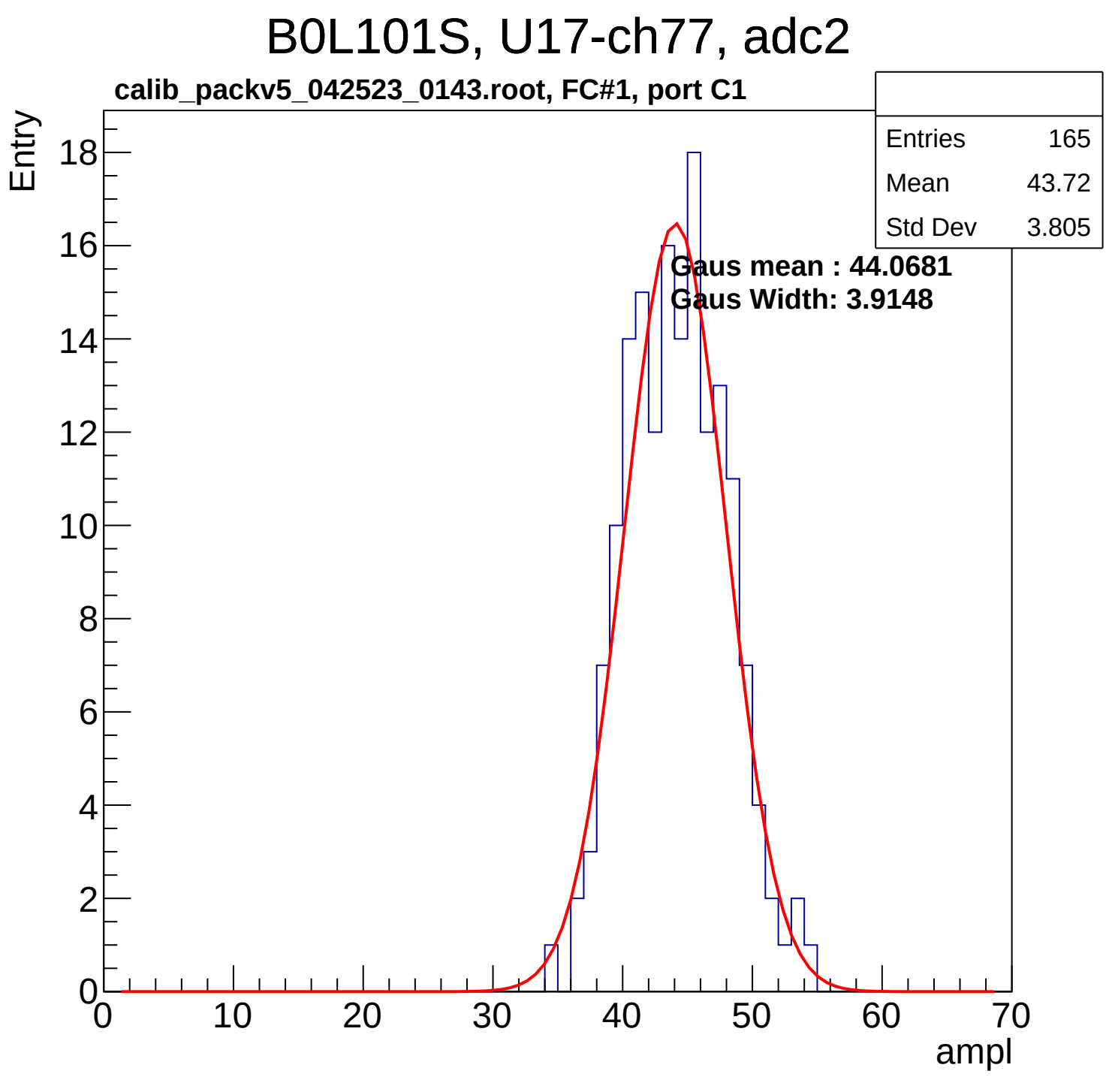

B0L101S, U17-ch76, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 95
Mean 58.73
Std Dev 8.985
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

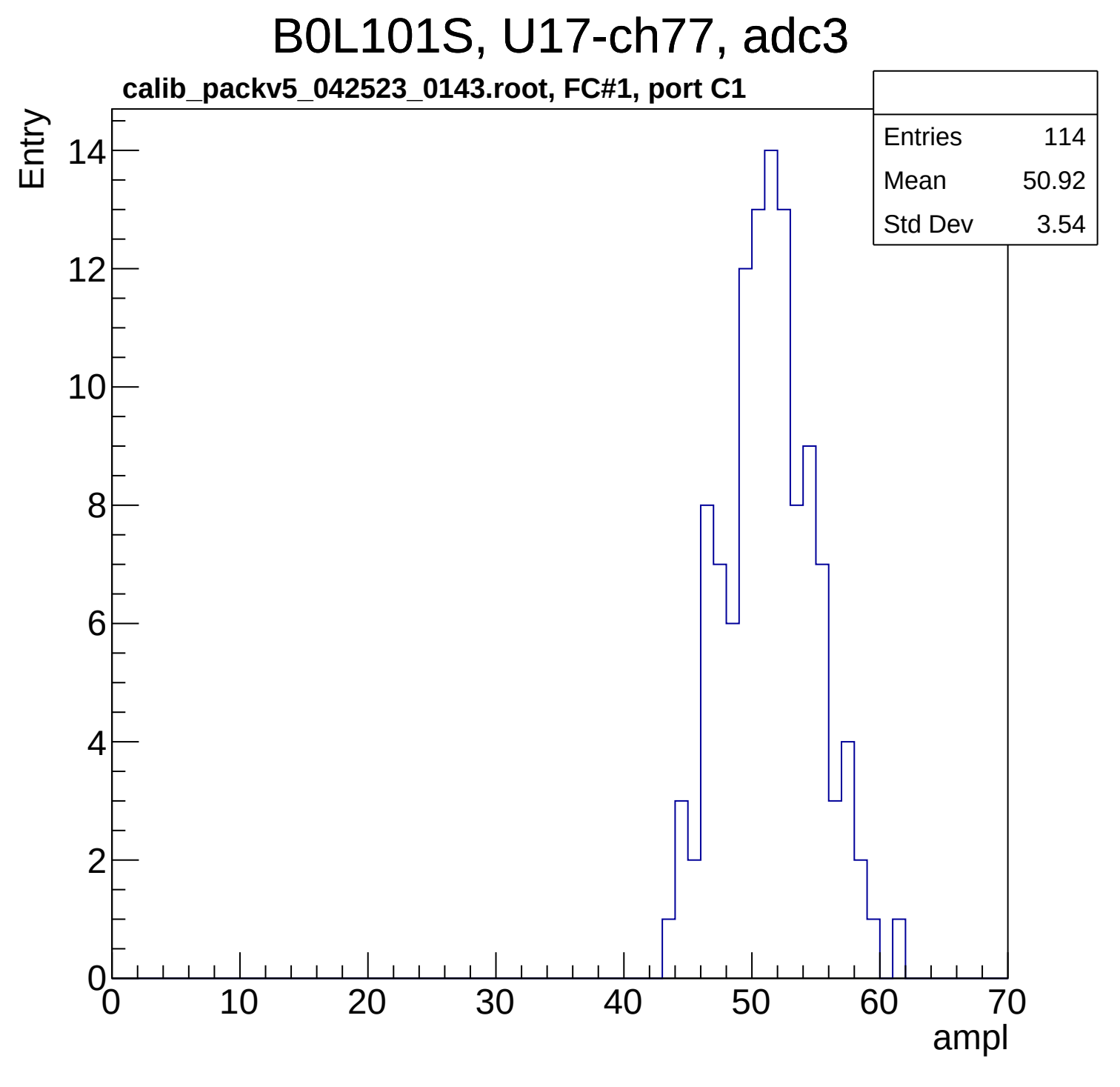

B0L101S, U17-ch76, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 7
Mean 61.71
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

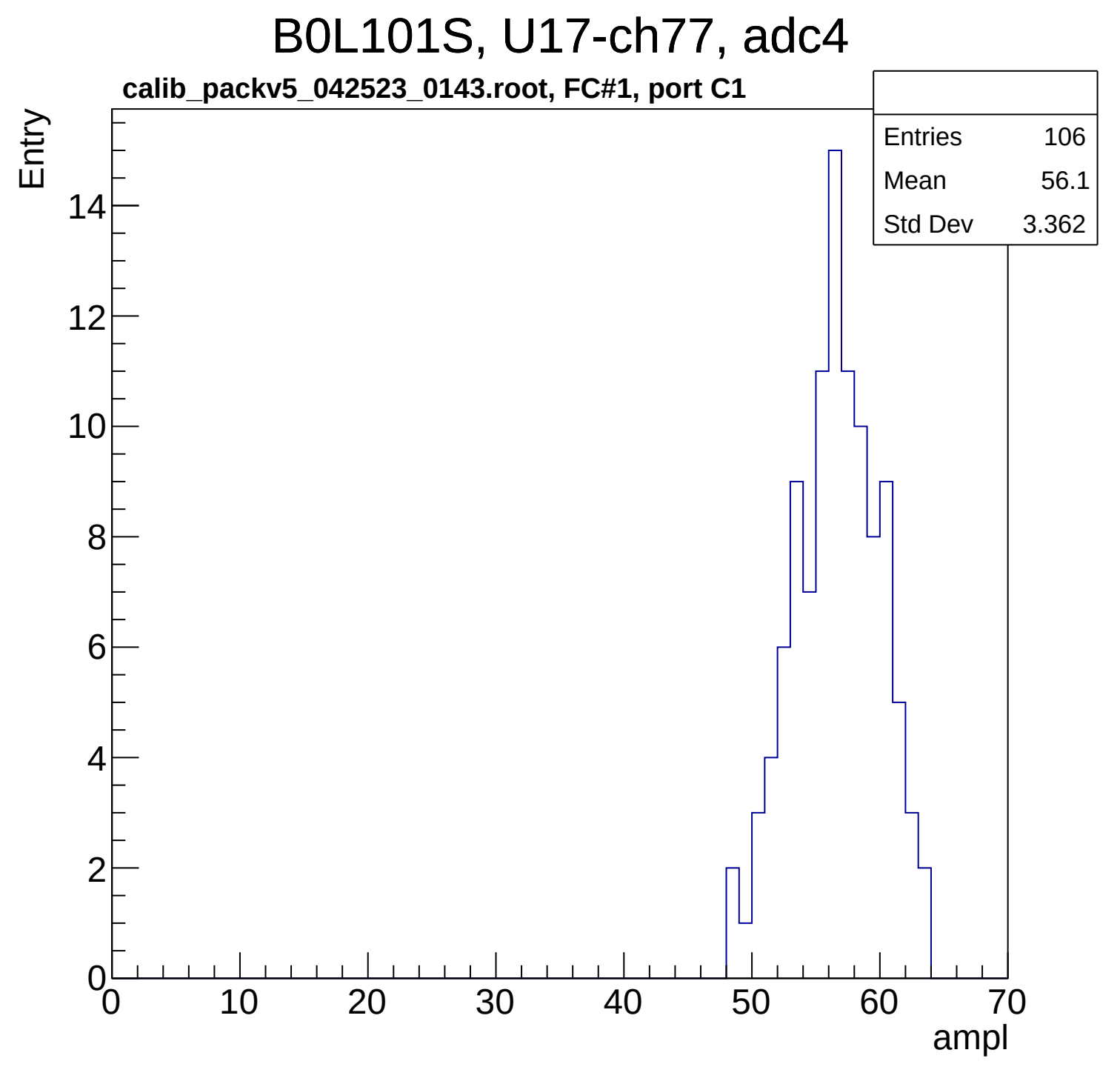

B0L101S, U17-ch76, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

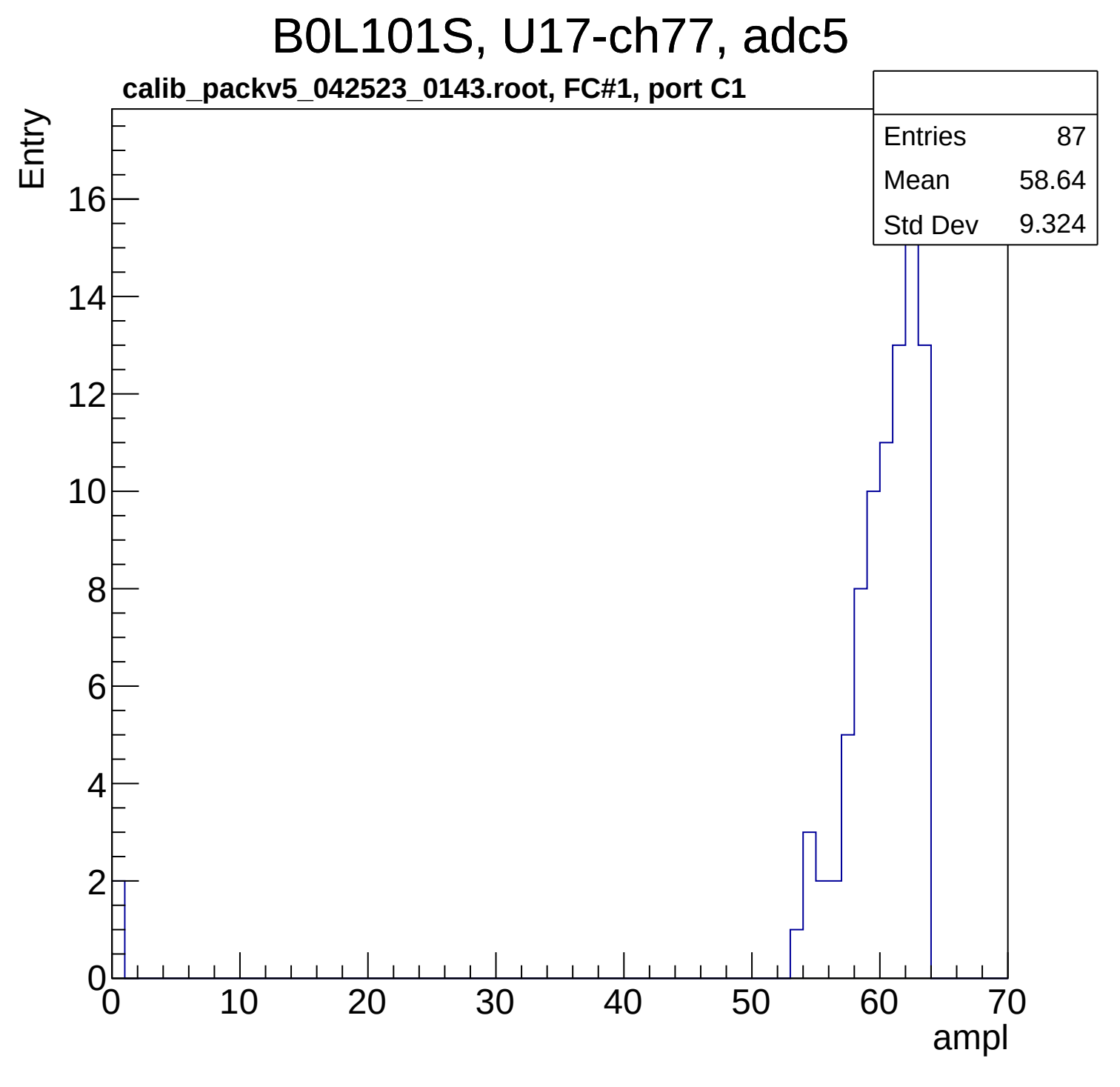

B0L101S, U17-ch77, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 29.65
Std Dev 4.326
Gaus mean : 30.2561
Gaus Width: 3.6412
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

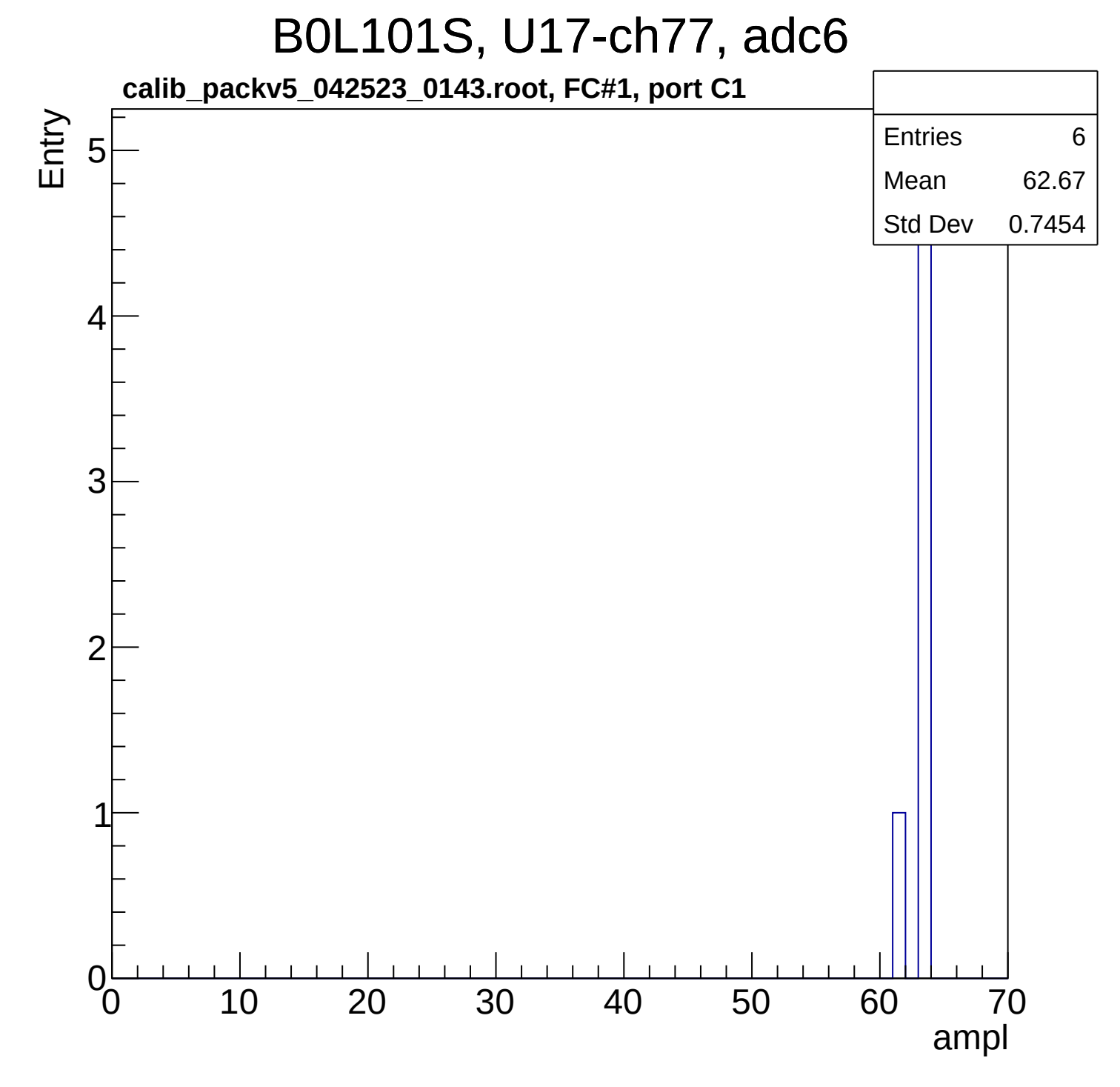

B0L101S, U17-ch77, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 135
Mean 36.12
Std Dev 3.944
Gaus mean : 36.7091
Gaus Width: 4.1040
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

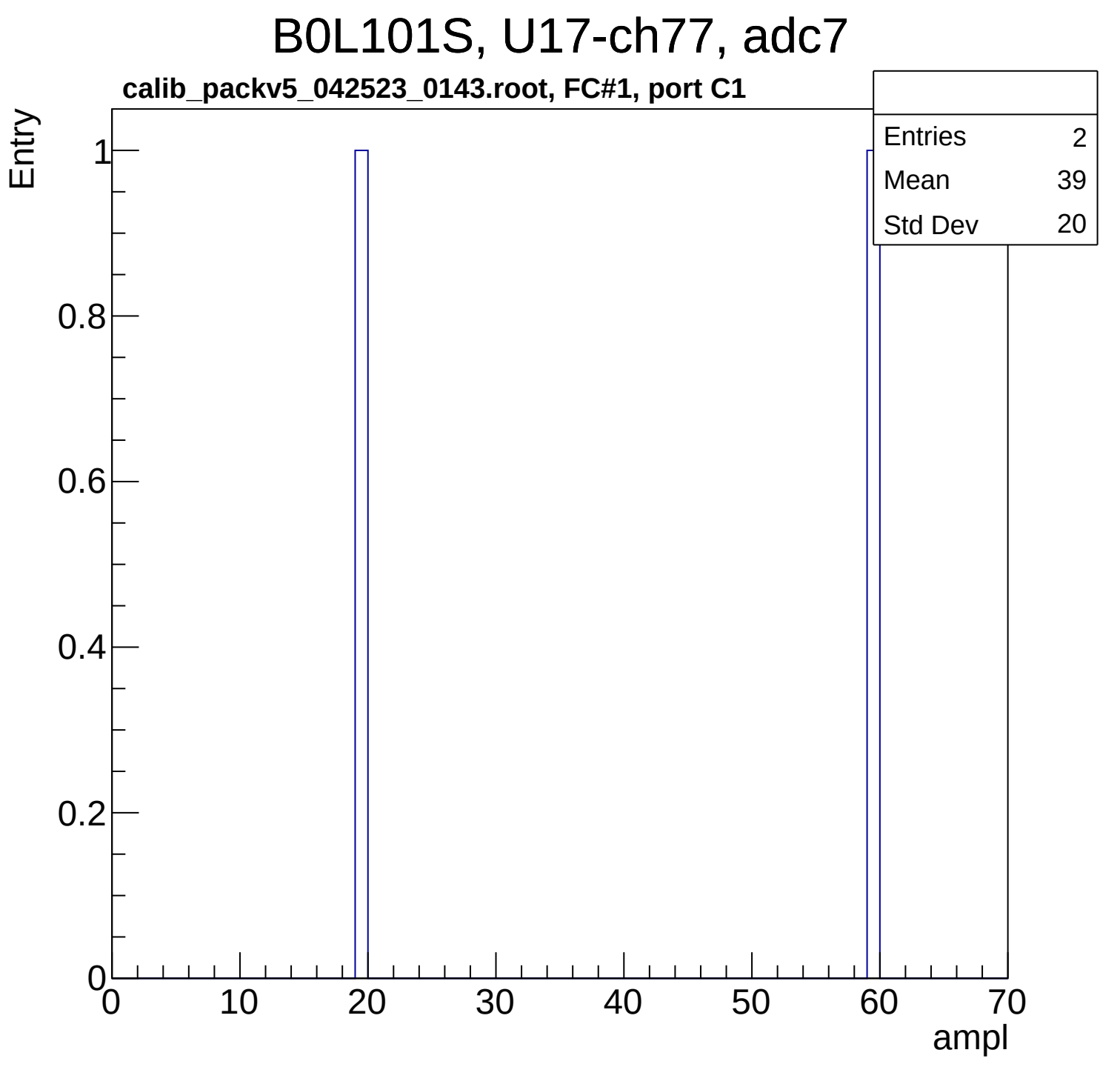

B0L101S, U17-ch77, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 165
Mean 43.72
Std Dev 3.805
Gaus mean : 44.0681
Gaus Width: 3.9148
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

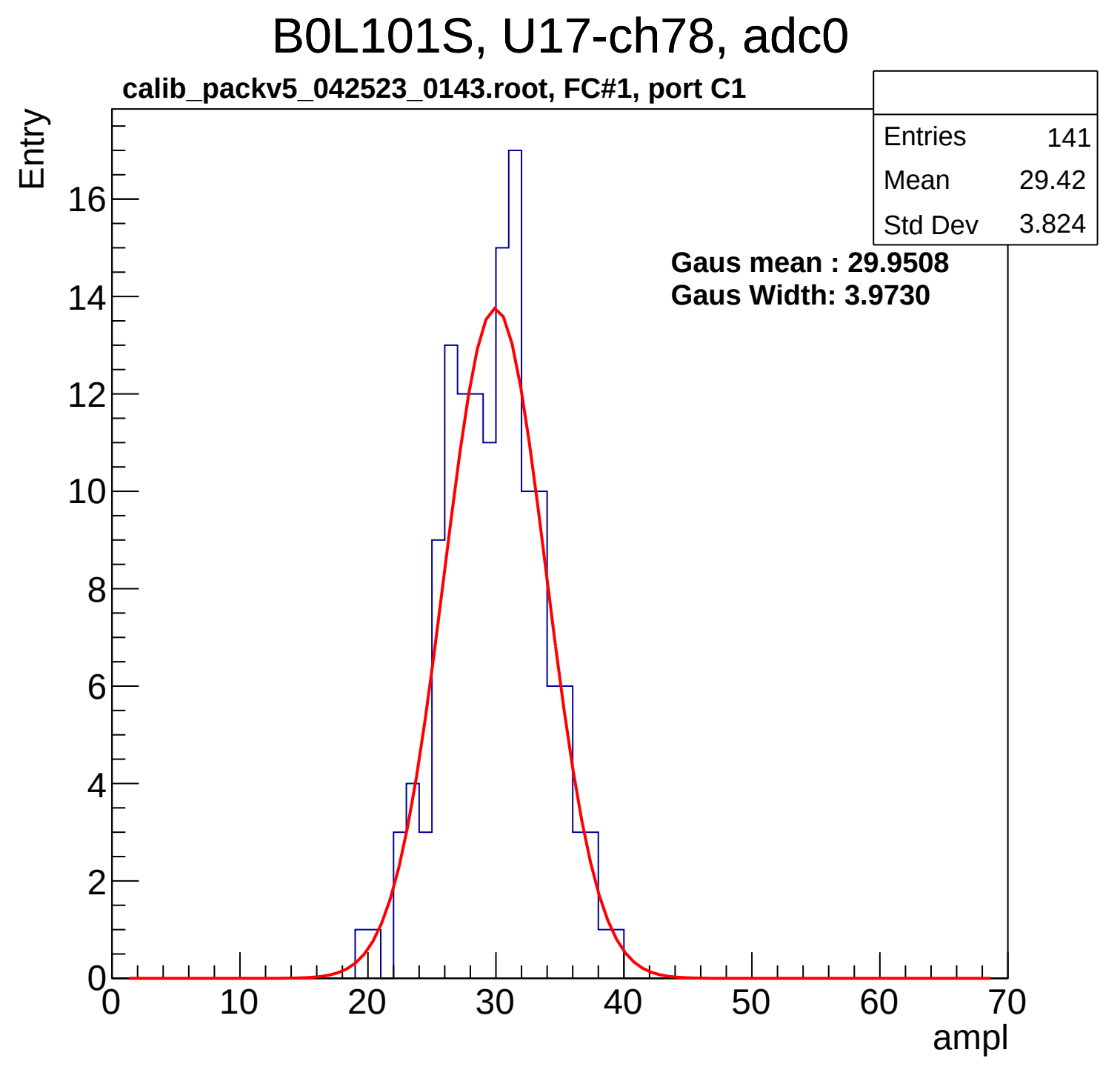

B0L101S, U17-ch77, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 50.92
Std Dev 3.54
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

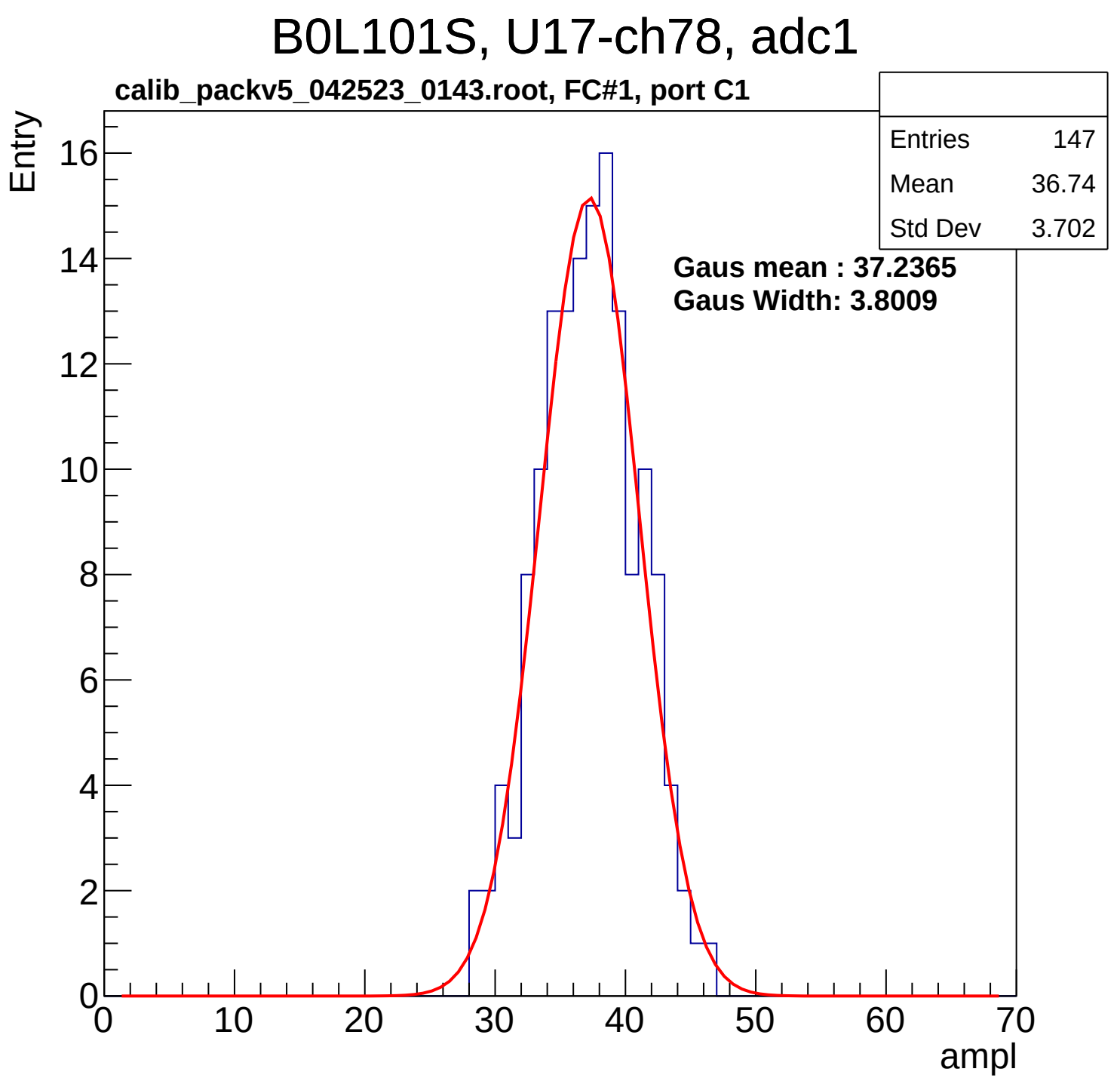

B0L101S, U17-ch77, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 106
Mean 56.1
Std Dev 3.362
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

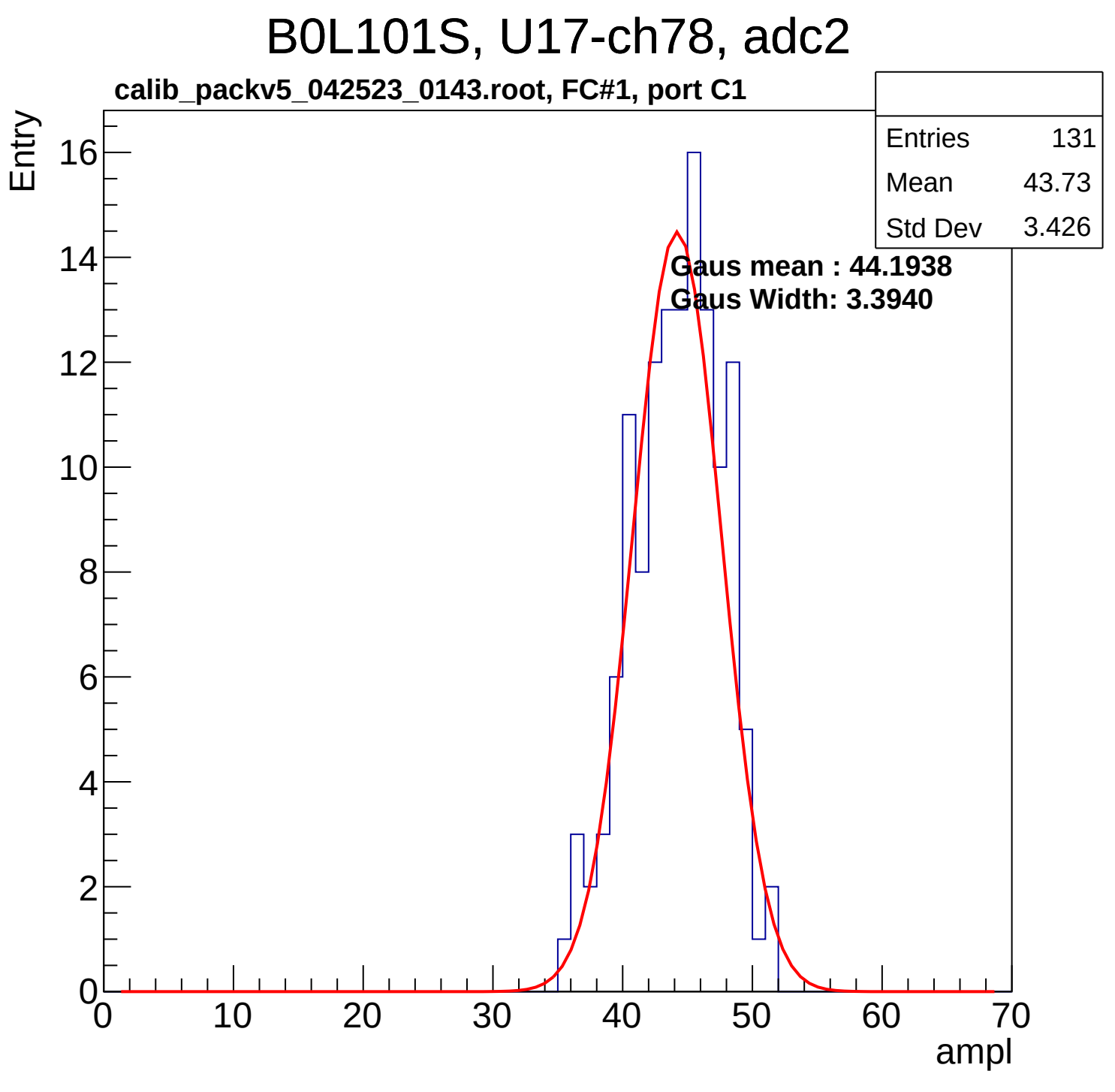

B0L101S, U17-ch77, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 58.64
Std Dev 9.324
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

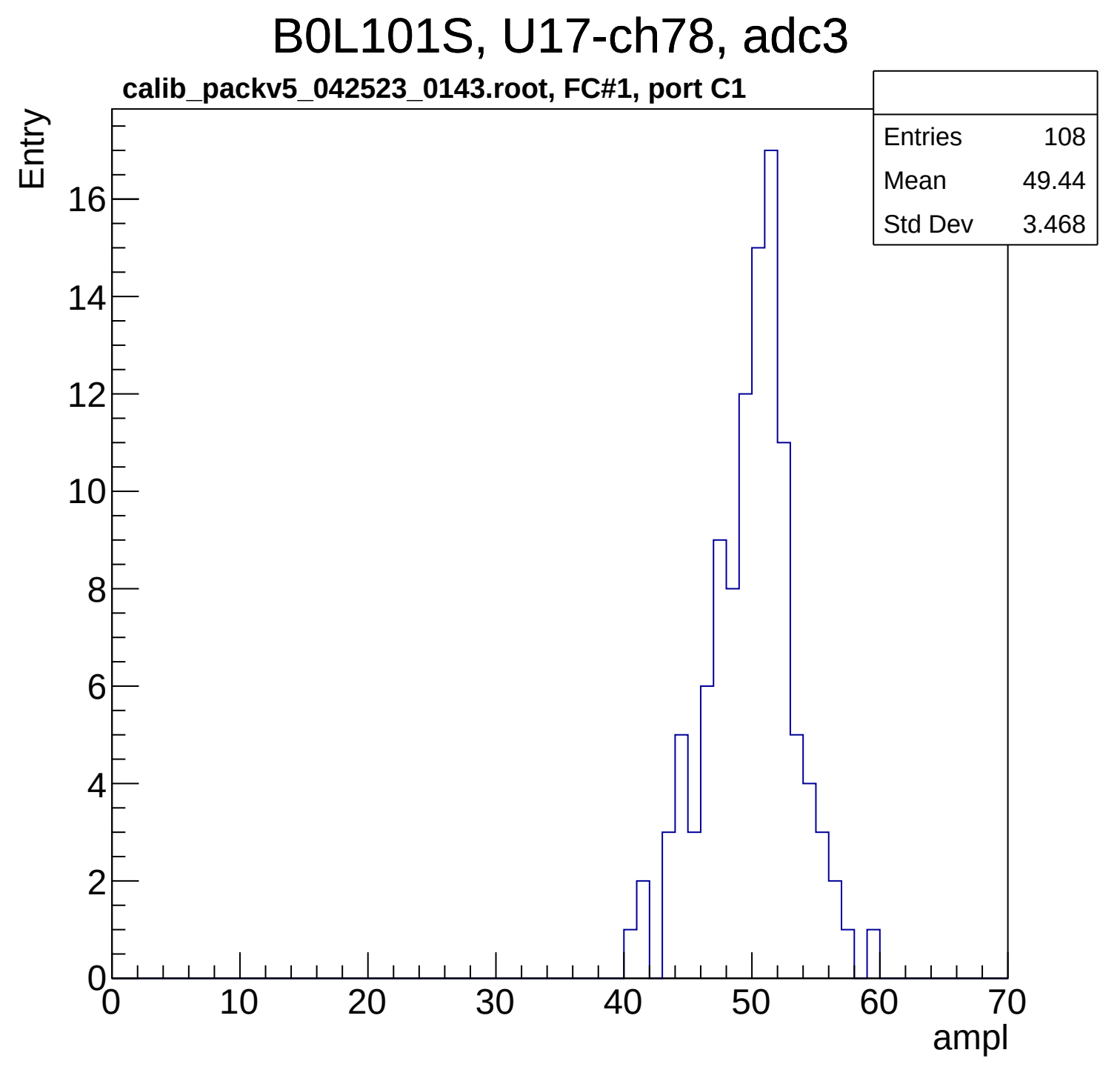

B0L101S, U17-ch77, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 6
Mean 62.67
Std Dev 0.7454
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

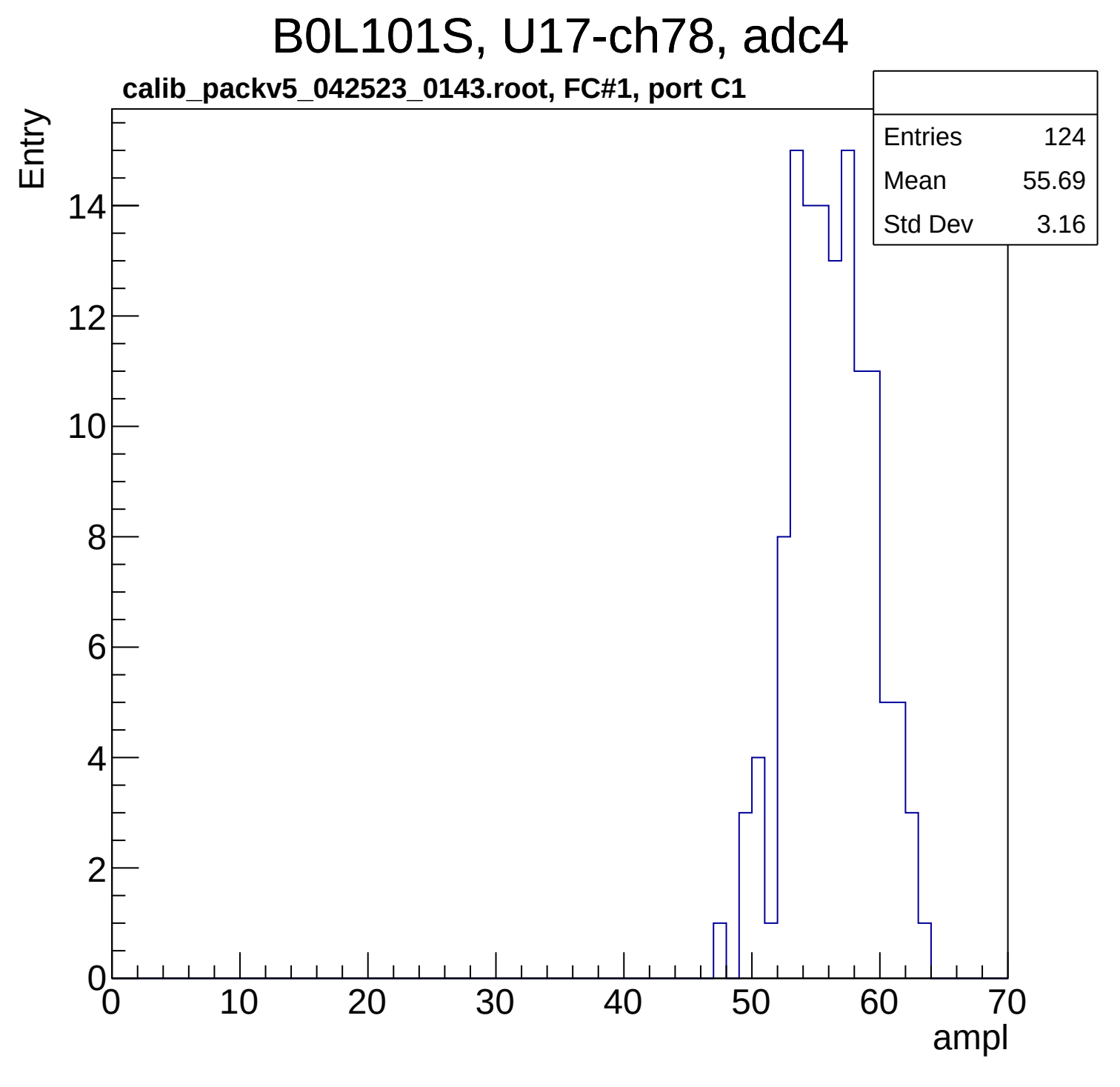

B0L101S, U17-ch77, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 39
Std Dev 20
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

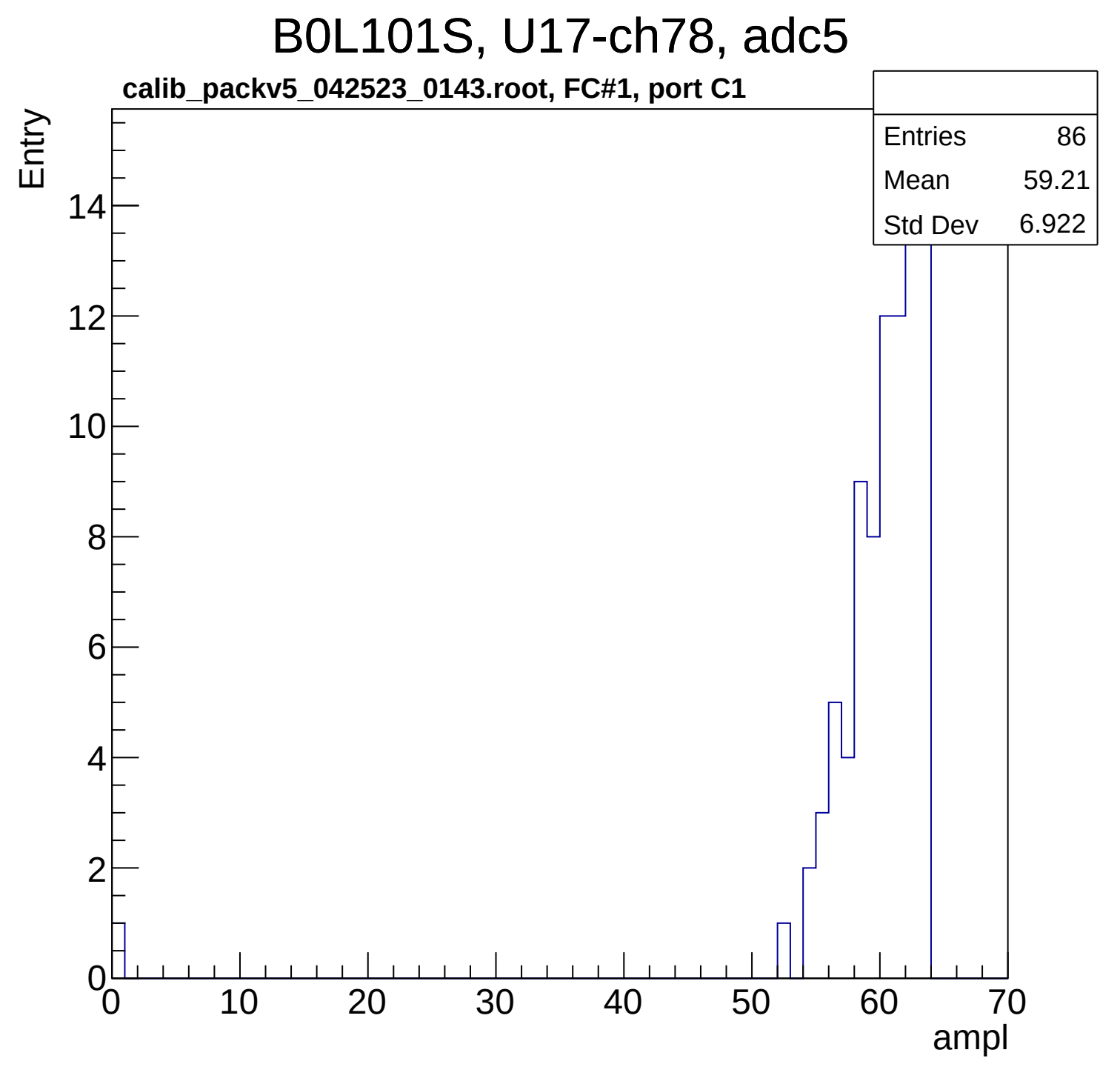

B0L101S, U17-ch78, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 29.42
Std Dev 3.824
Gaus mean : 29.9508
Gaus Width: 3.9730
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

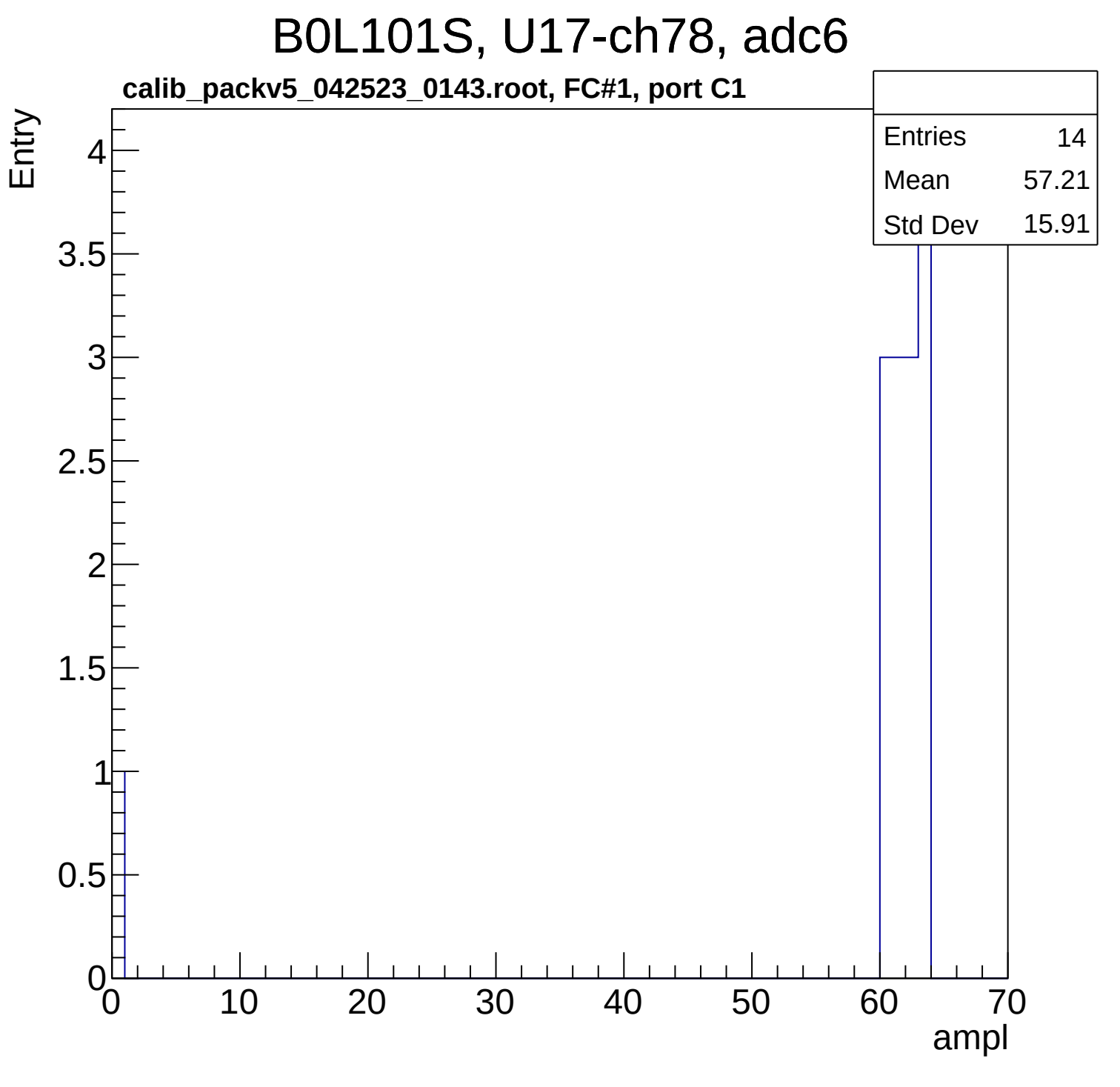

B0L101S, U17-ch78, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 36.74
Std Dev 3.702
Gaus mean : 37.2365
Gaus Width: 3.8009
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

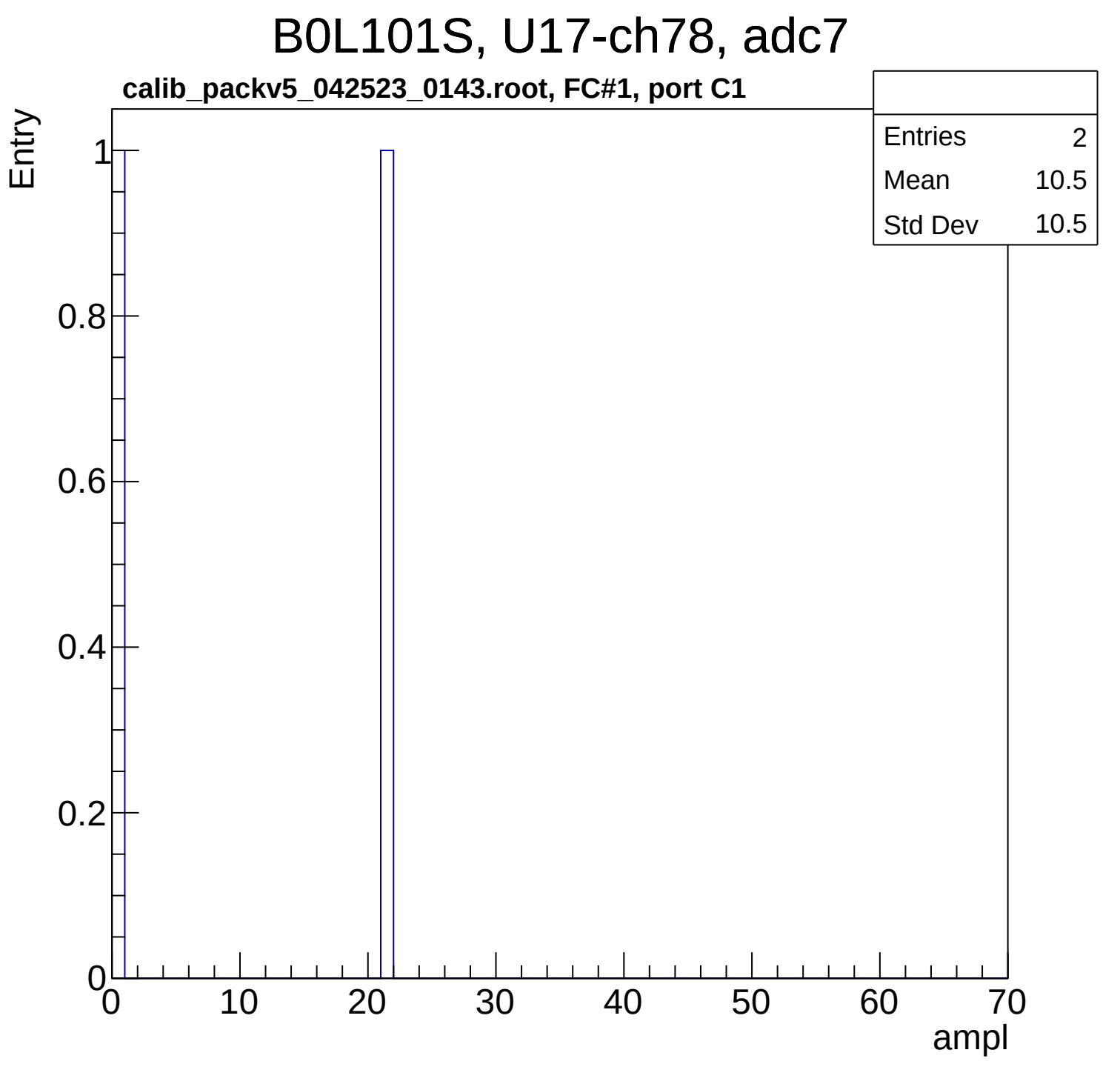

B0L101S, U17-ch78, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 43.73
Std Dev 3.426
Gaus mean : 44.1938
Gaus Width: 3.3940
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

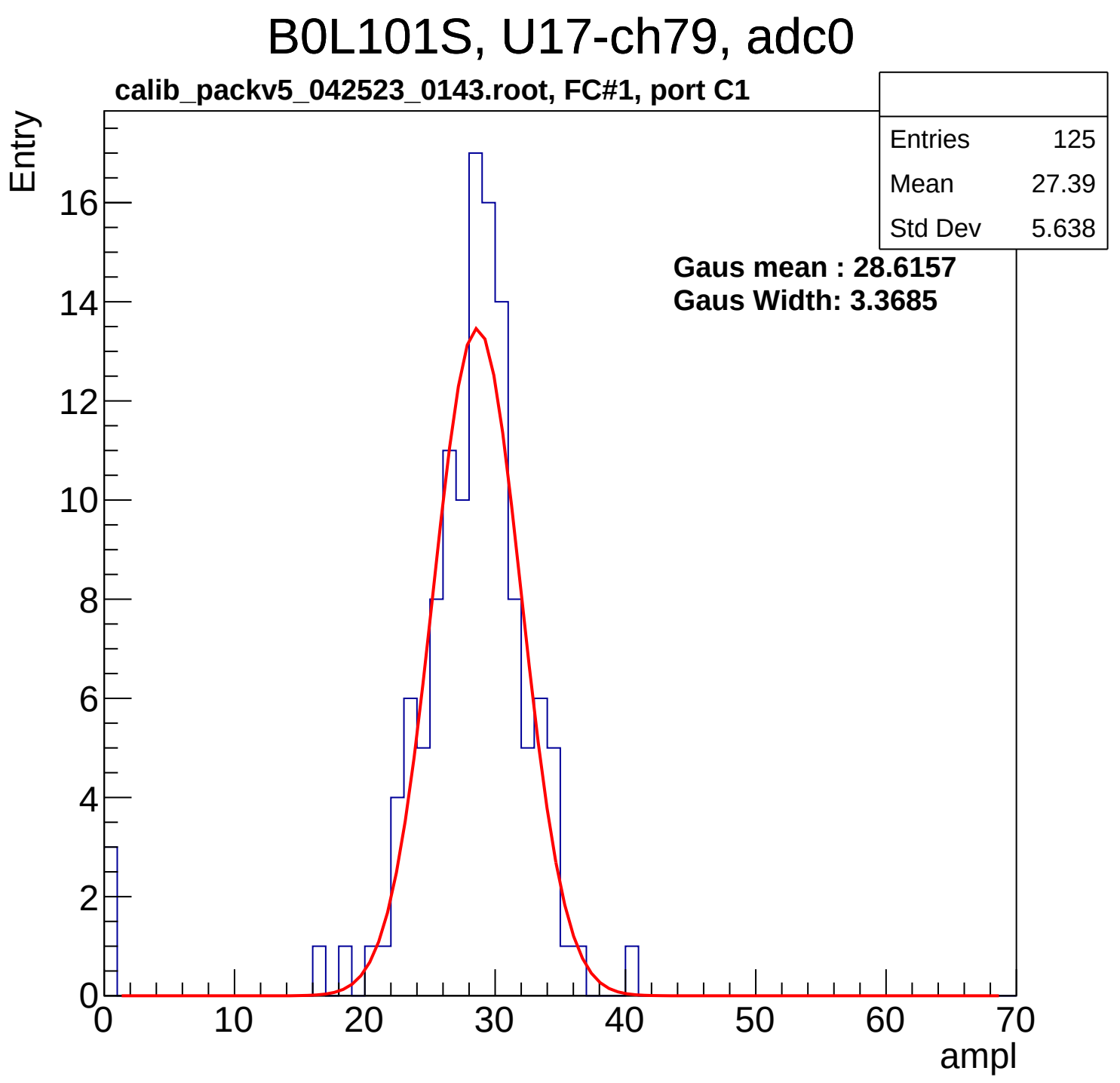

B0L101S, U17-ch78, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 108
Mean 49.44
Std Dev 3.468
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

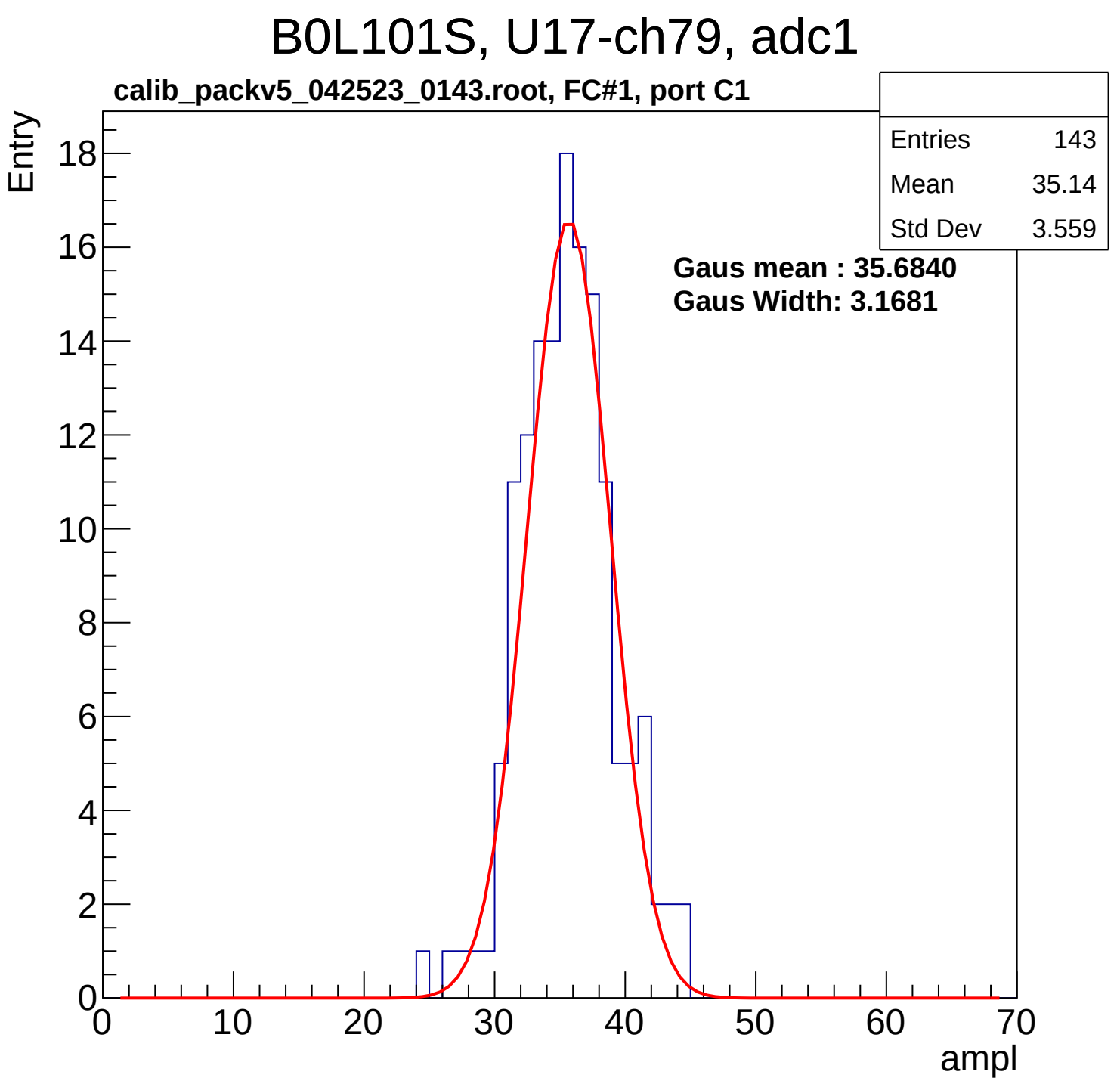

B0L101S, U17-ch78, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 55.69
Std Dev 3.16
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

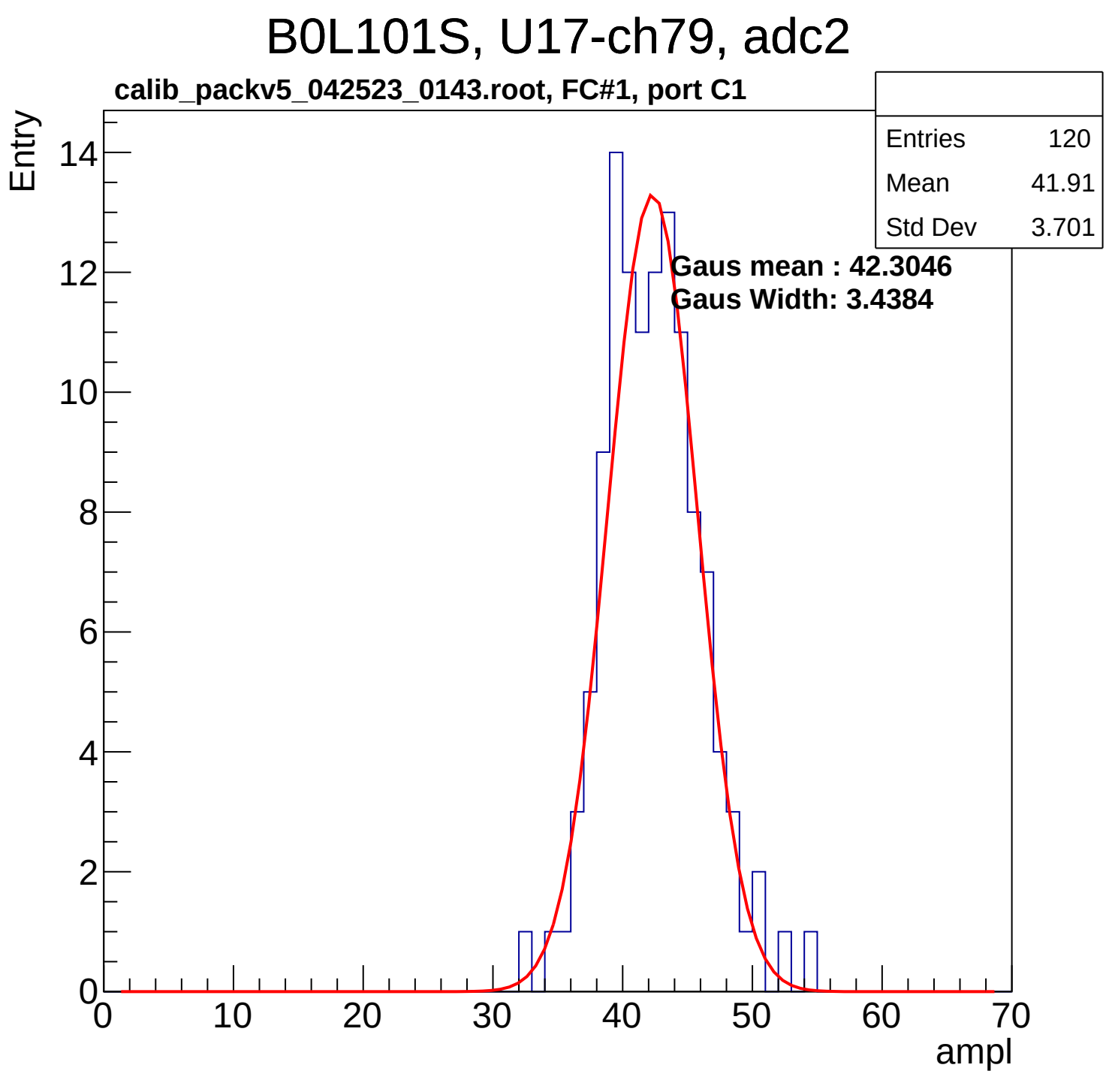

B0L101S, U17-ch78, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 86
Mean 59.21
Std Dev 6.922
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

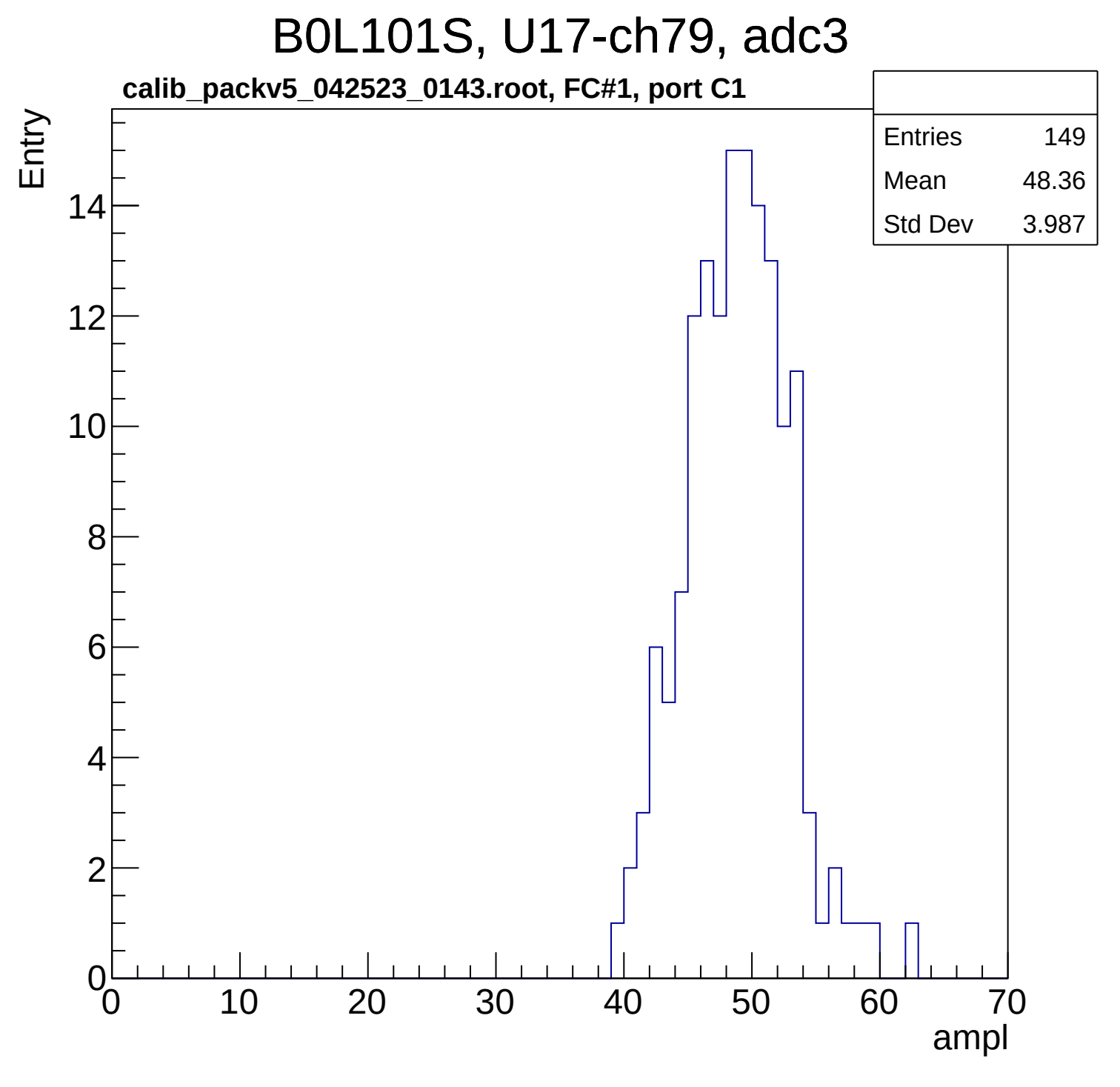

B0L101S, U17-ch78, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 57.21
Std Dev 15.91
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

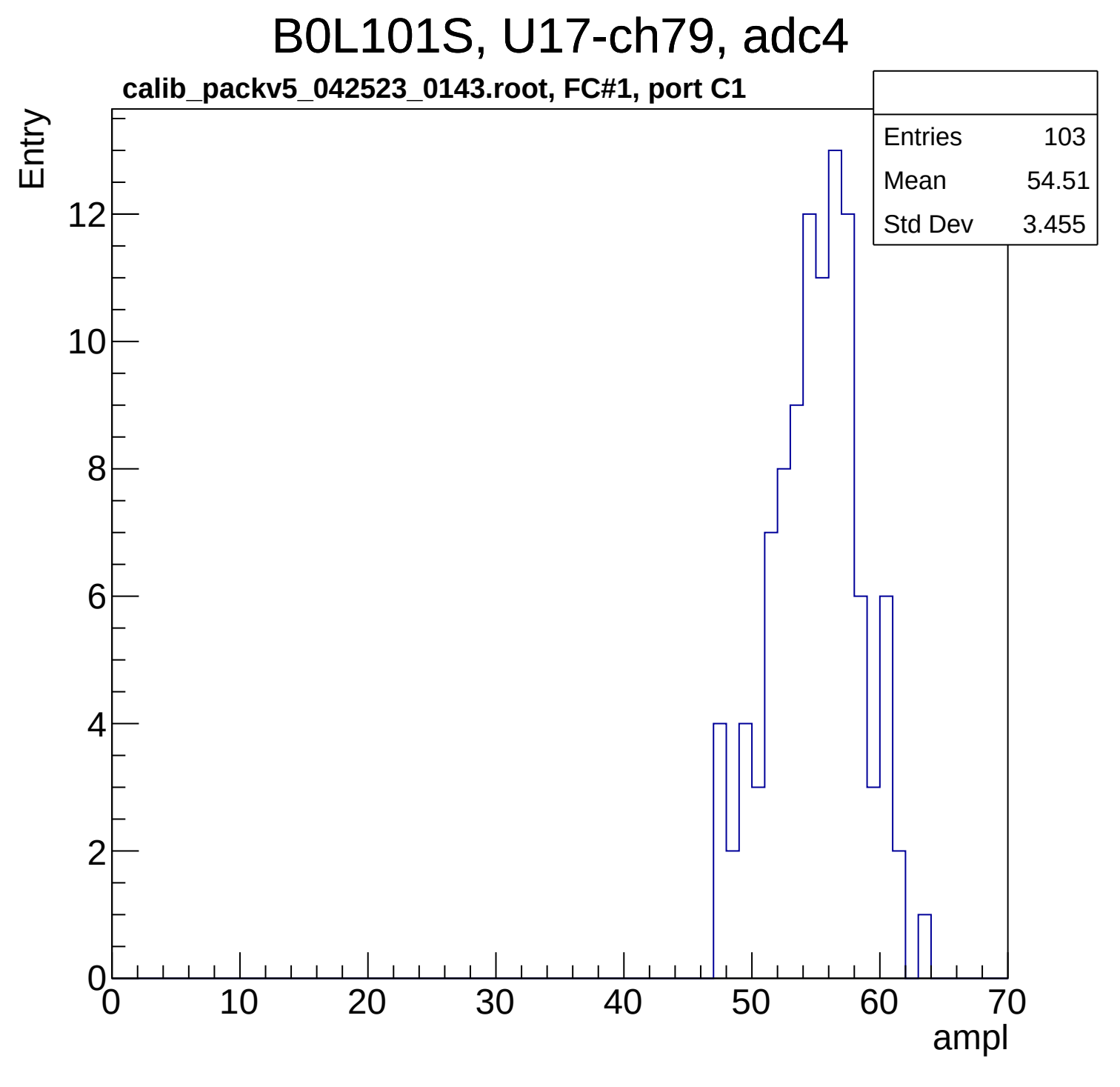

B0L101S, U17-ch78, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

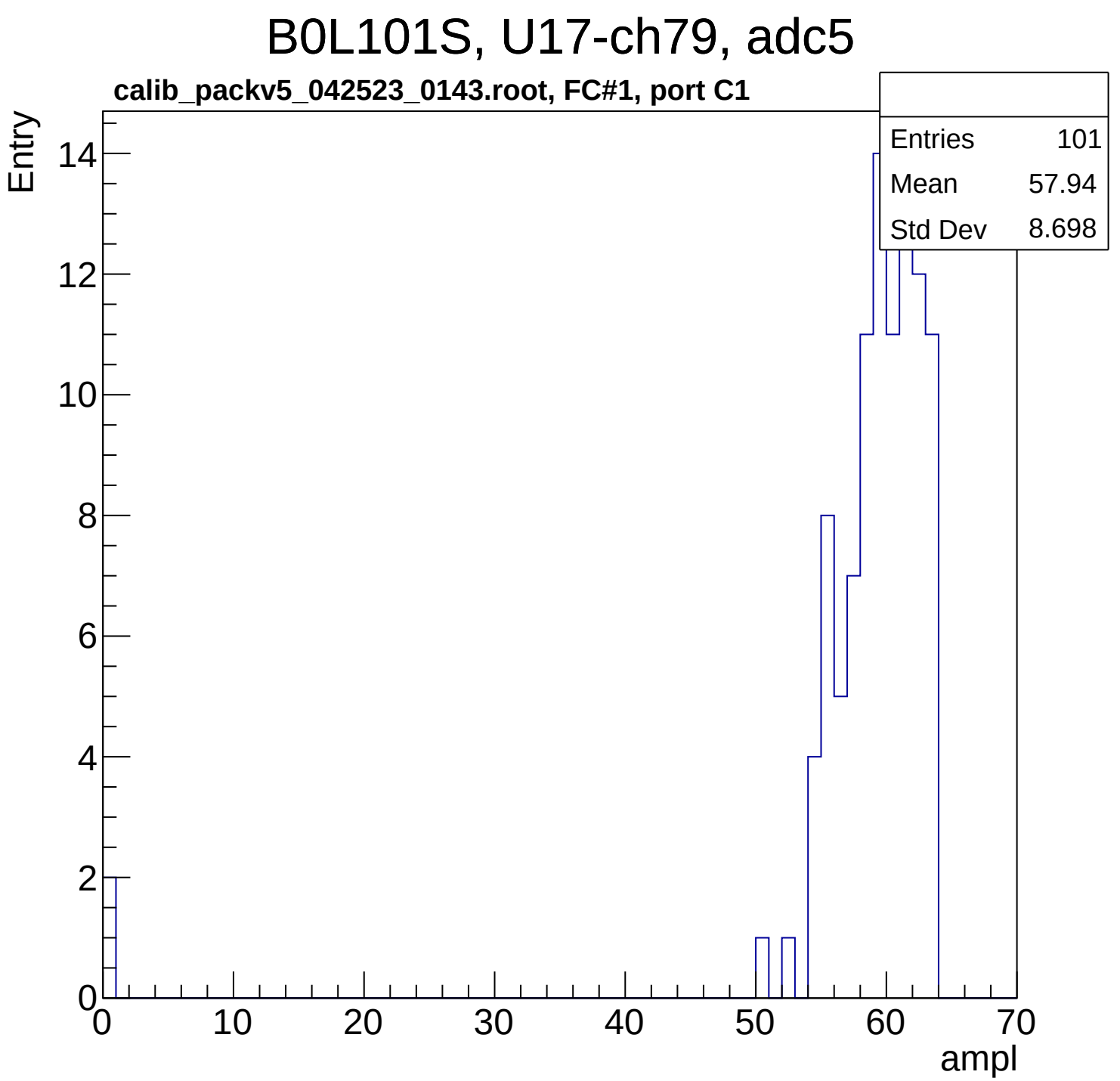

B0L101S, U17-ch79, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 27.39
Std Dev 5.638
Gaus mean : 28.6157
Gaus Width: 3.3685
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

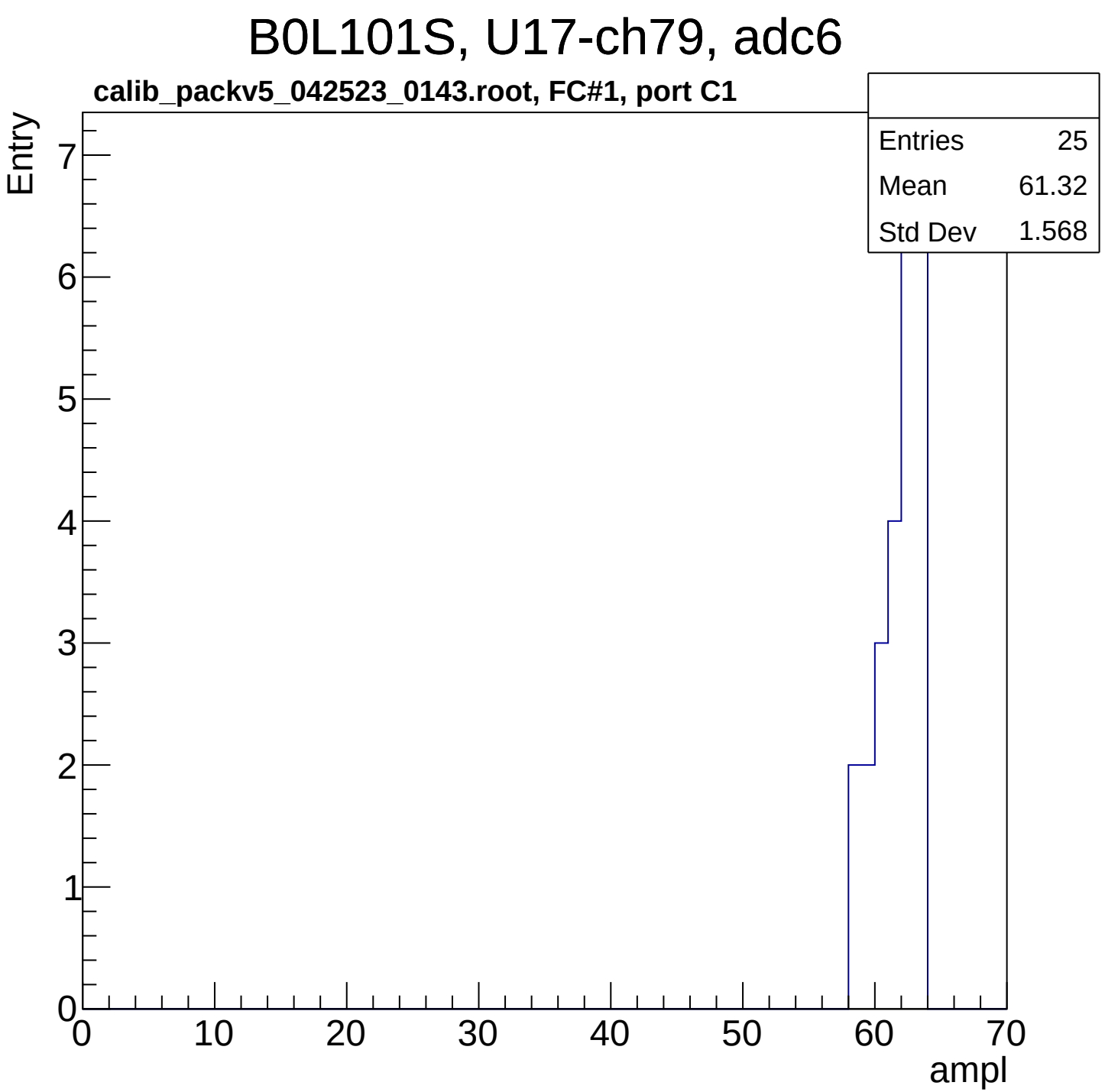

B0L101S, U17-ch79, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 35.14
Std Dev 3.559
Gaus mean : 35.6840
Gaus Width: 3.1681
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

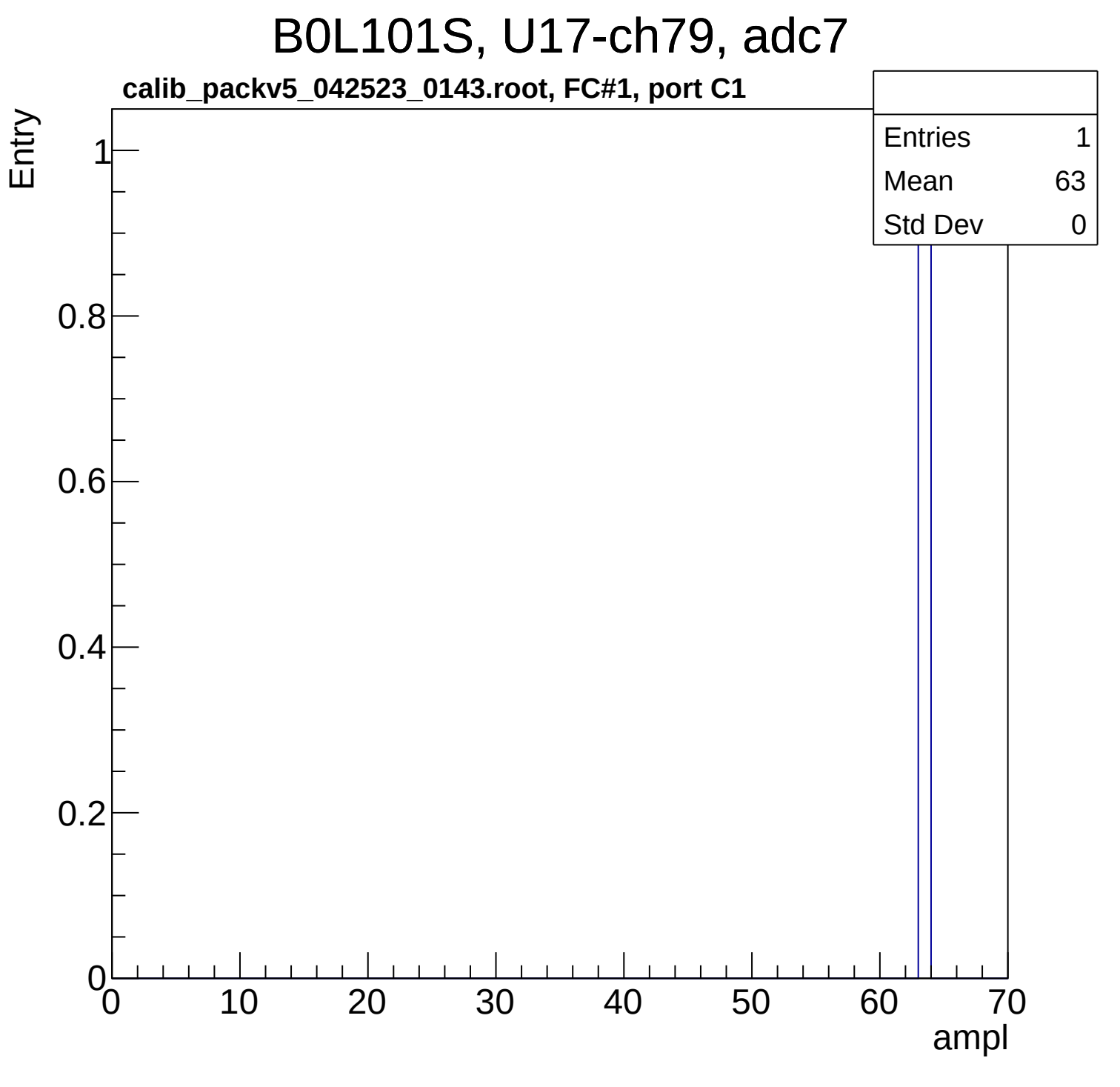

B0L101S, U17-ch79, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 41.91
Std Dev 3.701
Gaus mean : 42.3046
Gaus Width: 3.4384
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

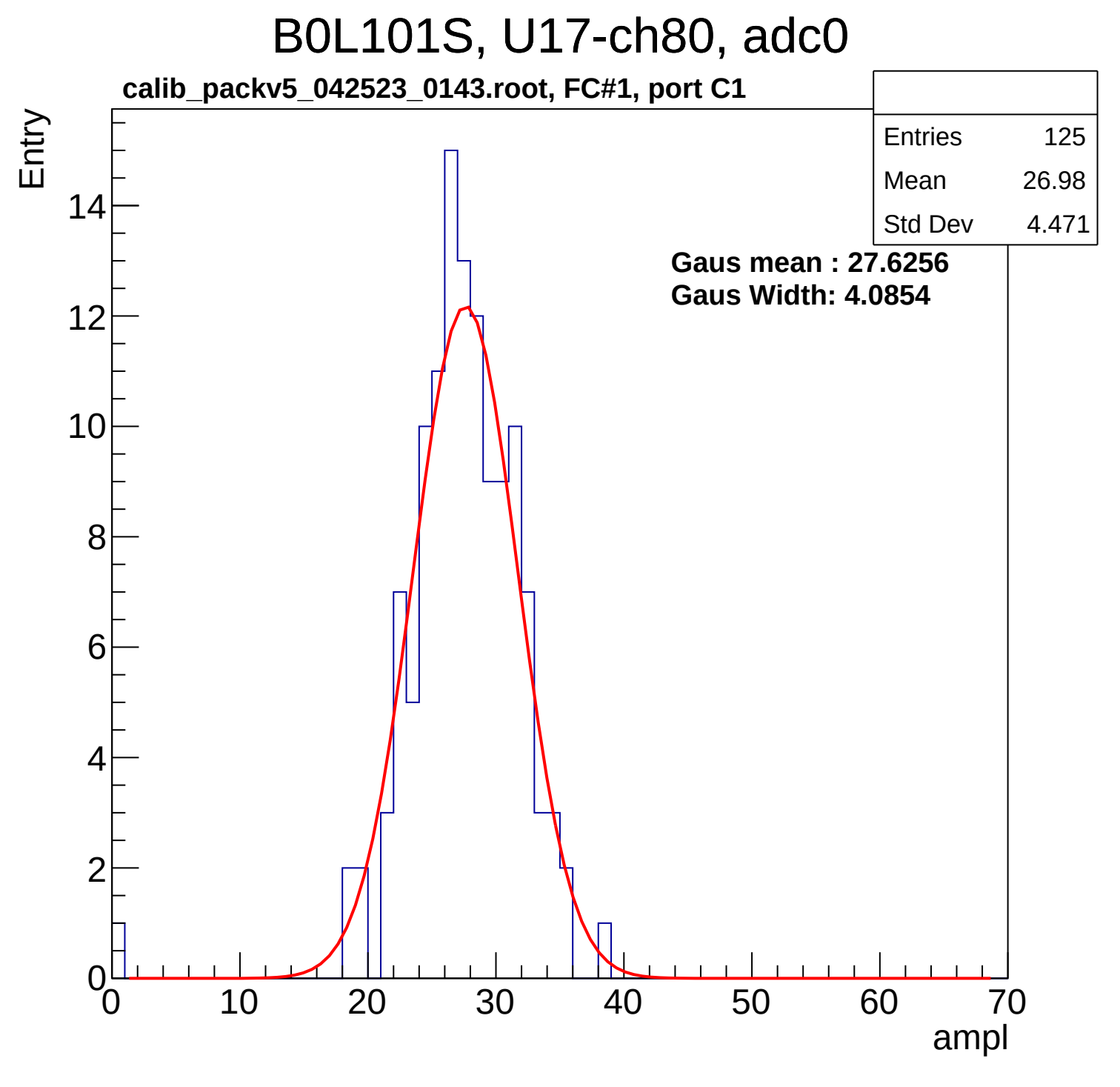

B0L101S, U17-ch79, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 48.36
Std Dev 3.987
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

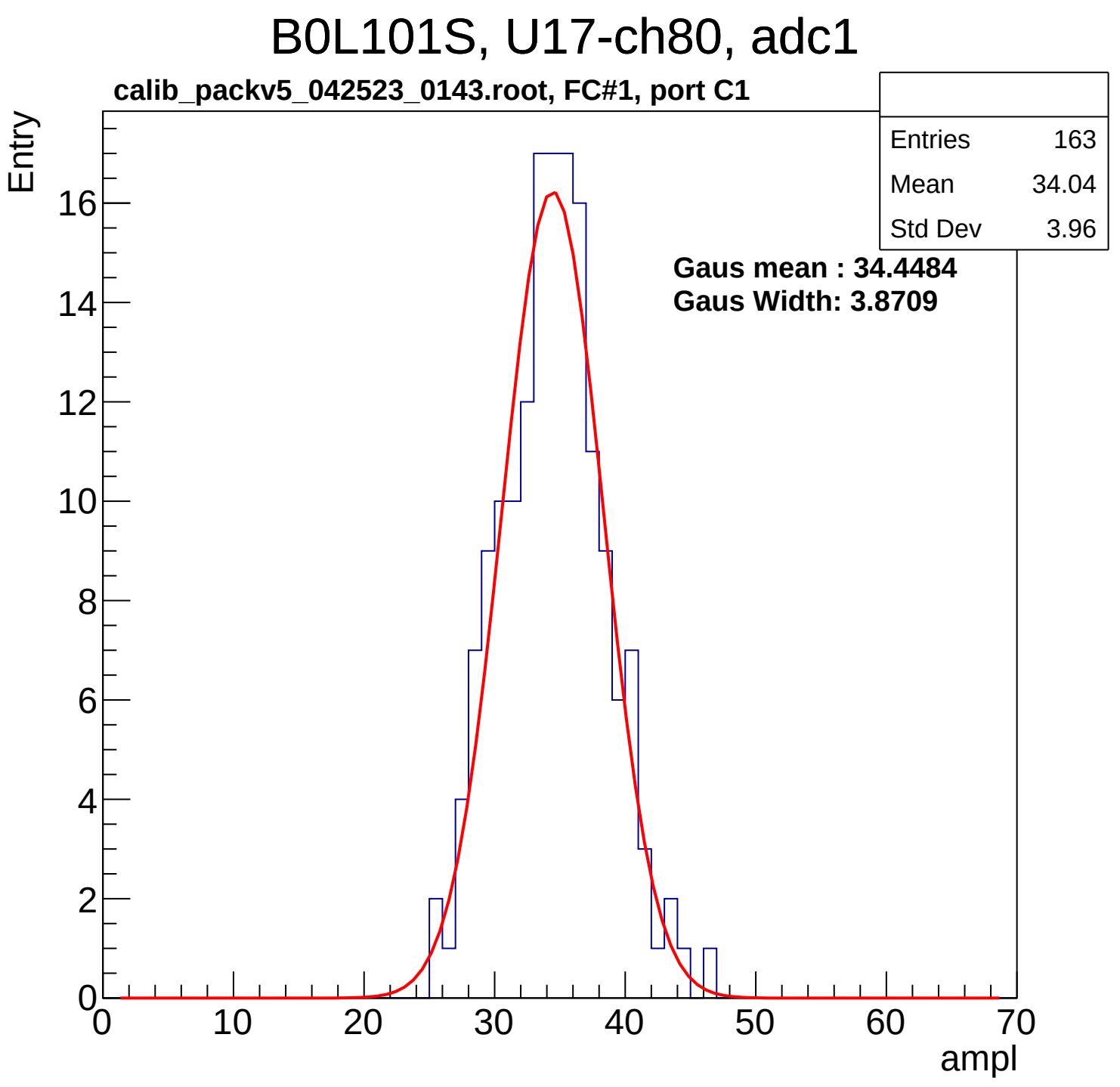

B0L101S, U17-ch79, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 54.51
Std Dev 3.455
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

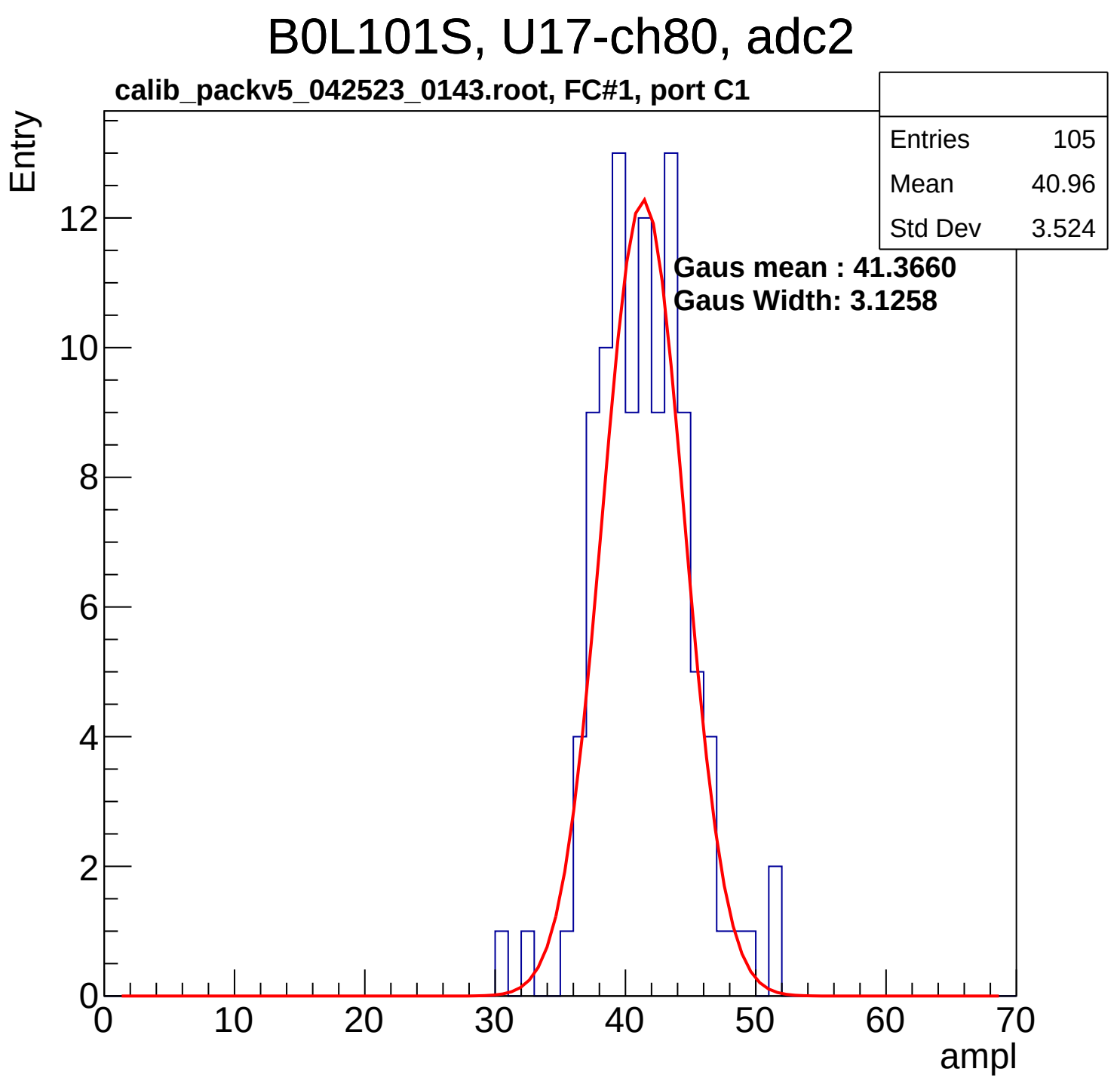

B0L101S, U17-ch79, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 57.94
Std Dev 8.698
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

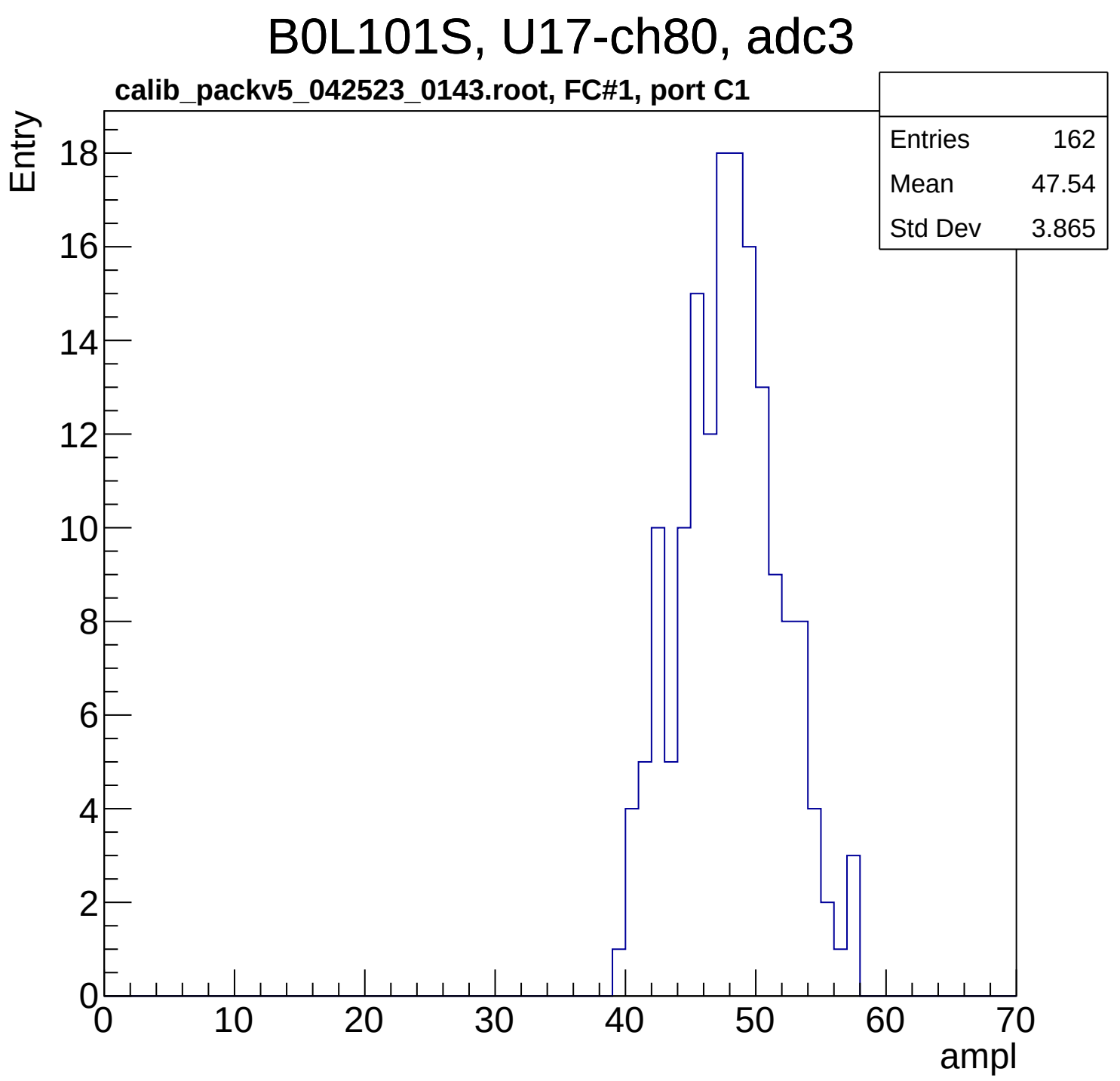

B0L101S, U17-ch79, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 25
Mean 61.32
Std Dev 1.568
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

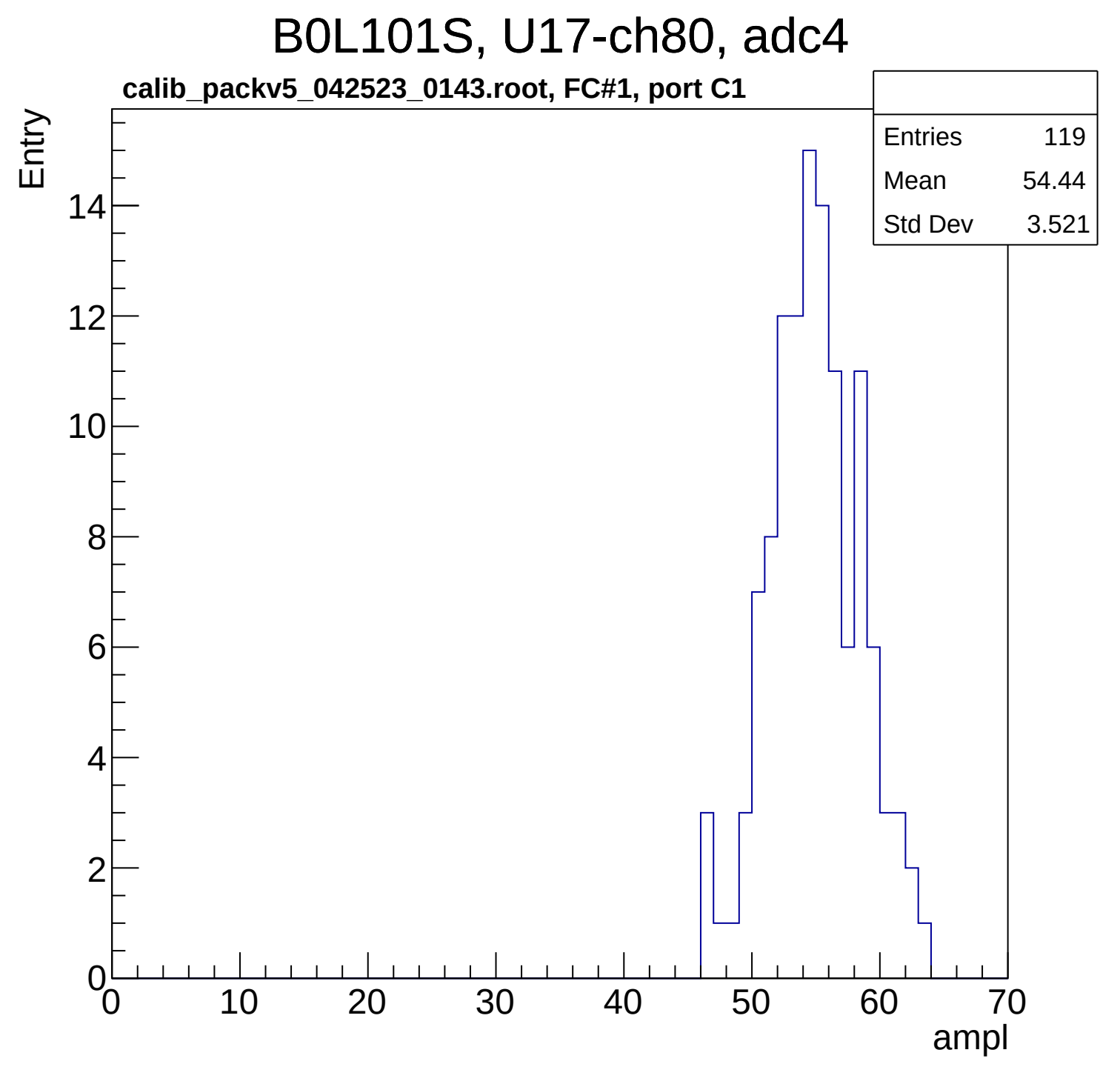

B0L101S, U17-ch79, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

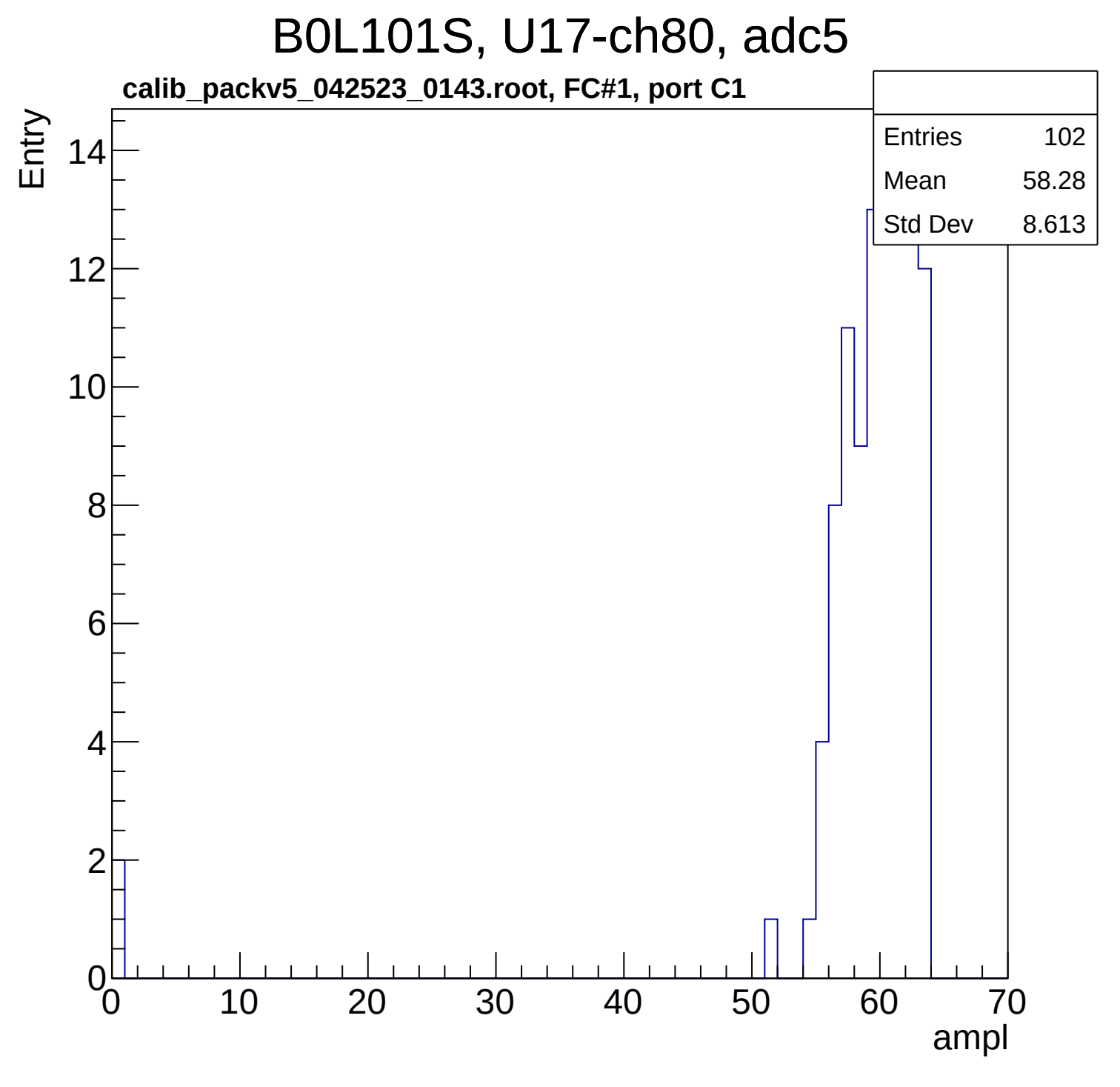

B0L101S, U17-ch80, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 26.98
Std Dev 4.471
Gaus mean : 27.6256
Gaus Width: 4.0854
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

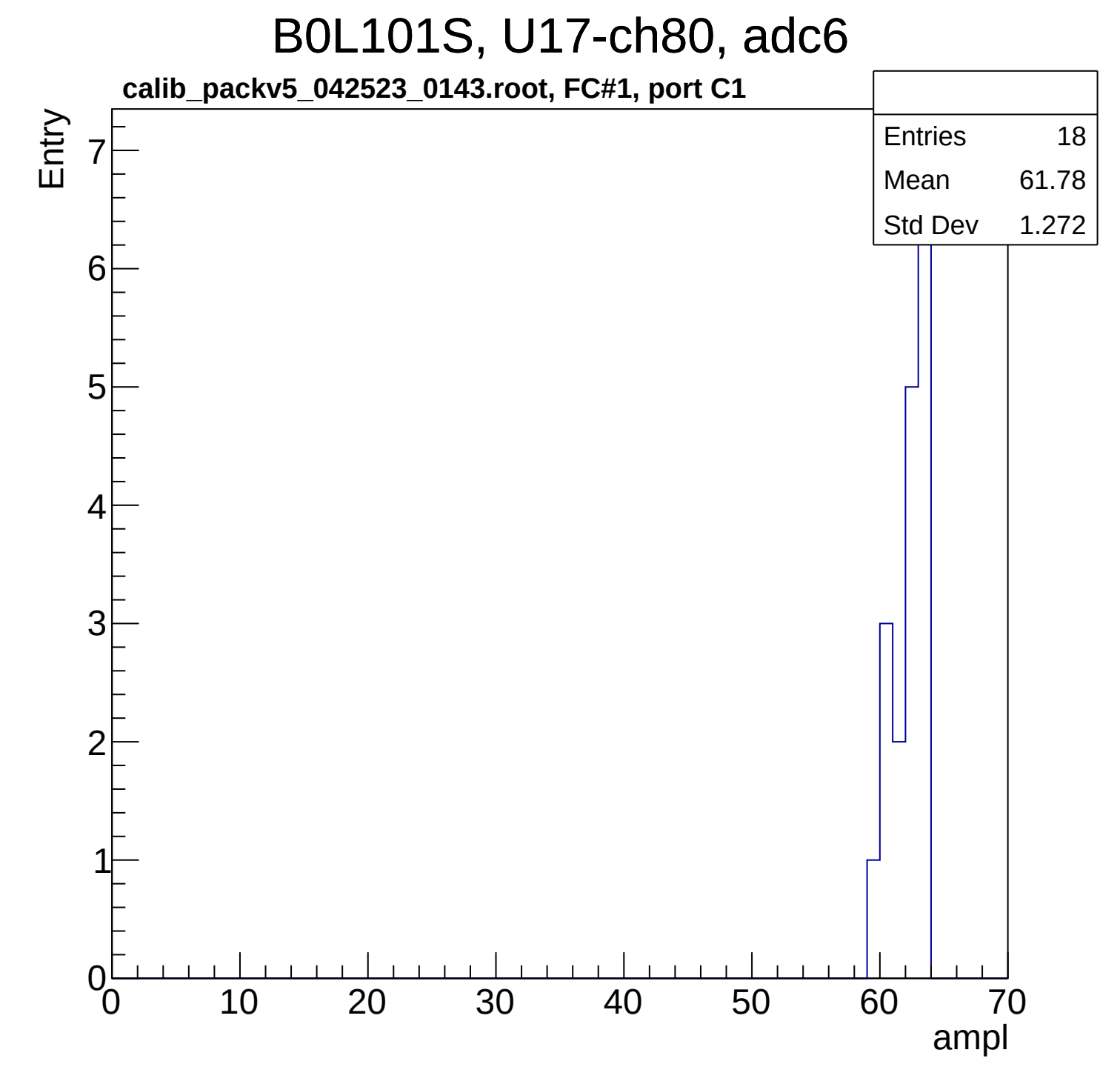

B0L101S, U17-ch80, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 34.04
Std Dev 3.96
Gaus mean : 34.4484
Gaus Width: 3.8709
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

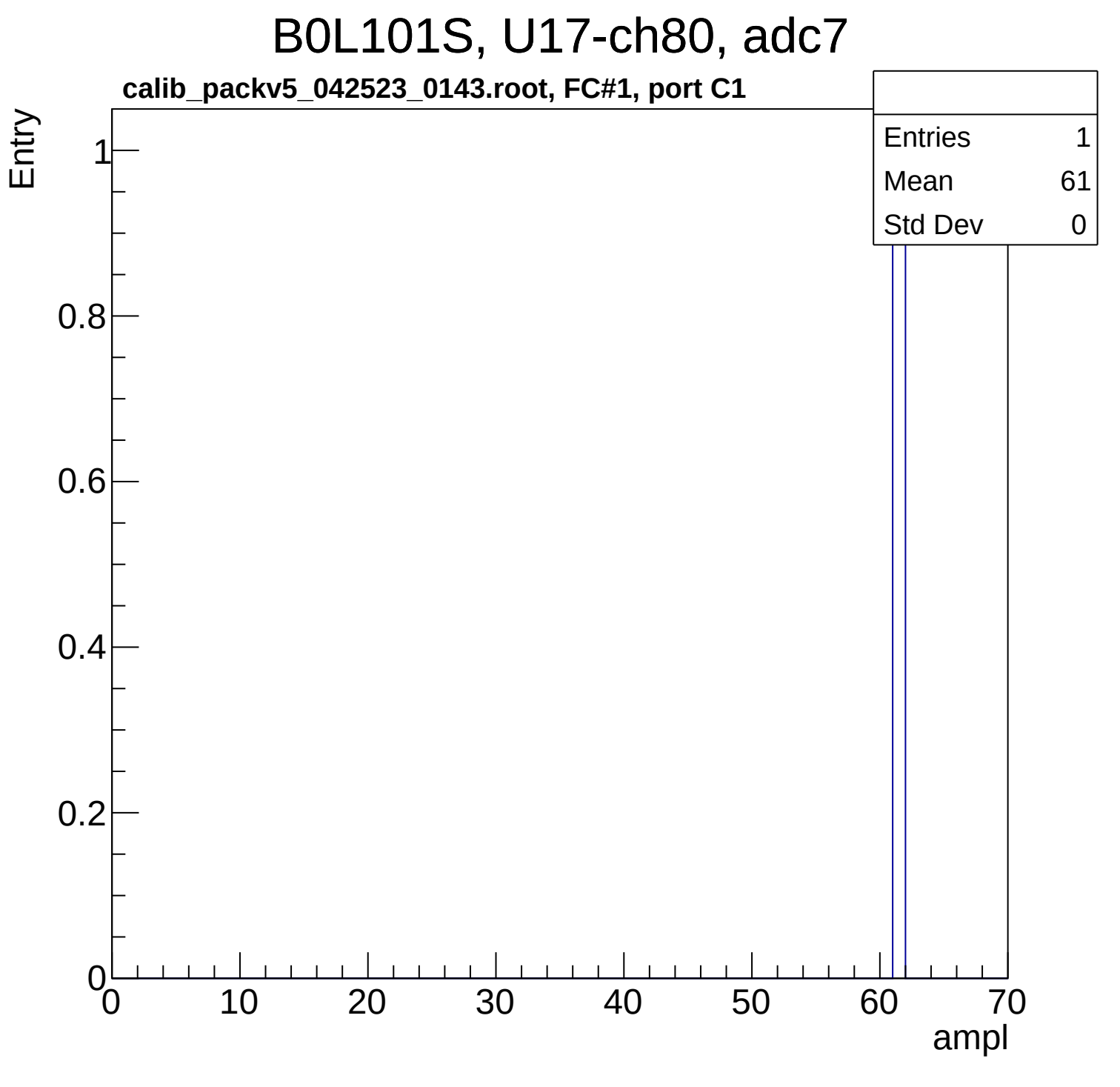

B0L101S, U17-ch80, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 40.96
Std Dev 3.524
Gaus mean : 41.3660
Gaus Width: 3.1258
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

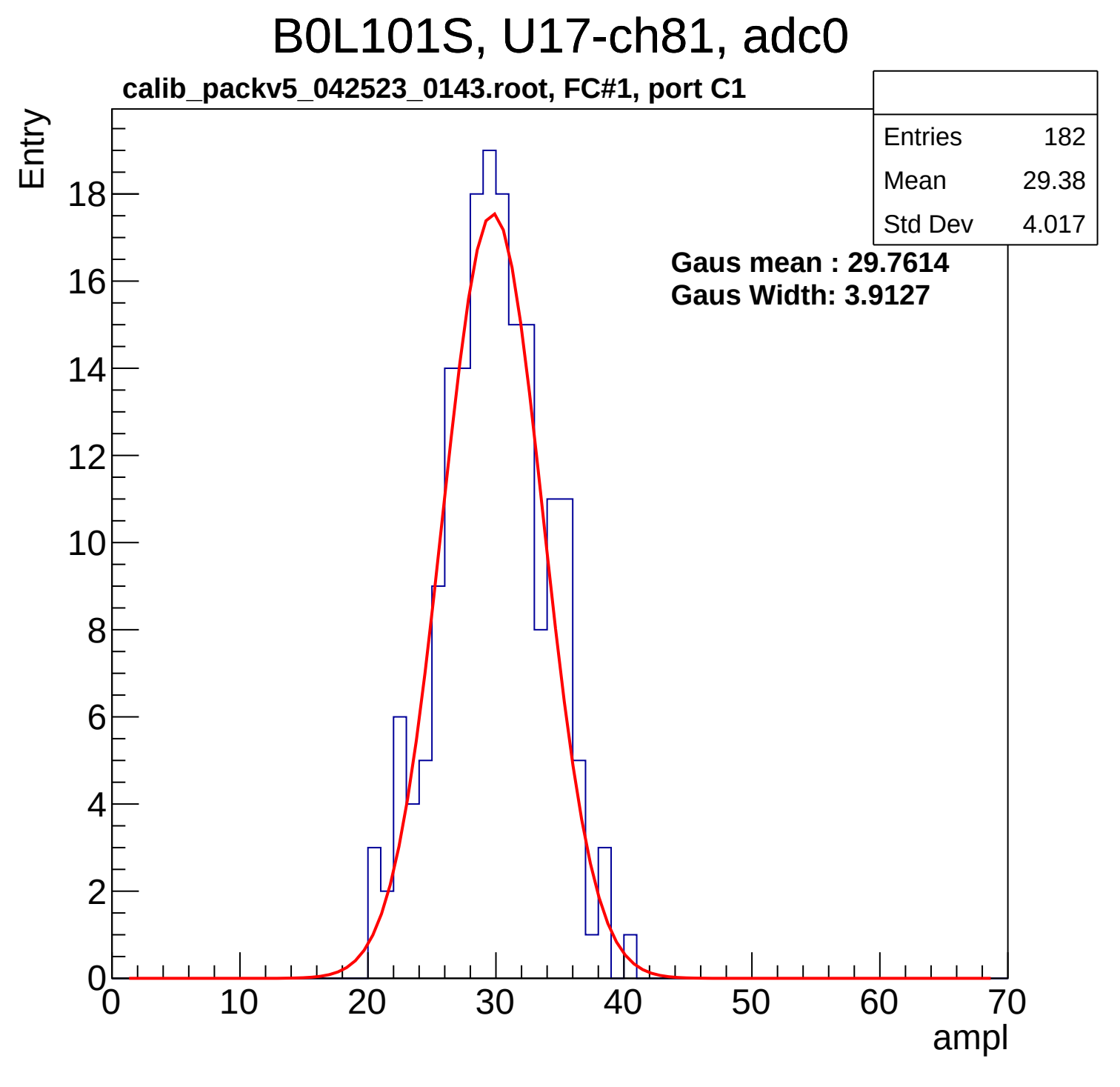

B0L101S, U17-ch80, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 162
Mean 47.54
Std Dev 3.865
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

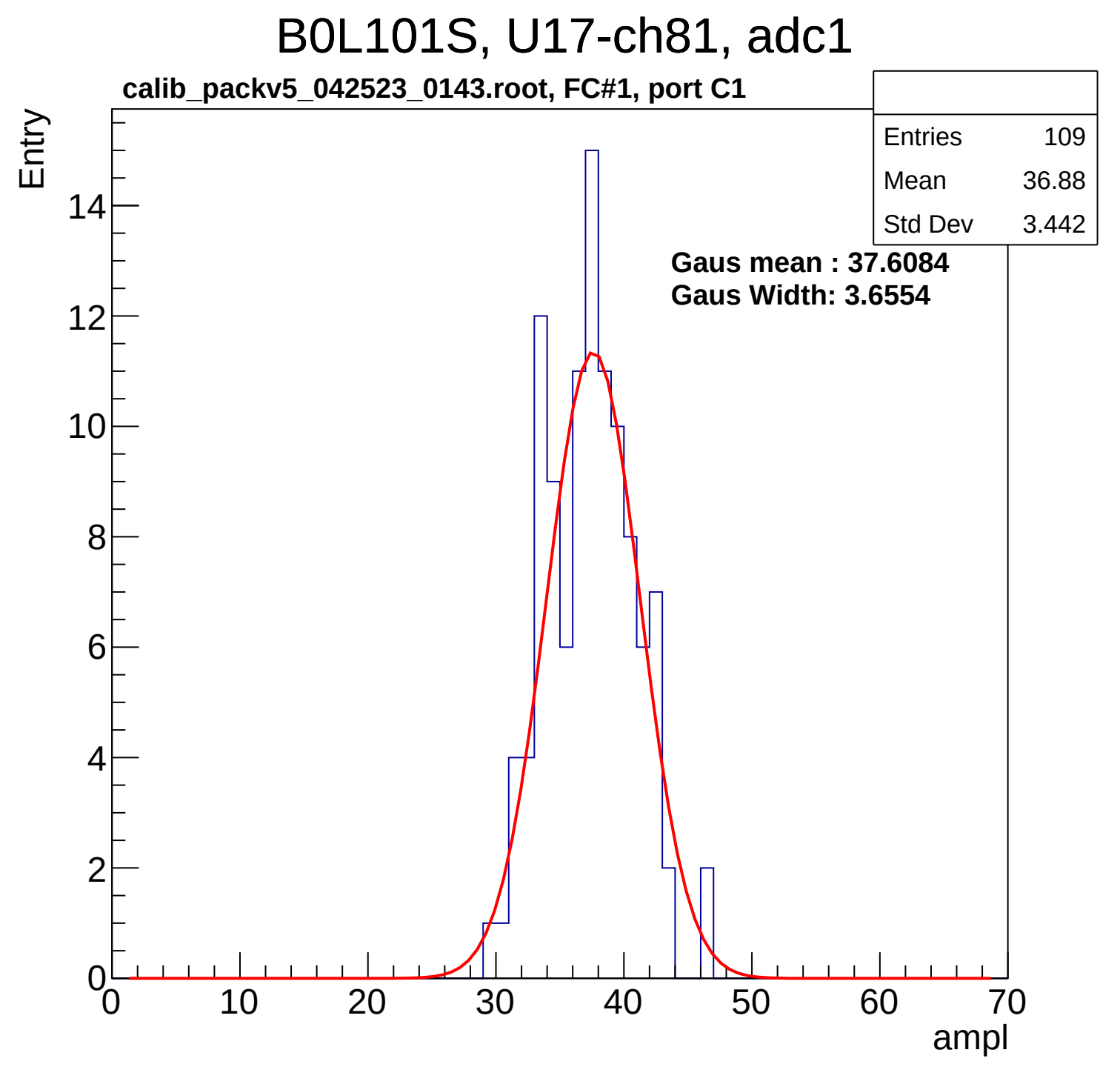

B0L101S, U17-ch80, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 54.44
Std Dev 3.521
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

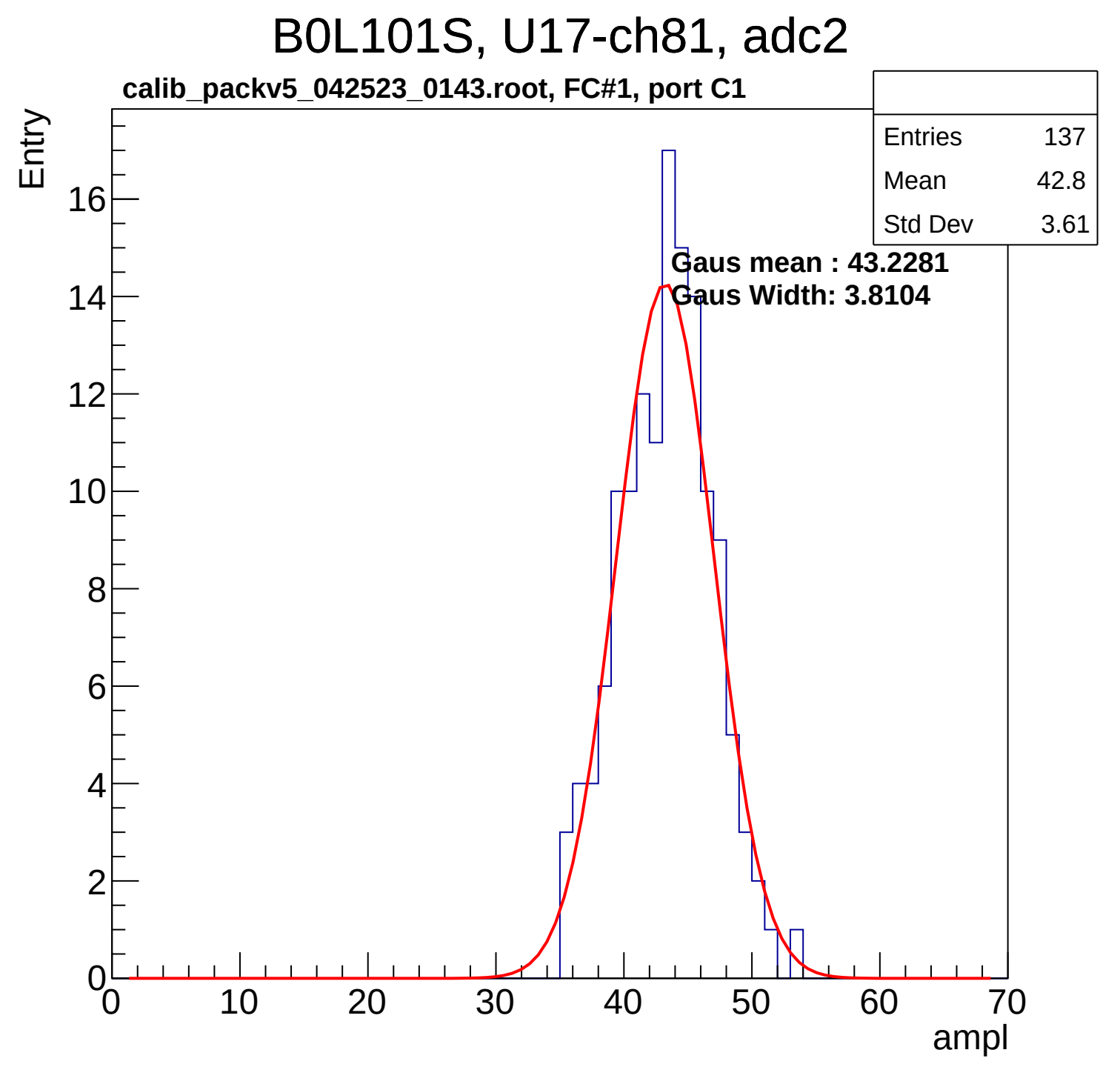

B0L101S, U17-ch80, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 58.28
Std Dev 8.613
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

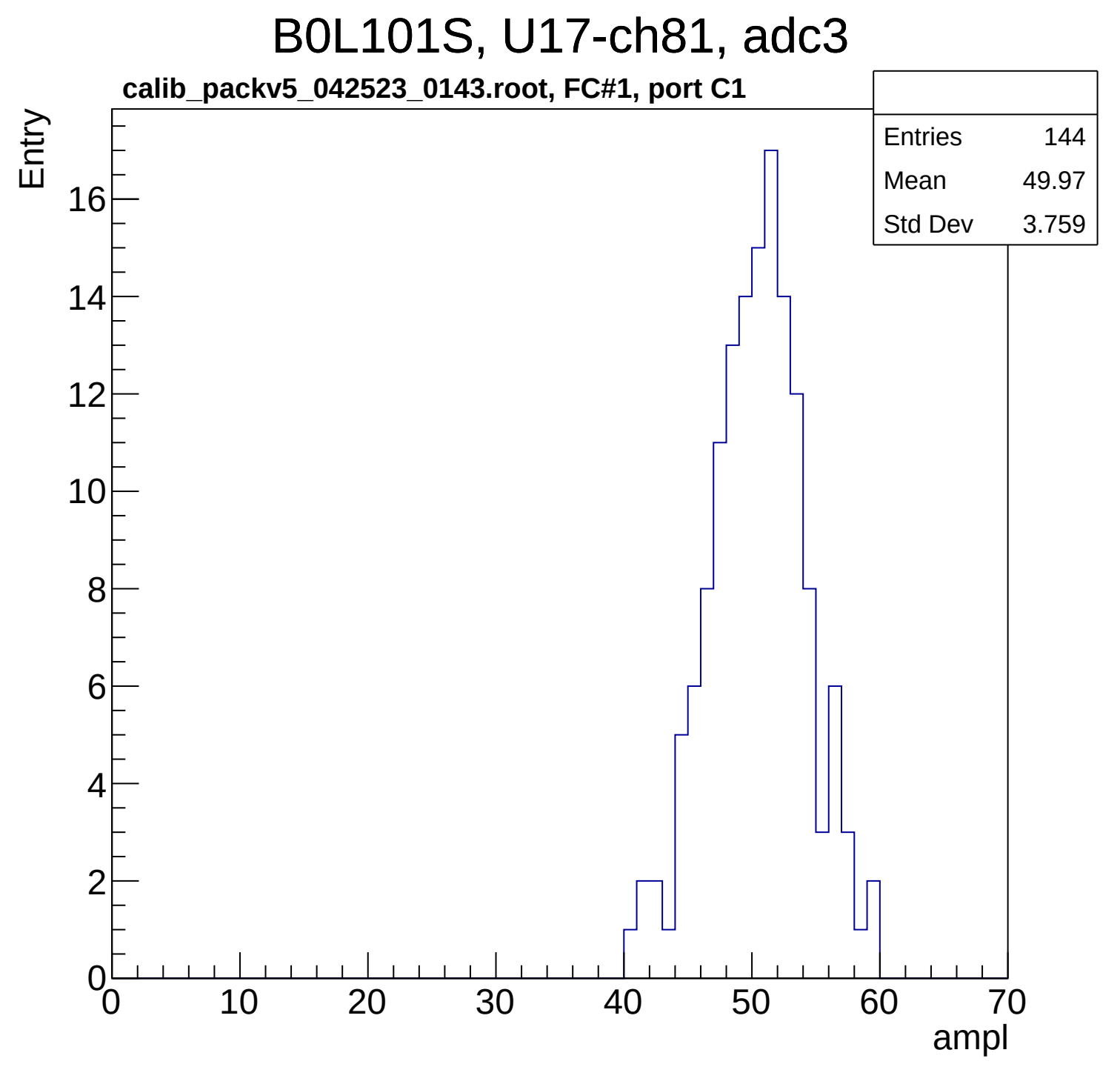

B0L101S, U17-ch80, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 18
Mean 61.78
Std Dev 1.272
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

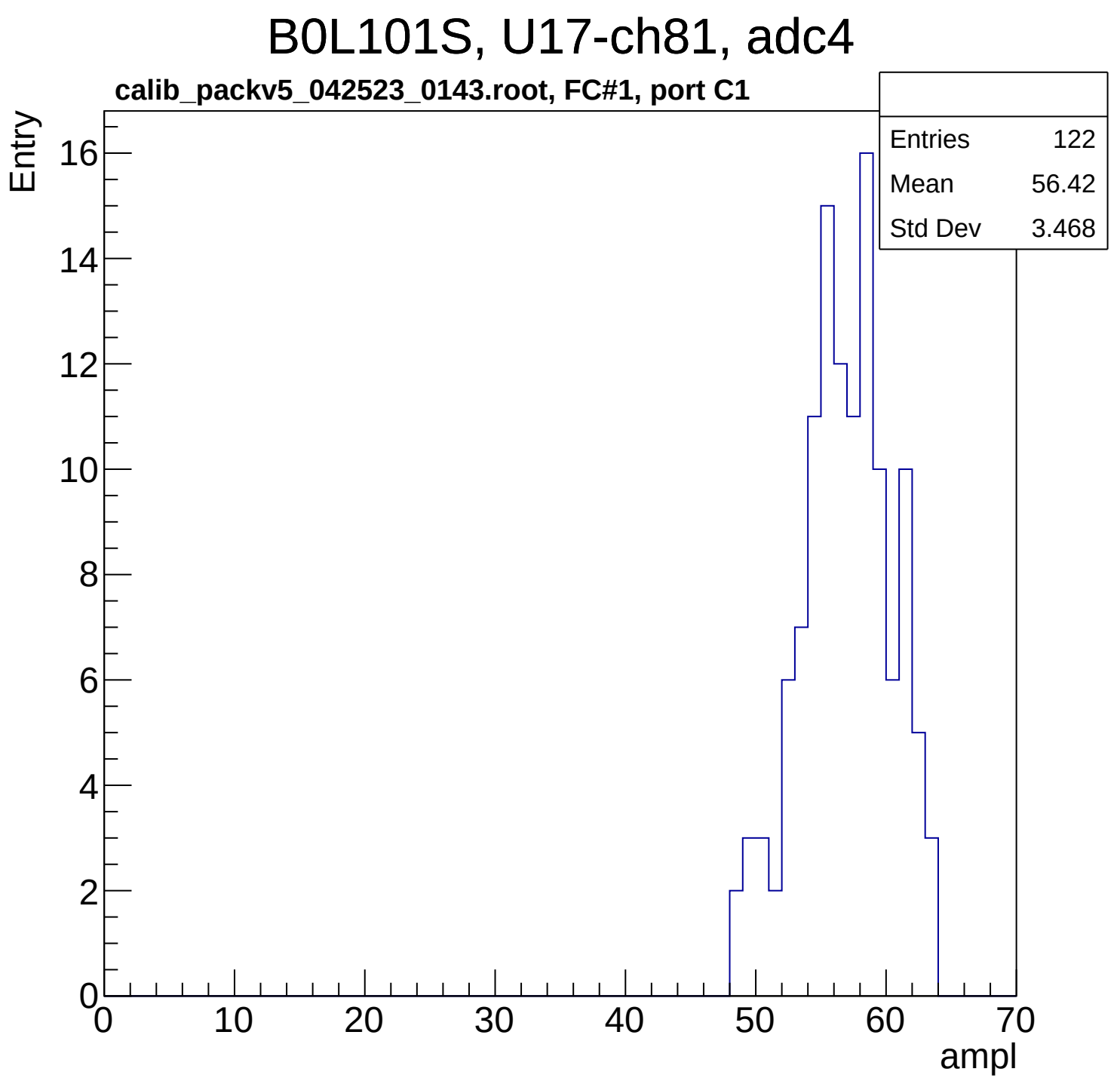

B0L101S, U17-ch80, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

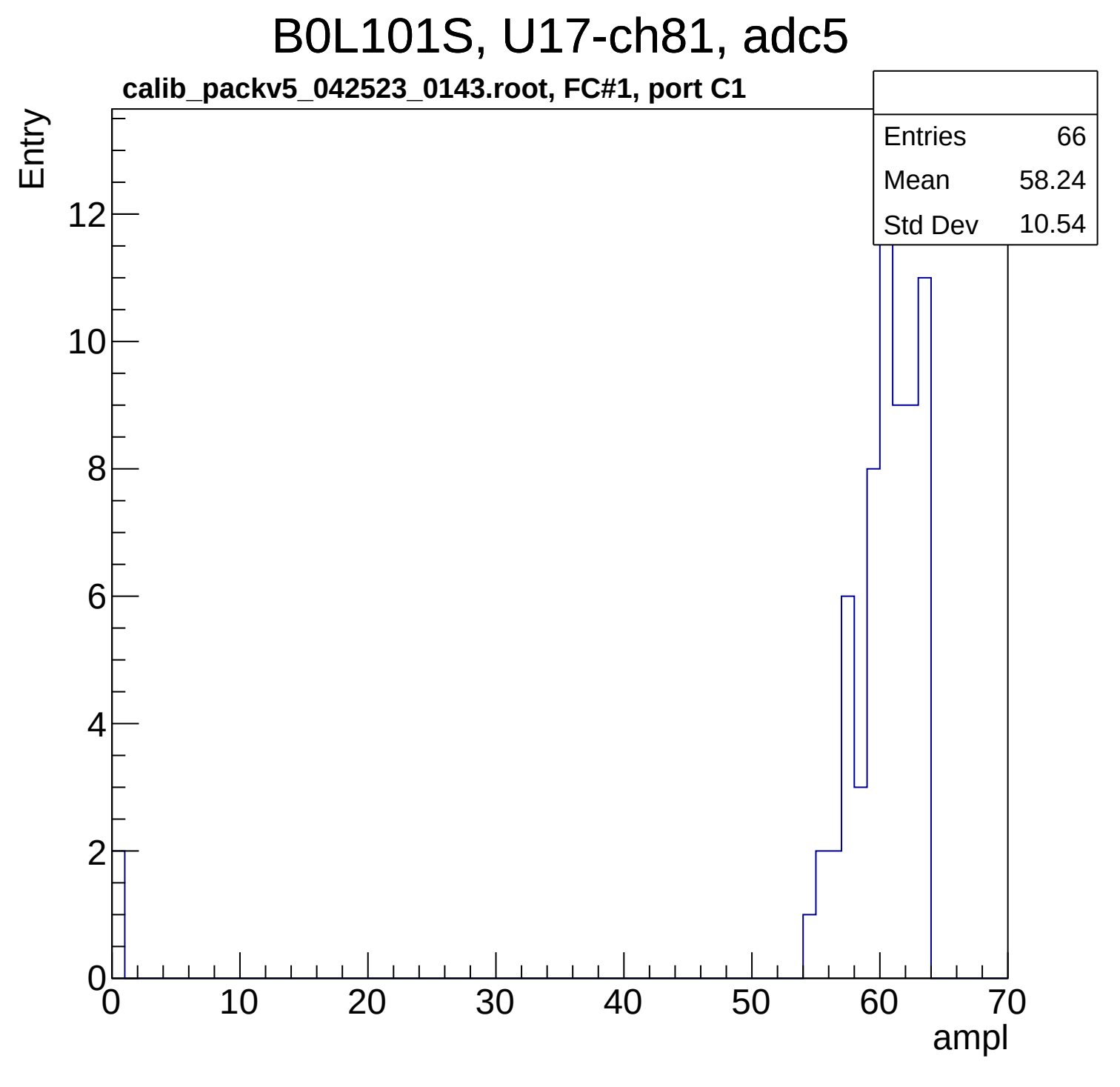

B0L101S, U17-ch81, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 182
Mean 29.38
Std Dev 4.017
Gaus mean : 29.7614
Gaus Width: 3.9127
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

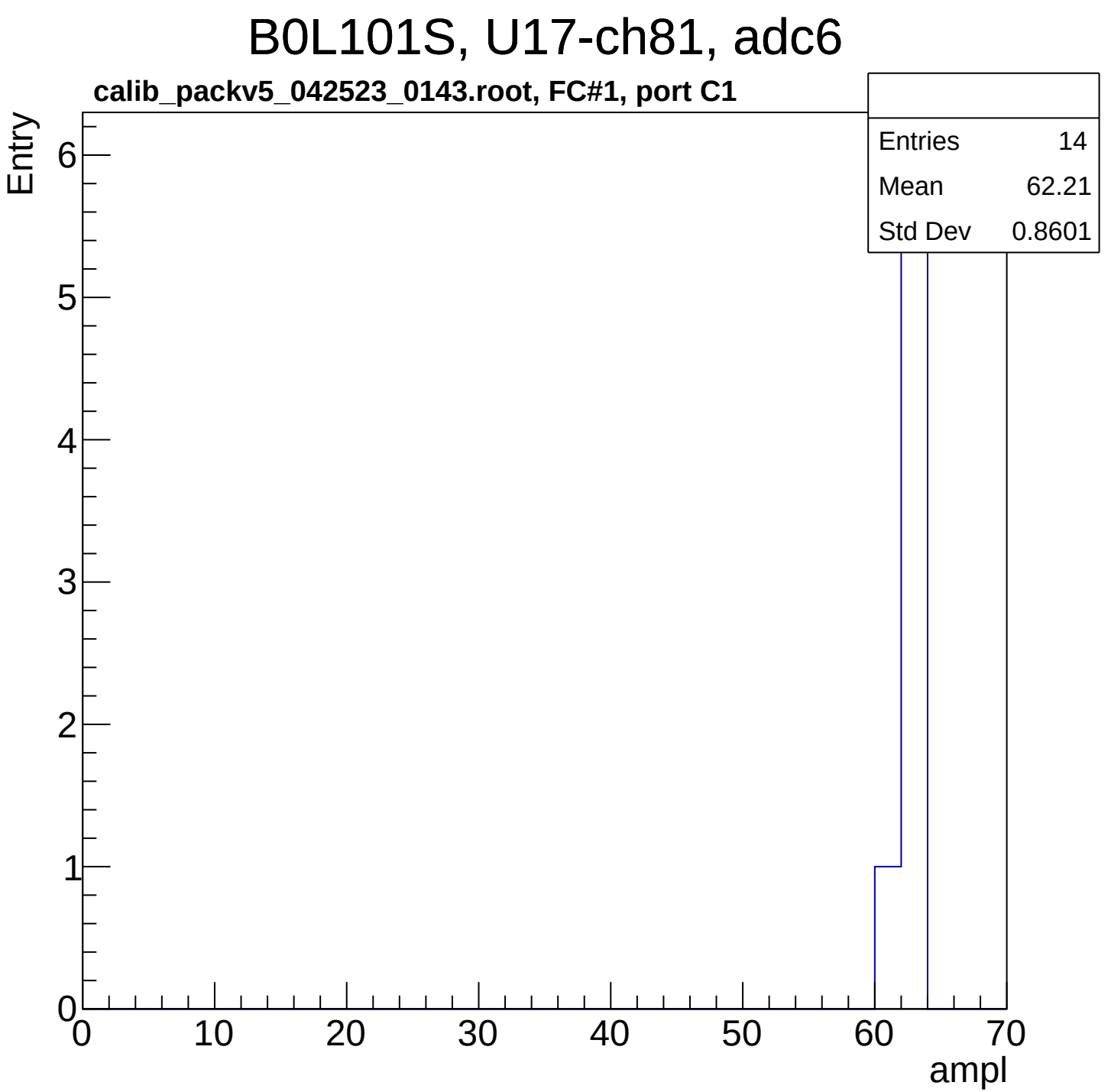

B0L101S, U17-ch81, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 36.88
Std Dev 3.442
Gaus mean : 37.6084
Gaus Width: 3.6554
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

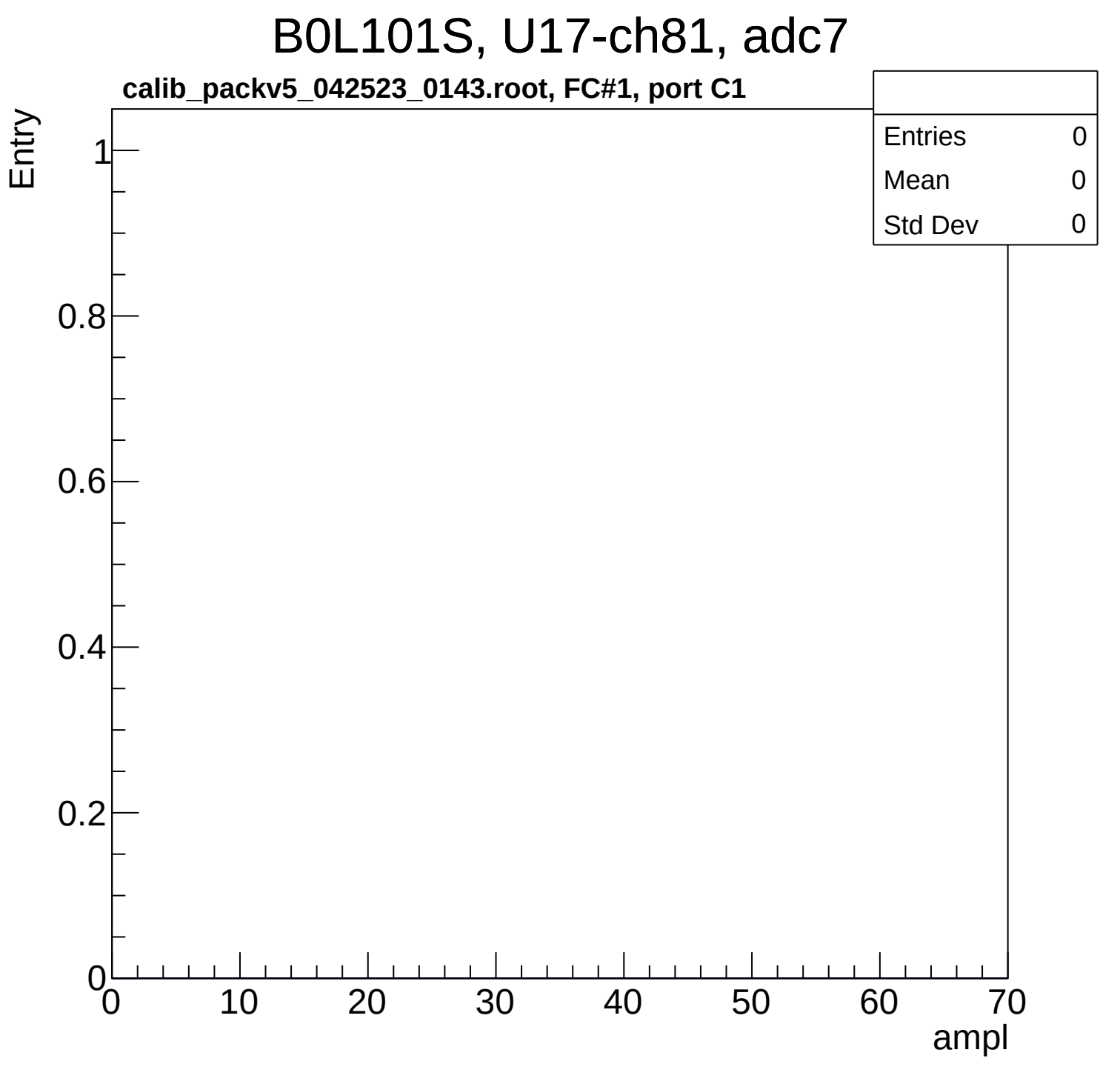

B0L101S, U17-ch81, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 42.8
Std Dev 3.61
Gaus mean : 43.2281
Gaus Width: 3.8104
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

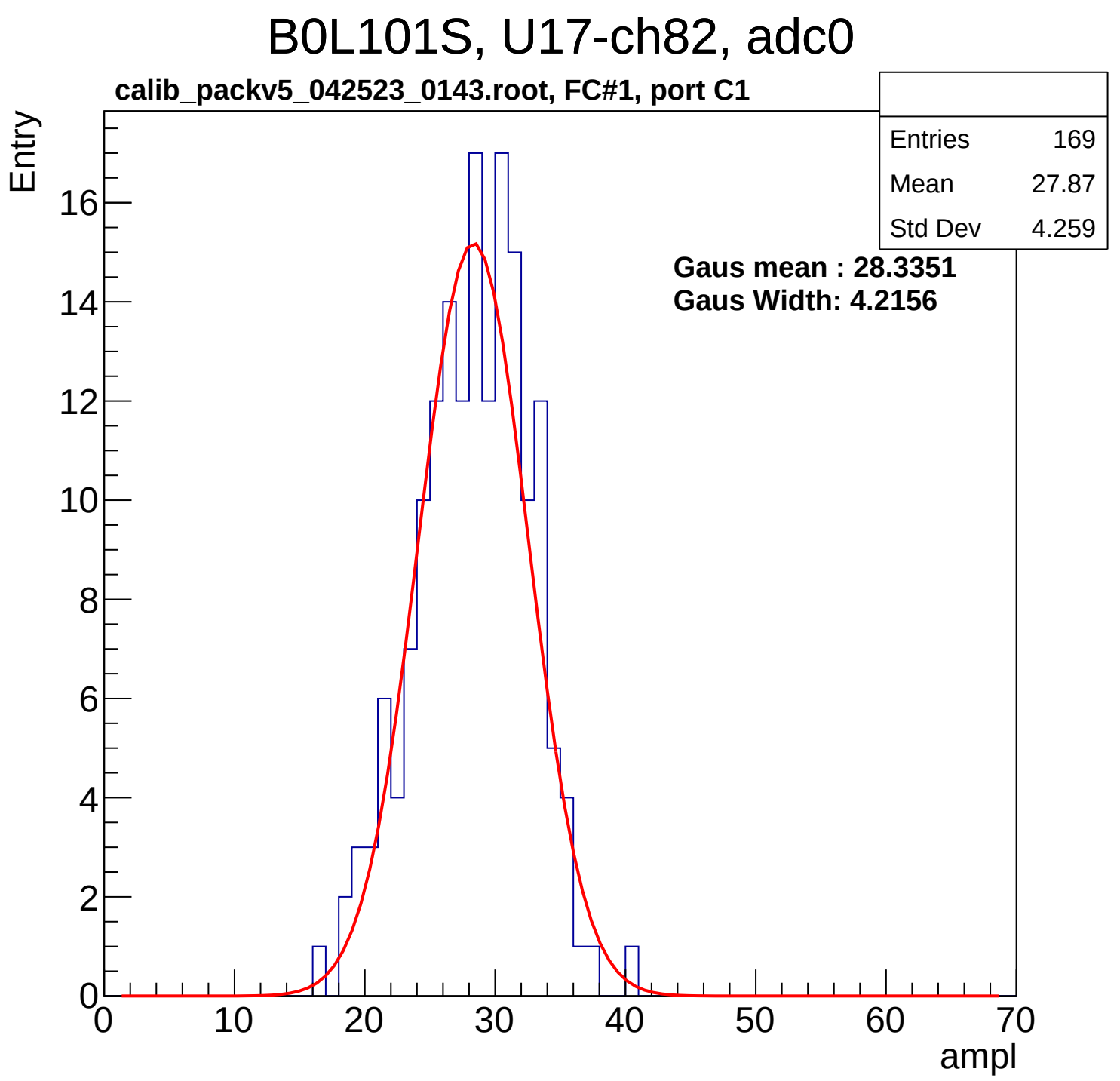

B0L101S, U17-ch81, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 49.97
Std Dev 3.759
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

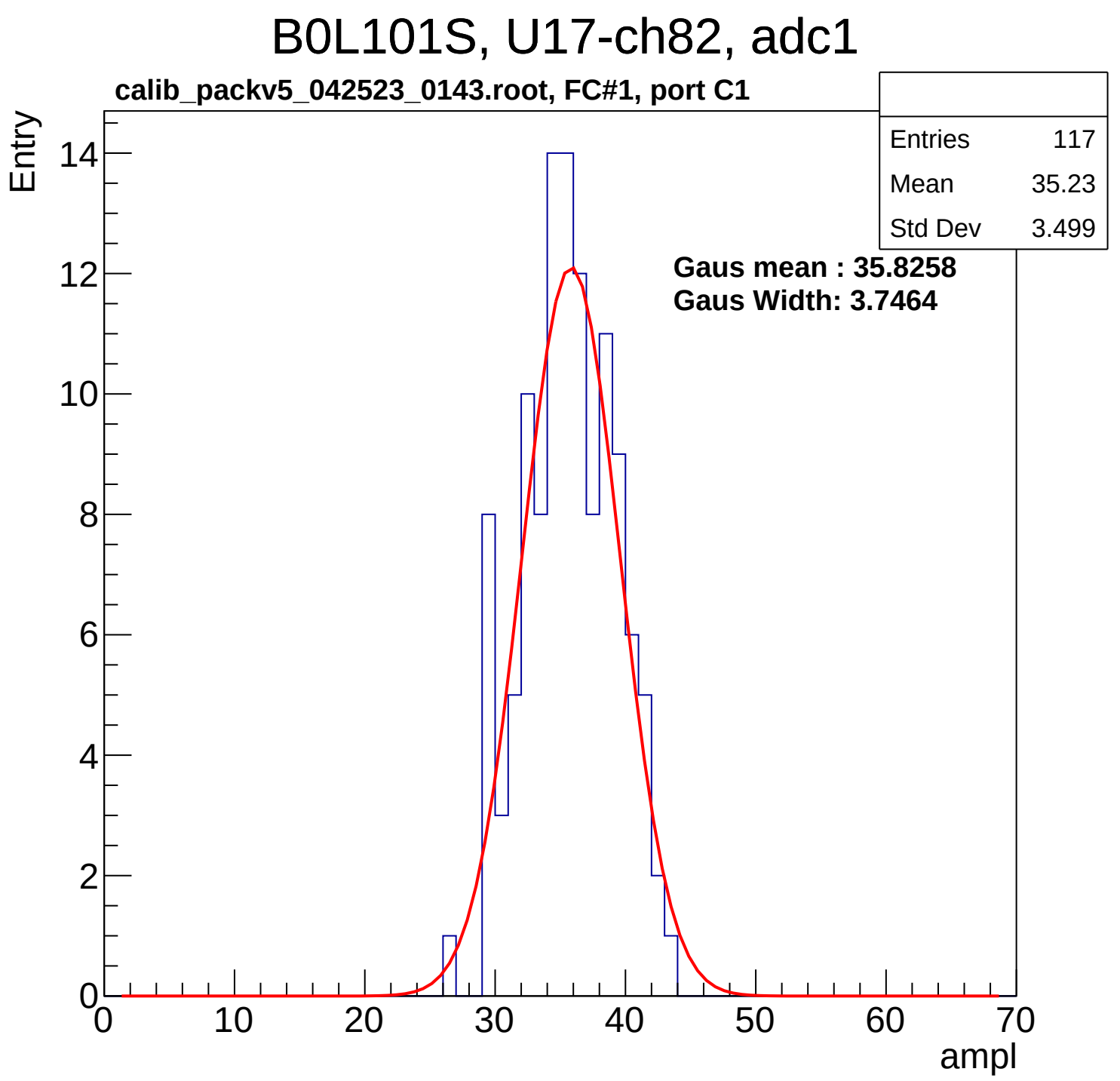

B0L101S, U17-ch81, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 56.42
Std Dev 3.468
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

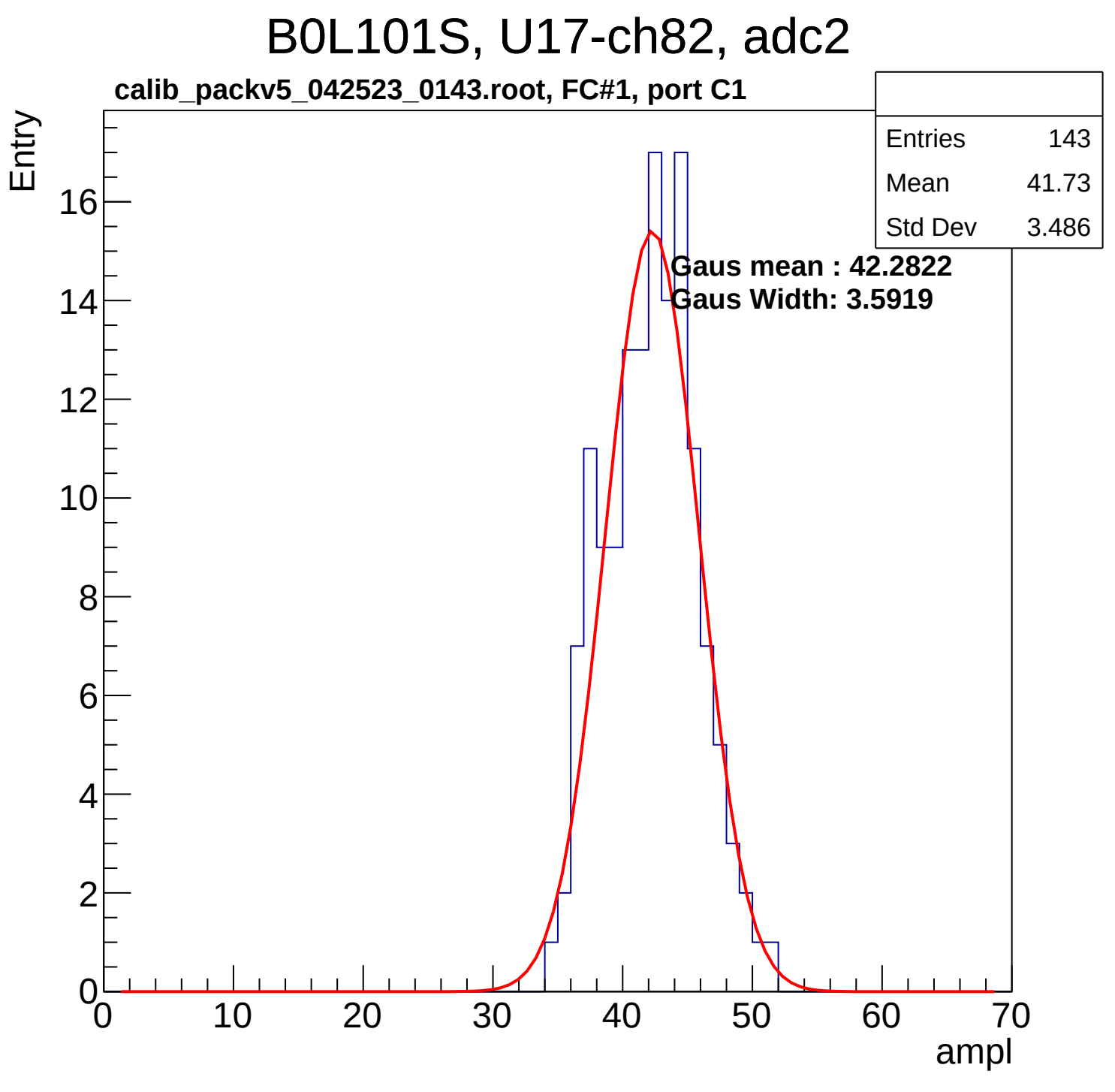

B0L101S, U17-ch81, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 66
Mean 58.24
Std Dev 10.54
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

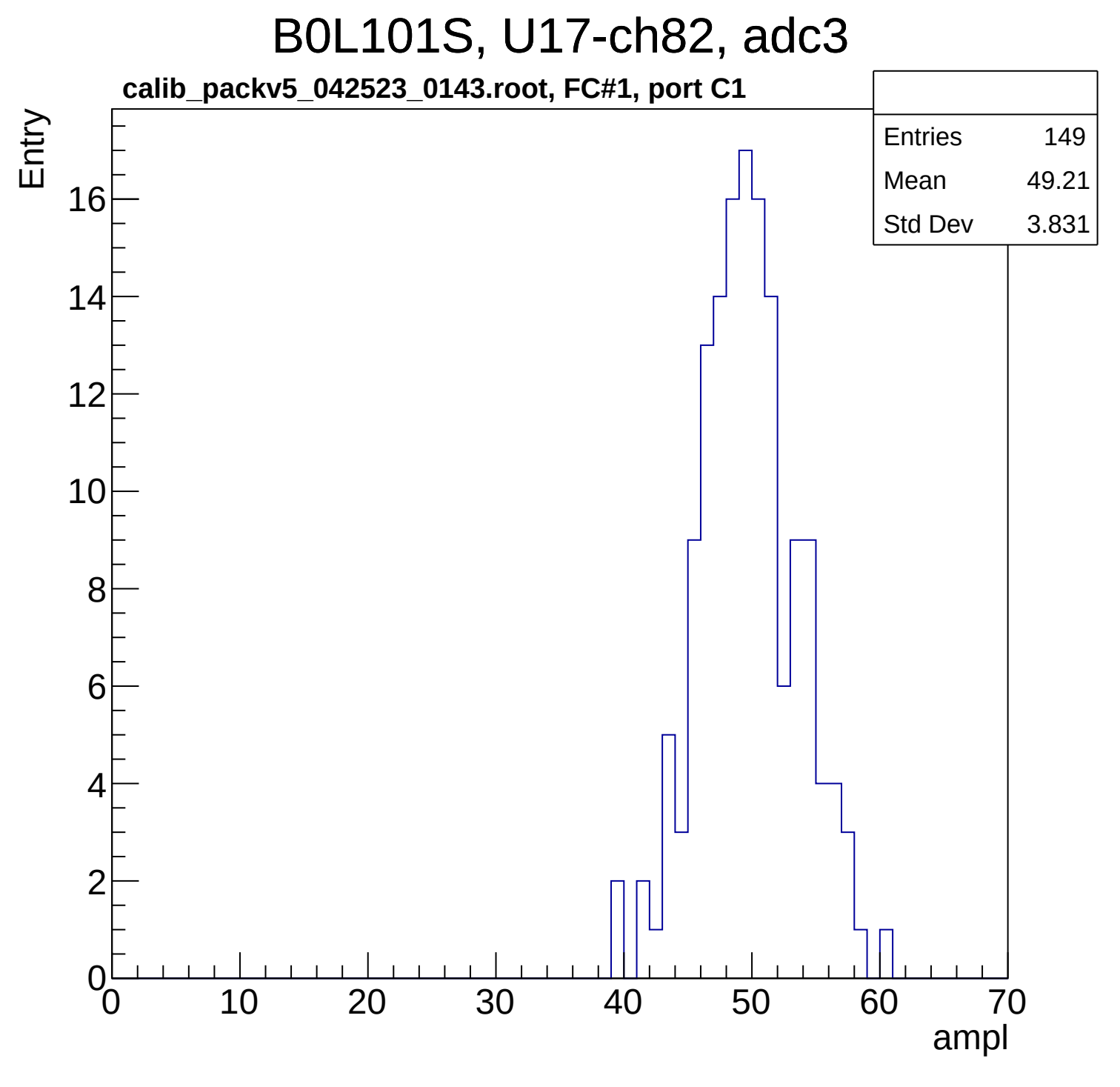

B0L101S, U17-ch81, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 62.21
Std Dev 0.8601
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

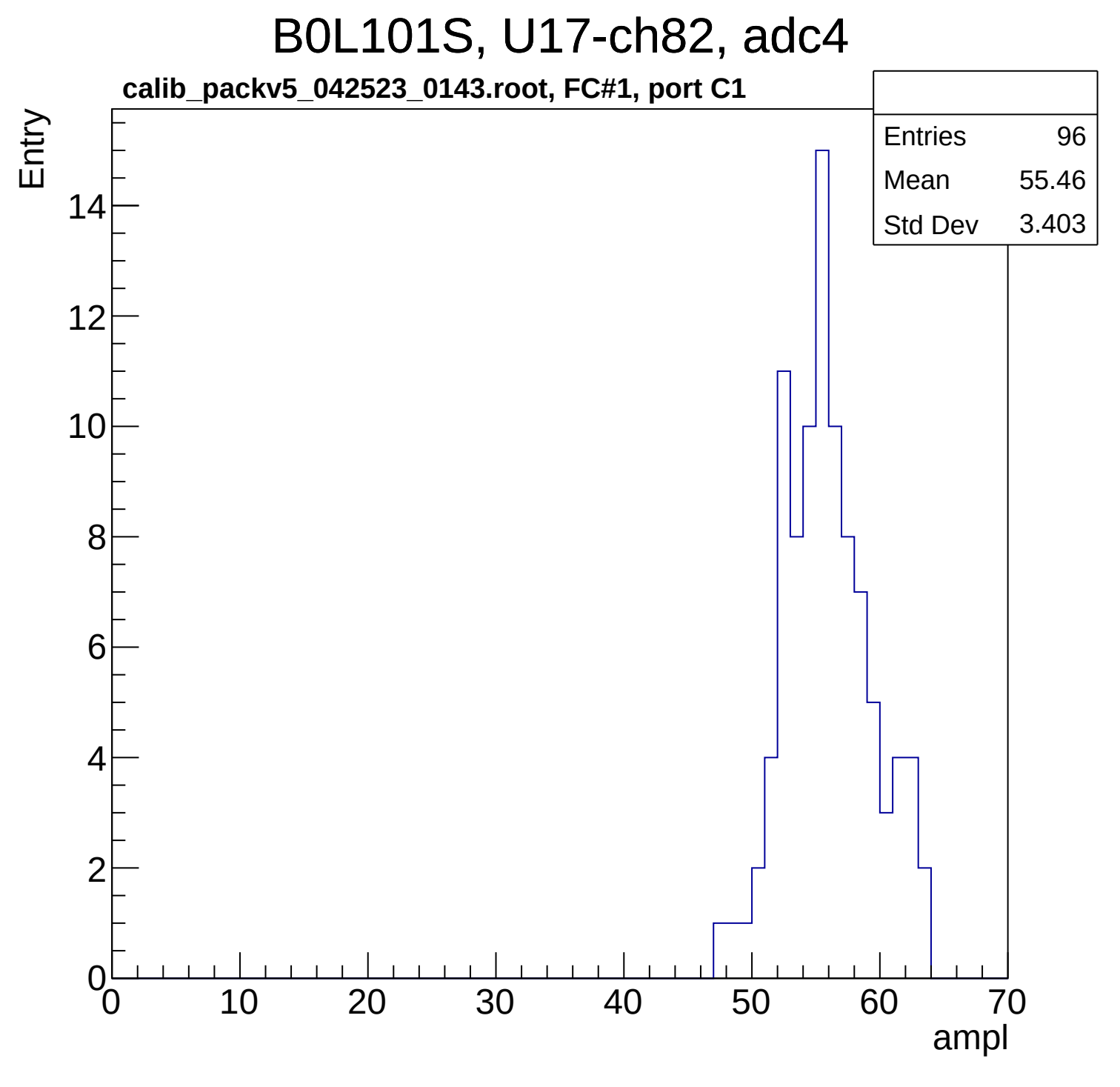

B0L101S, U17-ch81, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

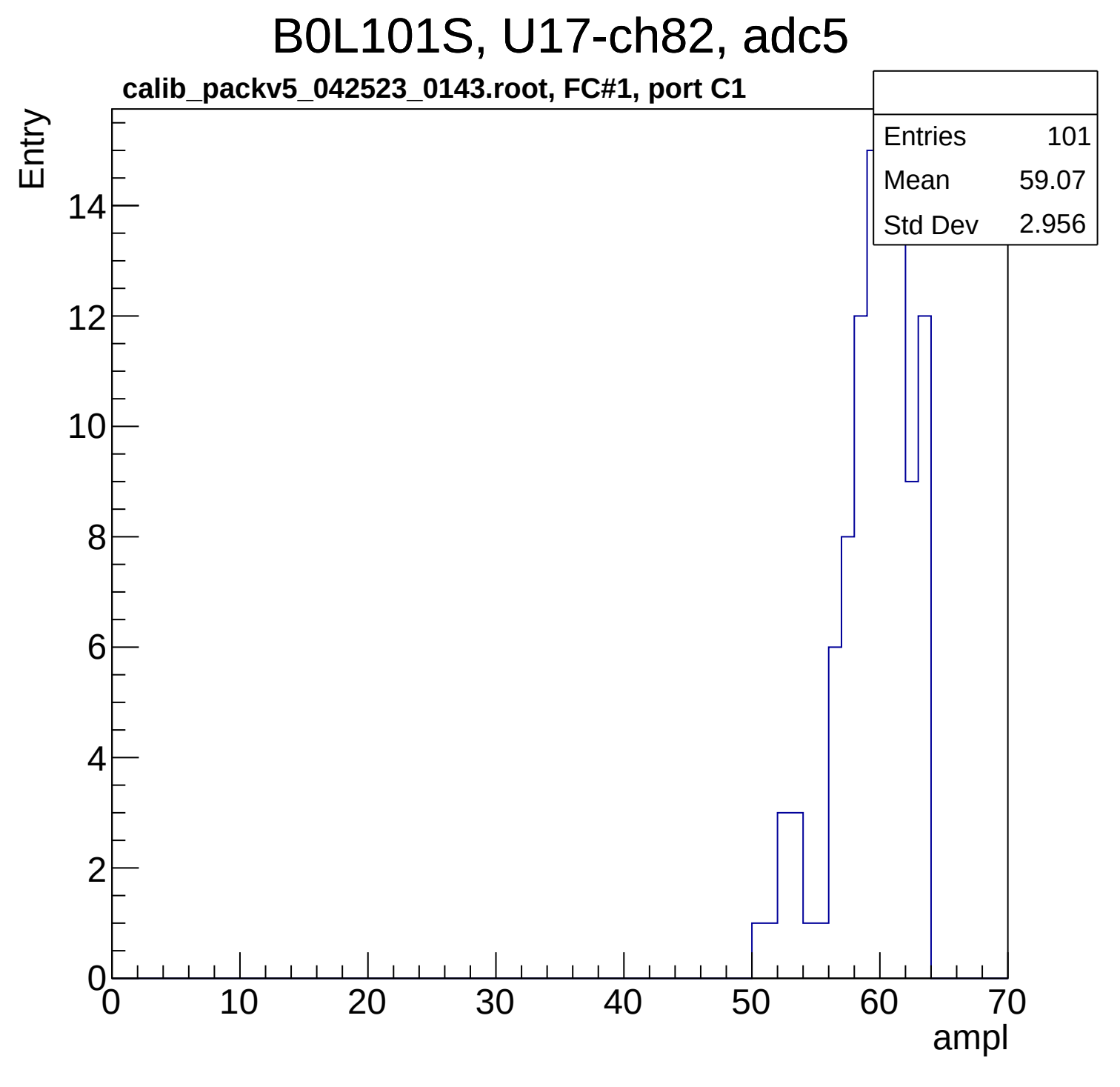

B0L101S, U17-ch82, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 169
Mean 27.87
Std Dev 4.259
Gaus mean : 28.3351
Gaus Width: 4.2156
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

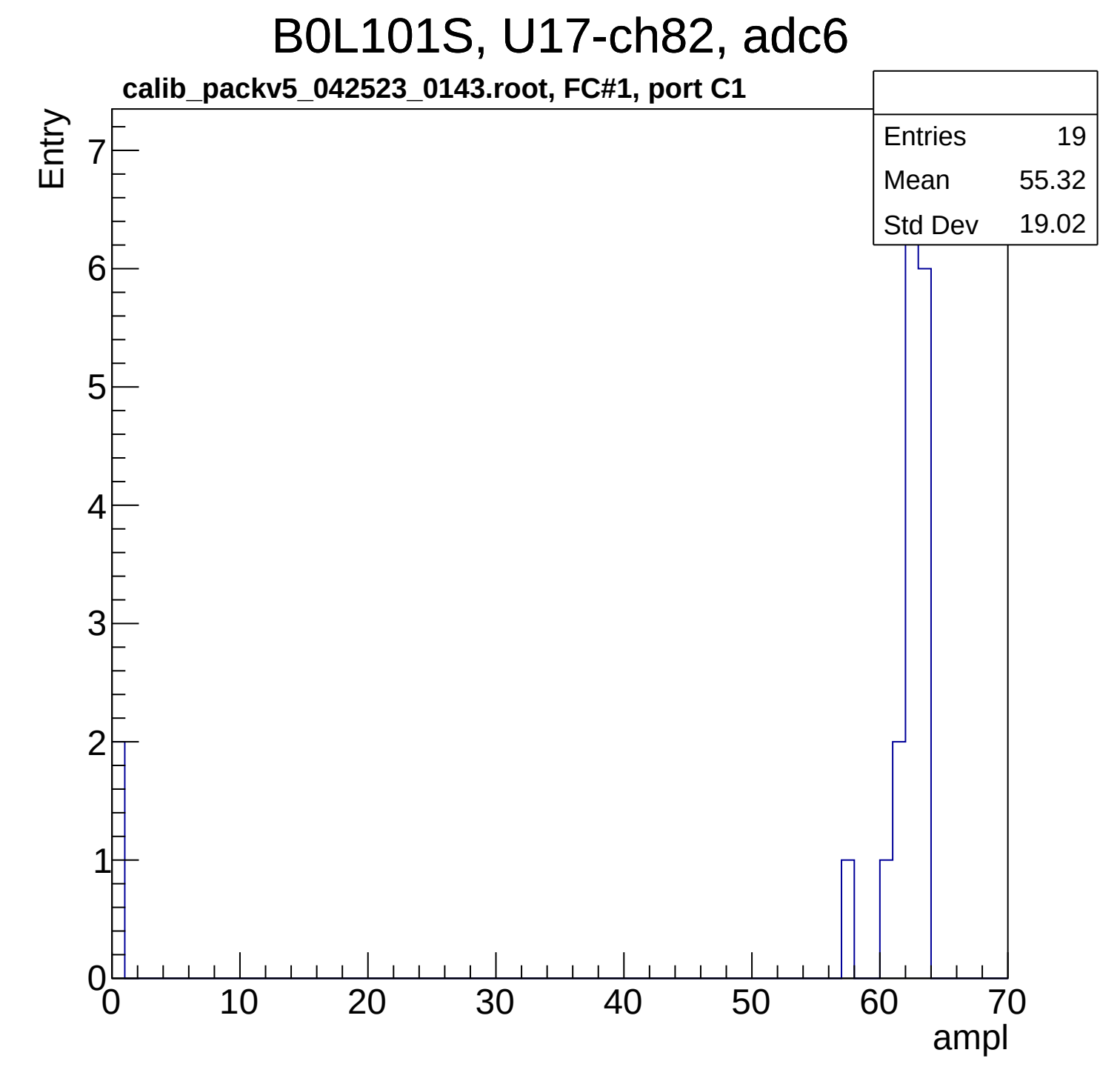

B0L101S, U17-ch82, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 35.23
Std Dev 3.499
Gaus mean : 35.8258
Gaus Width: 3.7464
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

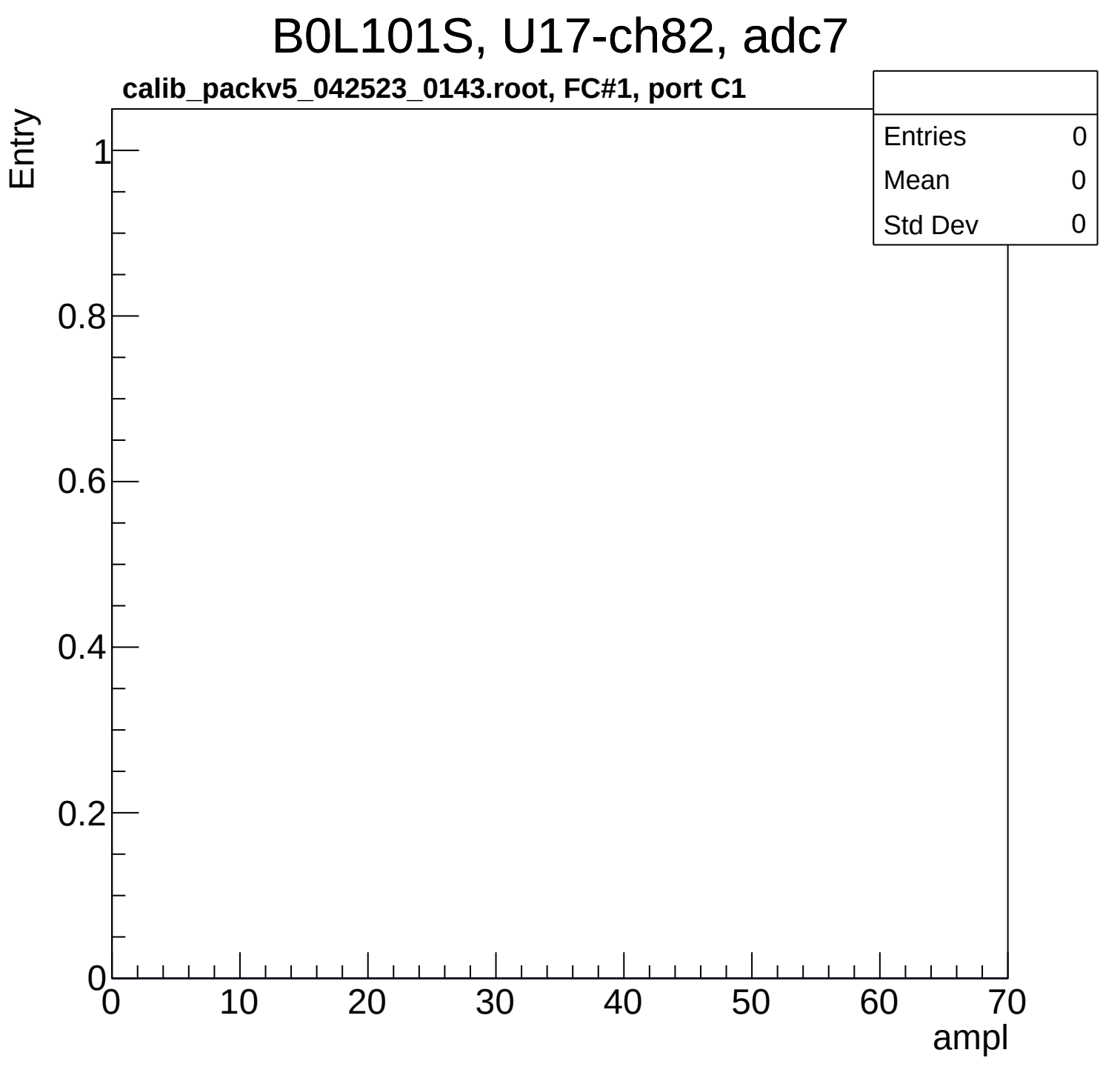

B0L101S, U17-ch82, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 41.73
Std Dev 3.486
Gaus mean : 42.2822
Gaus Width: 3.5919
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

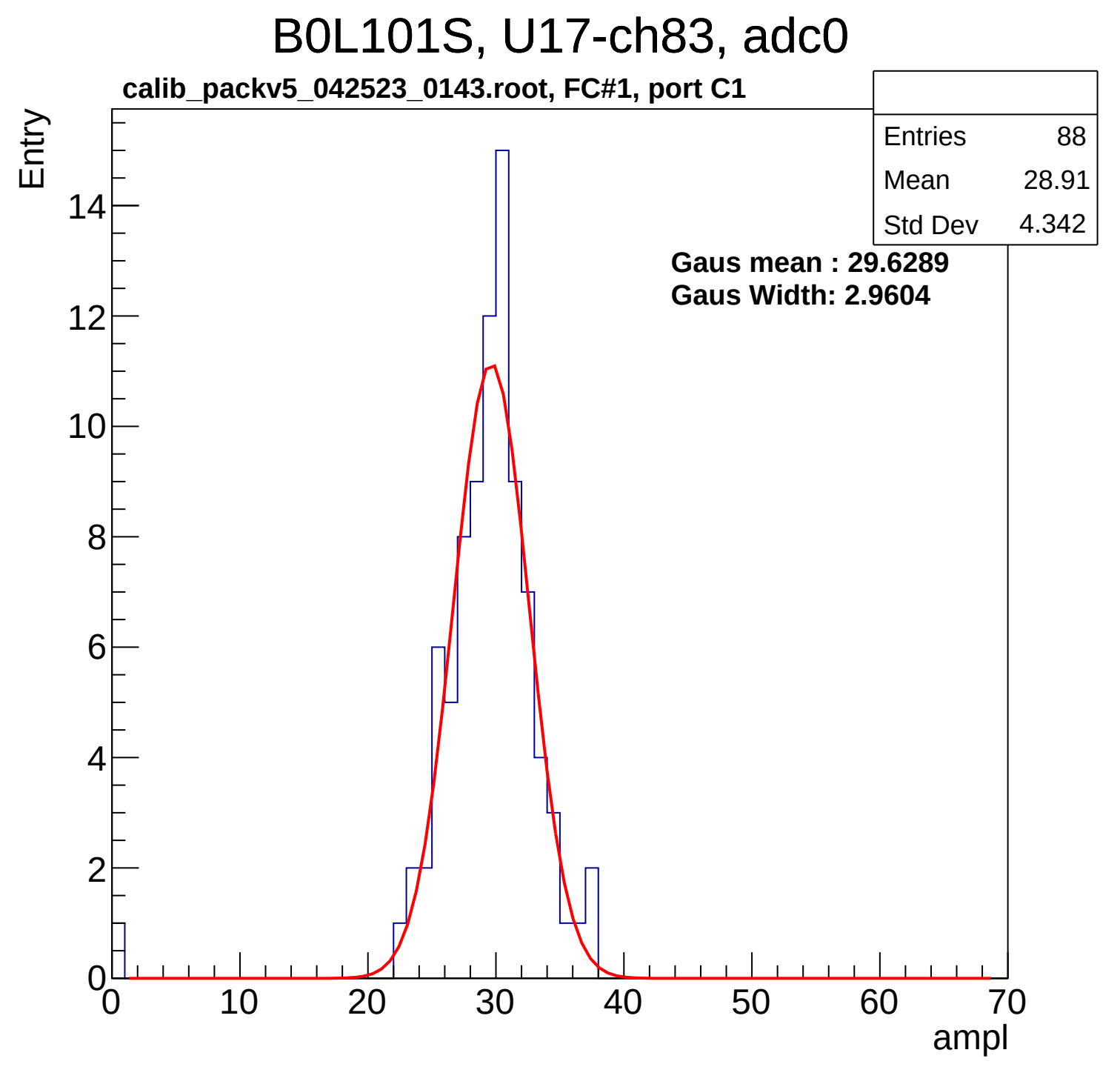

B0L101S, U17-ch82, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 49.21
Std Dev 3.831
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

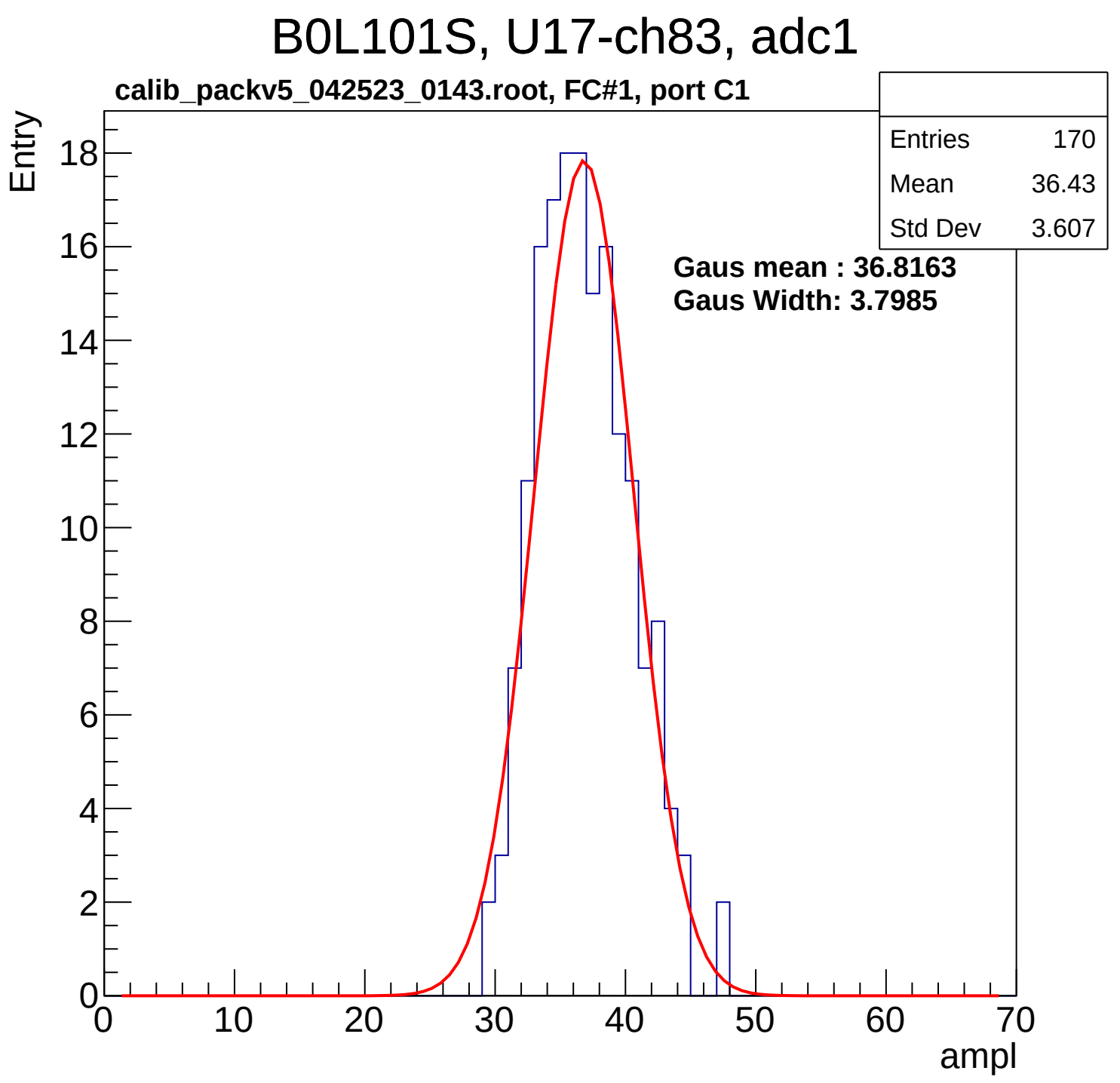

B0L101S, U17-ch82, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 55.46
Std Dev 3.403
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

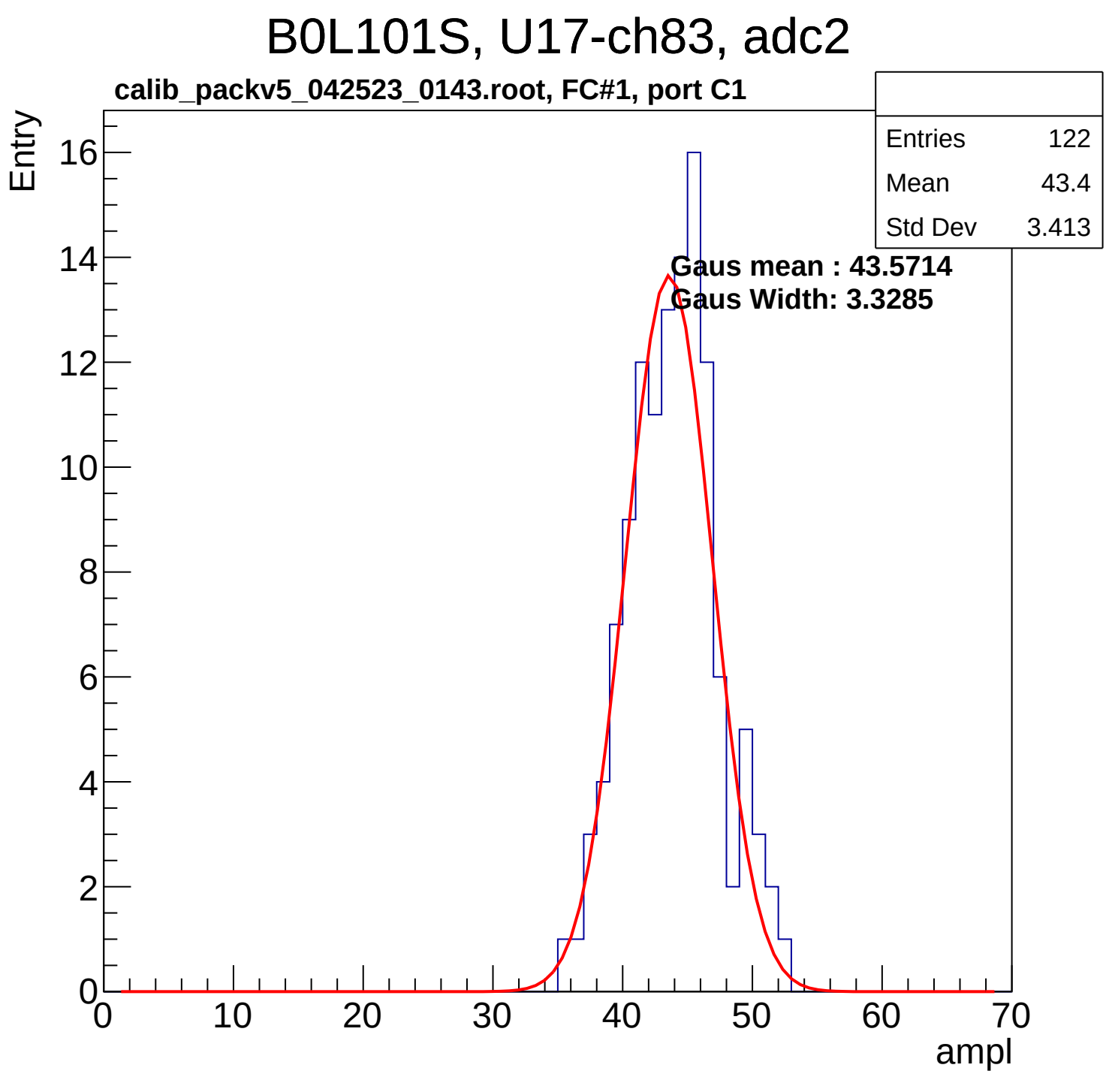

B0L101S, U17-ch82, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 59.07
Std Dev 2.956
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

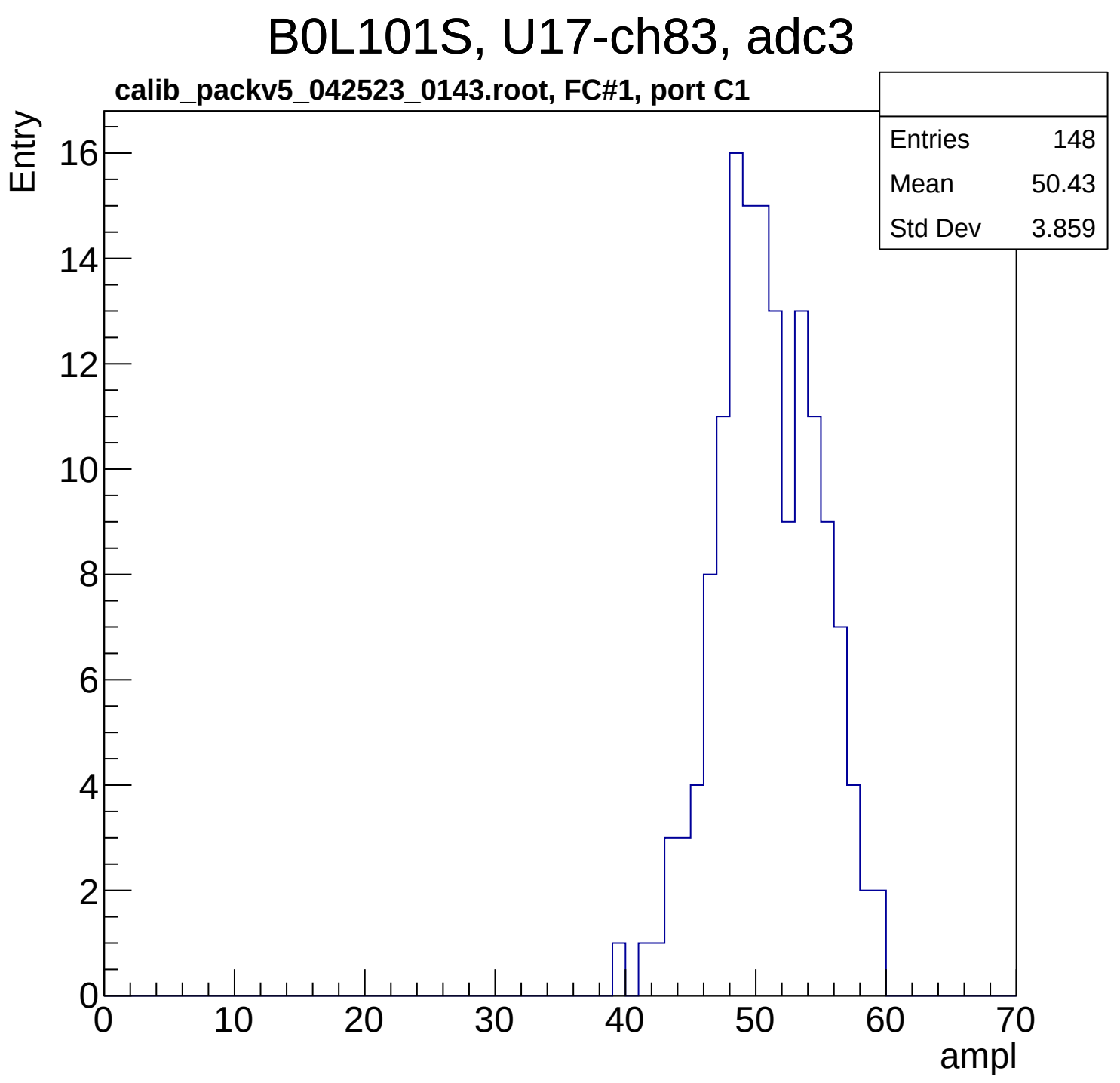

B0L101S, U17-ch82, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 19
Mean 55.32
Std Dev 19.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

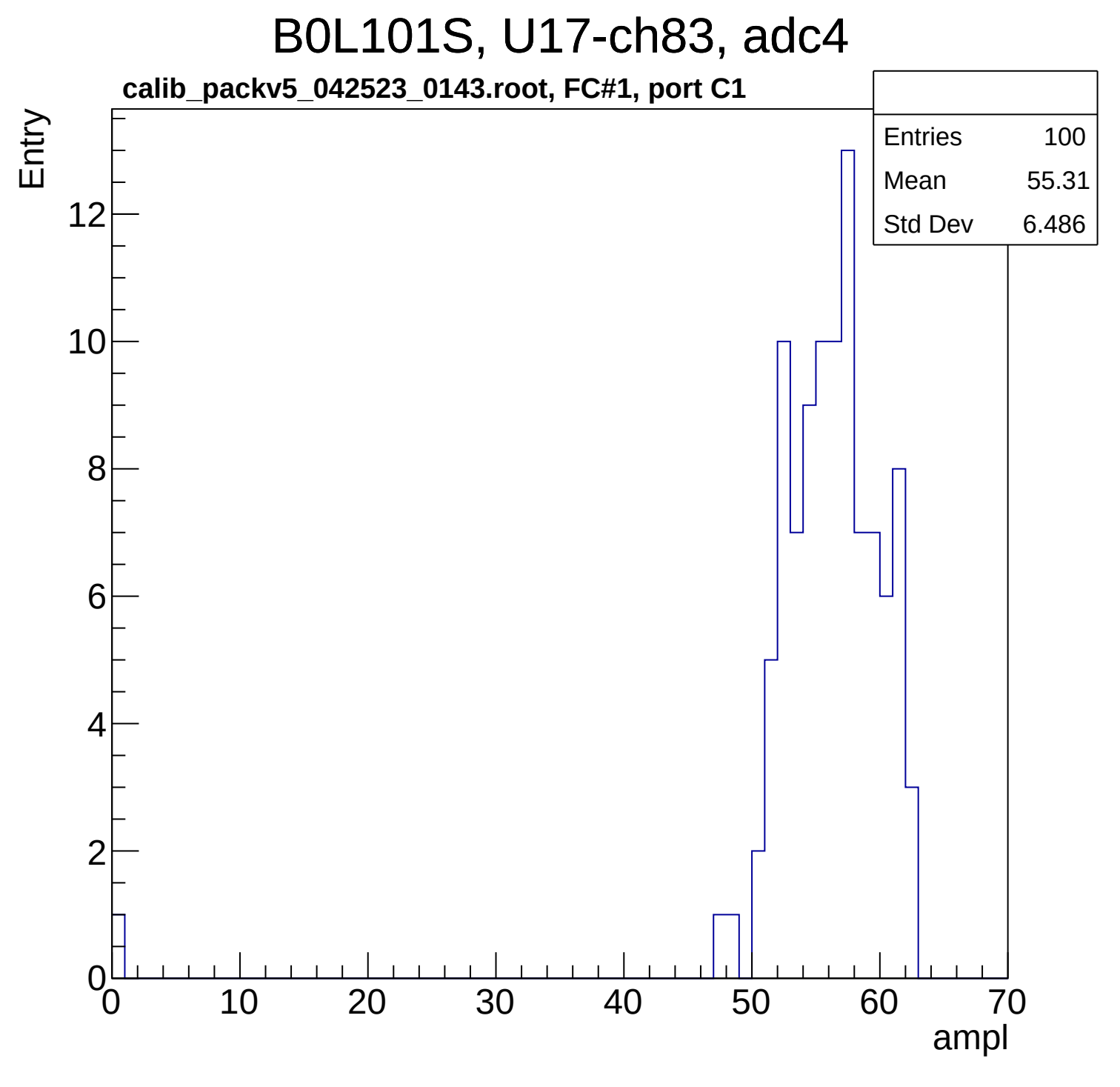

B0L101S, U17-ch82, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

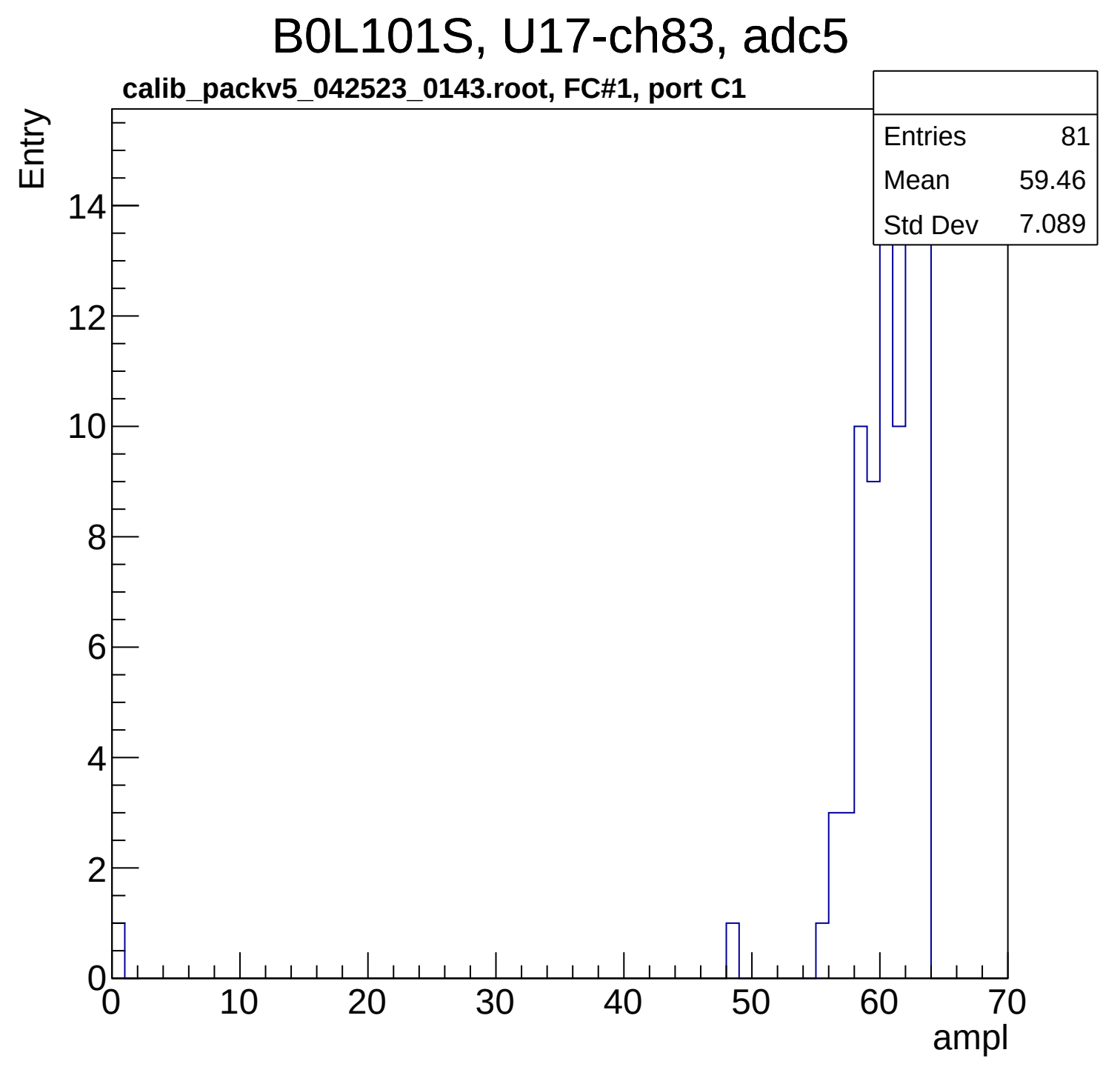

B0L101S, U17-ch83, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 88
Mean 28.91
Std Dev 4.342
Gaus mean : 29.6289
Gaus Width: 2.9604
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

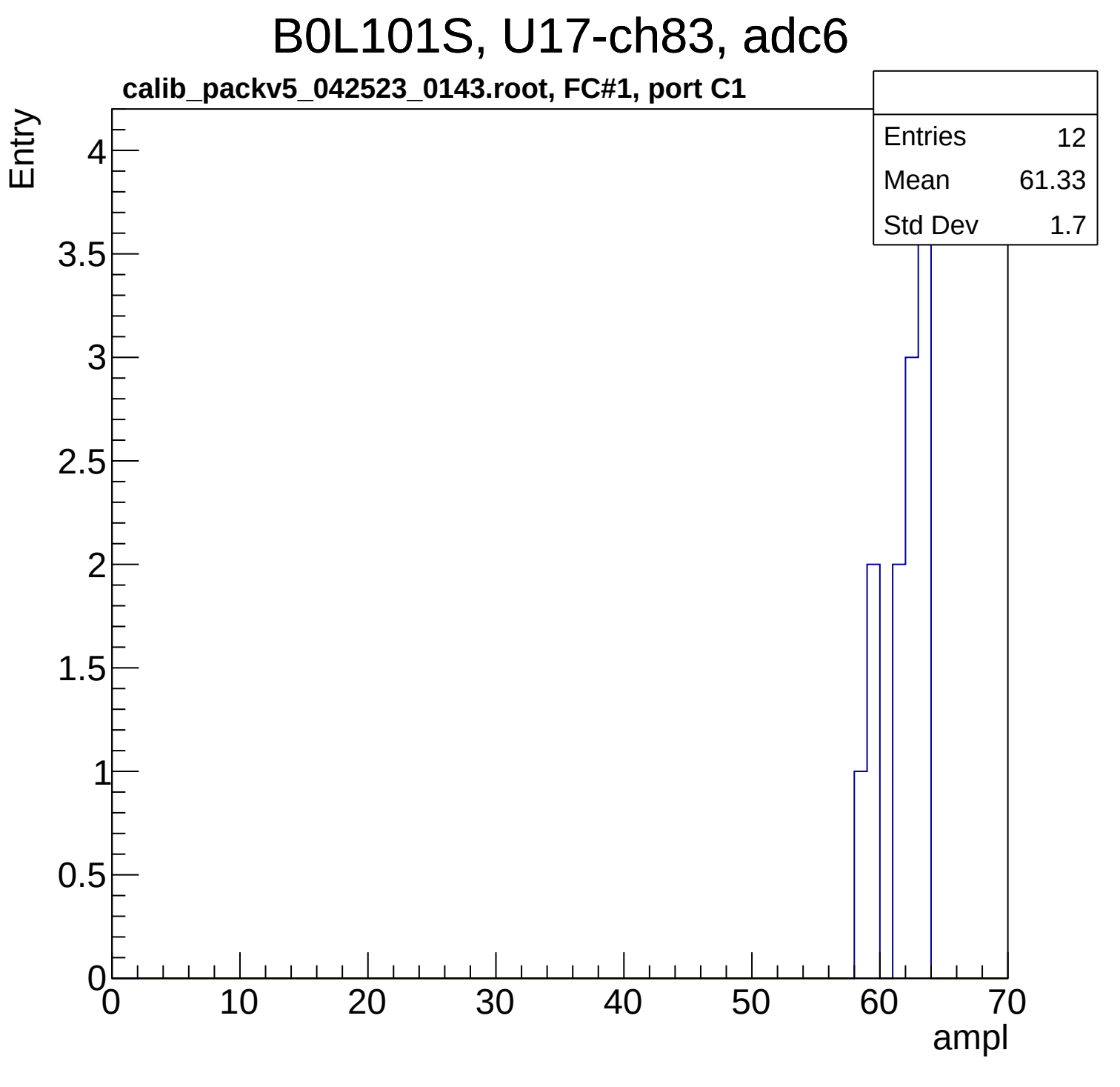

B0L101S, U17-ch83, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 170
Mean 36.43
Std Dev 3.607
Gaus mean : 36.8163
Gaus Width: 3.7985
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

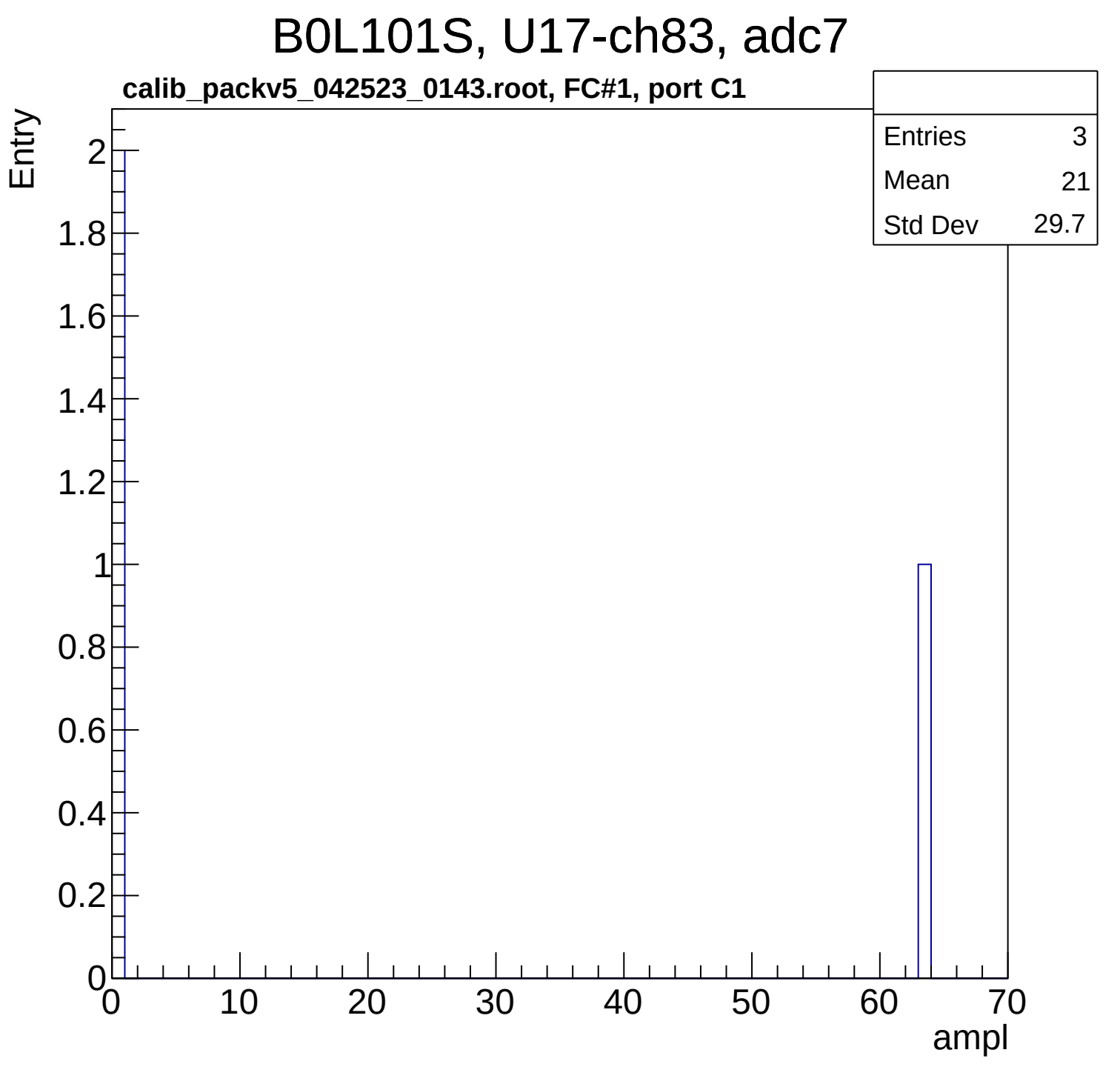

B0L101S, U17-ch83, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 43.4
Std Dev 3.413
Gaus mean : 43.5714
Gaus Width: 3.3285
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

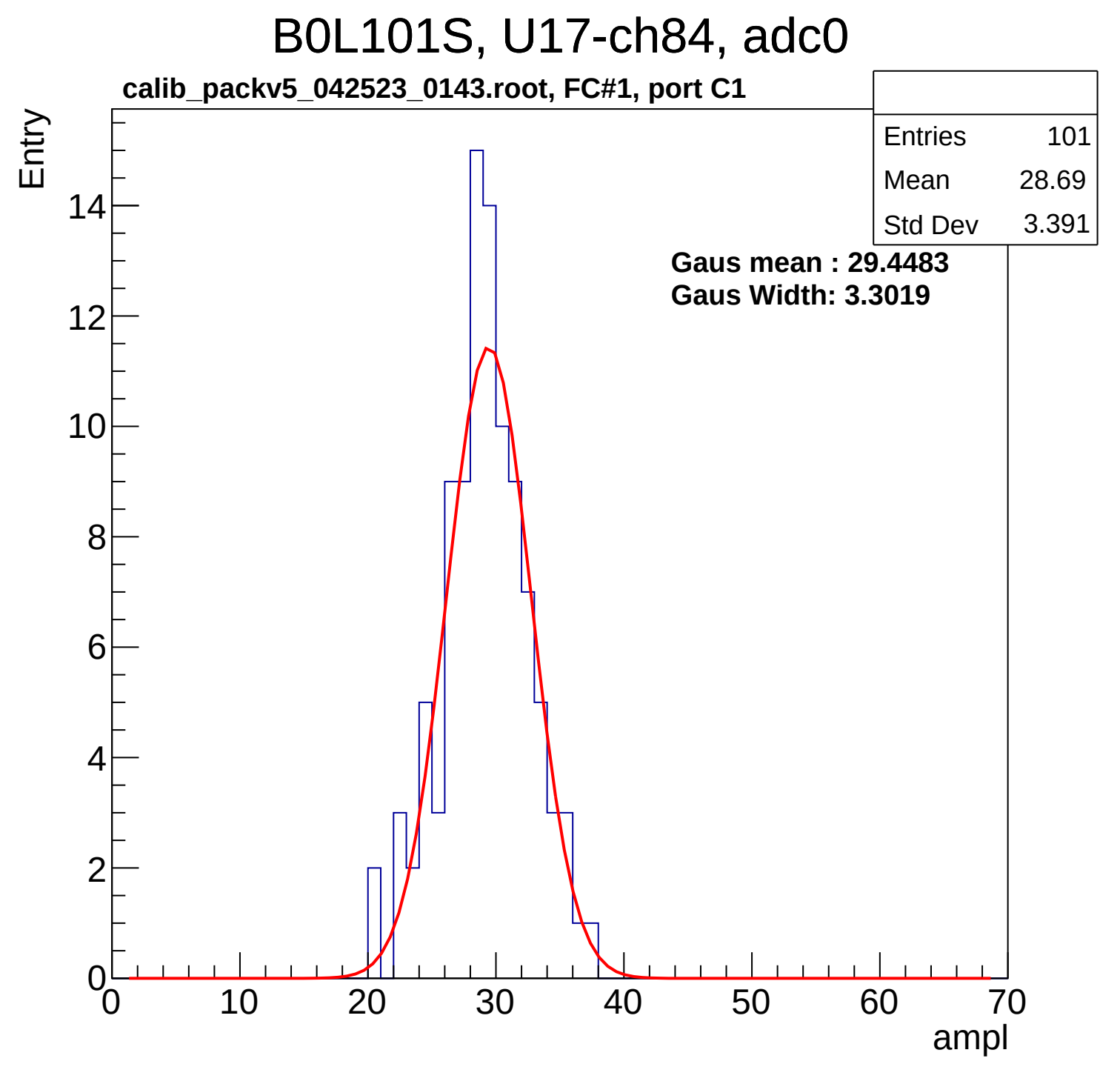

B0L101S, U17-ch83, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 50.43
Std Dev 3.859
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

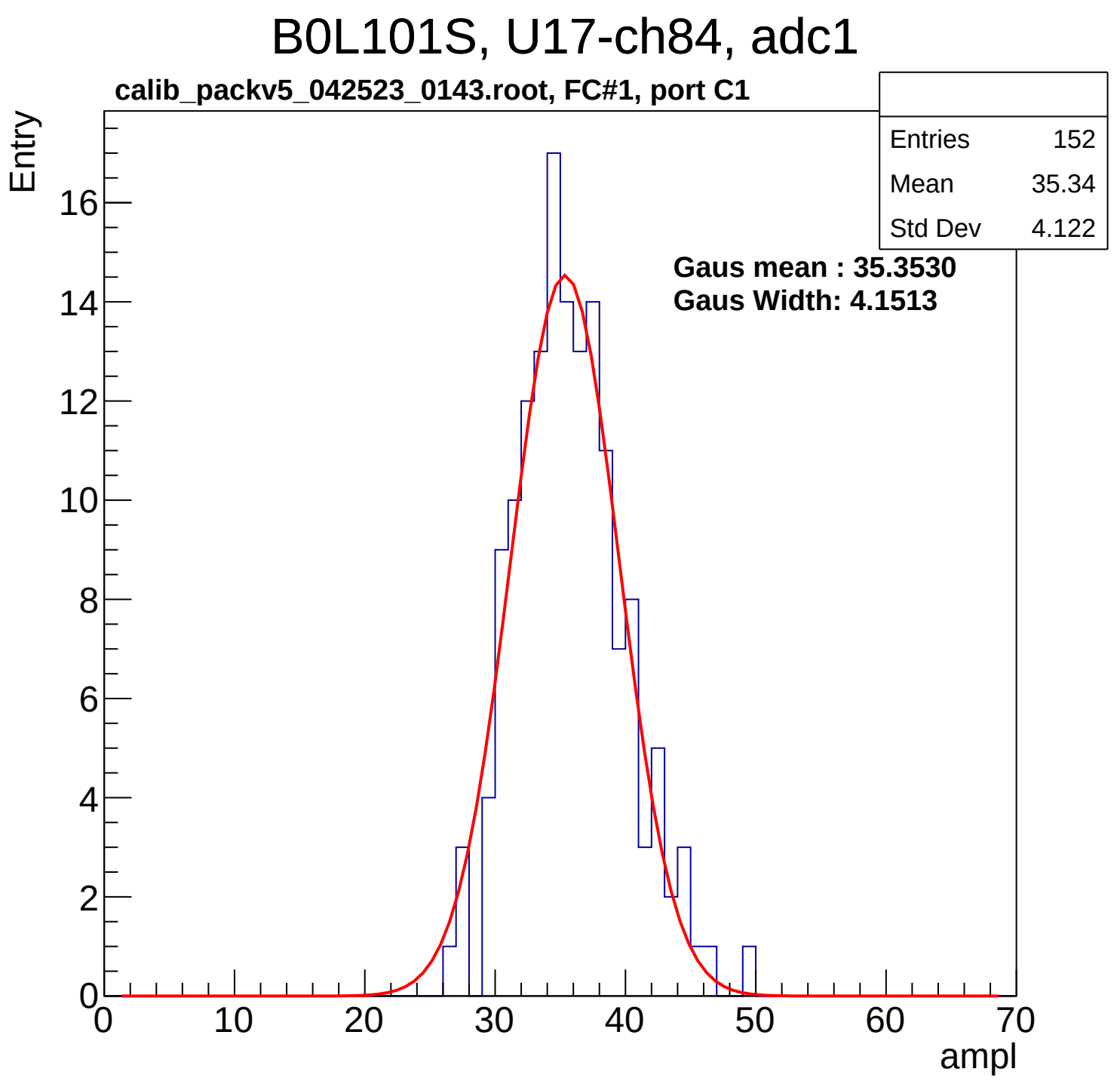

B0L101S, U17-ch83, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 55.31
Std Dev 6.486
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

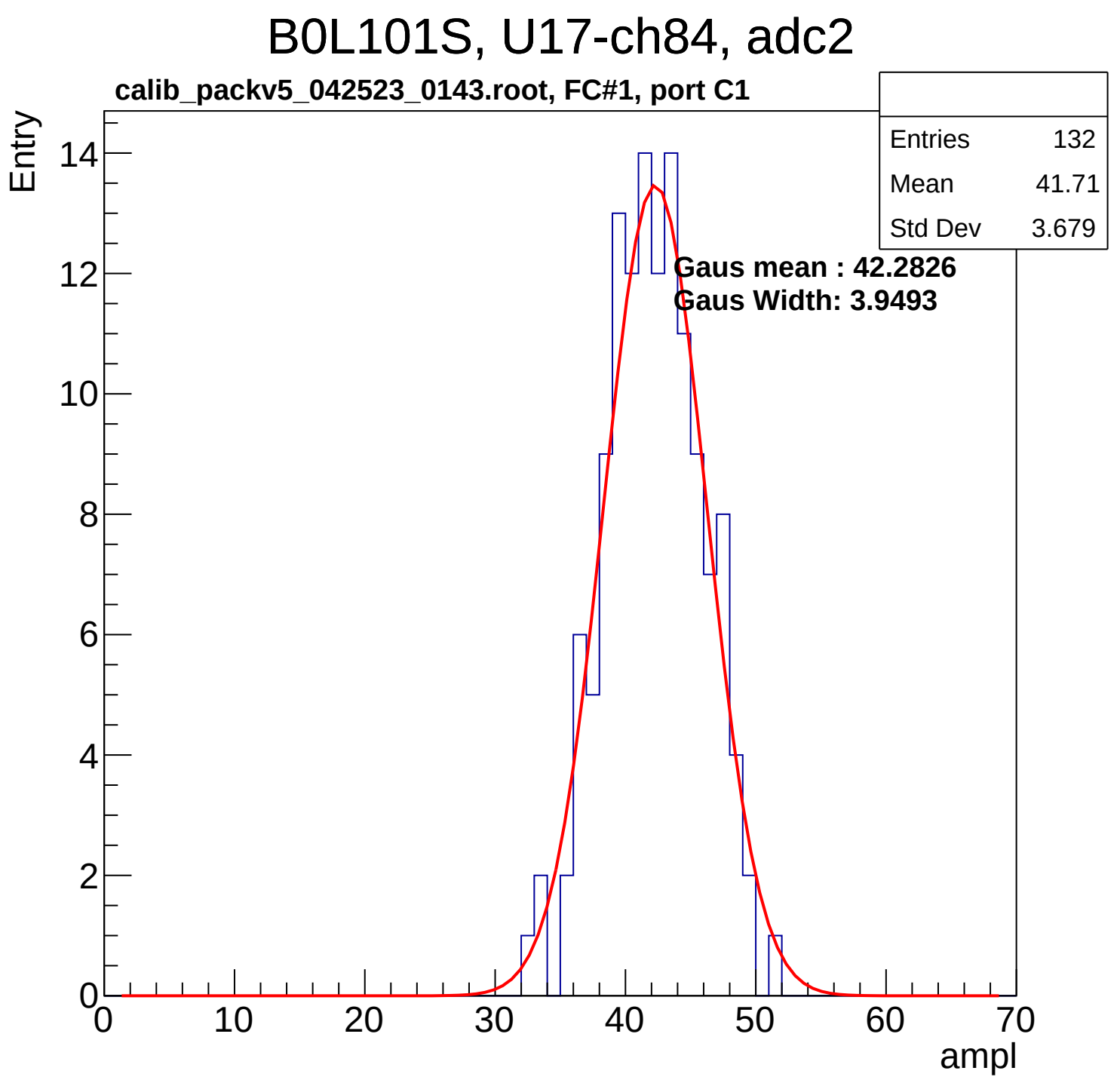

B0L101S, U17-ch83, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.46
Std Dev 7.089
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

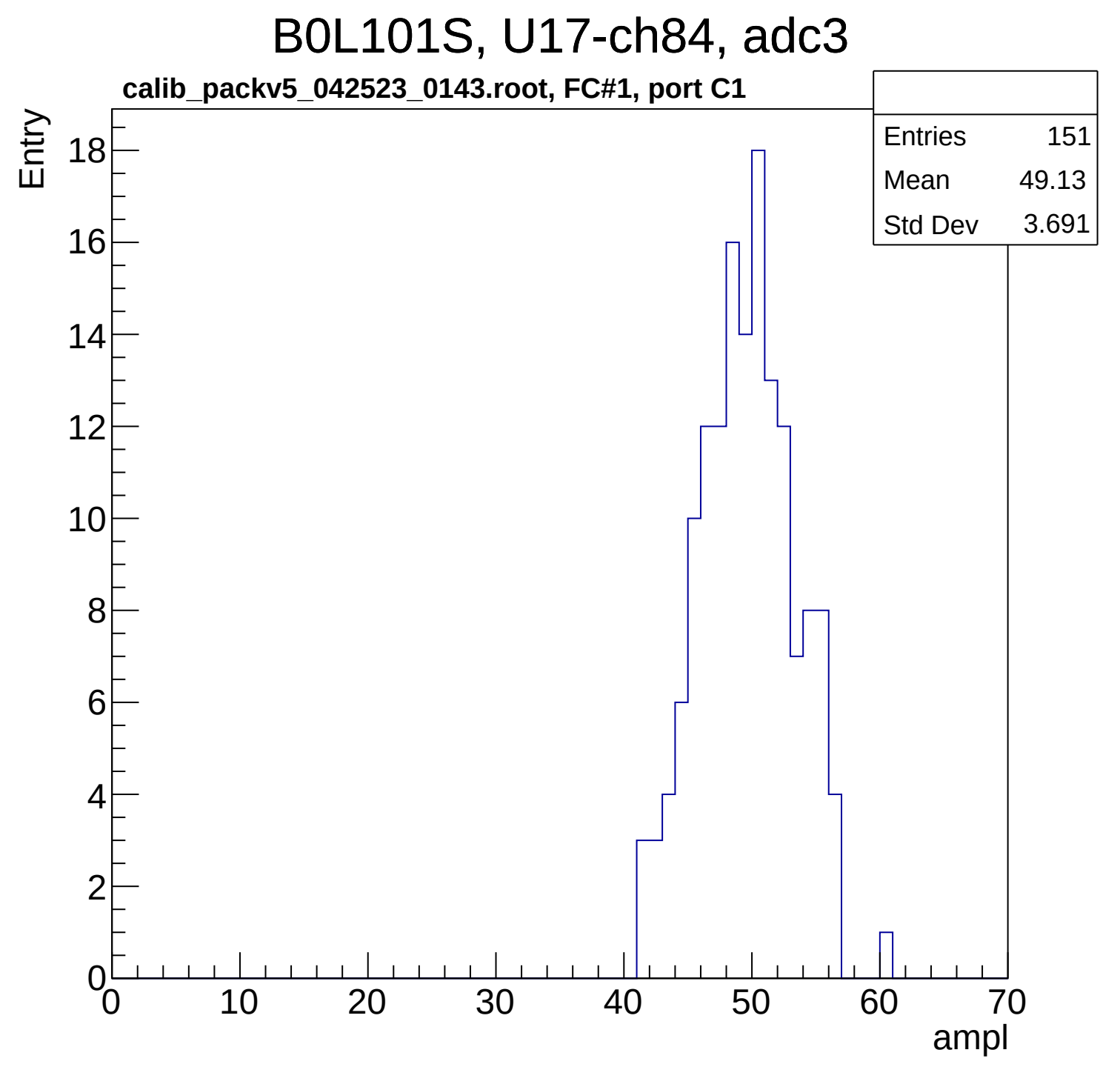

B0L101S, U17-ch83, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

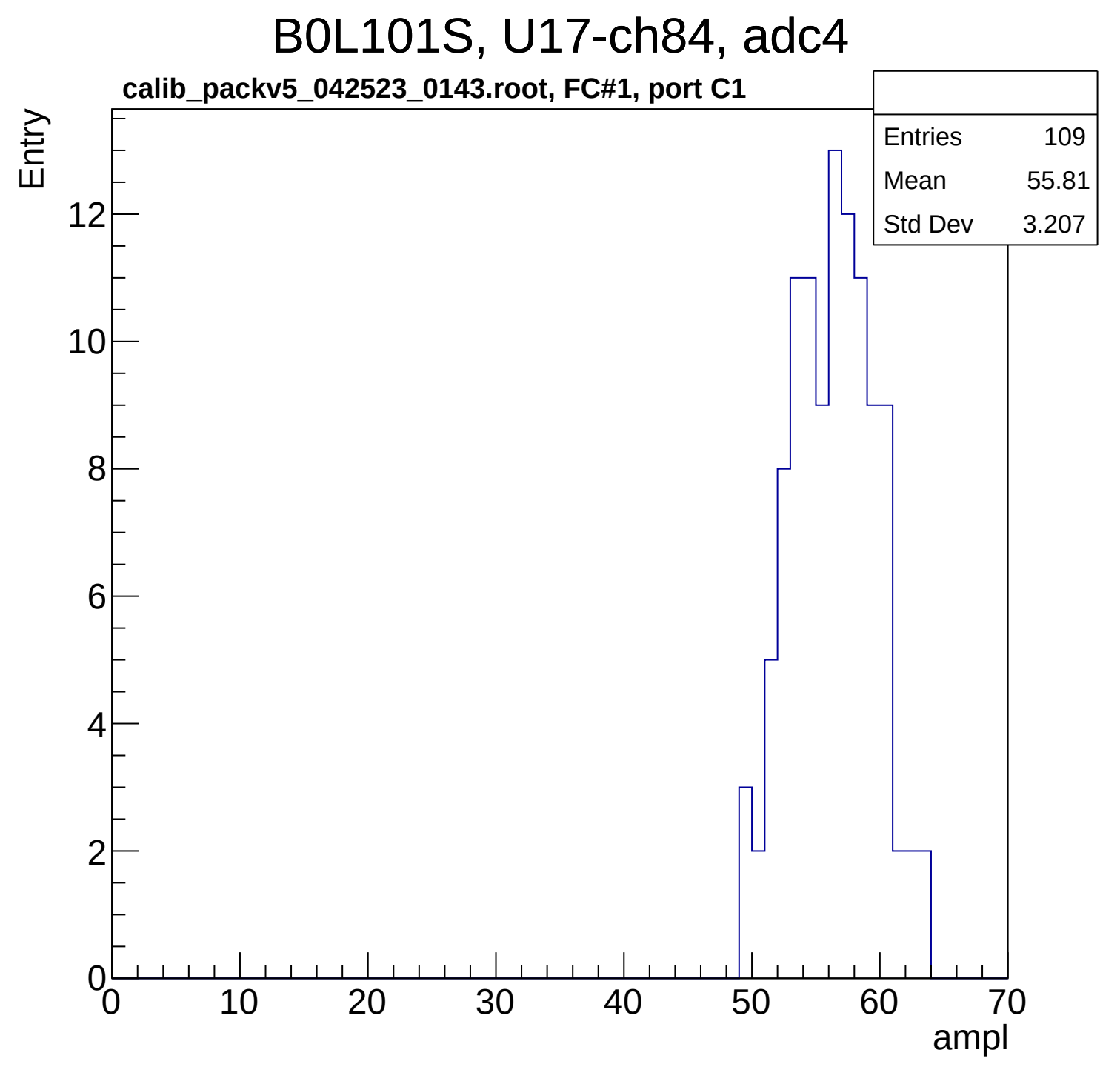

B0L101S, U17-ch83, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

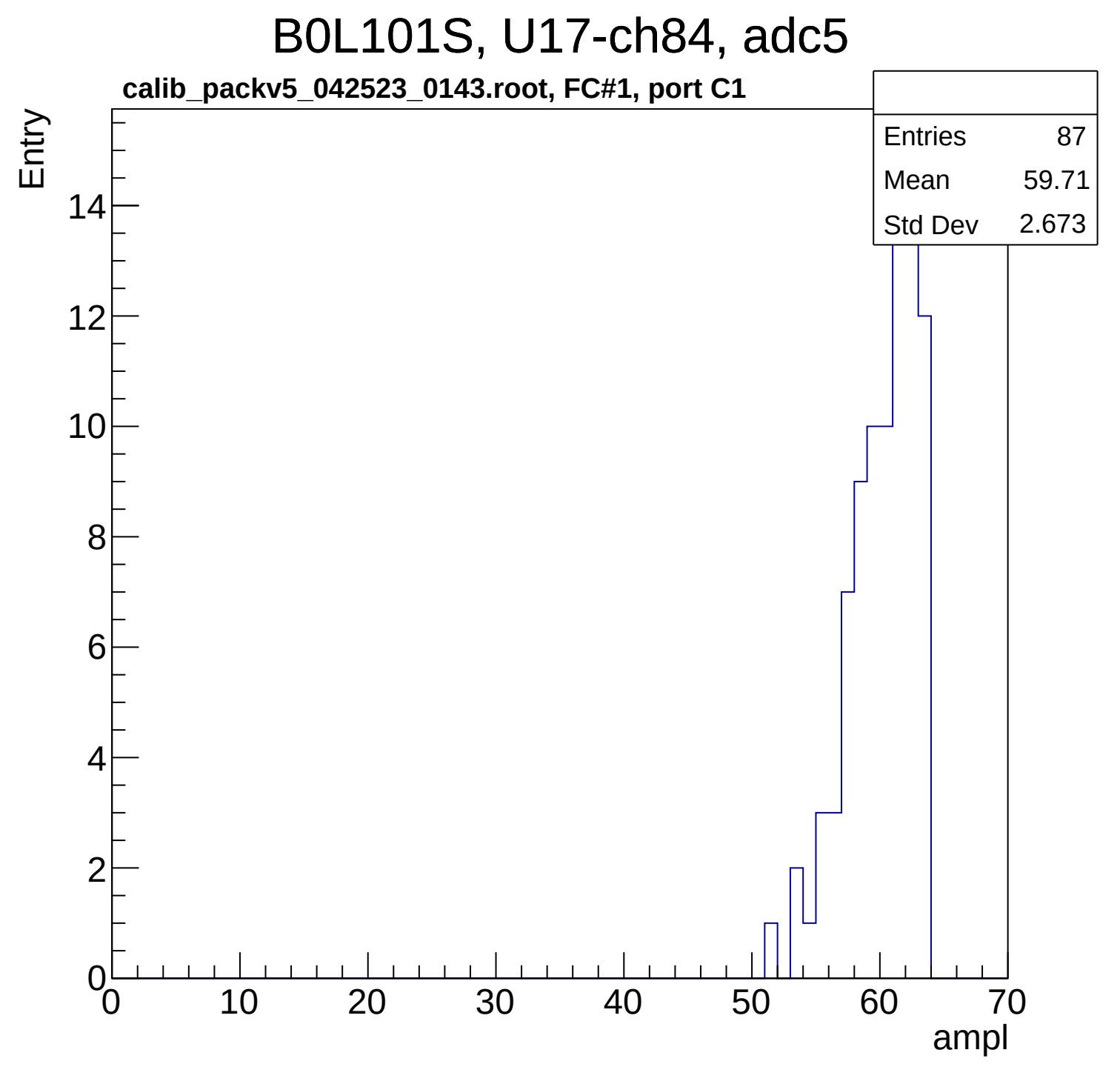

B0L101S, U17-ch84, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 28.69
Std Dev 3.391
Gaus mean : 29.4483
Gaus Width: 3.3019
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

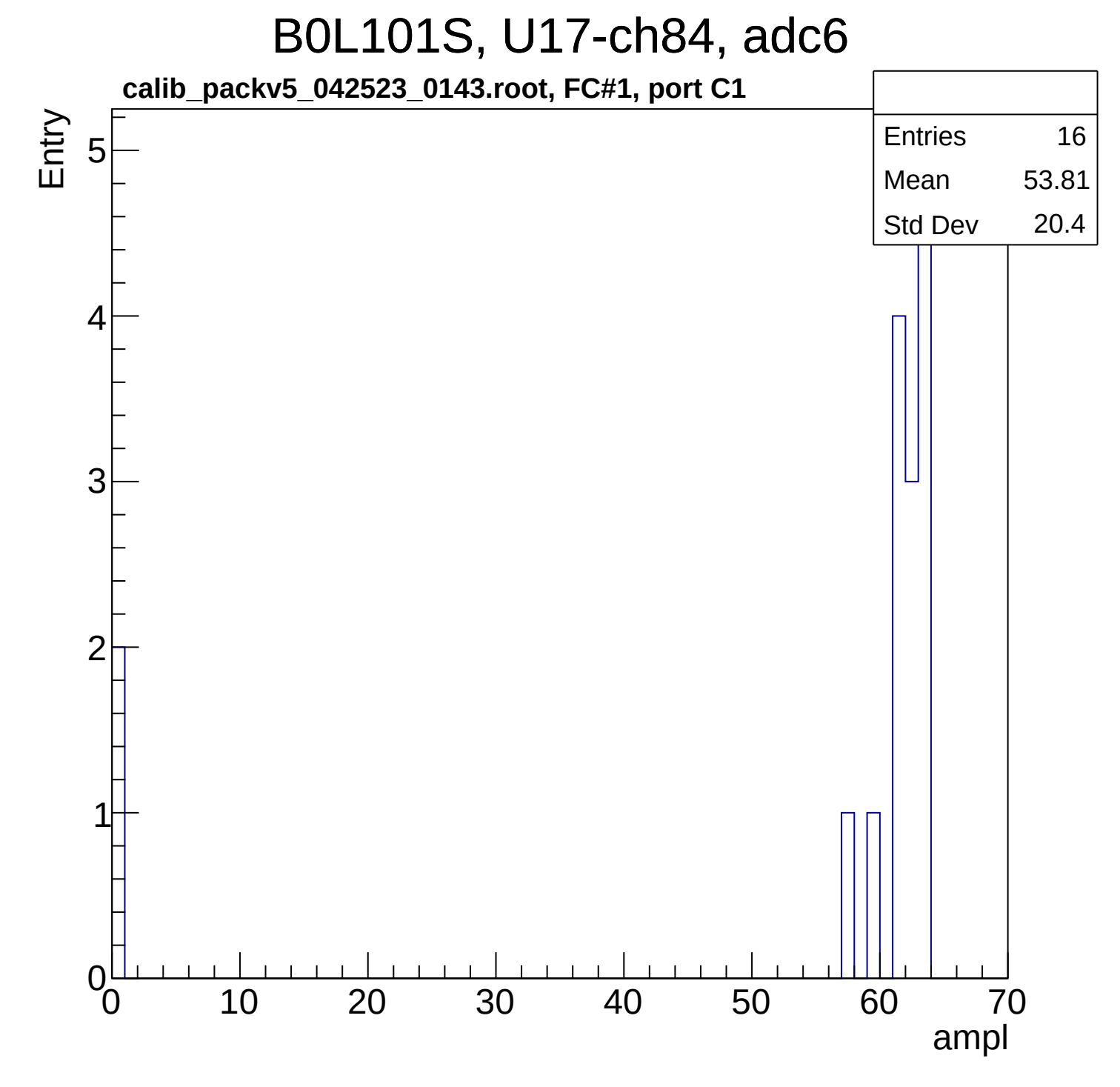

B0L101S, U17-ch84, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 152
Mean 35.34
Std Dev 4.122
Gaus mean : 35.3530
Gaus Width: 4.1513
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

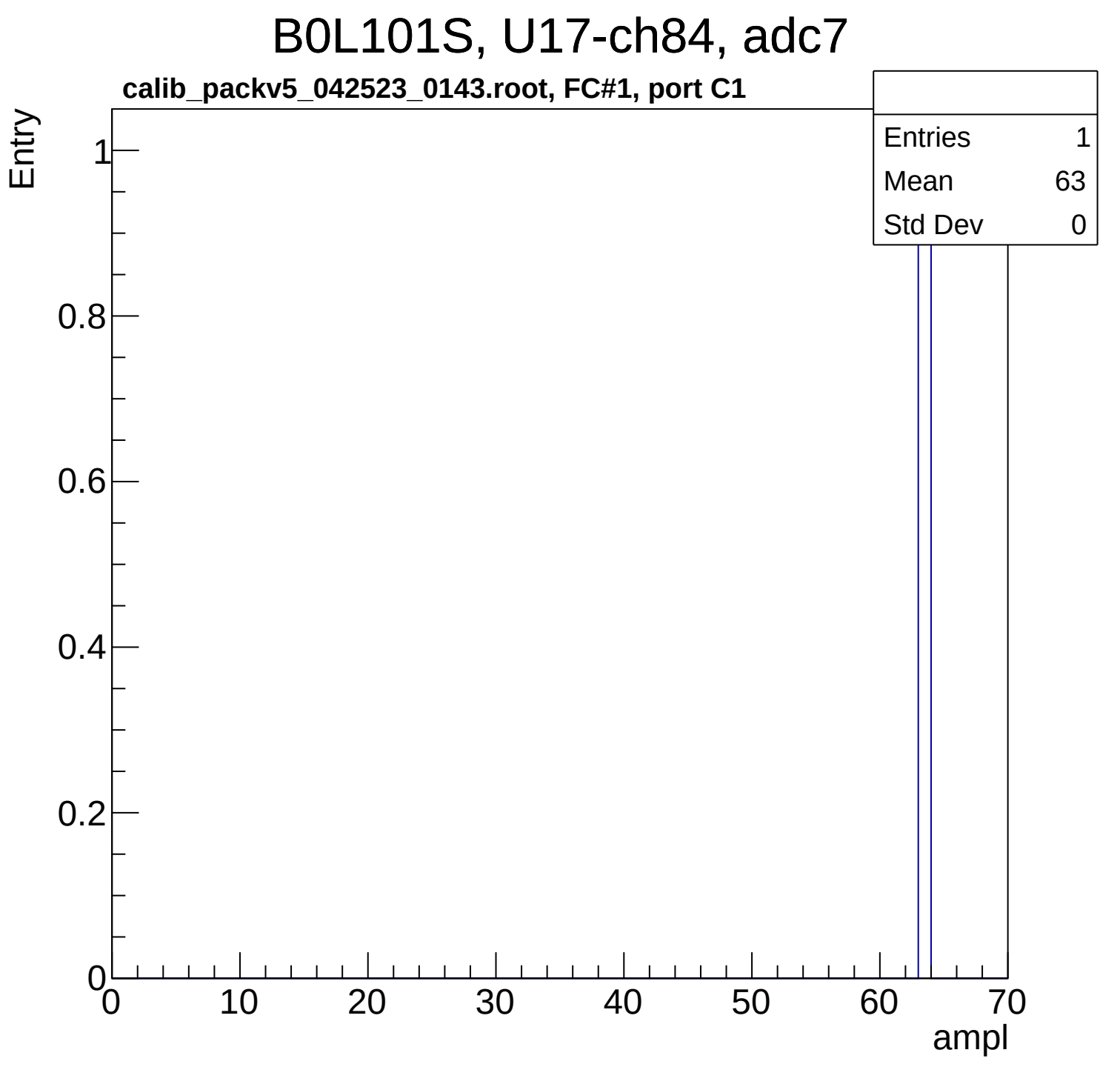

B0L101S, U17-ch84, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 41.71
Std Dev 3.679
Gaus mean : 42.2826
Gaus Width: 3.9493
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

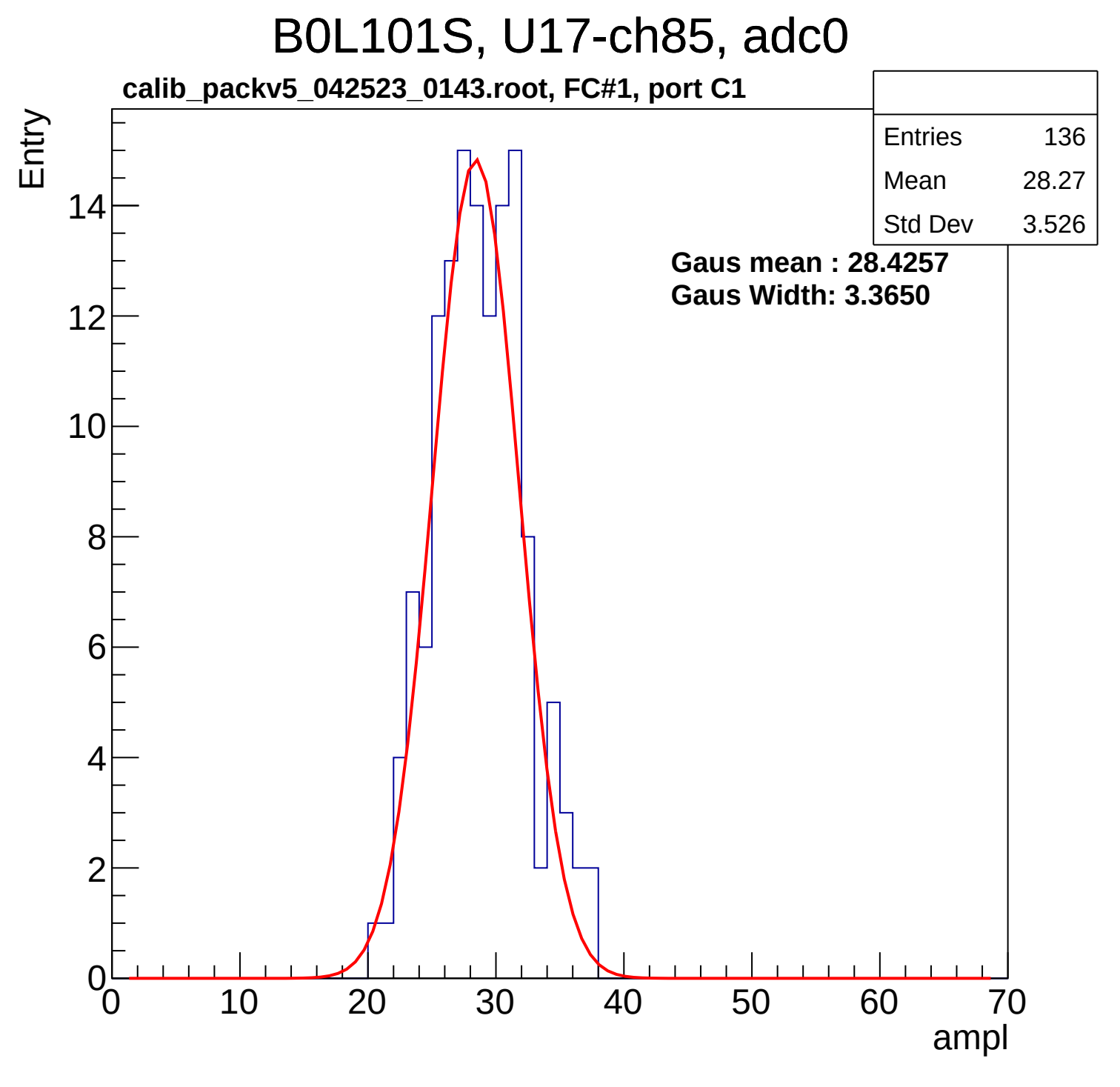

B0L101S, U17-ch84, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 49.13
Std Dev 3.691
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

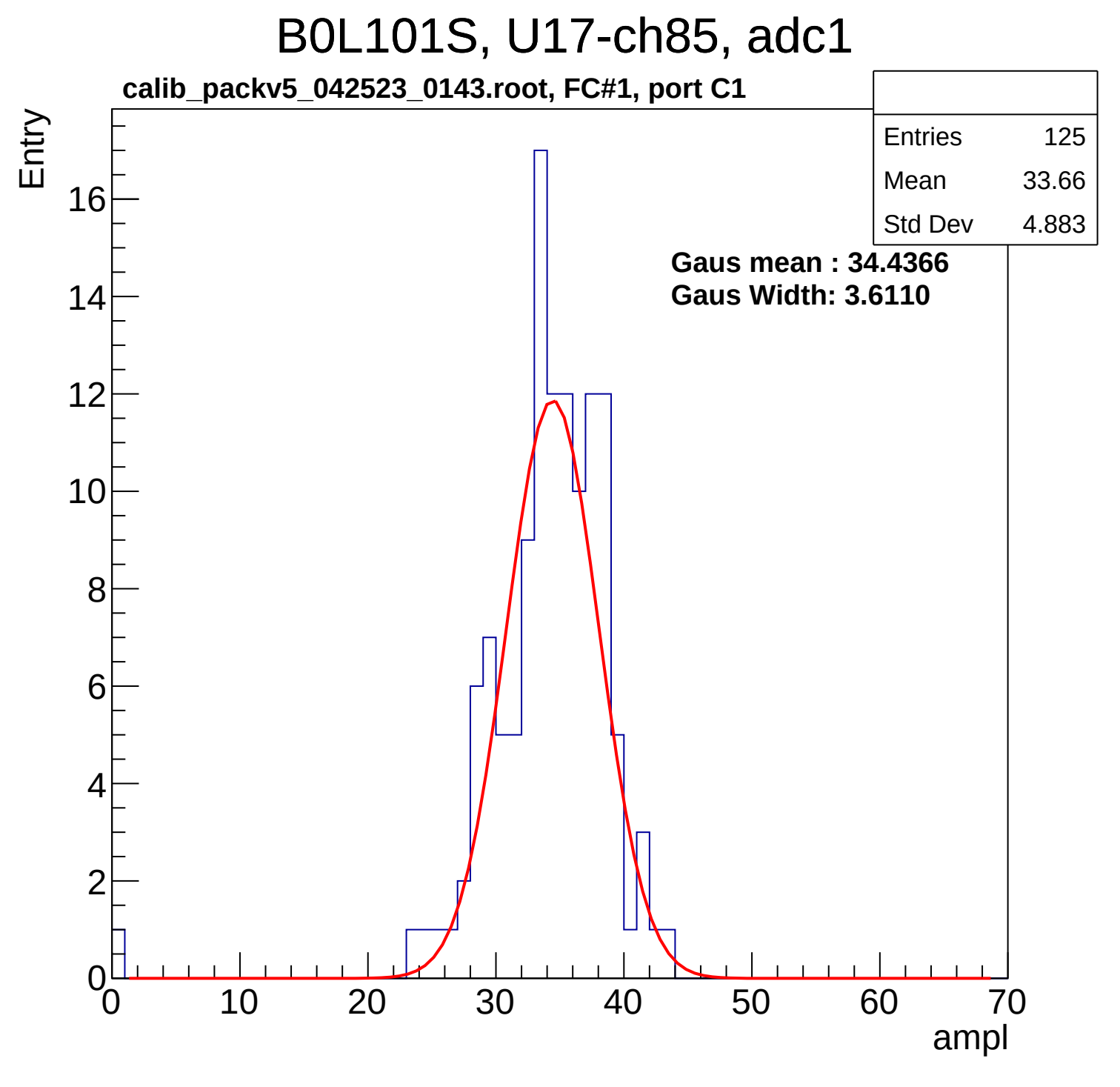

B0L101S, U17-ch84, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 55.81
Std Dev 3.207
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

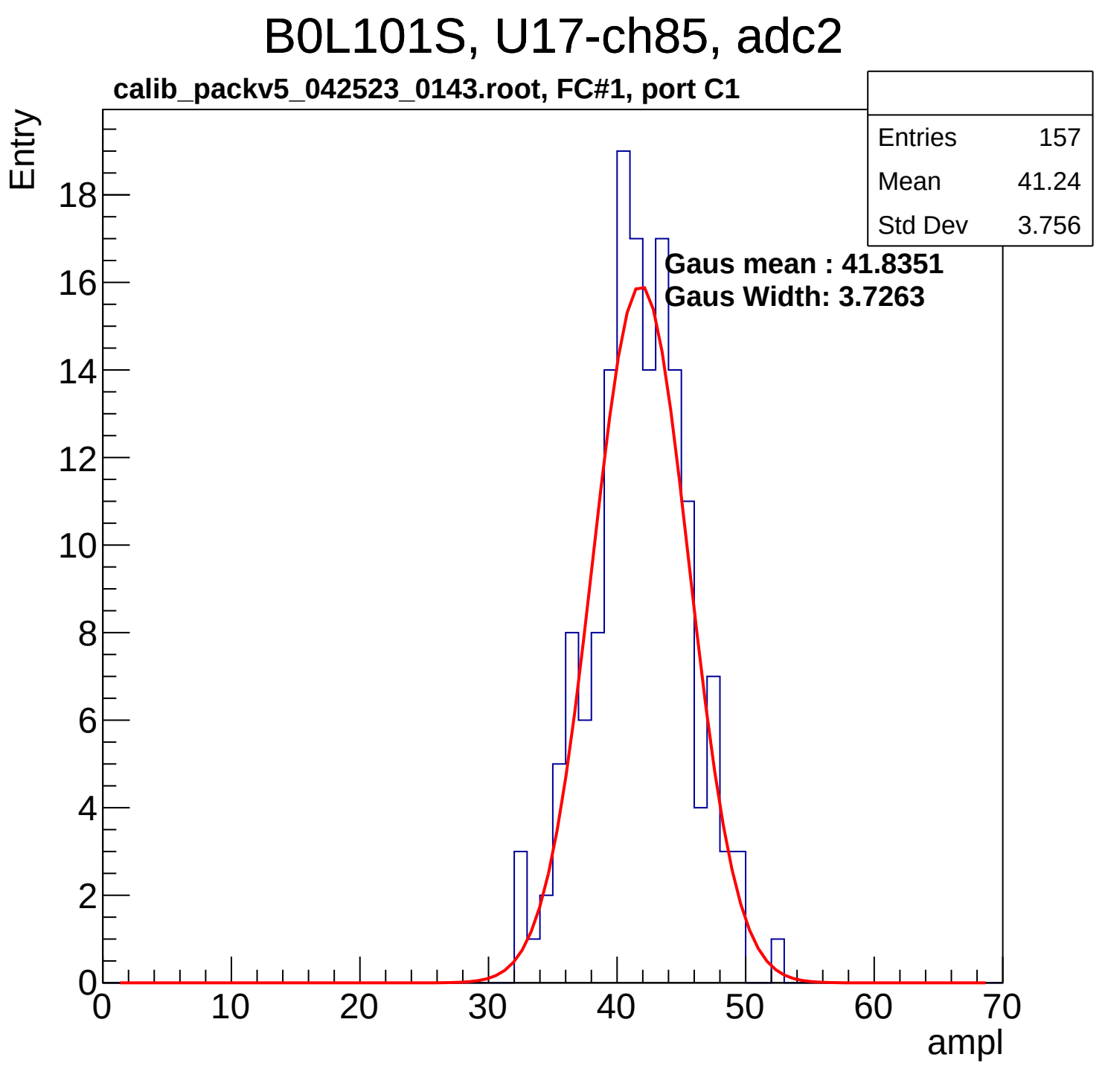

B0L101S, U17-ch84, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 59.71
Std Dev 2.673
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

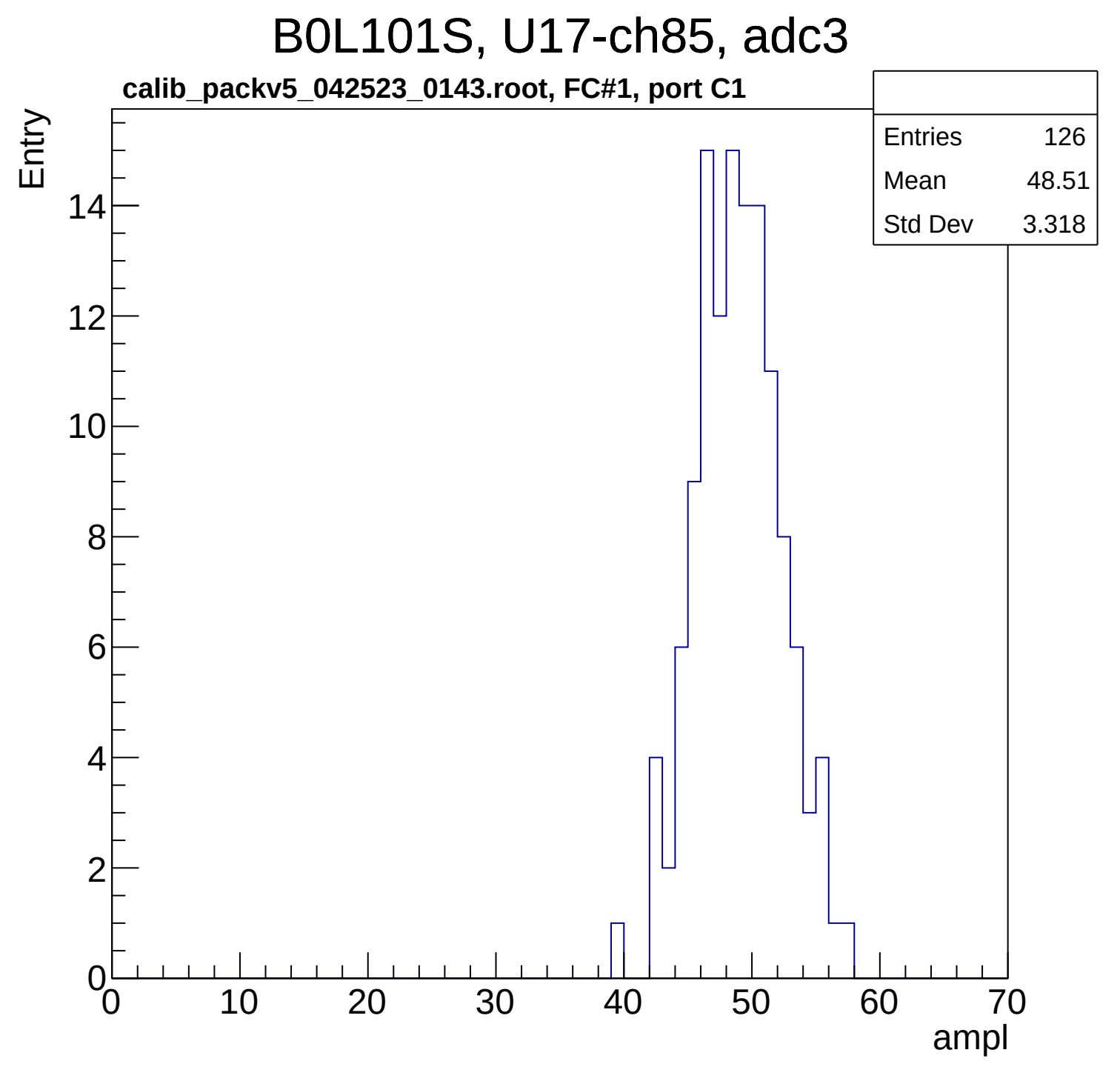

B0L101S, U17-ch84, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 53.81
Std Dev 20.4
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

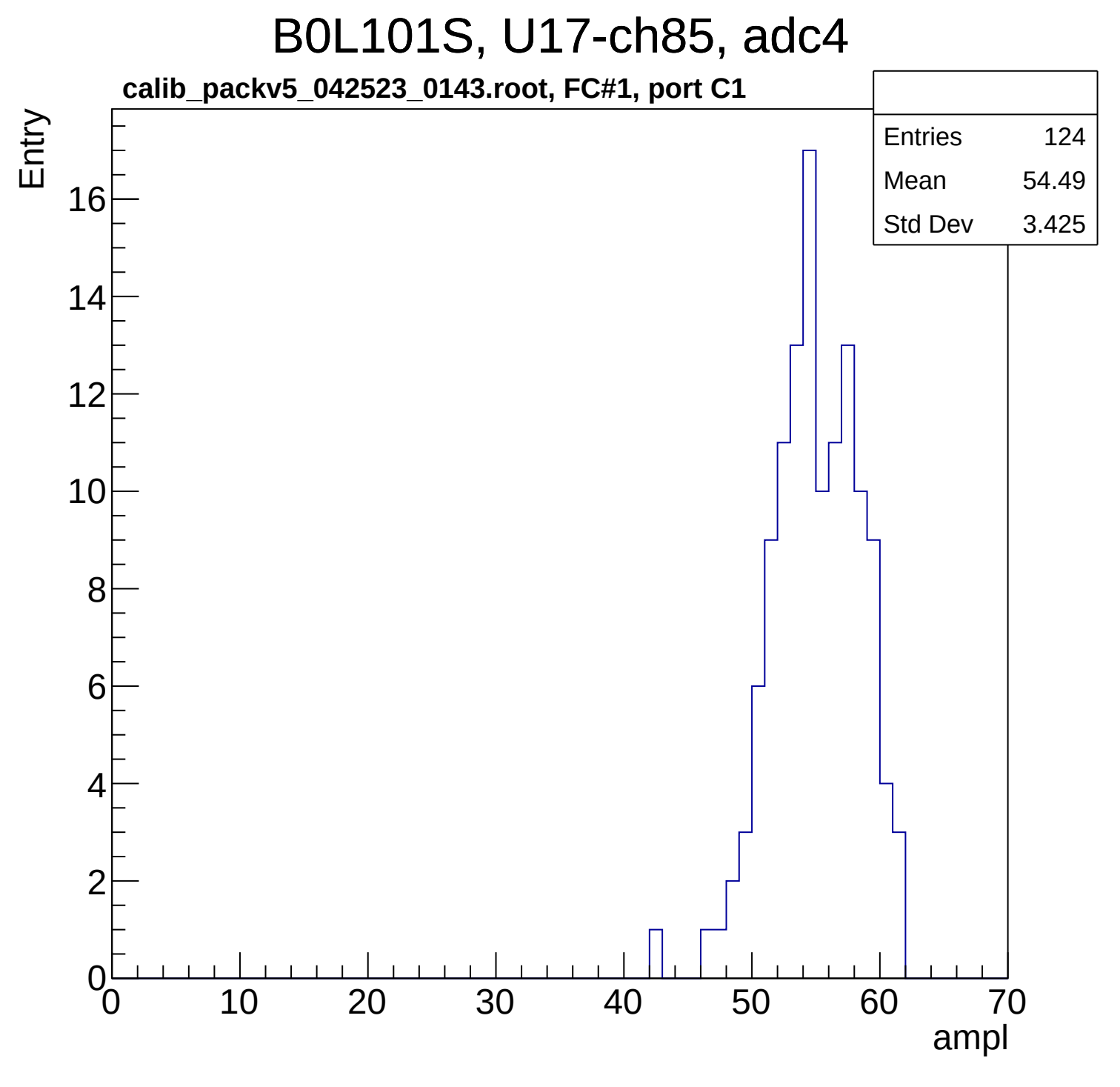

B0L101S, U17-ch84, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

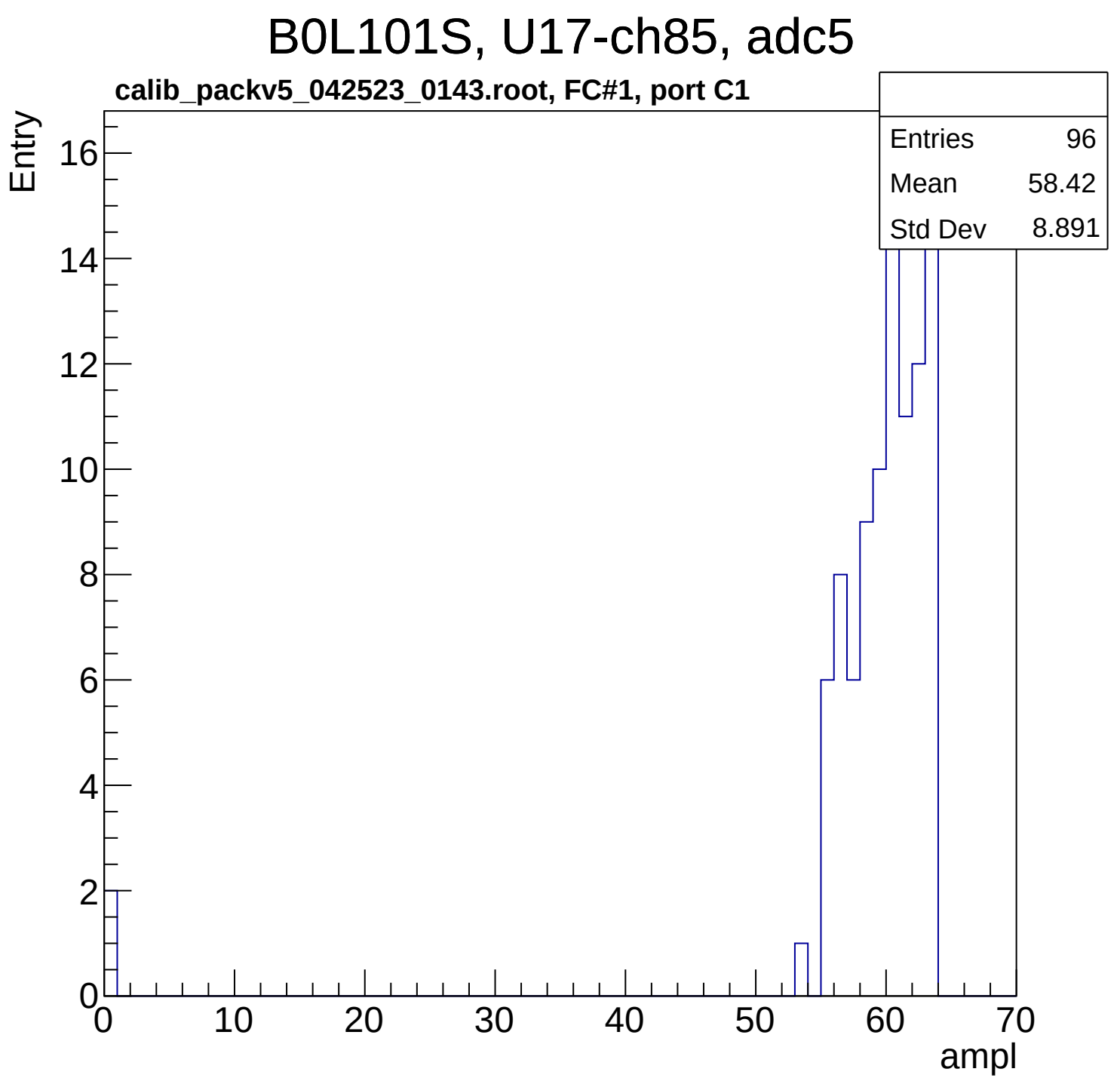

B0L101S, U17-ch85, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 28.27
Std Dev 3.526
Gaus mean : 28.4257
Gaus Width: 3.3650
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

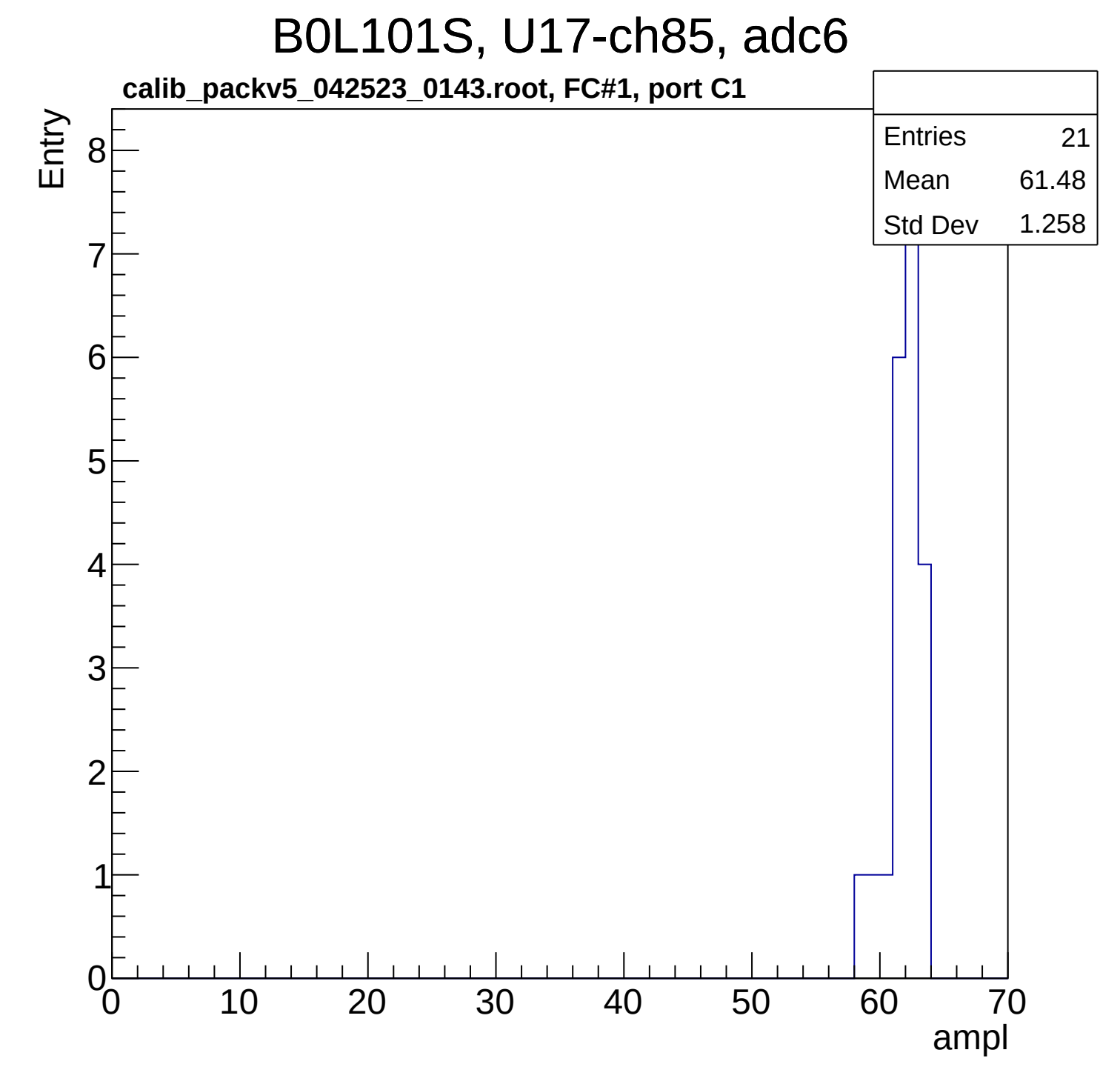

B0L101S, U17-ch85, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 33.66
Std Dev 4.883
Gaus mean : 34.4366
Gaus Width: 3.6110
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

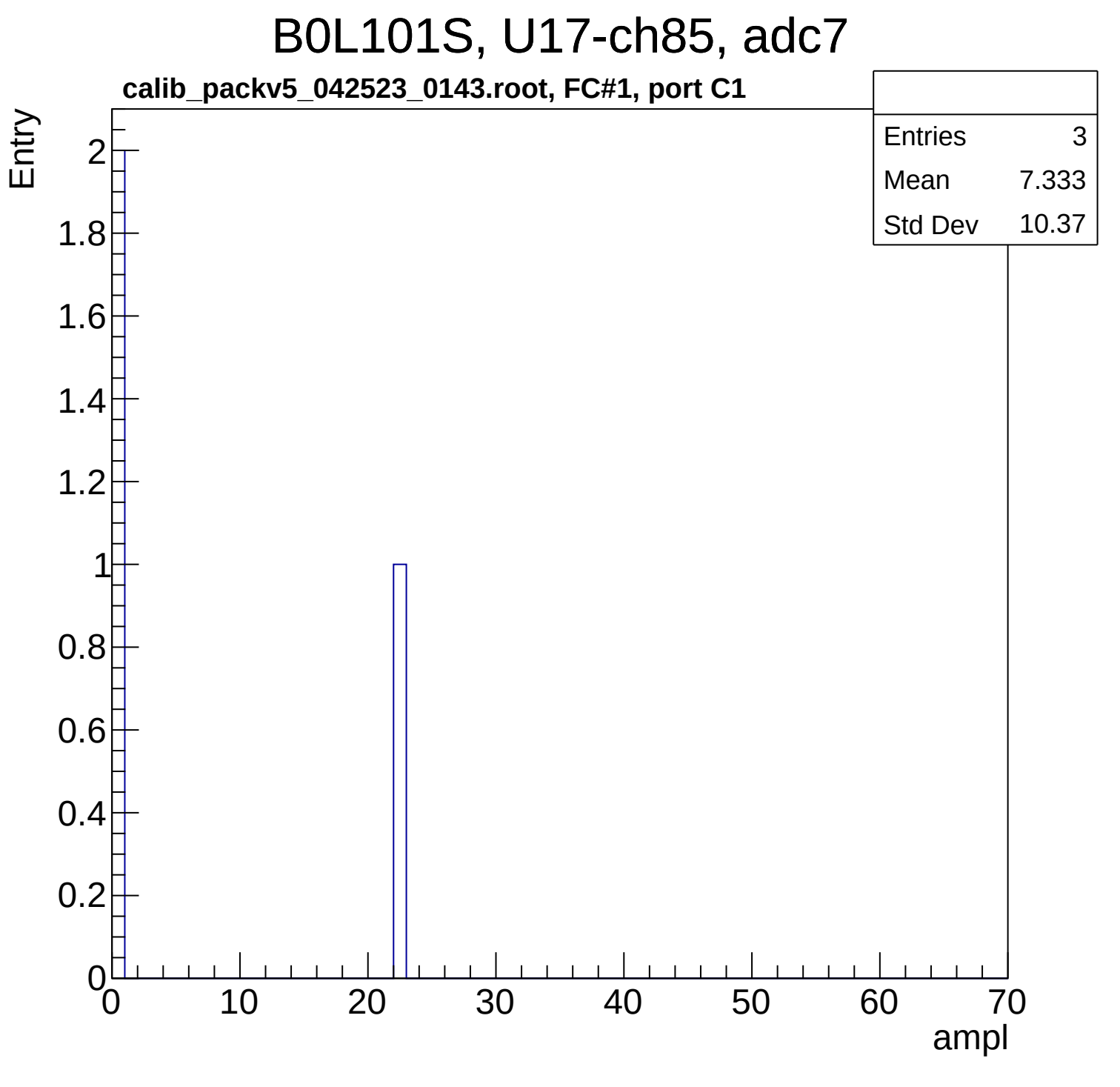

B0L101S, U17-ch85, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 41.24
Std Dev 3.756
Gaus mean : 41.8351
Gaus Width: 3.7263
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

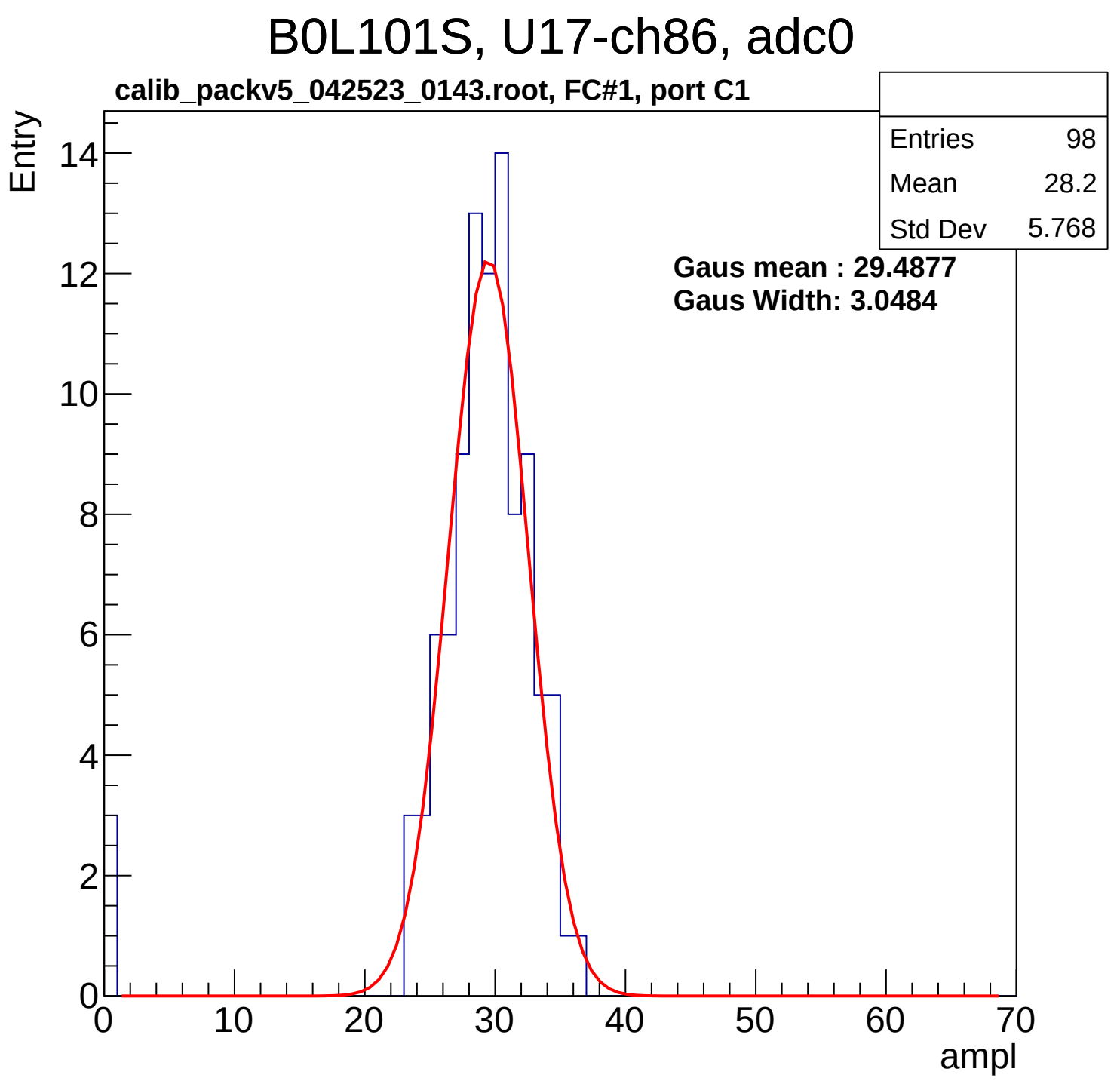

B0L101S, U17-ch85, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 48.51
Std Dev 3.318
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

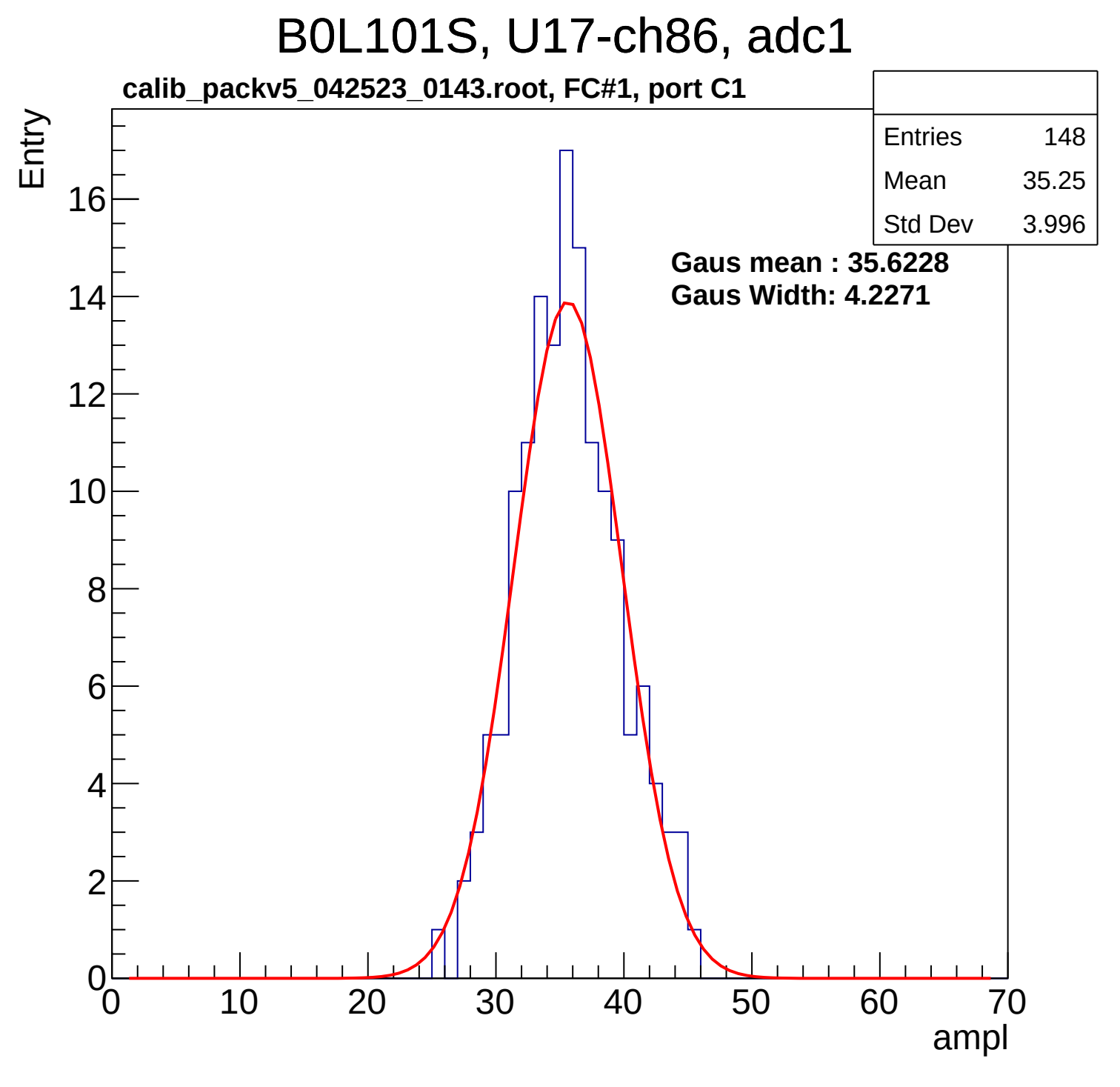

B0L101S, U17-ch85, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 54.49
Std Dev 3.425
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

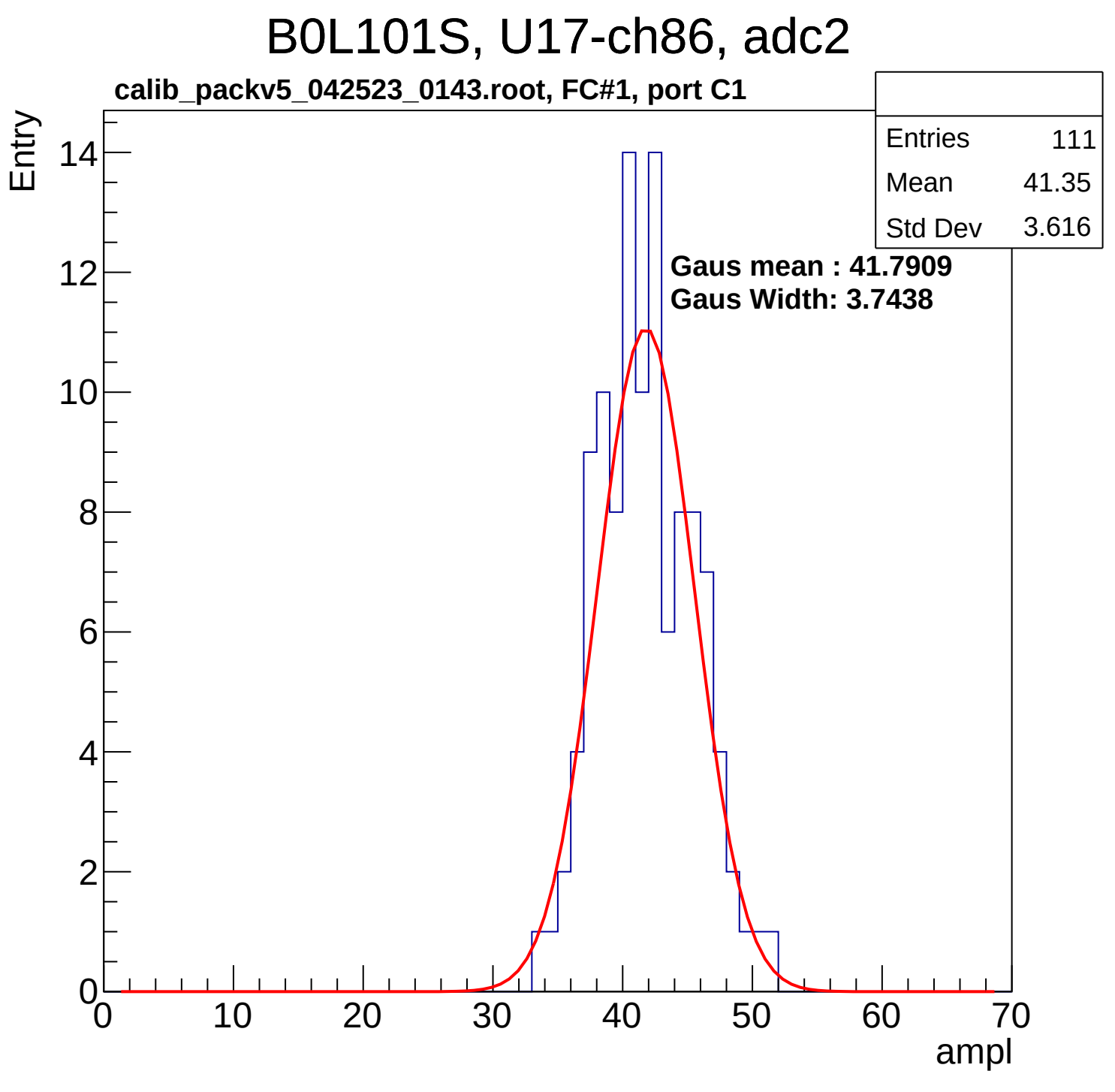

B0L101S, U17-ch85, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 58.42
Std Dev 8.891
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

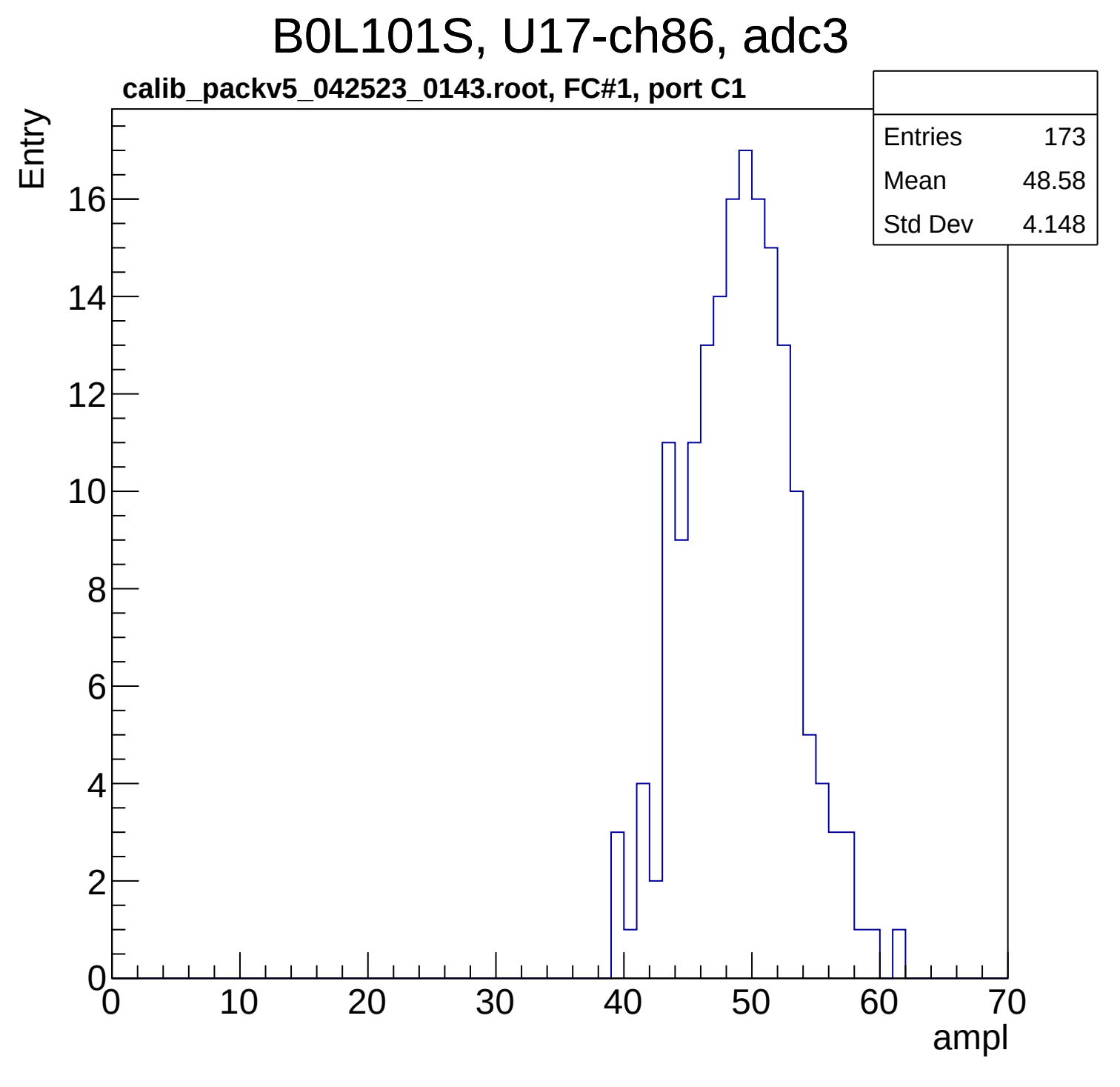

B0L101S, U17-ch85, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 21
Mean 61.48
Std Dev 1.258
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

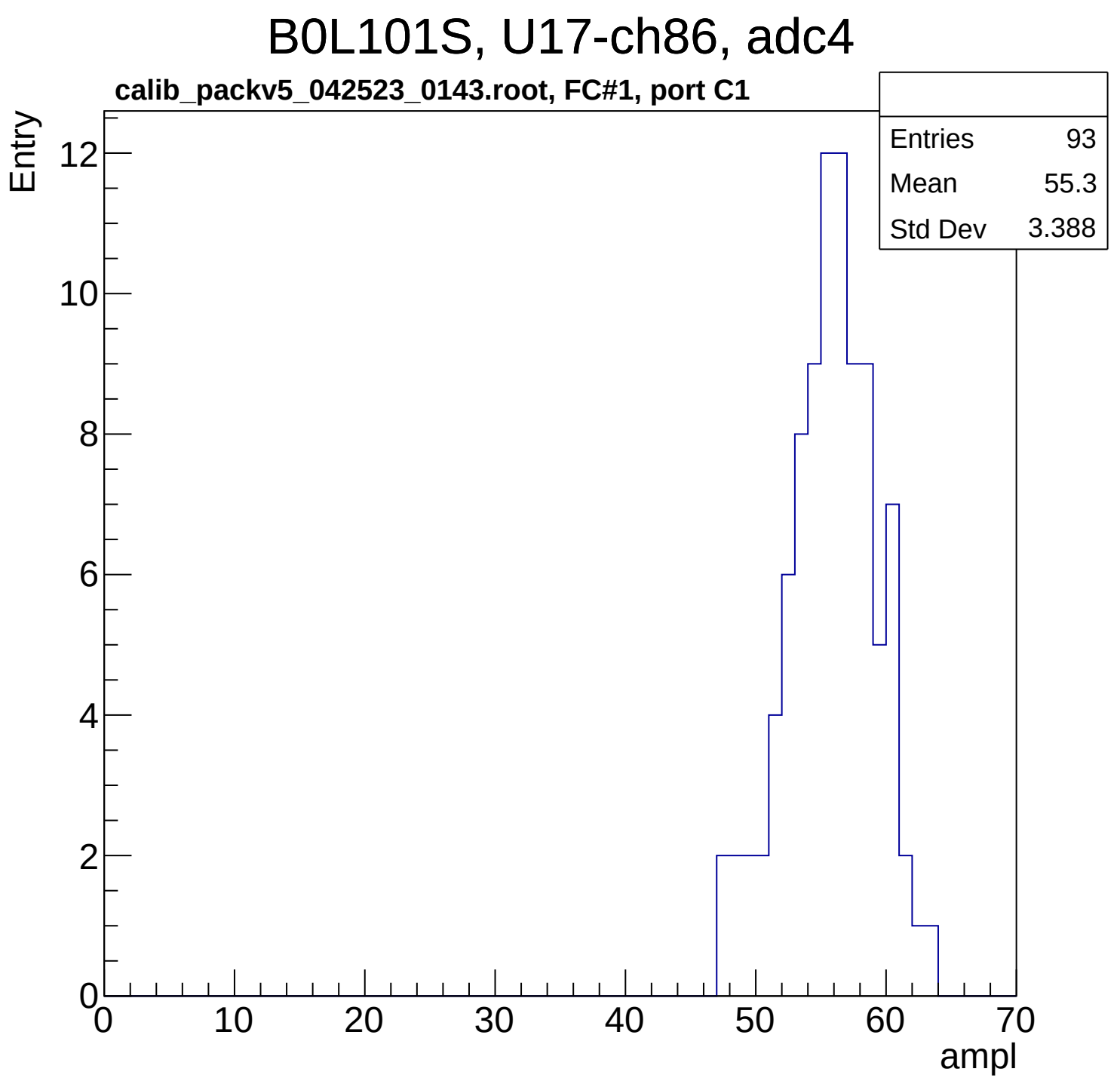

B0L101S, U17-ch85, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

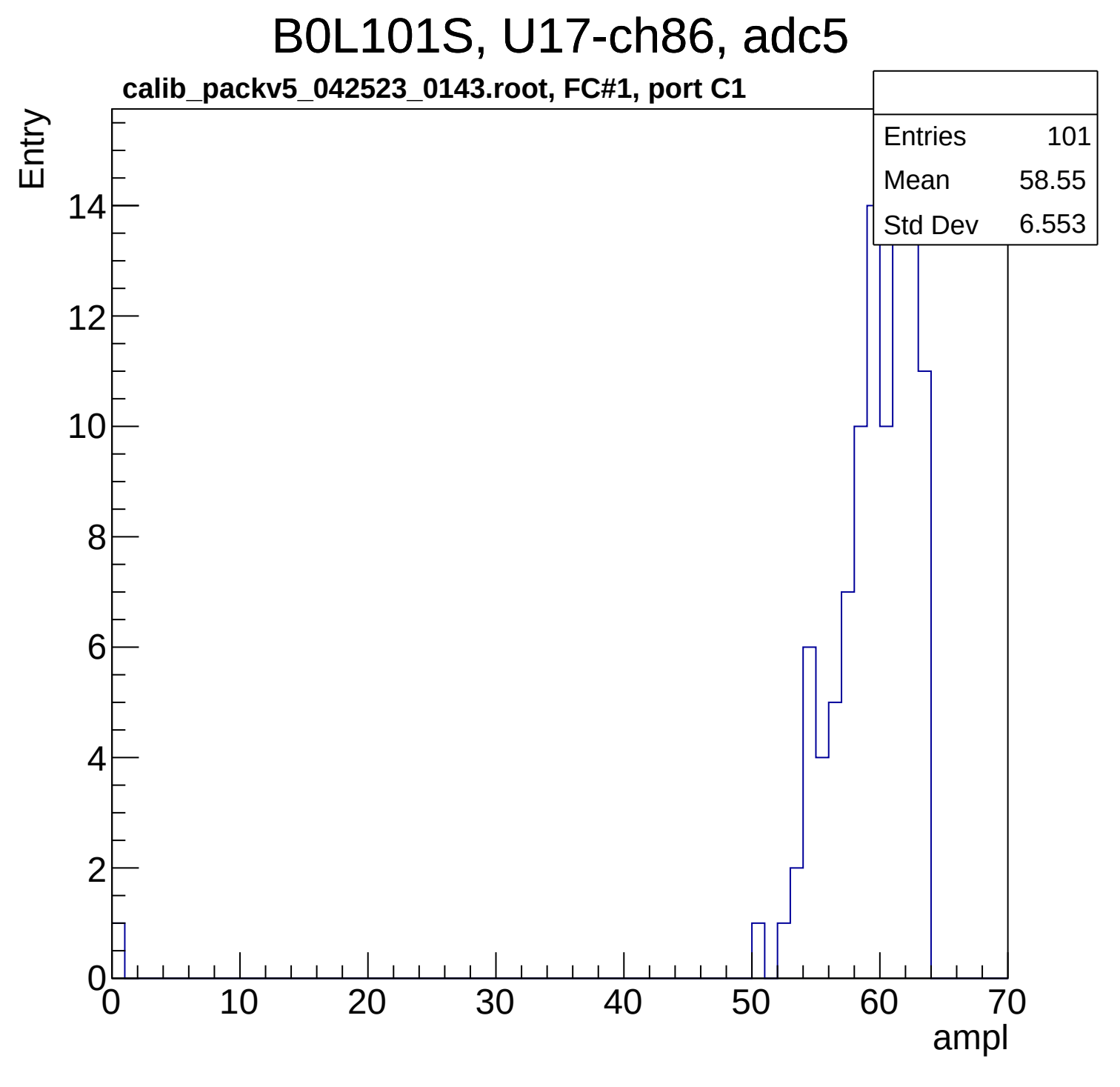

B0L101S, U17-ch86, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 98
Mean 28.2
Std Dev 5.768
Gaus mean : 29.4877
Gaus Width: 3.0484
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

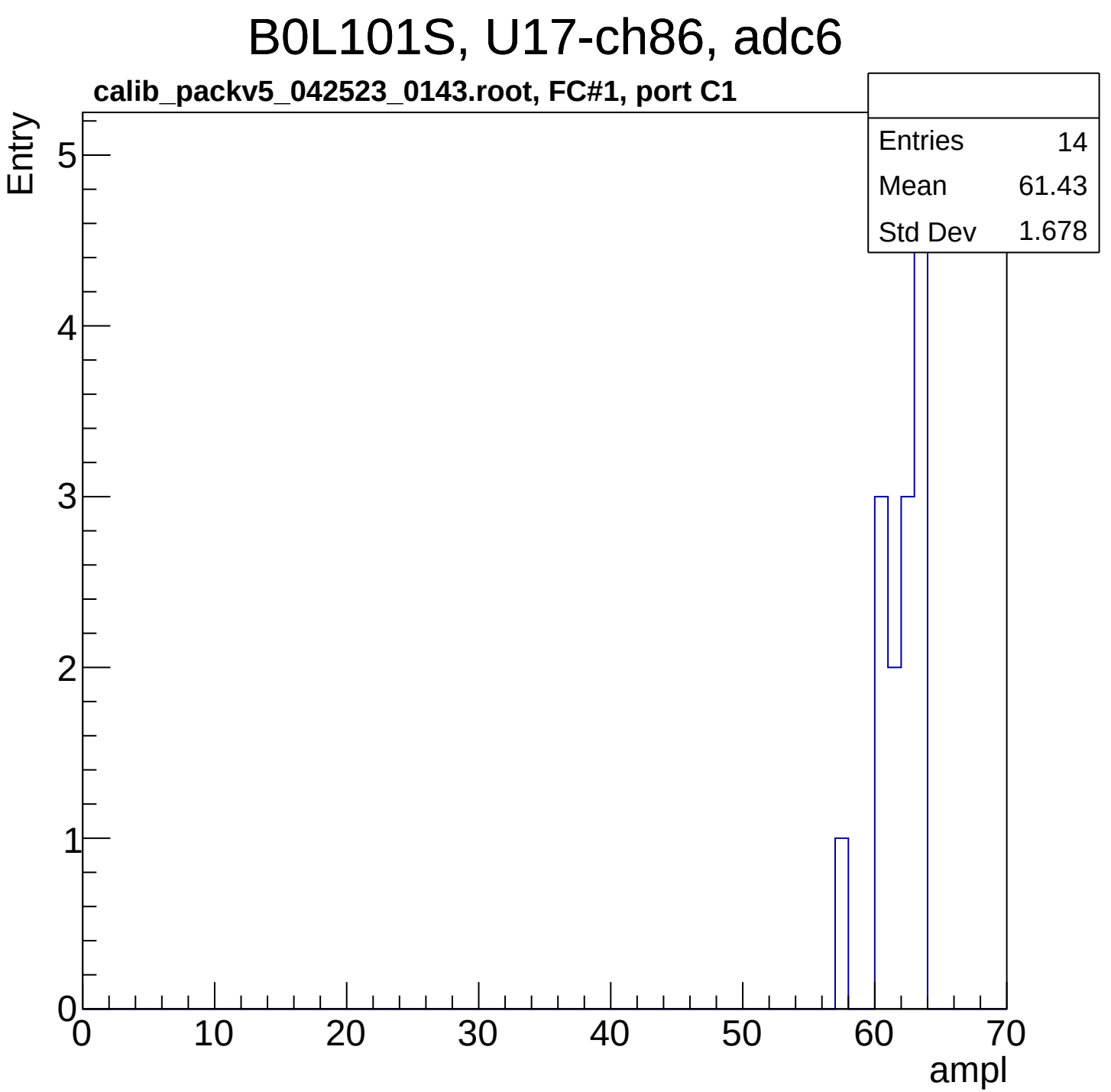

B0L101S, U17-ch86, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 35.25
Std Dev 3.996
Gaus mean : 35.6228
Gaus Width: 4.2271
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

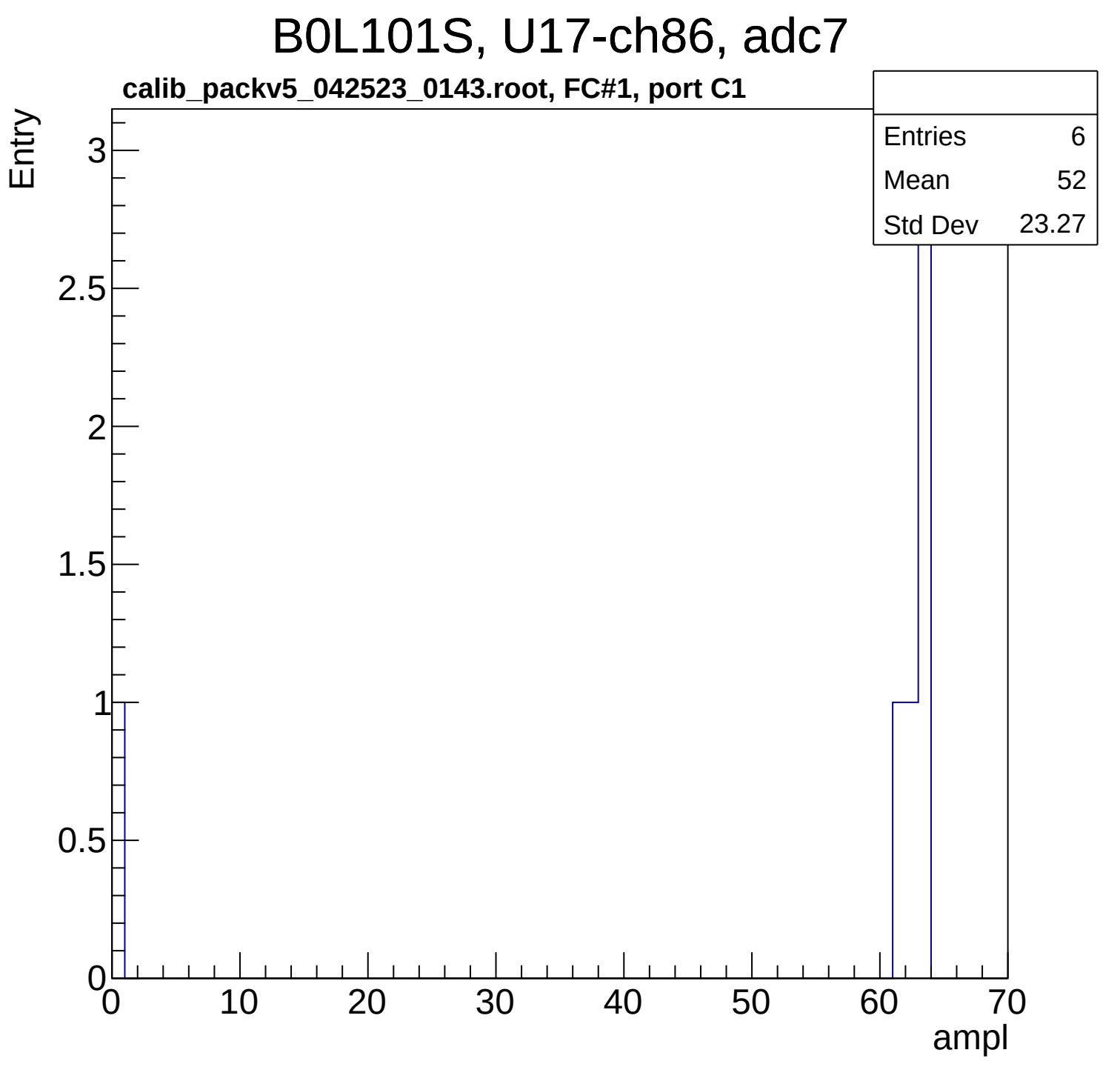

B0L101S, U17-ch86, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 41.35
Std Dev 3.616
Gaus mean : 41.7909
Gaus Width: 3.7438
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

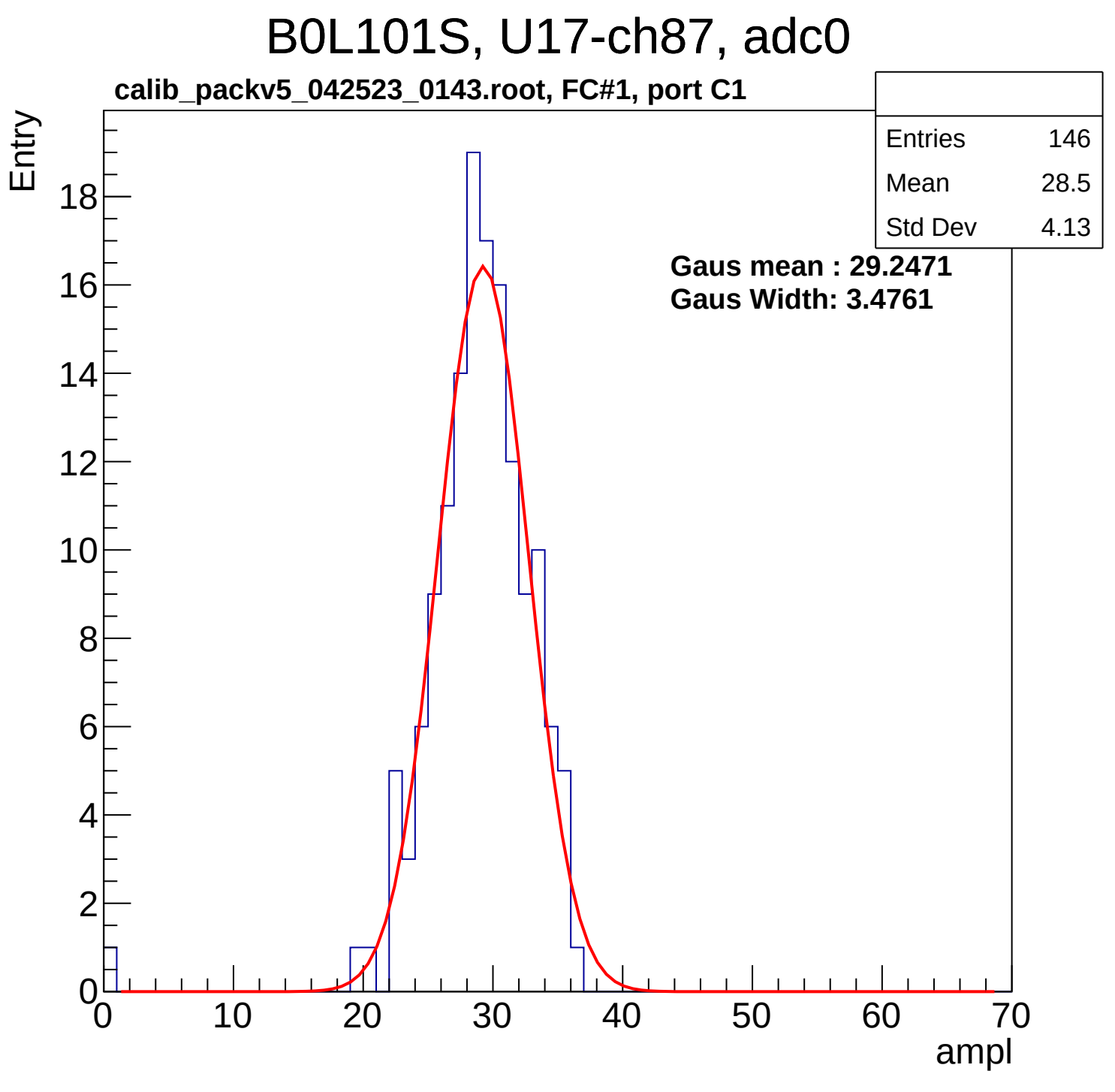

B0L101S, U17-ch86, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 173
Mean 48.58
Std Dev 4.148
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

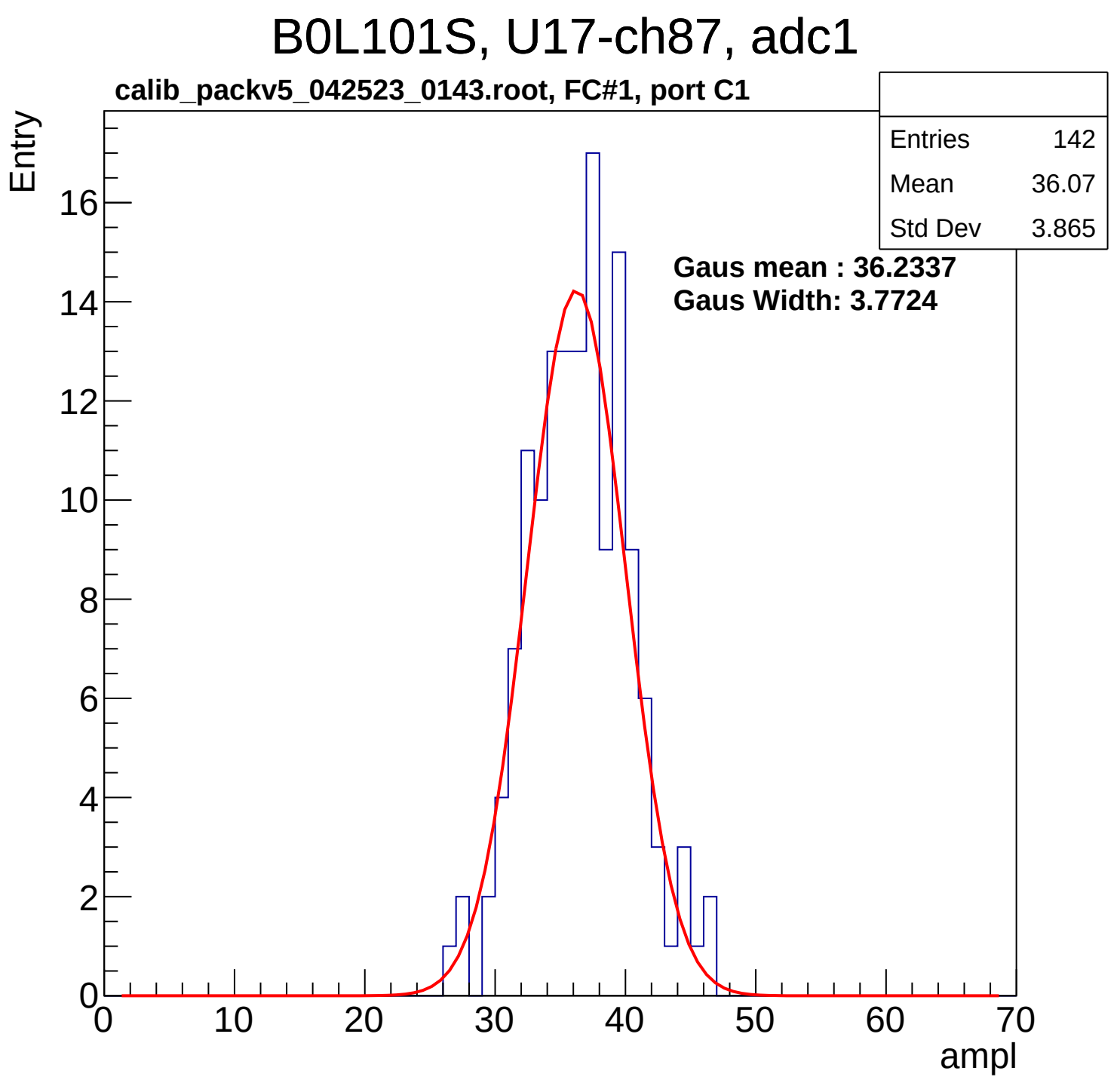

B0L101S, U17-ch86, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 93
Mean 55.3
Std Dev 3.388
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

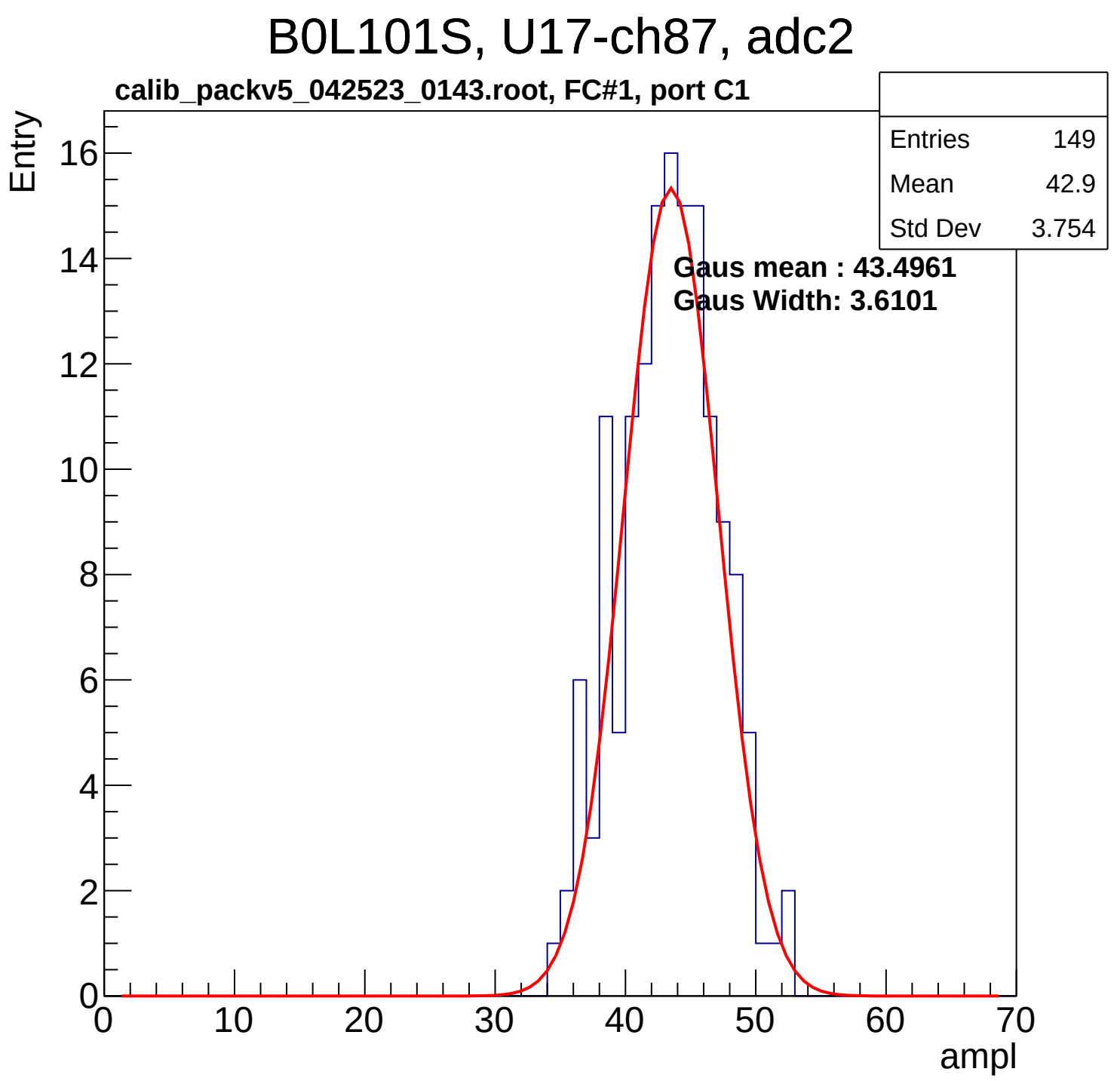

B0L101S, U17-ch86, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 58.55
Std Dev 6.553
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

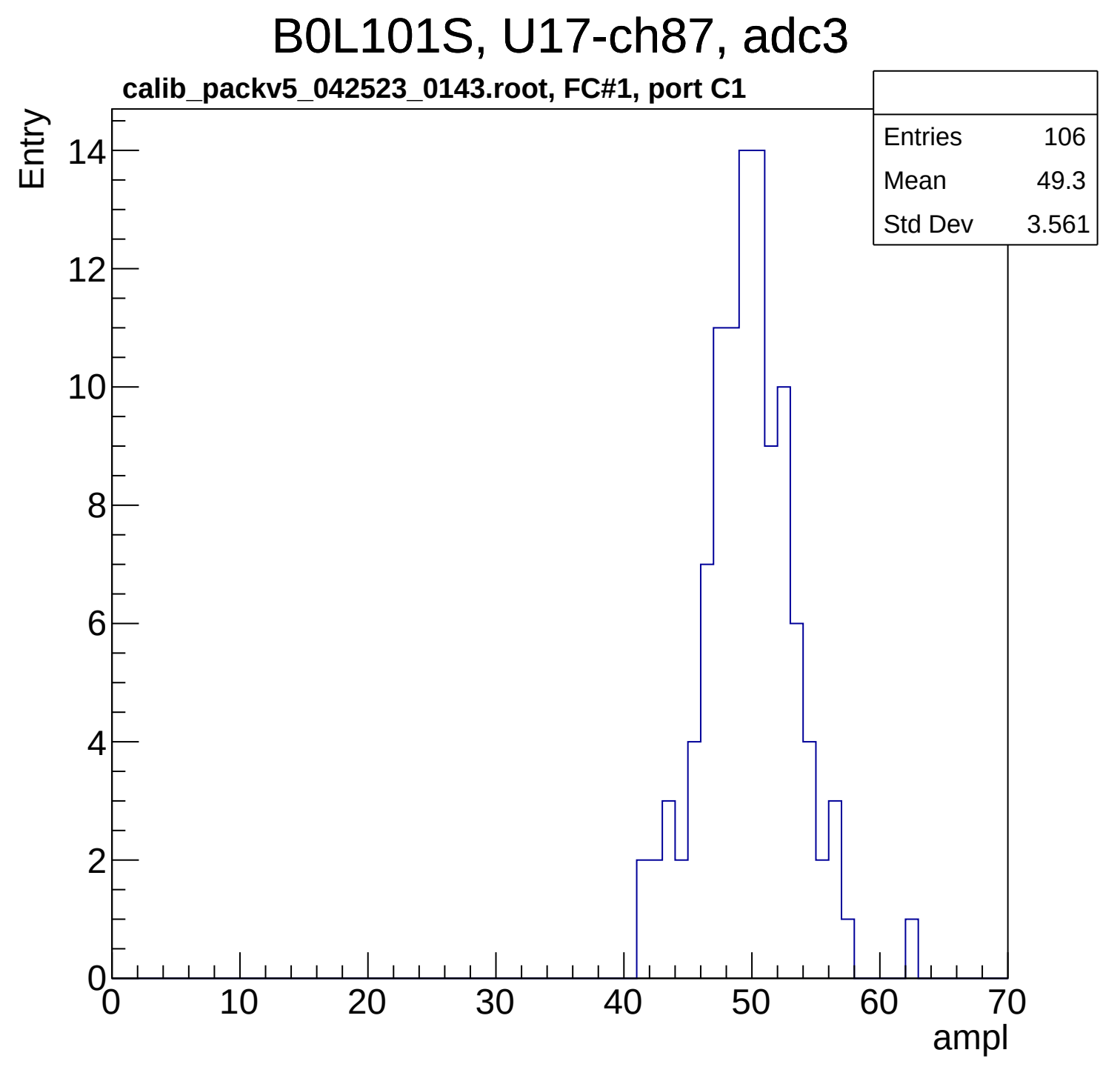

B0L101S, U17-ch86, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 61.43
Std Dev 1.678
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

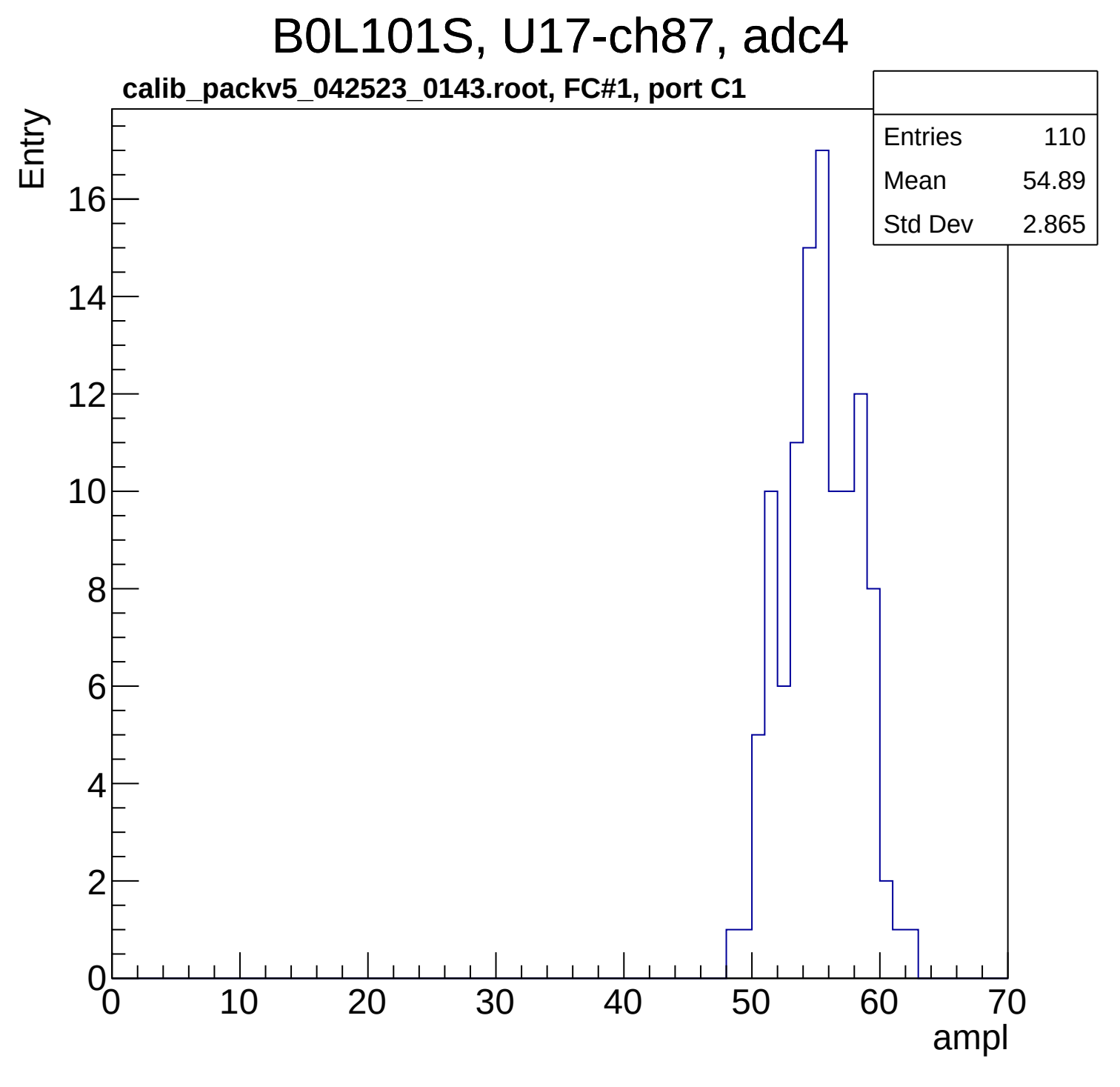

B0L101S, U17-ch86, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

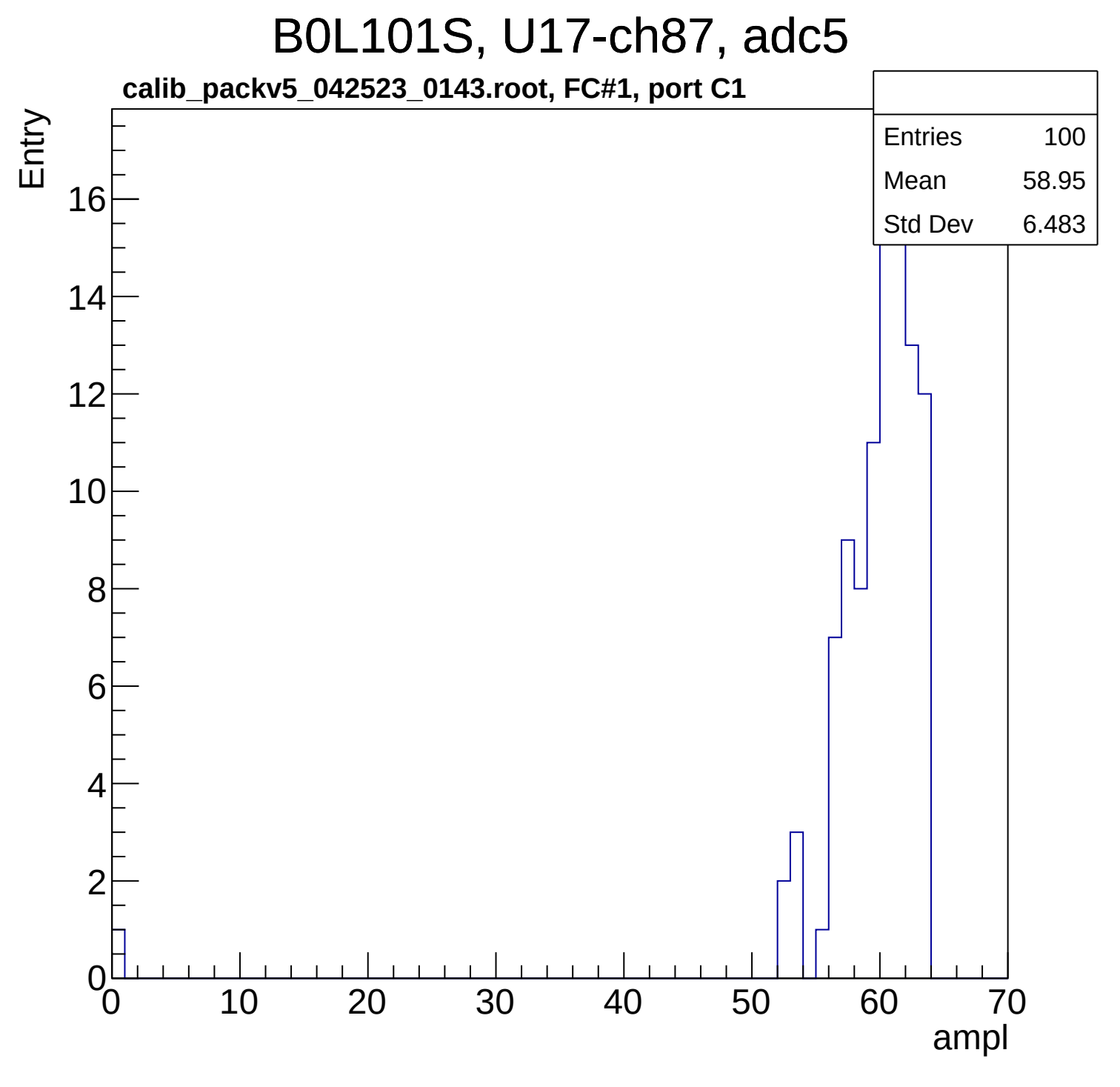

B0L101S, U17-ch87, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 146
Mean 28.5
Std Dev 4.13
Gaus mean : 29.2471
Gaus Width: 3.4761
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

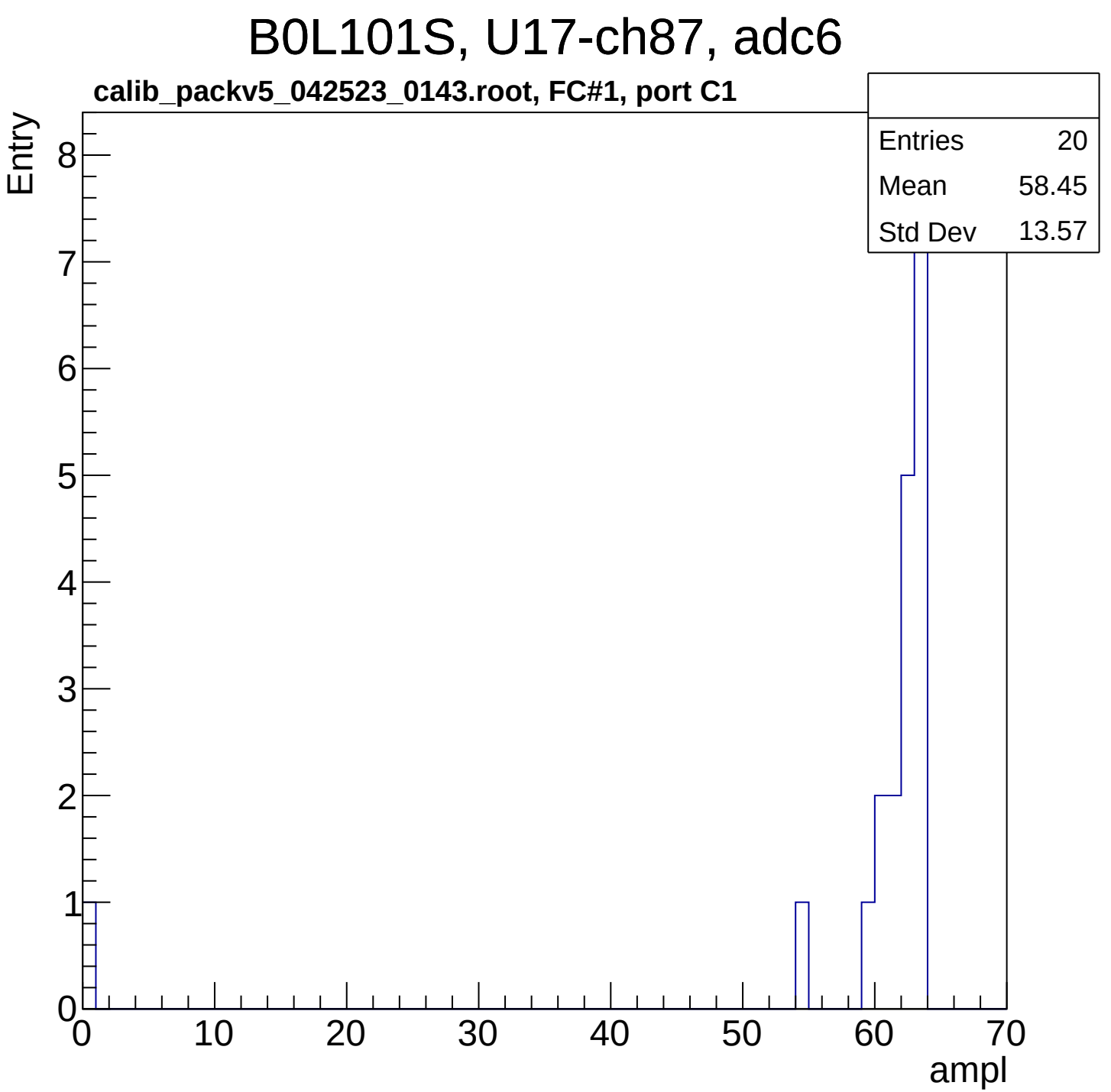

B0L101S, U17-ch87, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 36.07
Std Dev 3.865
Gaus mean : 36.2337
Gaus Width: 3.7724
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

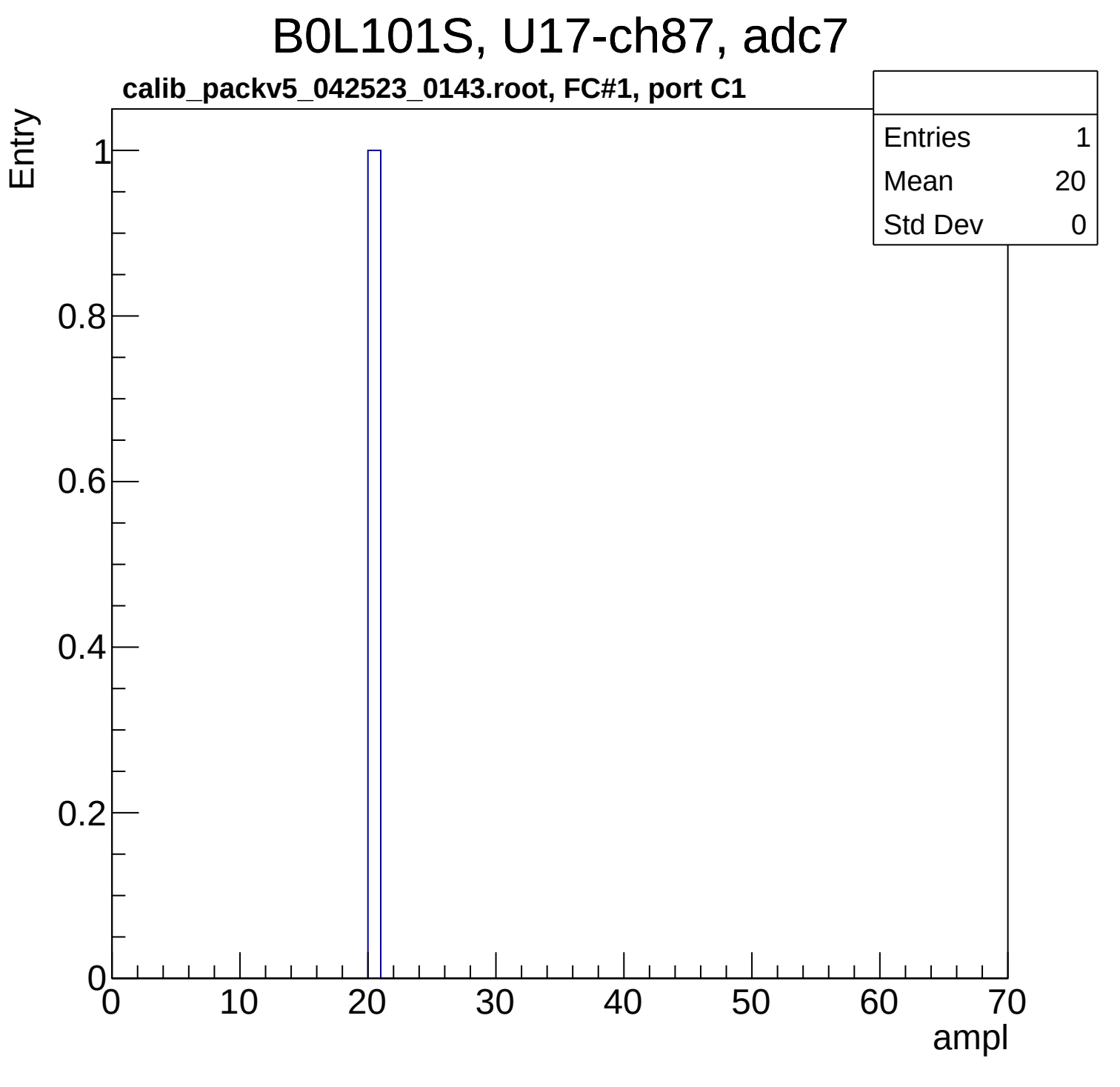

B0L101S, U17-ch87, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 42.9
Std Dev 3.754
Gaus mean : 43.4961
Gaus Width: 3.6101
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

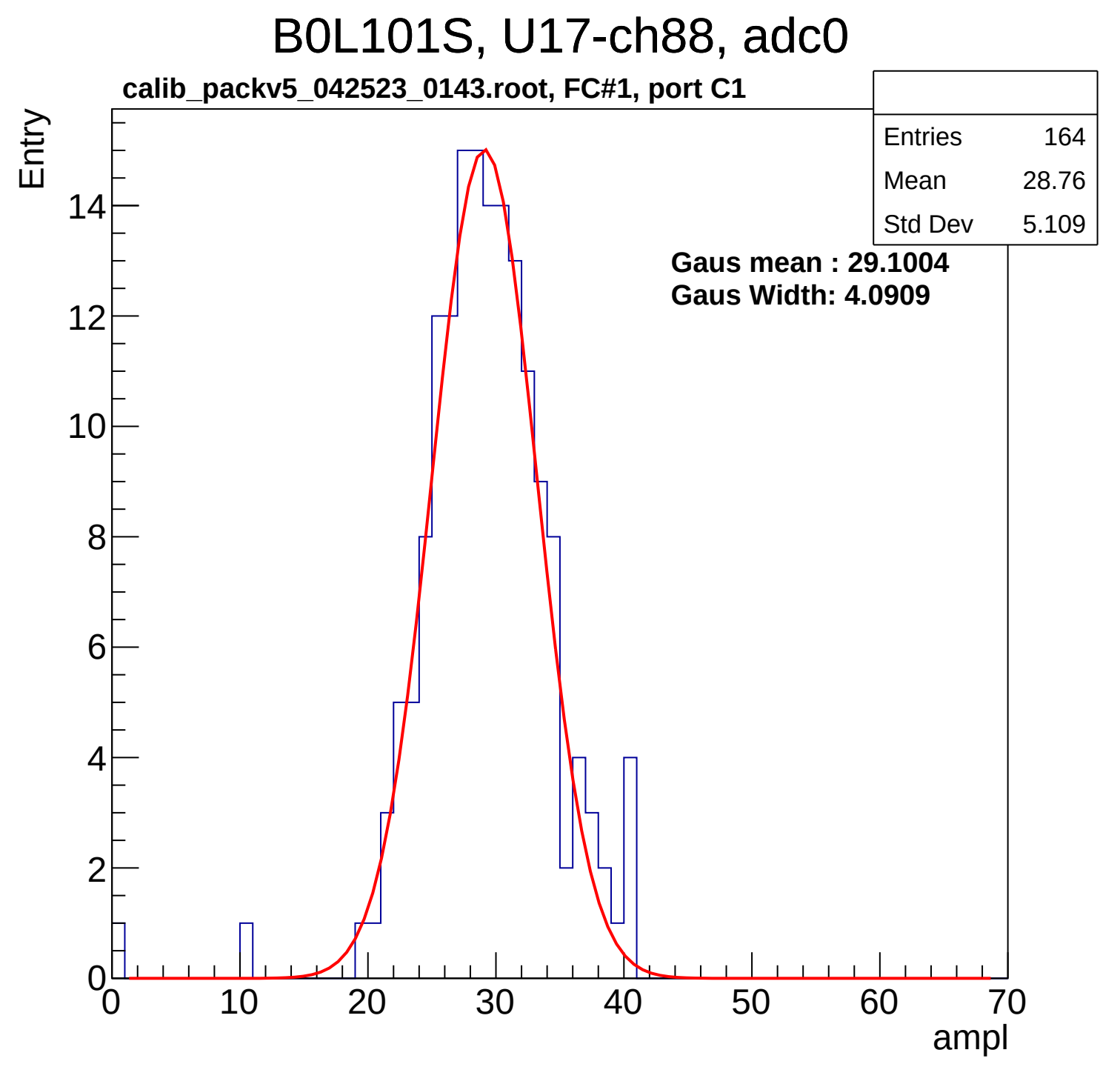

B0L101S, U17-ch87, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 106
Mean 49.3
Std Dev 3.561
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

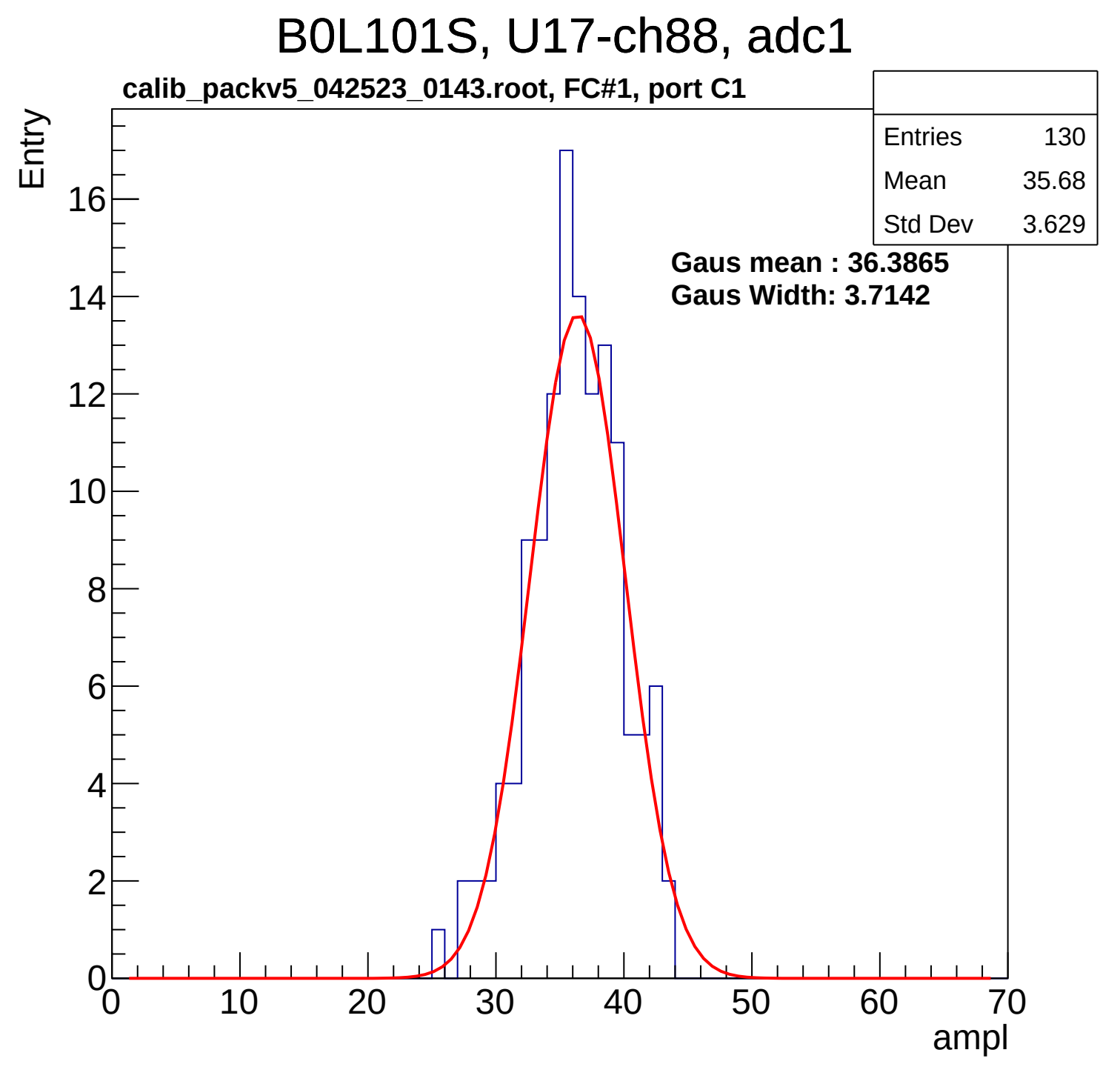

B0L101S, U17-ch87, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 54.89
Std Dev 2.865
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

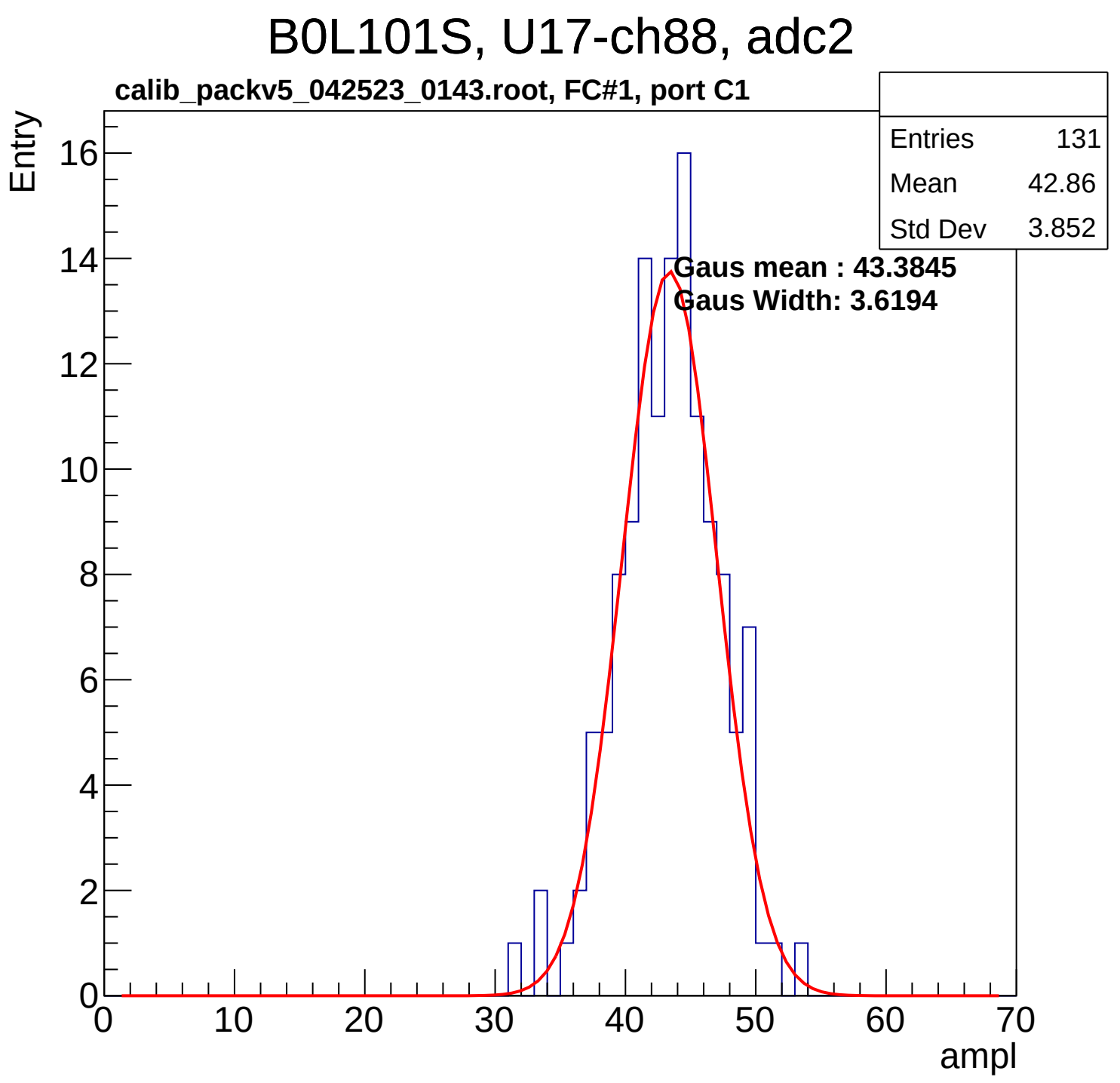

B0L101S, U17-ch87, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 58.95
Std Dev 6.483
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

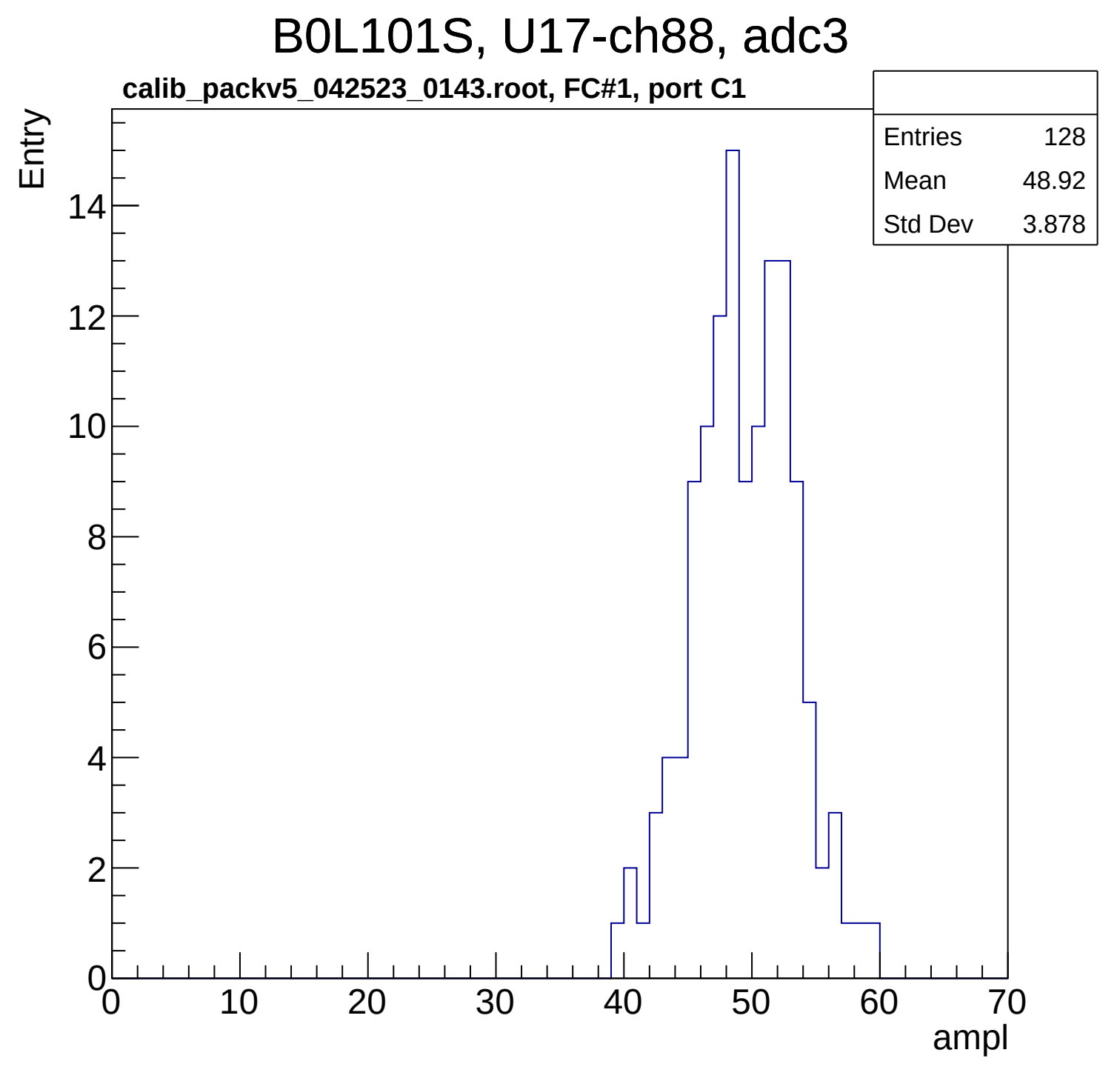

B0L101S, U17-ch87, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 20
Mean 58.45
Std Dev 13.57
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

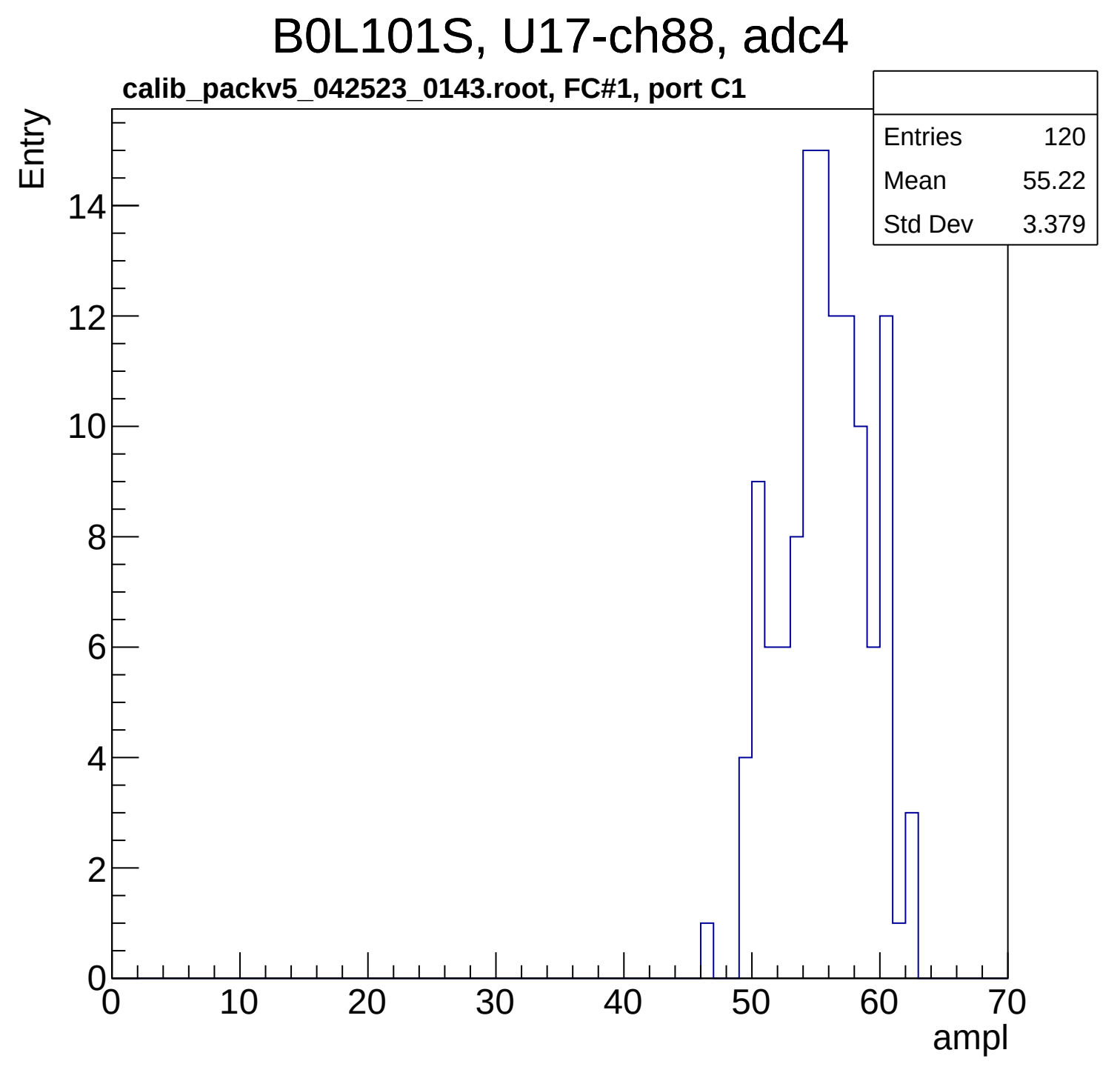

B0L101S, U17-ch87, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

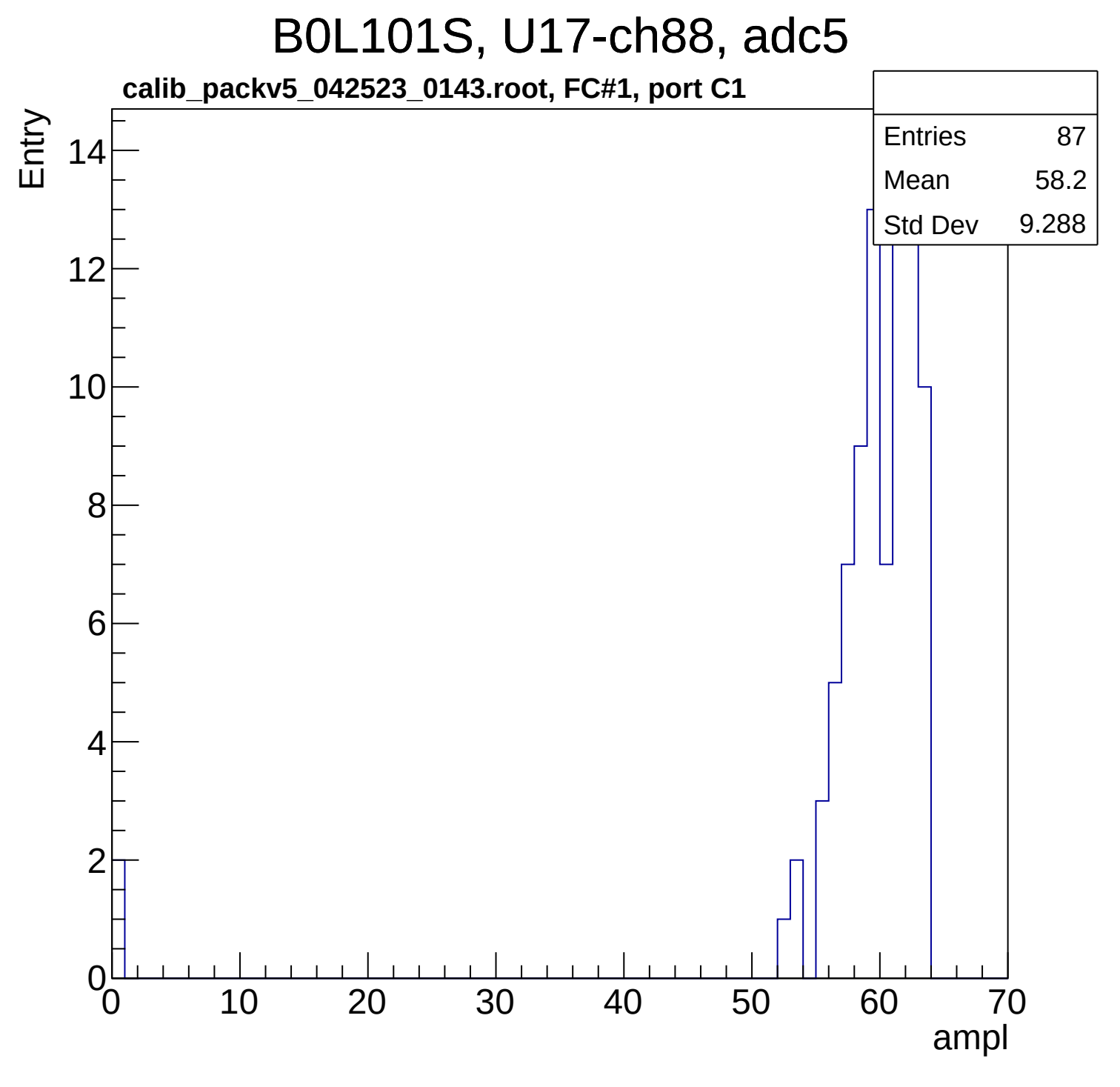

B0L101S, U17-ch88, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 164
Mean 28.76
Std Dev 5.109
Gaus mean : 29.1004
Gaus Width: 4.0909
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

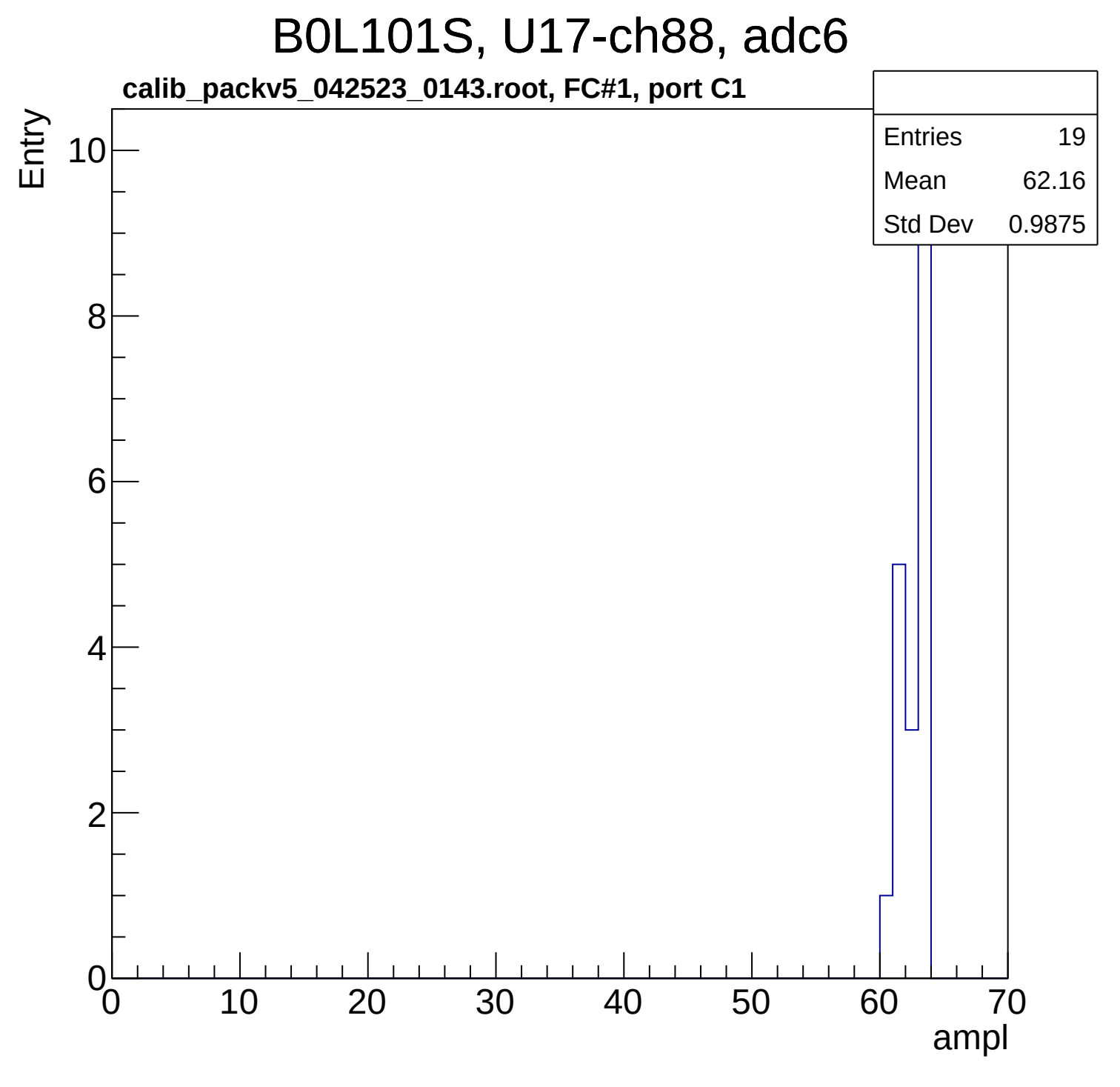

B0L101S, U17-ch88, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 35.68
Std Dev 3.629
Gaus mean : 36.3865
Gaus Width: 3.7142
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

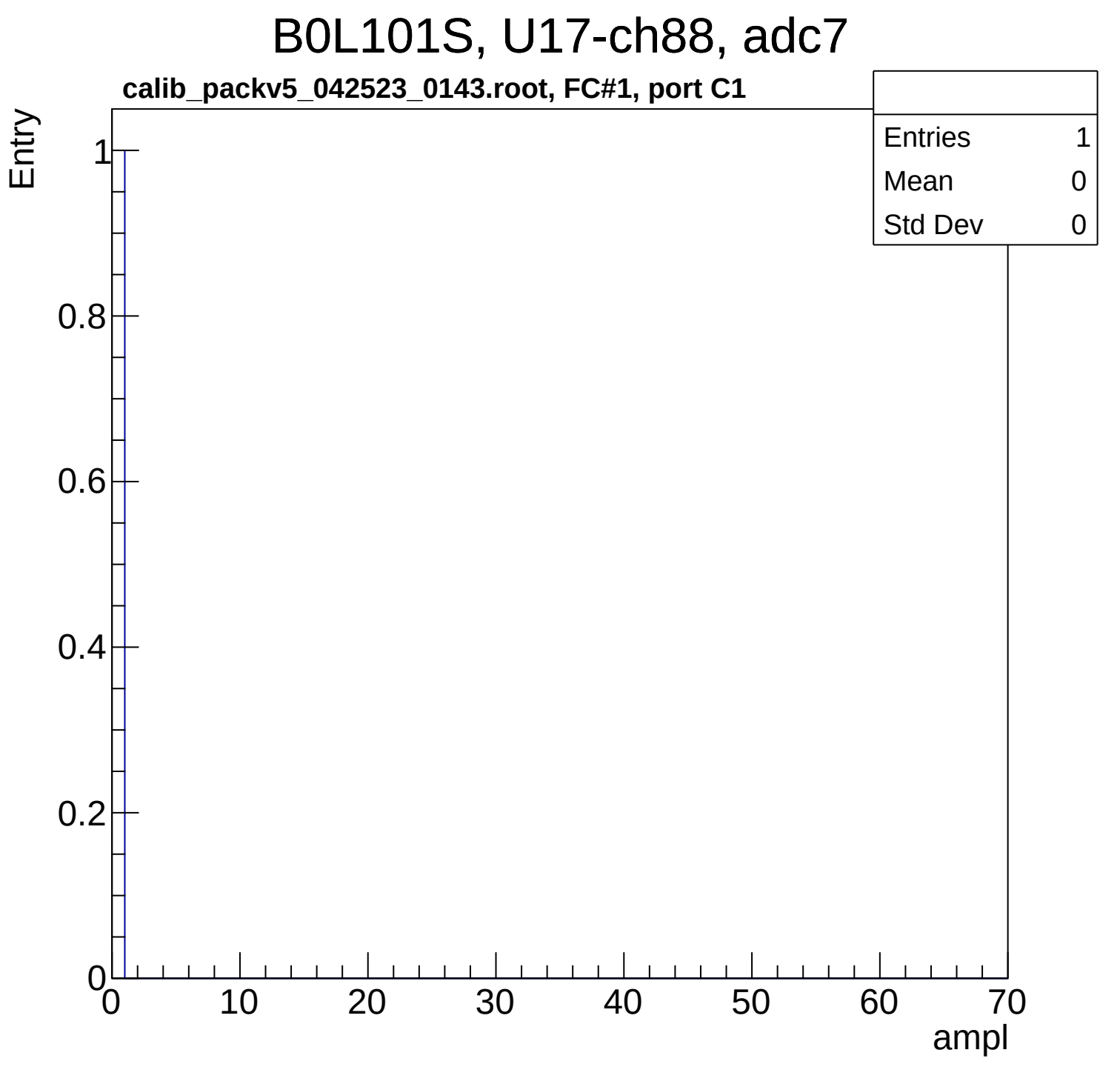

B0L101S, U17-ch88, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 42.86
Std Dev 3.852
Gaus mean : 43.3845
Gaus Width: 3.6194
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

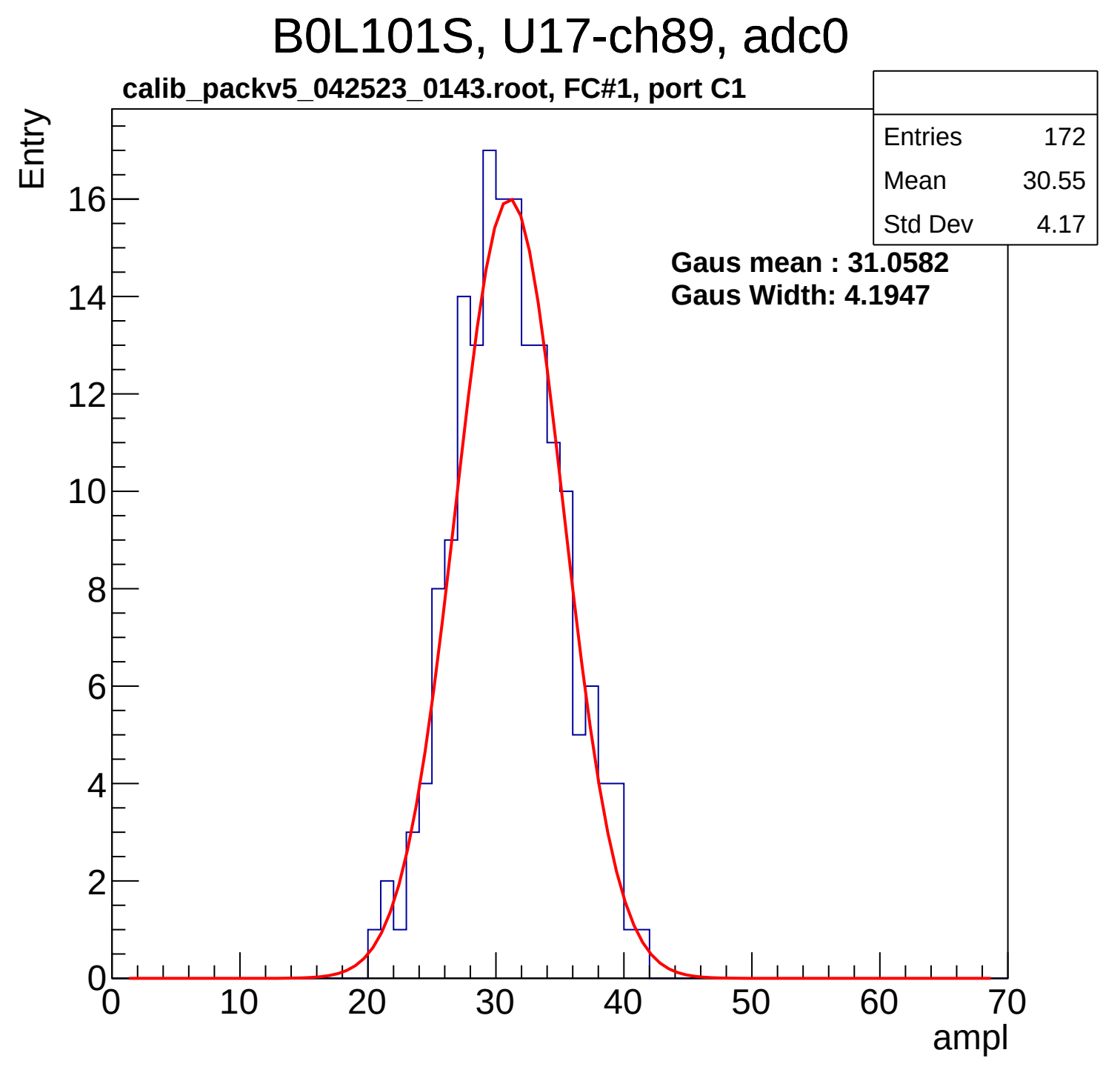

B0L101S, U17-ch88, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 48.92
Std Dev 3.878
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

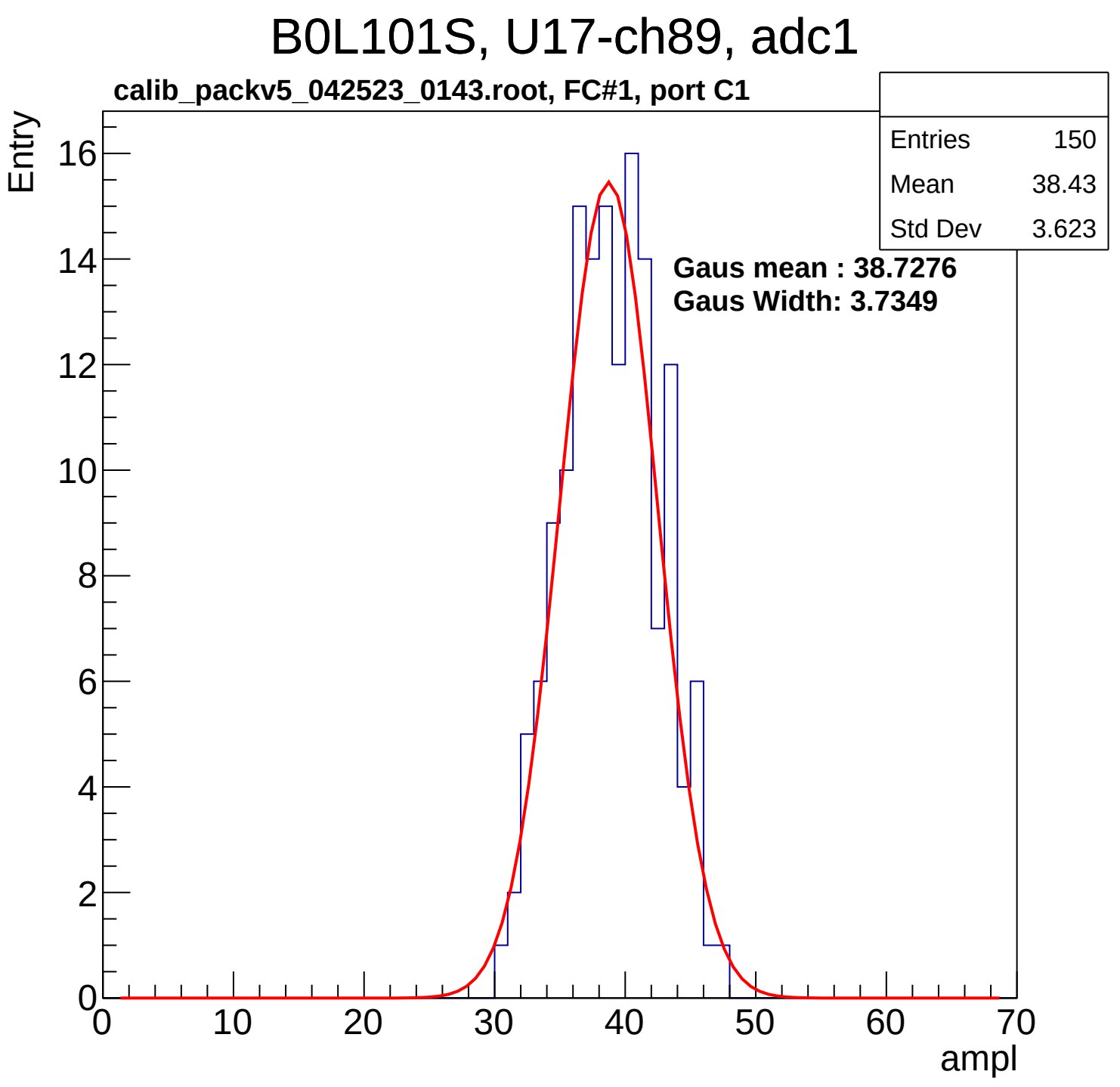

B0L101S, U17-ch88, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 55.22
Std Dev 3.379
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

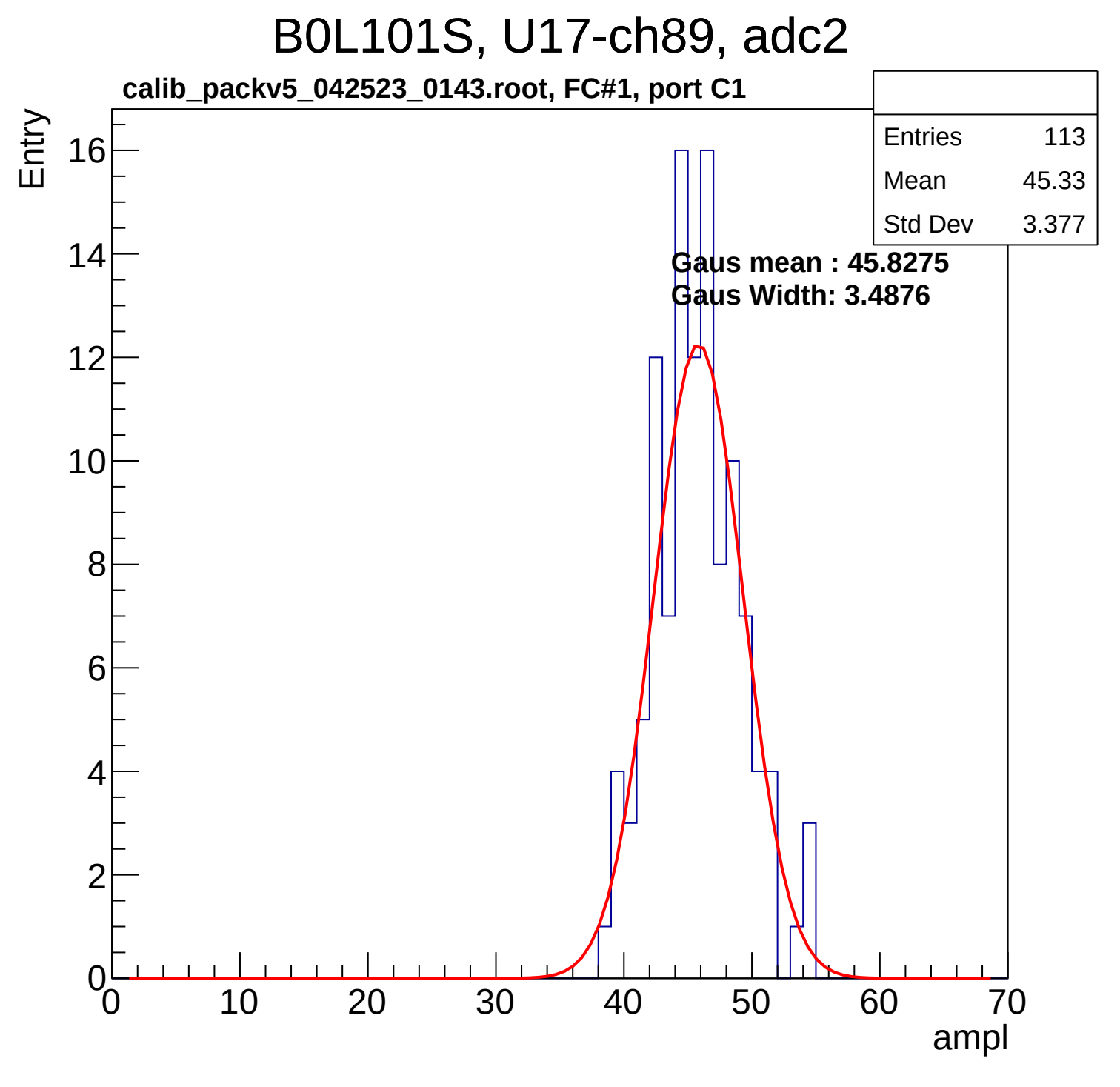

B0L101S, U17-ch88, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 58.2
Std Dev 9.288
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

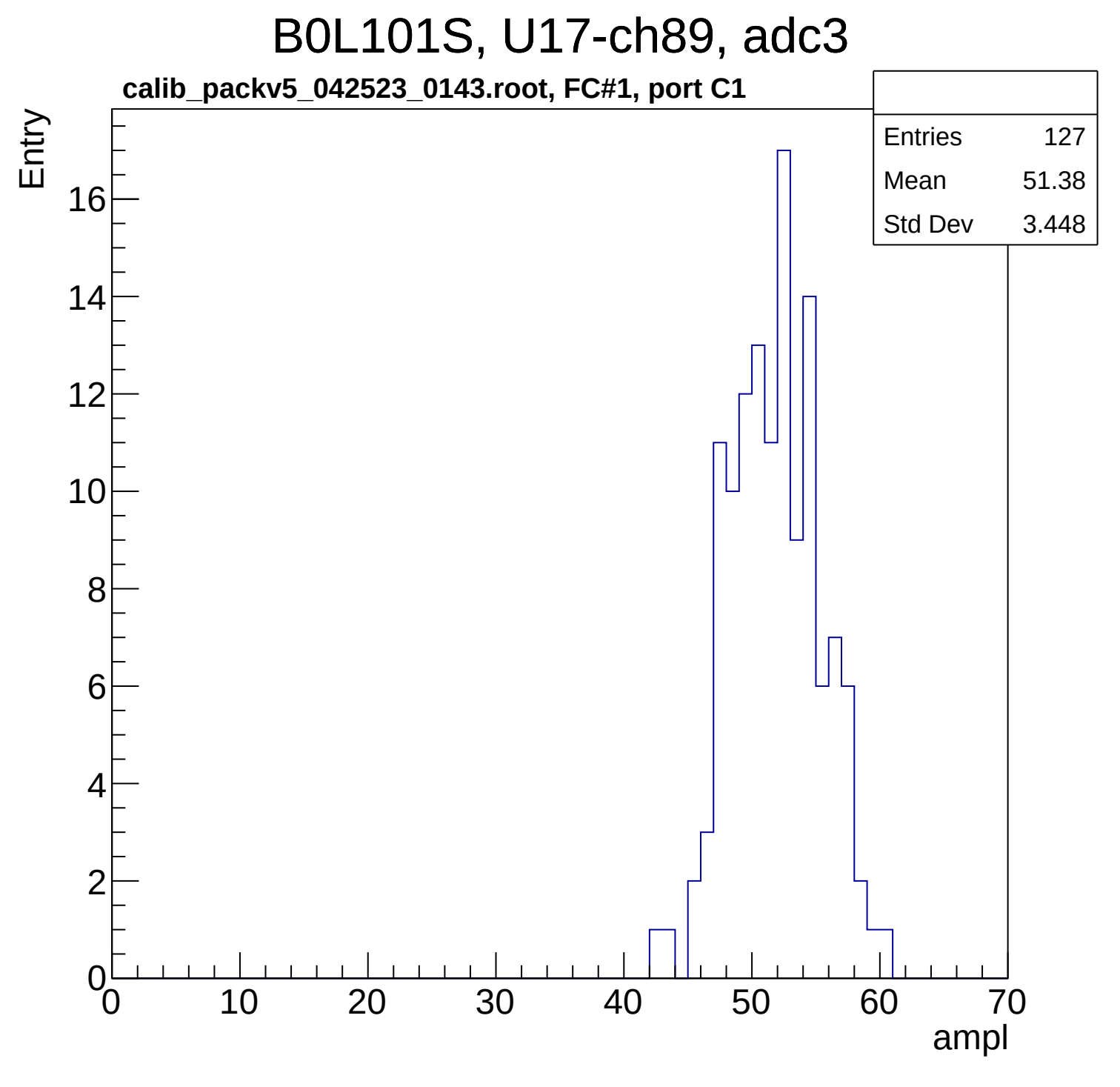

B0L101S, U17-ch88, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 19
Mean 62.16
Std Dev 0.9875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

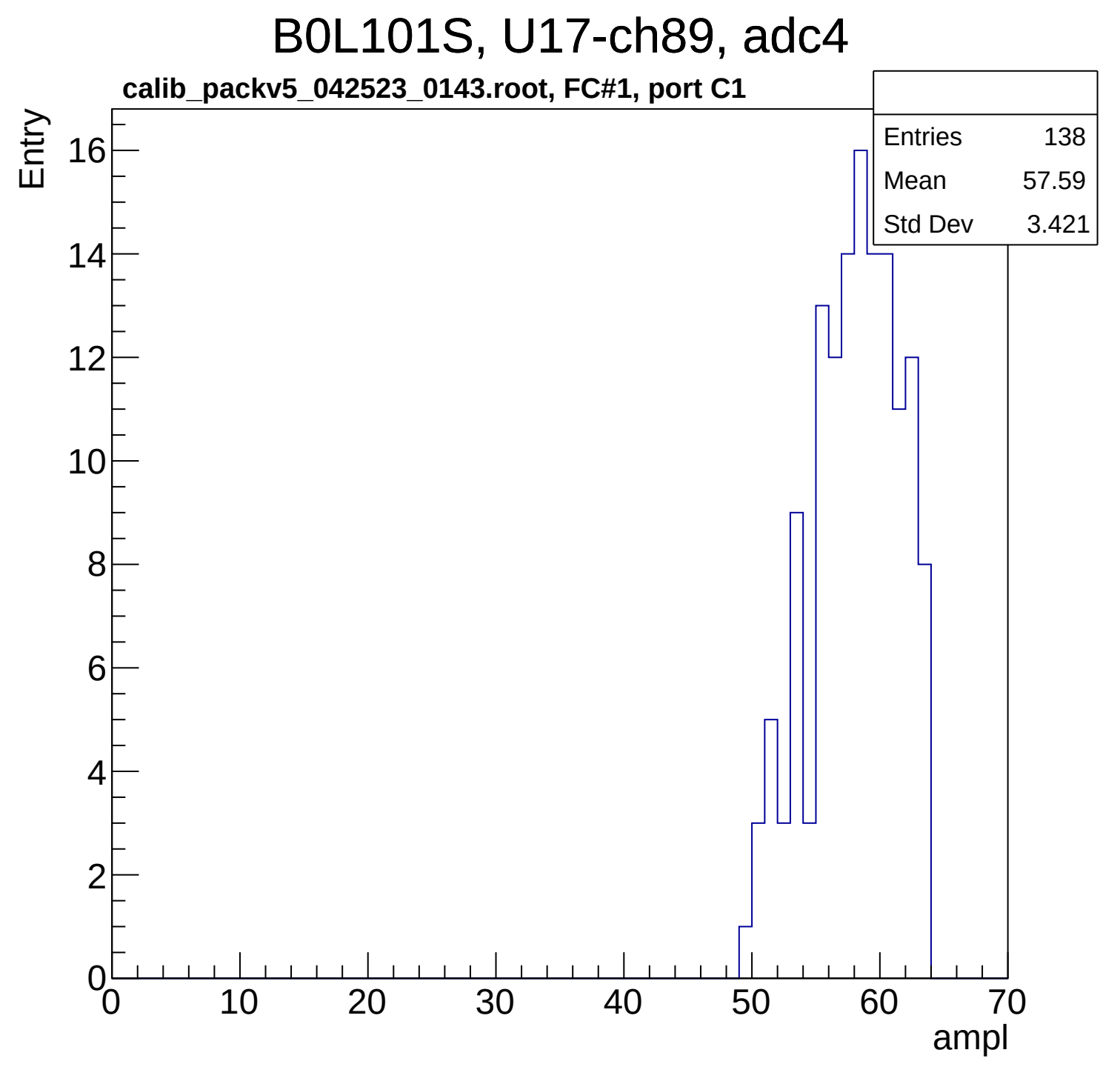

B0L101S, U17-ch88, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

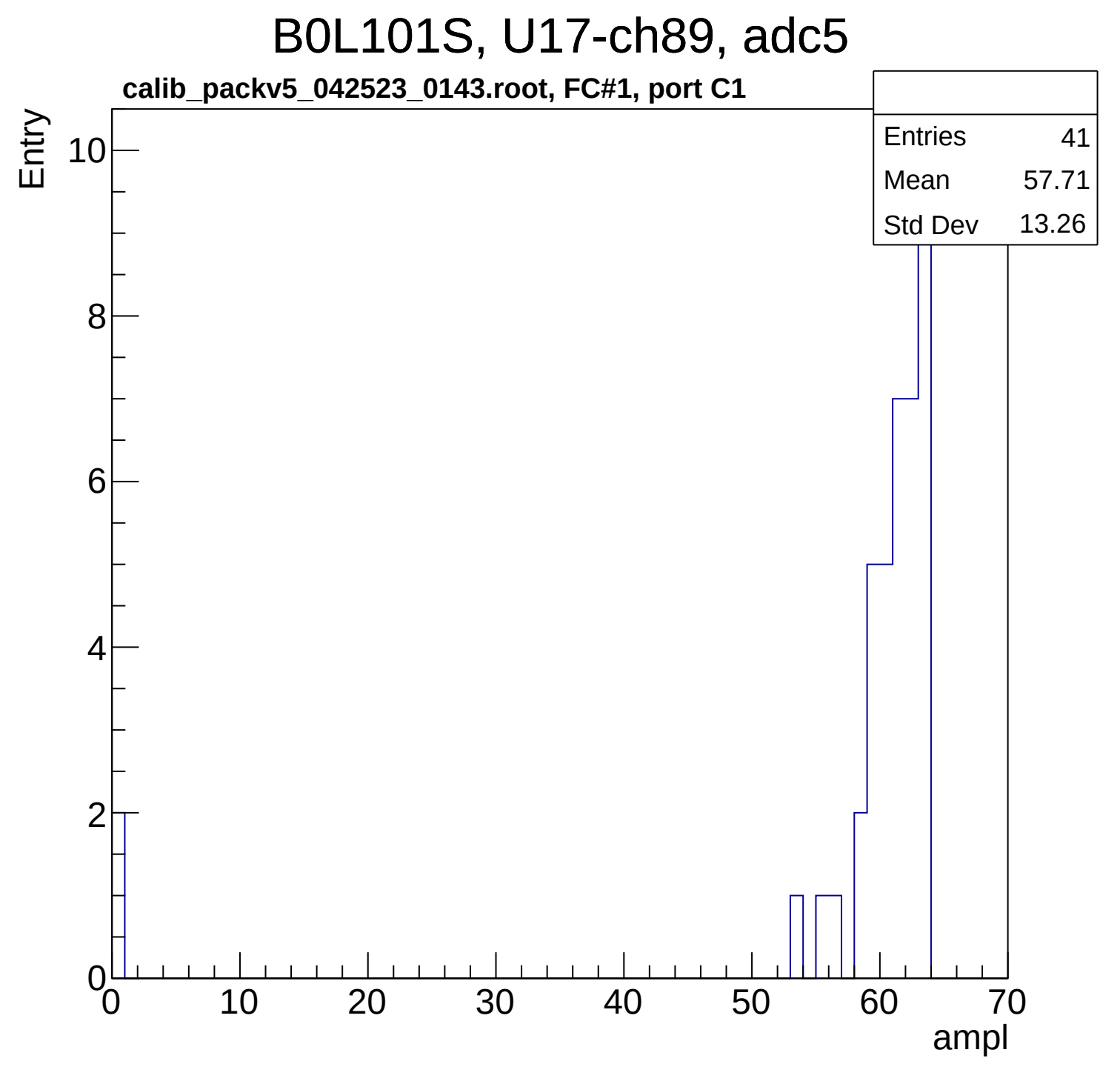

B0L101S, U17-ch89, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 172
Mean 30.55
Std Dev 4.17
Gaus mean : 31.0582
Gaus Width: 4.1947
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

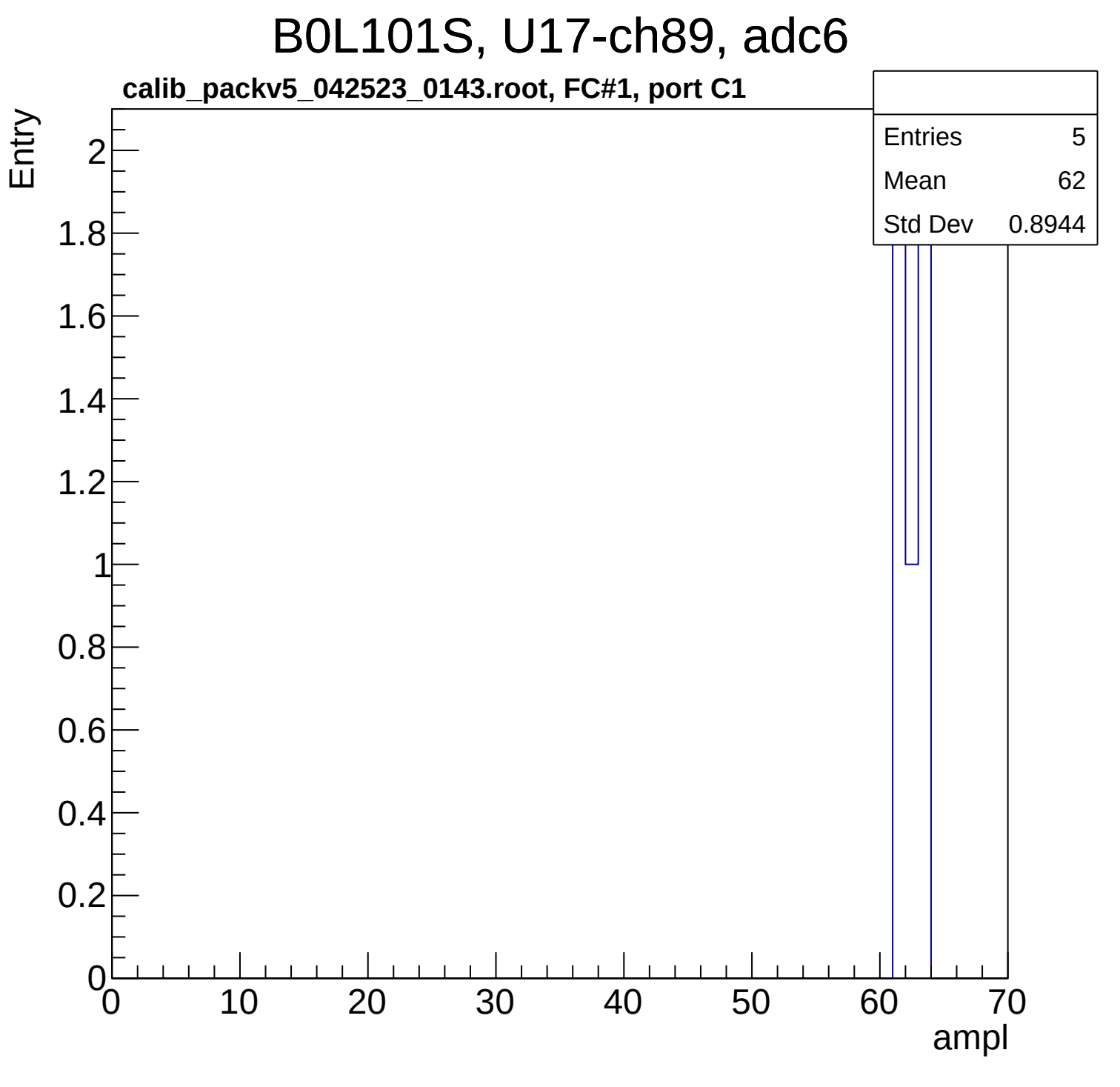

B0L101S, U17-ch89, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 38.43
Std Dev 3.623
Gaus mean : 38.7276
Gaus Width: 3.7349
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

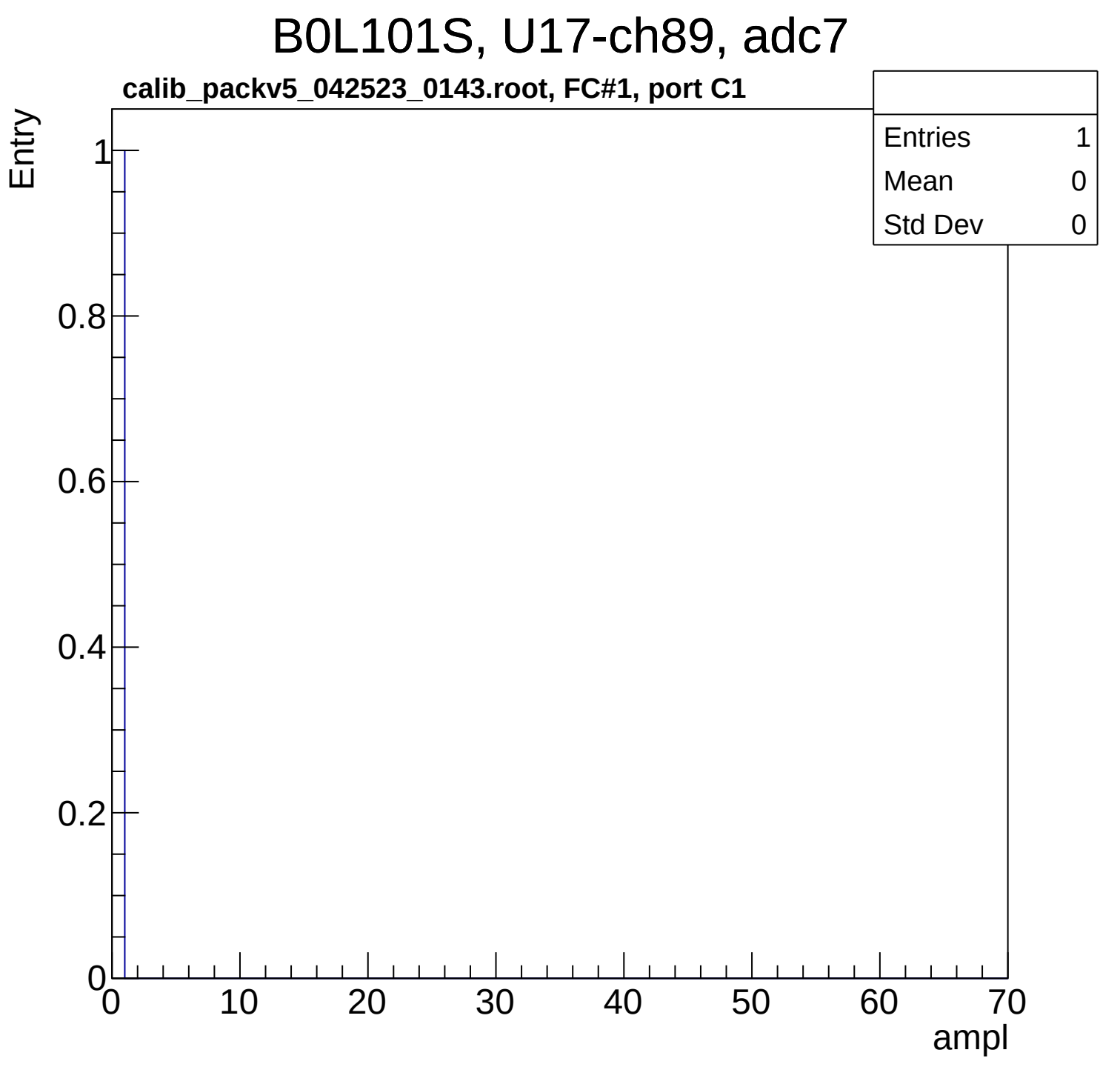

B0L101S, U17-ch89, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 45.33
Std Dev 3.377
Gaus mean : 45.8275
Gaus Width: 3.4876
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

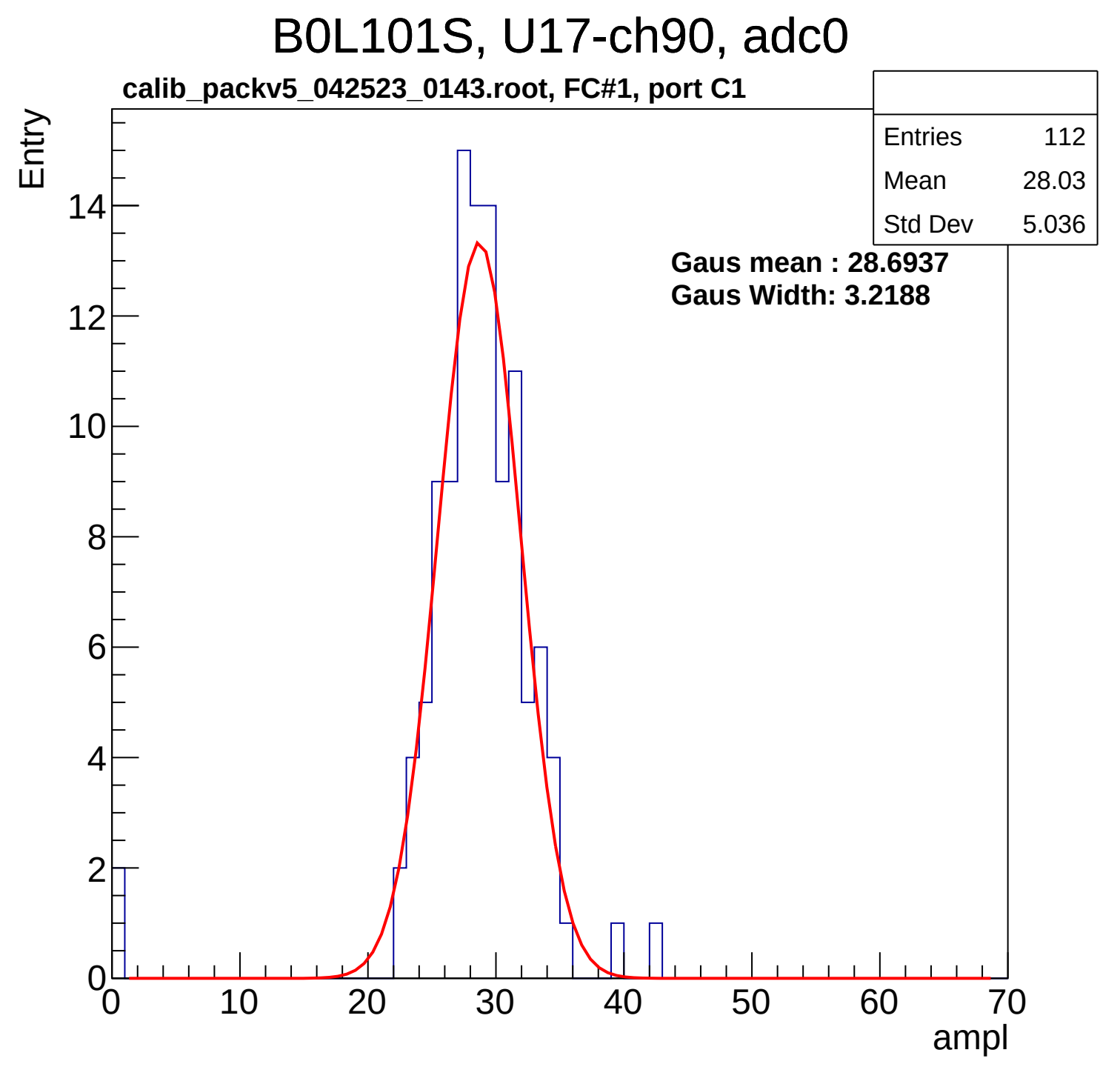

B0L101S, U17-ch89, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 51.38
Std Dev 3.448
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

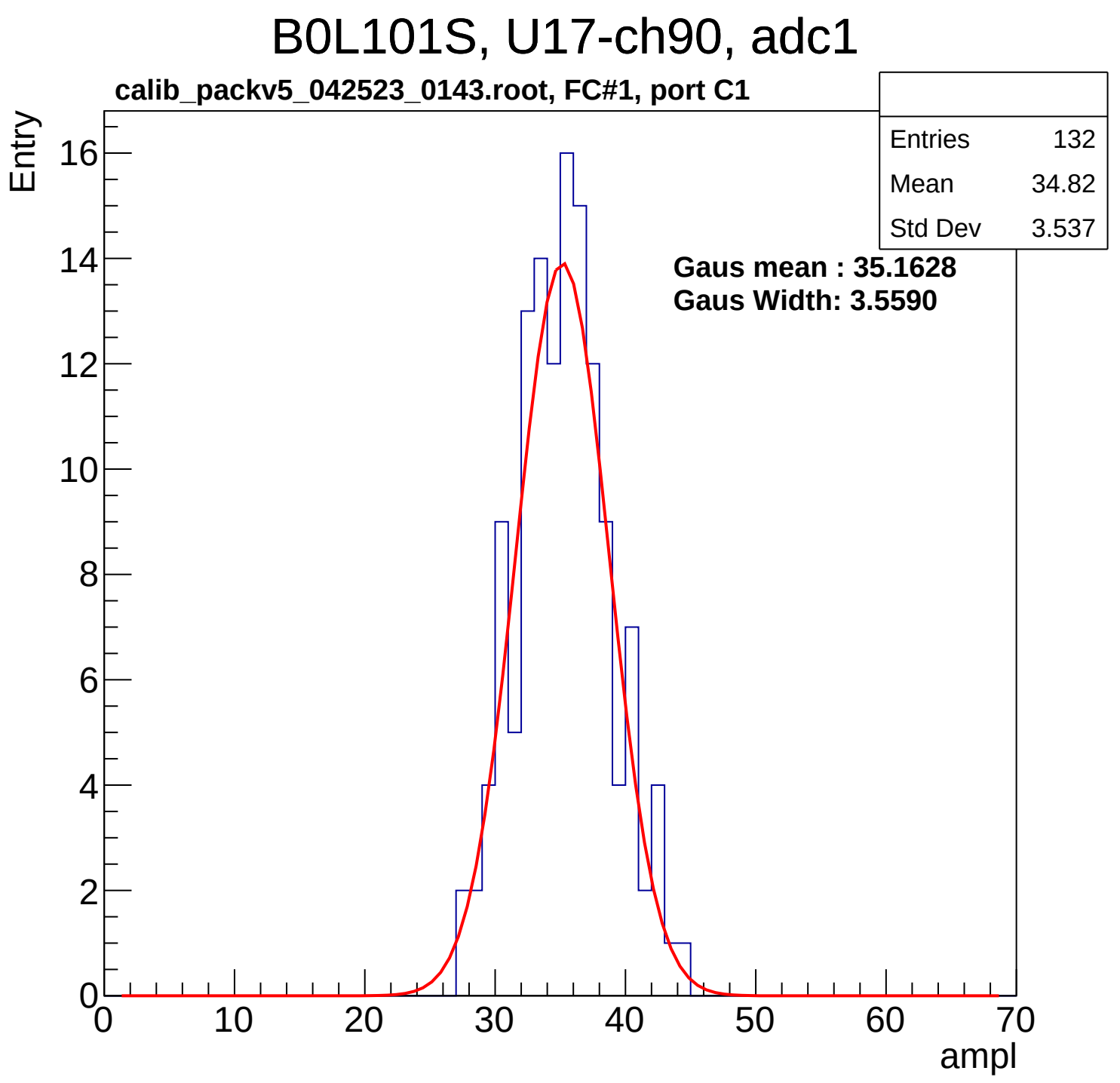

B0L101S, U17-ch89, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 57.59
Std Dev 3.421
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

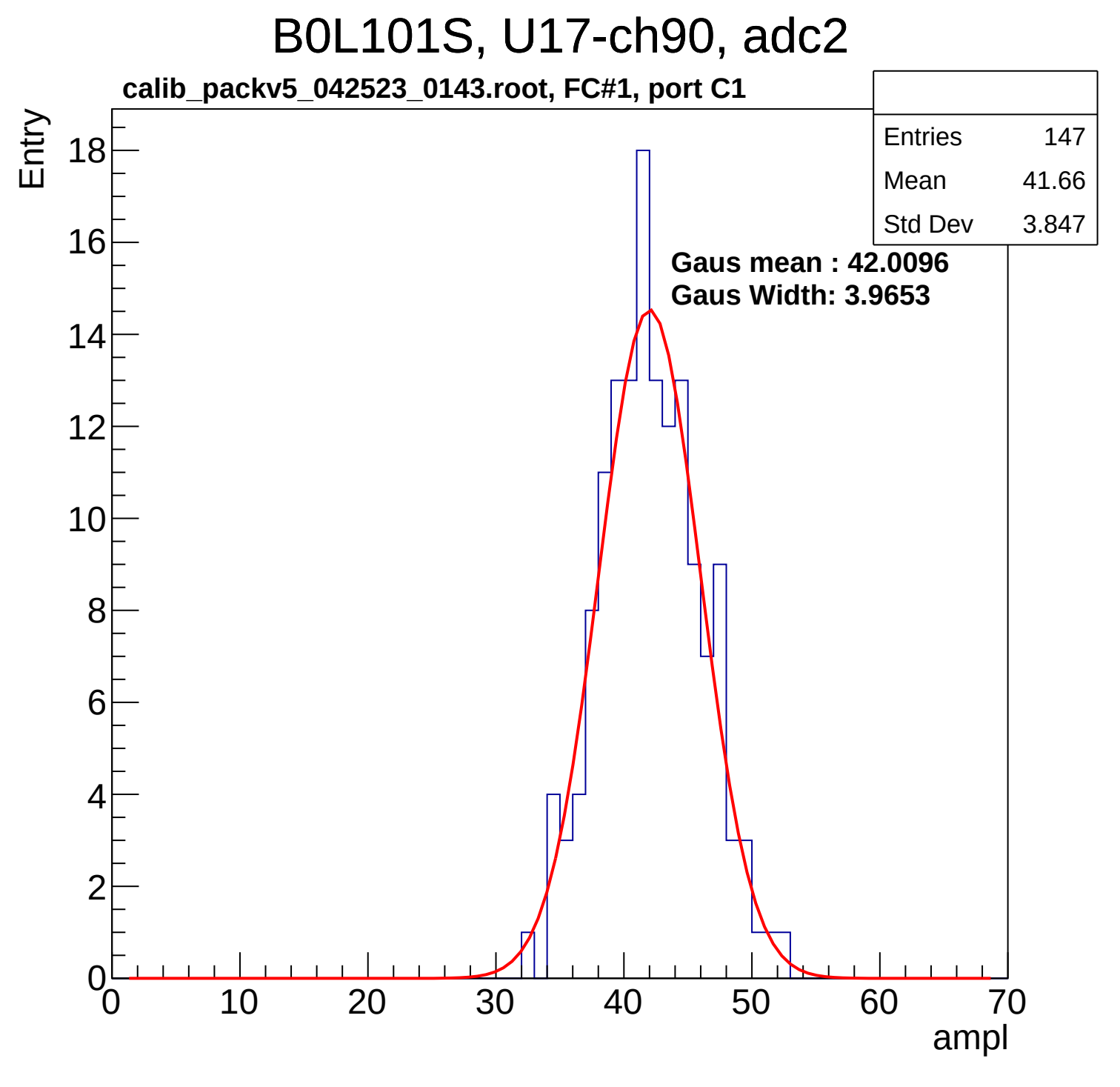

B0L101S, U17-ch89, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 41
Mean 57.71
Std Dev 13.26
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

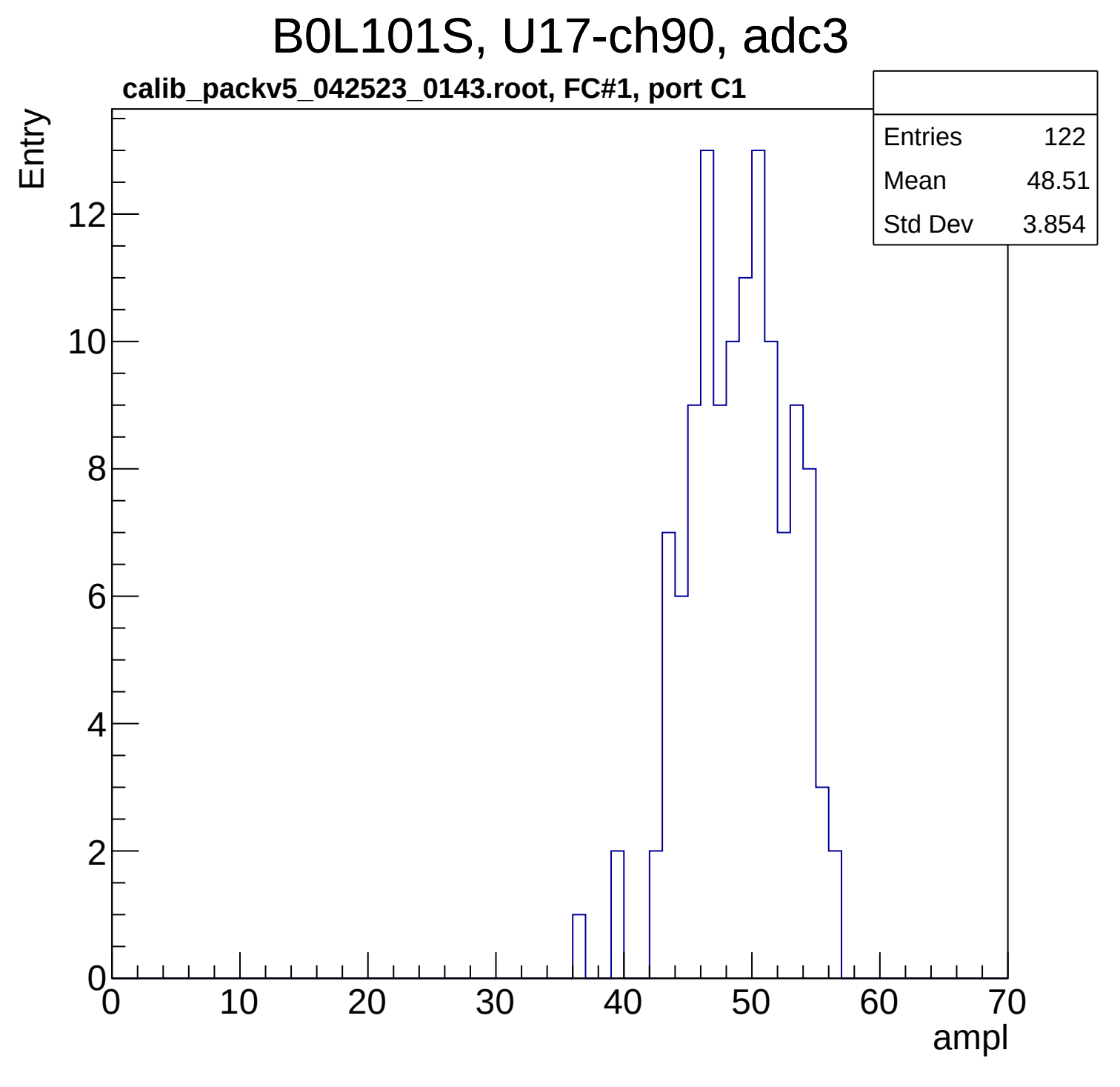

B0L101S, U17-ch89, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

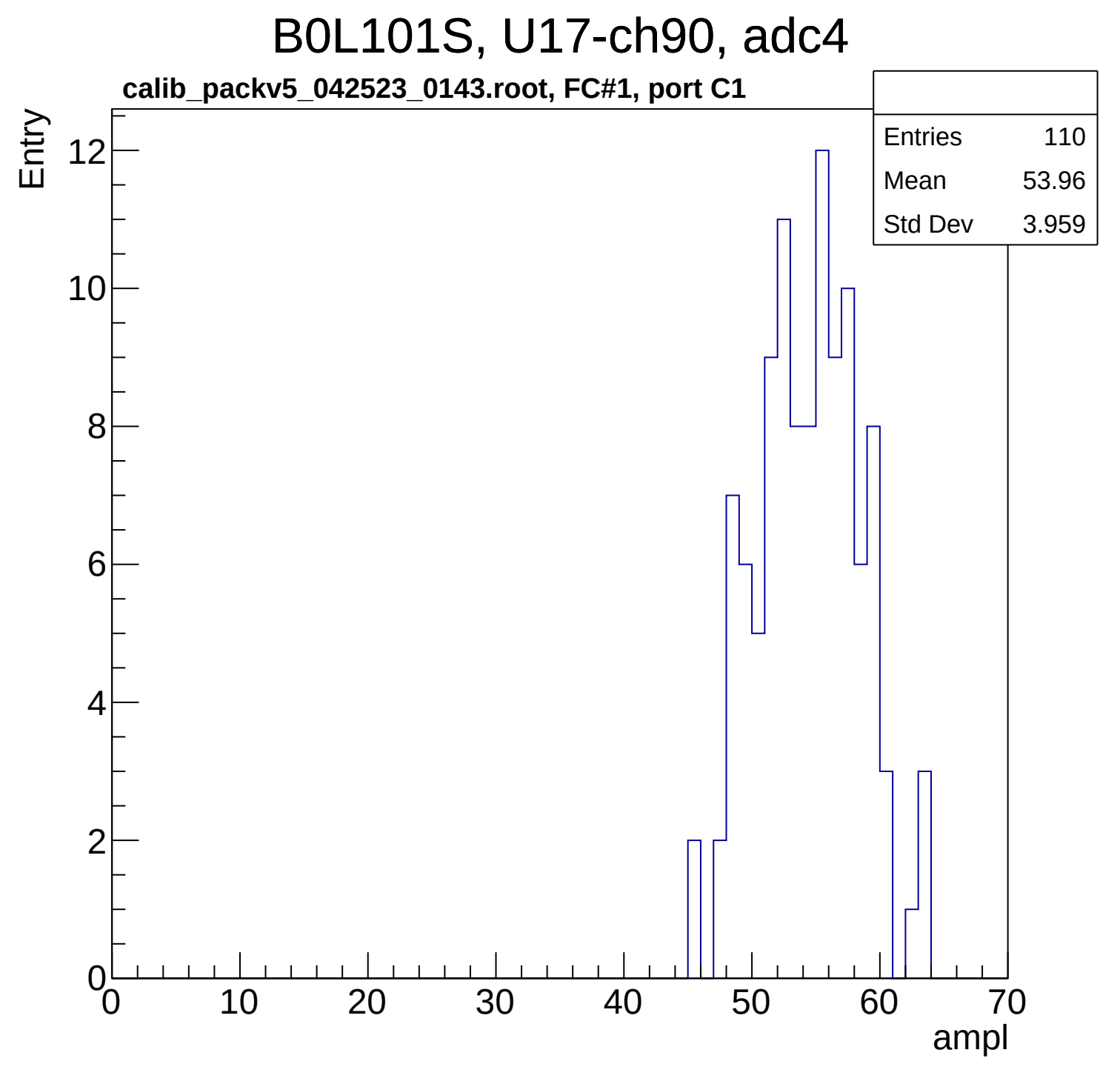

B0L101S, U17-ch89, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

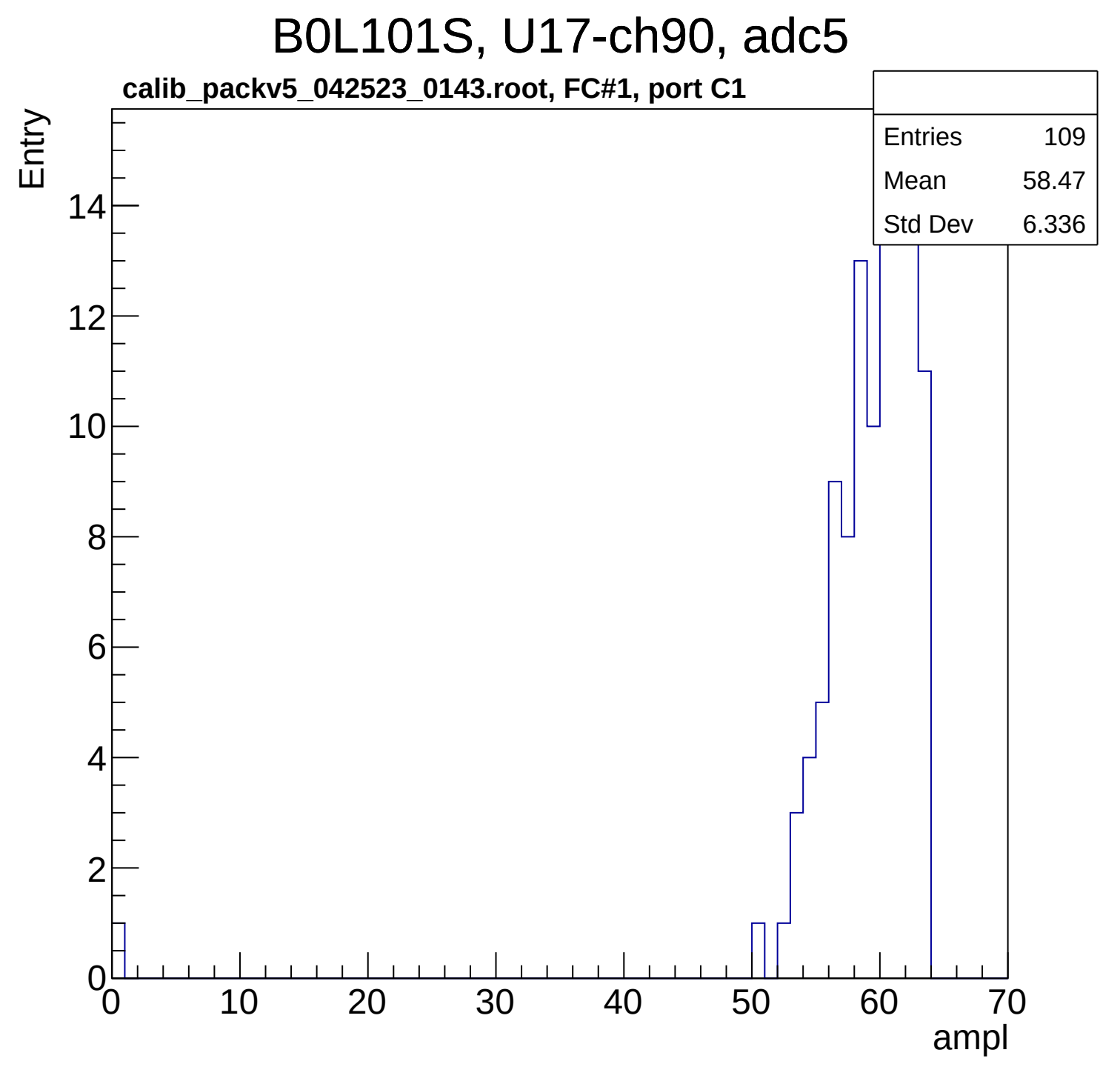

B0L101S, U17-ch90, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 112
Mean 28.03
Std Dev 5.036
Gaus mean : 28.6937
Gaus Width: 3.2188
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

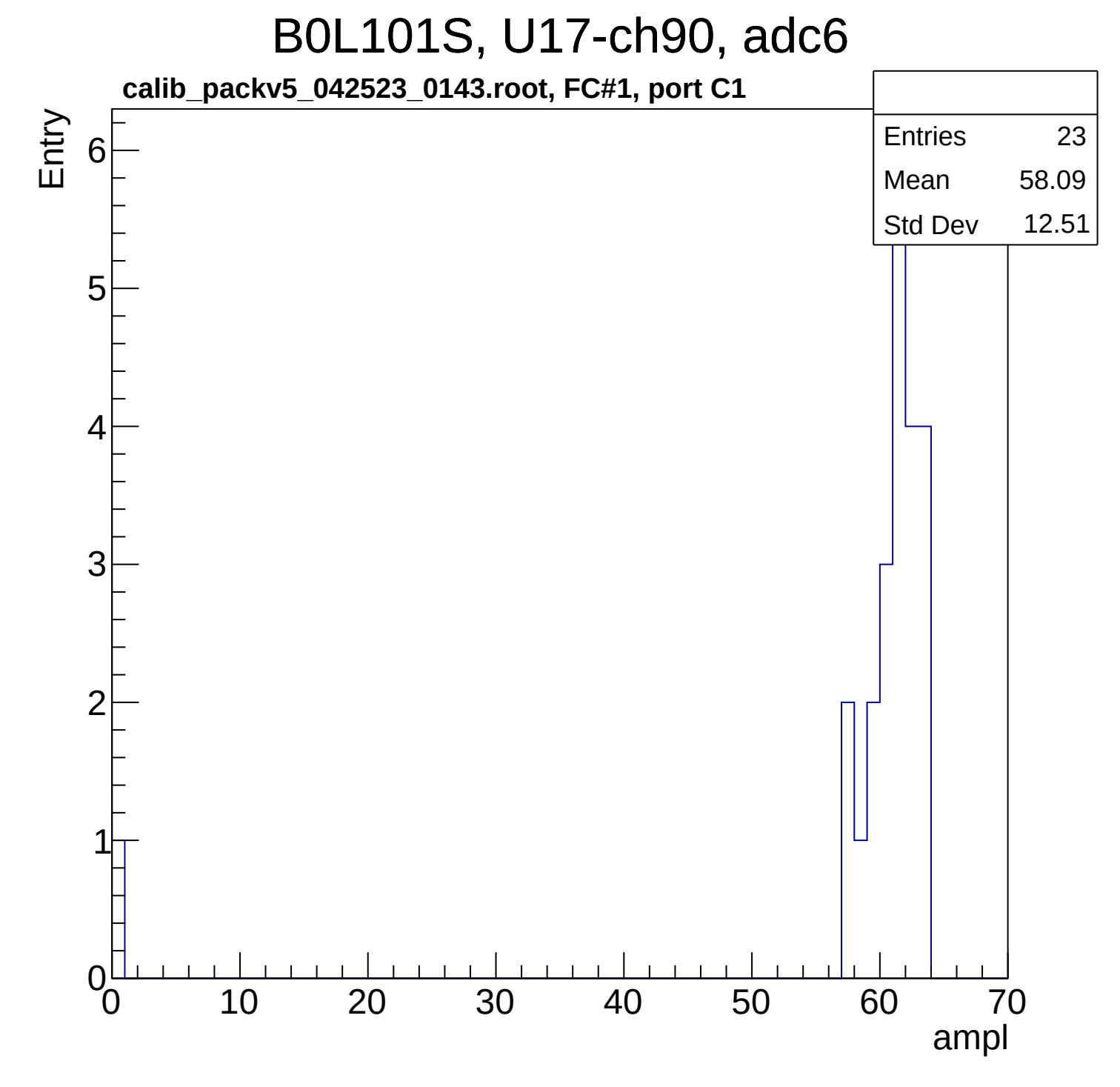

B0L101S, U17-ch90, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 34.82
Std Dev 3.537
Gaus mean : 35.1628
Gaus Width: 3.5590
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

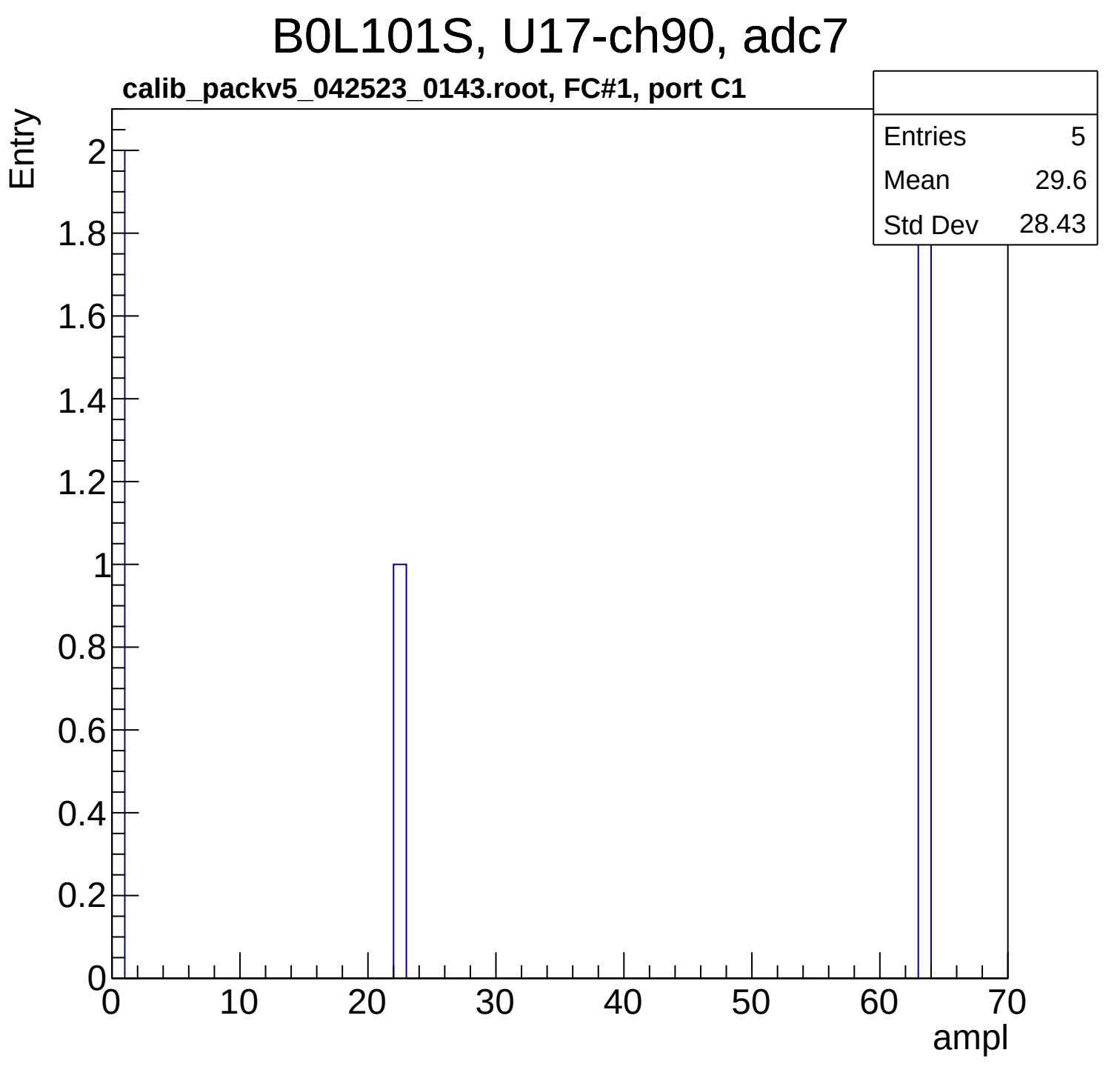

B0L101S, U17-ch90, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 41.66
Std Dev 3.847
Gaus mean : 42.0096
Gaus Width: 3.9653
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

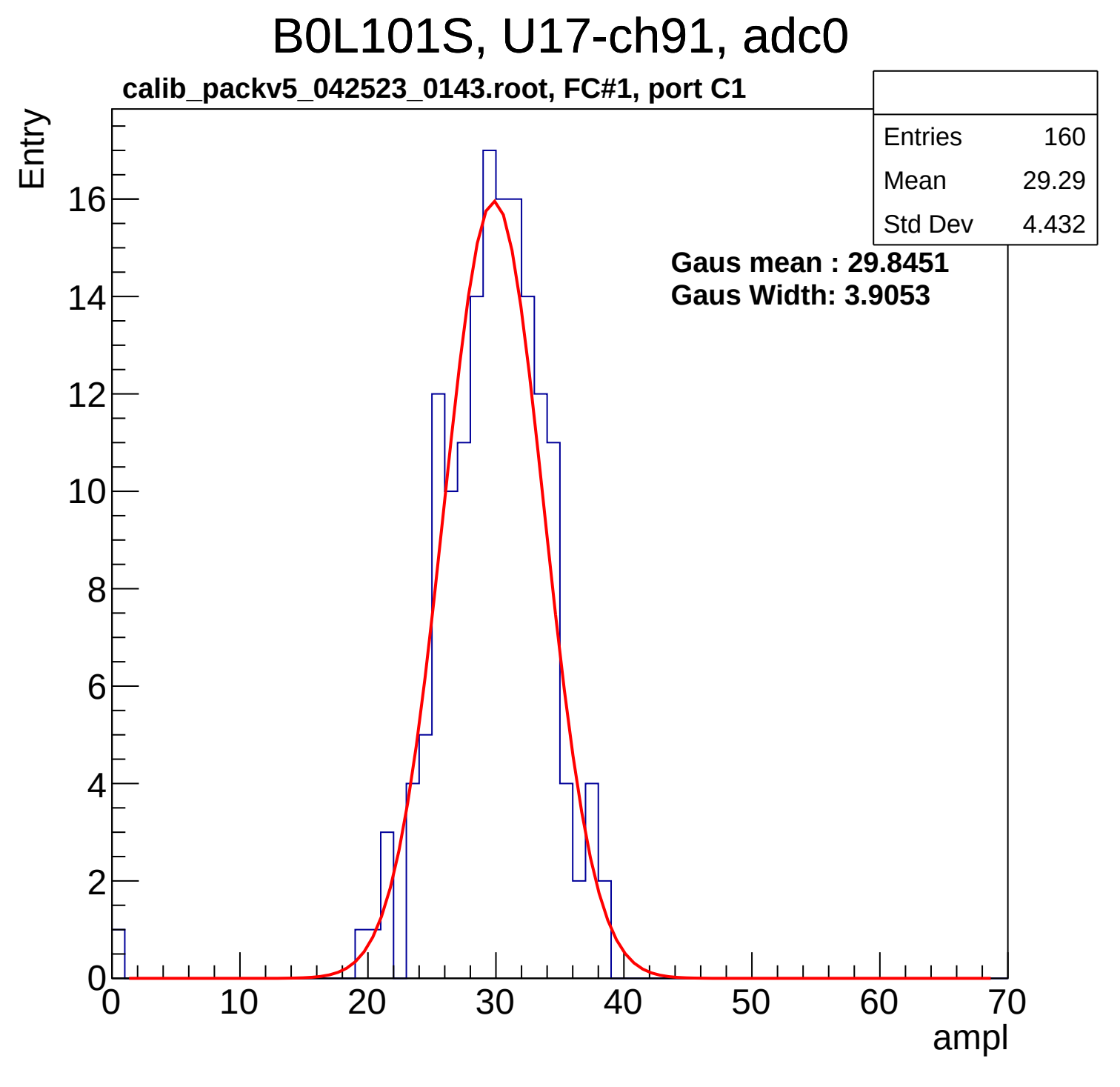

B0L101S, U17-ch90, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 48.51
Std Dev 3.854
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

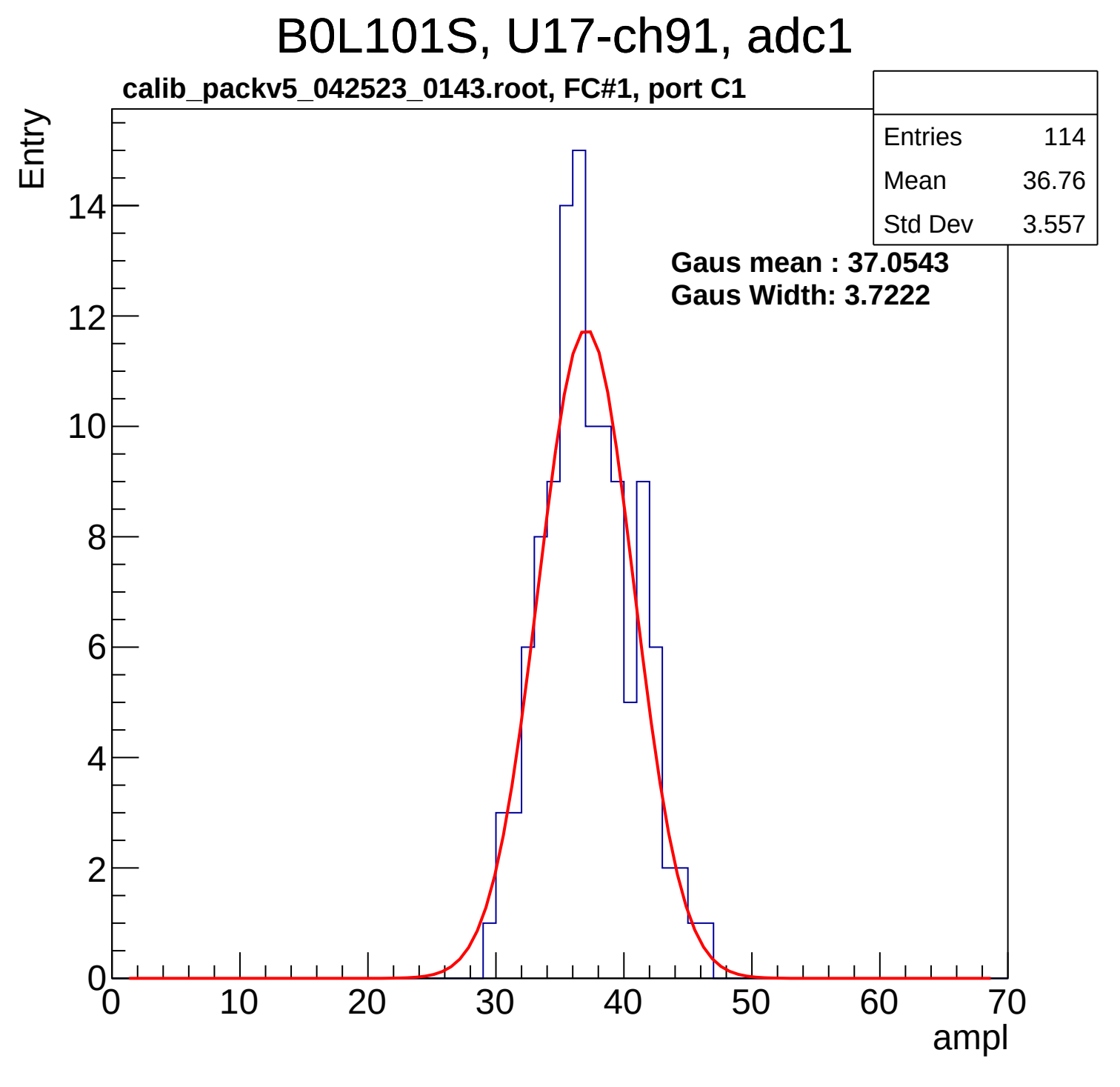

B0L101S, U17-ch90, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 53.96
Std Dev 3.959
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

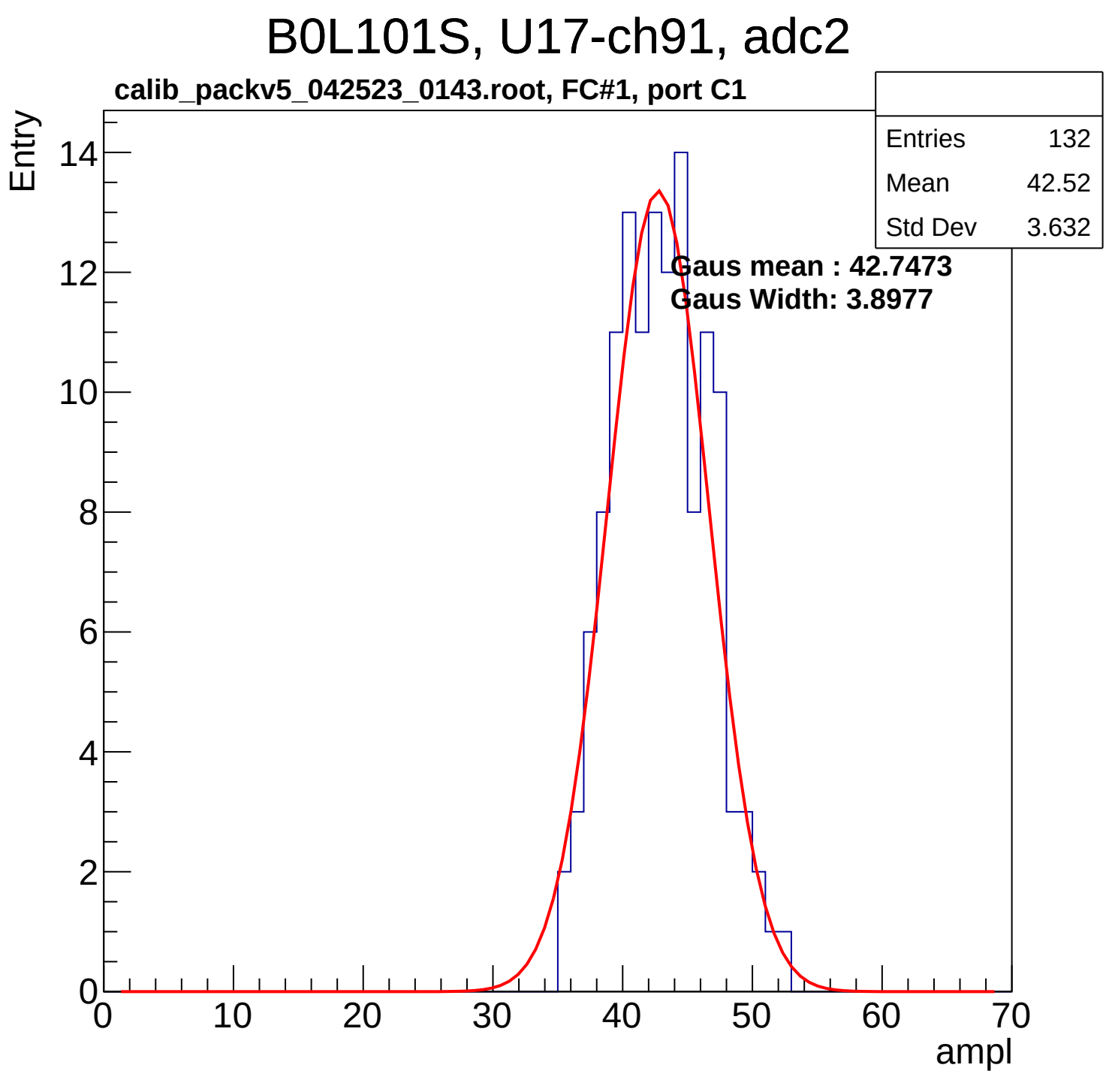

B0L101S, U17-ch90, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 58.47
Std Dev 6.336
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

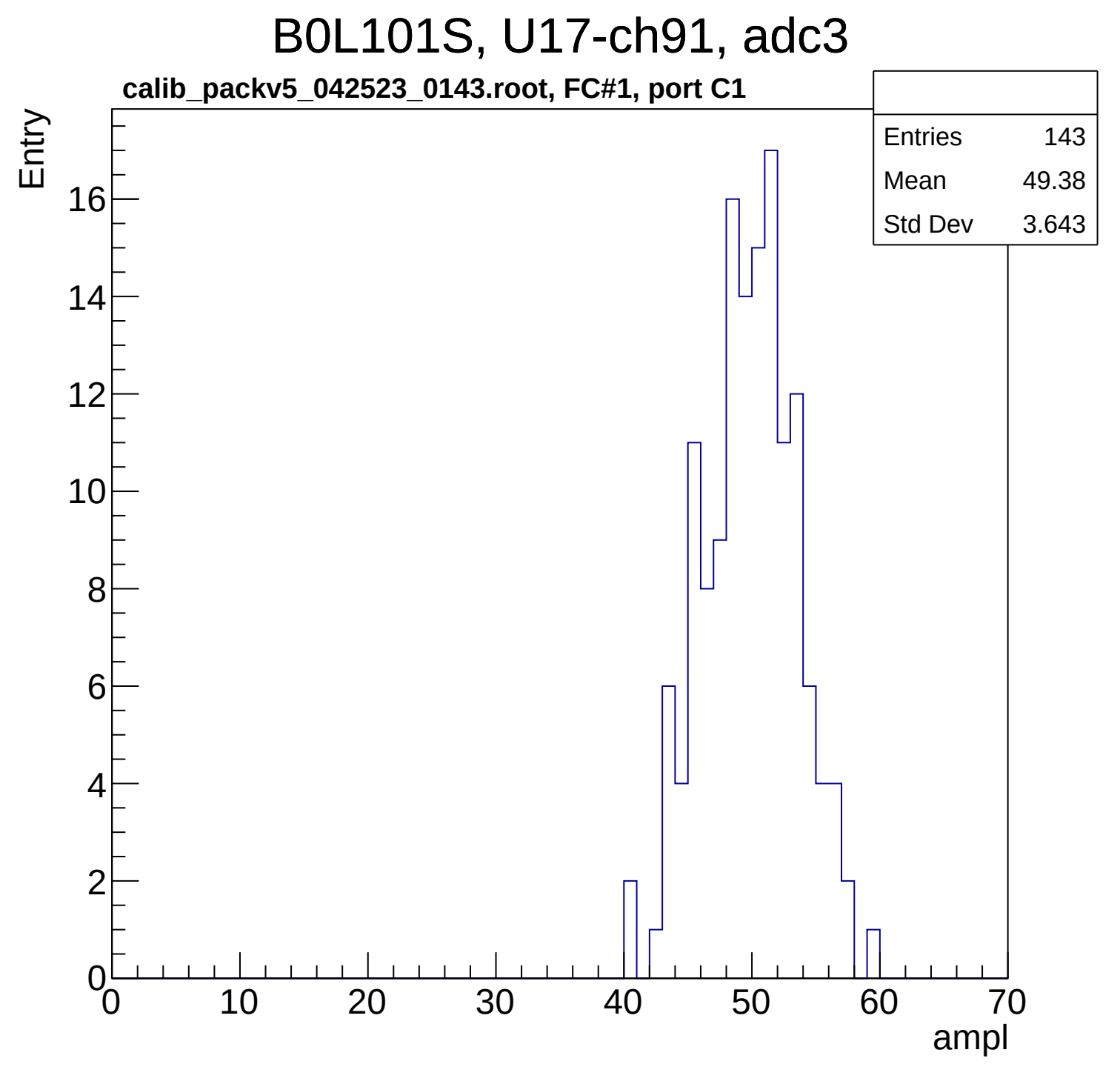

B0L101S, U17-ch90, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 23
Mean 58.09
Std Dev 12.51
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

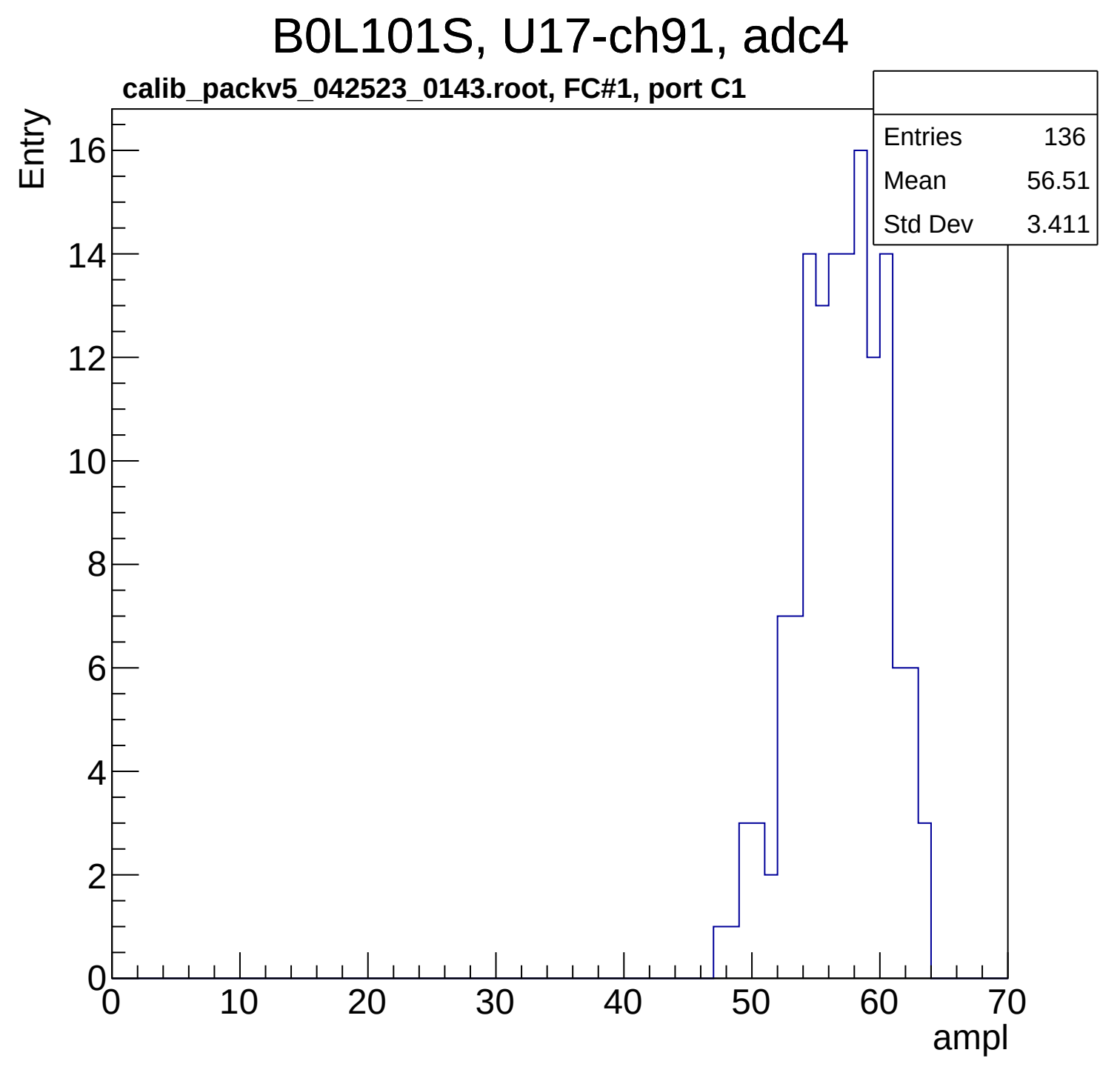

B0L101S, U17-ch90, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 29.6
Std Dev 28.43
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

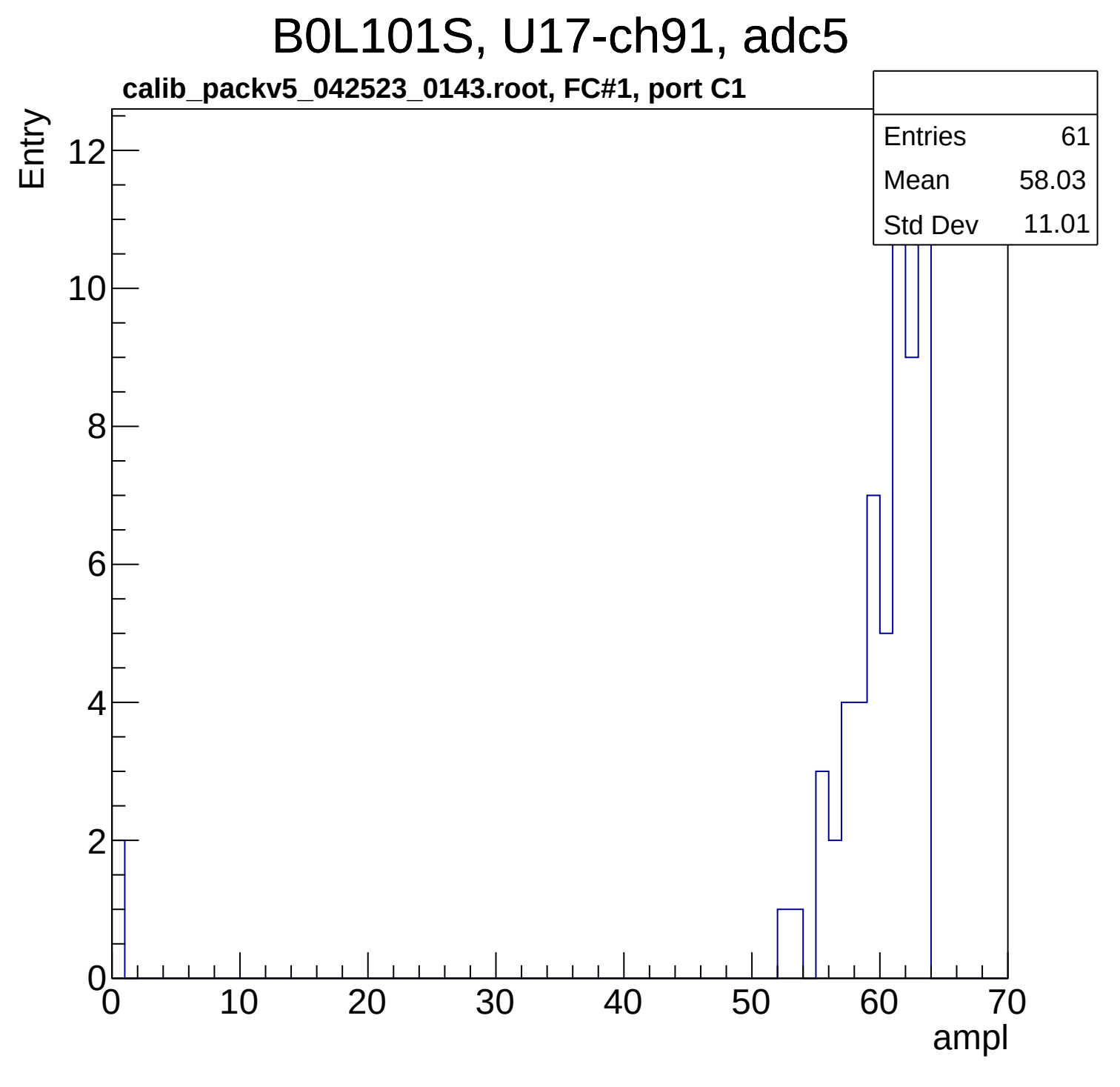

B0L101S, U17-ch91, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 160
Mean 29.29
Std Dev 4.432
Gaus mean : 29.8451
Gaus Width: 3.9053
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

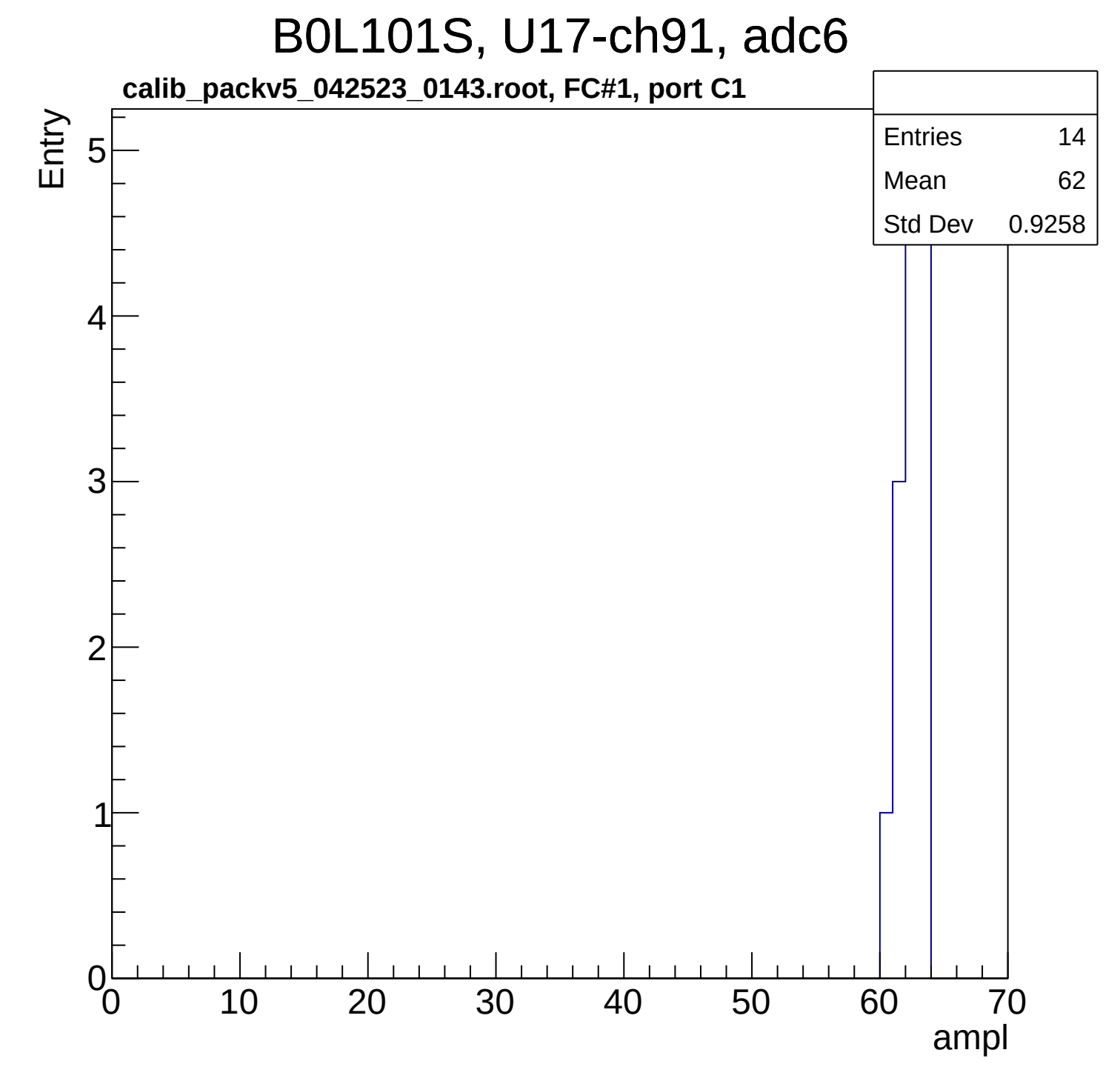

B0L101S, U17-ch91, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 36.76
Std Dev 3.557
Gaus mean : 37.0543
Gaus Width: 3.7222
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

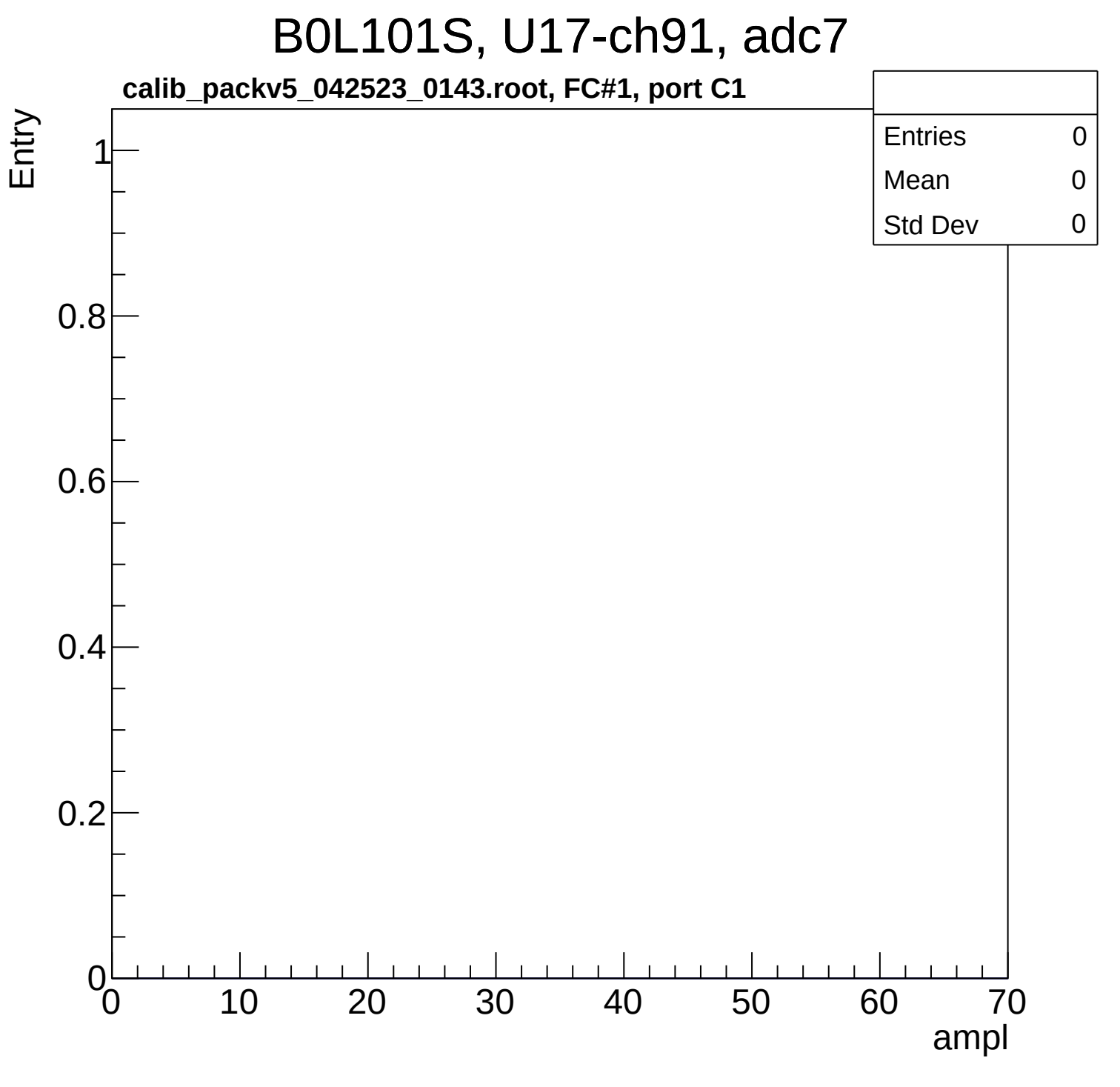

B0L101S, U17-ch91, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 42.52
Std Dev 3.632
Gaus mean : 42.7473
Gaus Width: 3.8977
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

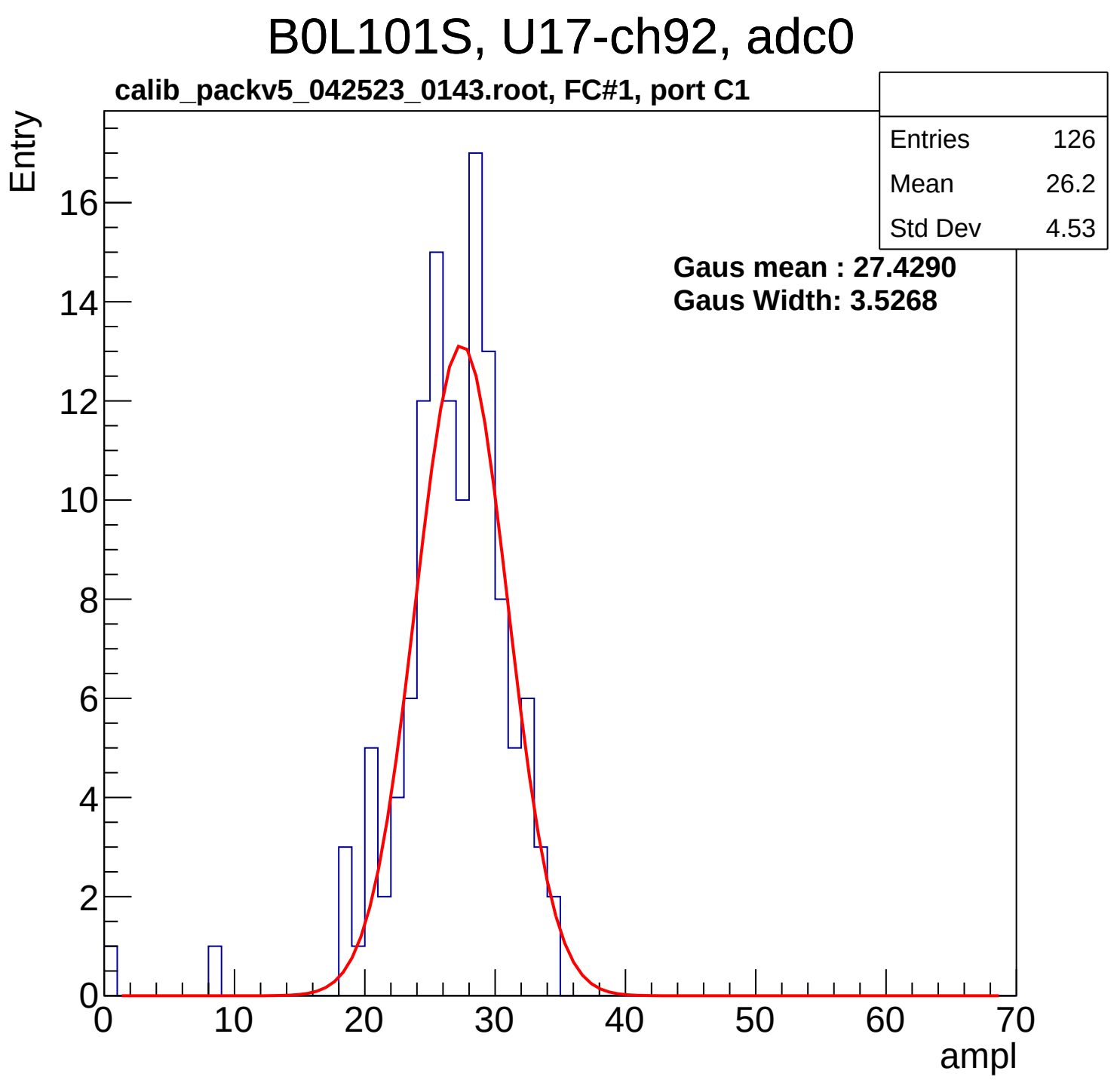

B0L101S, U17-ch91, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 49.38
Std Dev 3.643
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

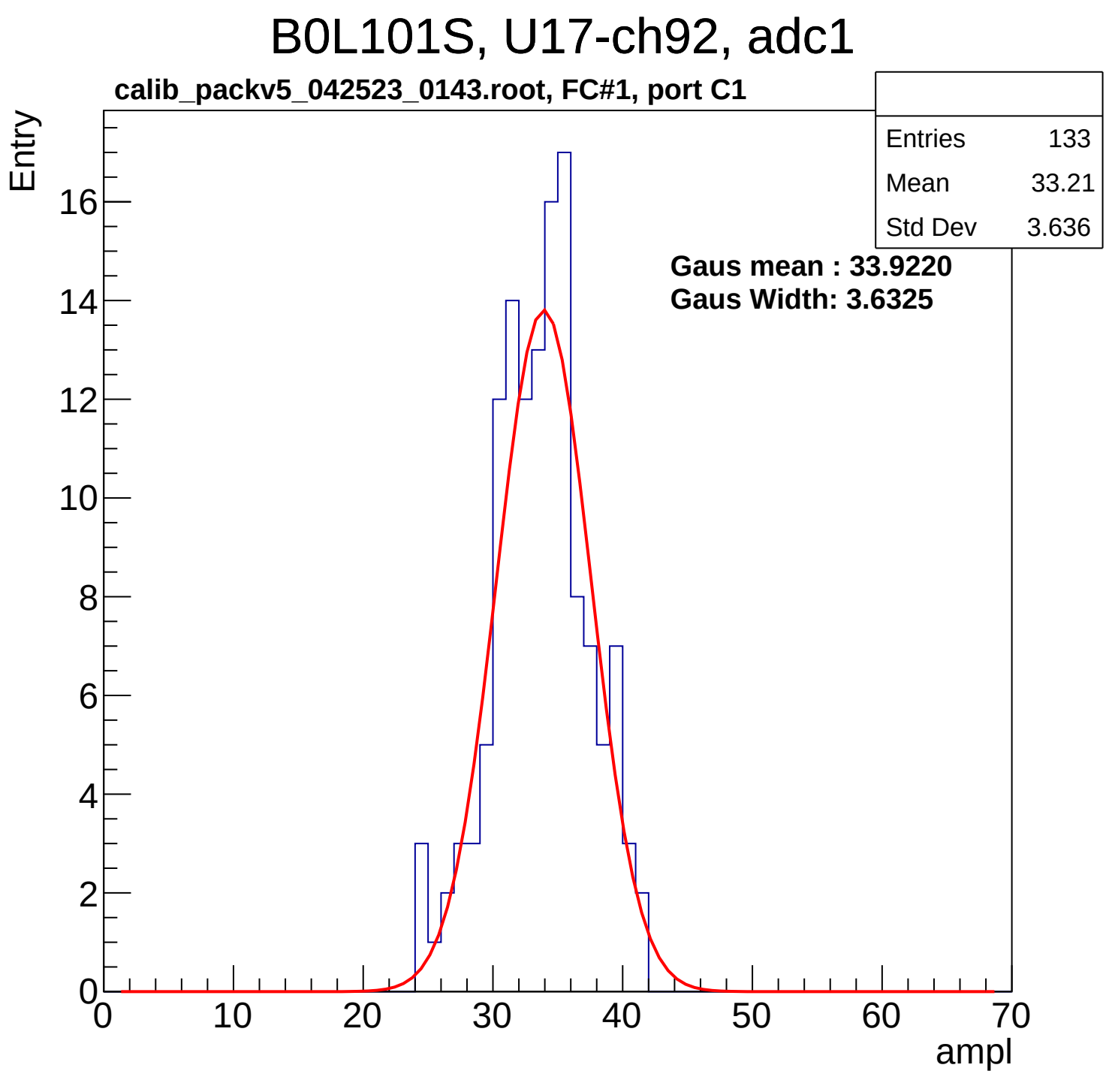

B0L101S, U17-ch91, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 56.51
Std Dev 3.411
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

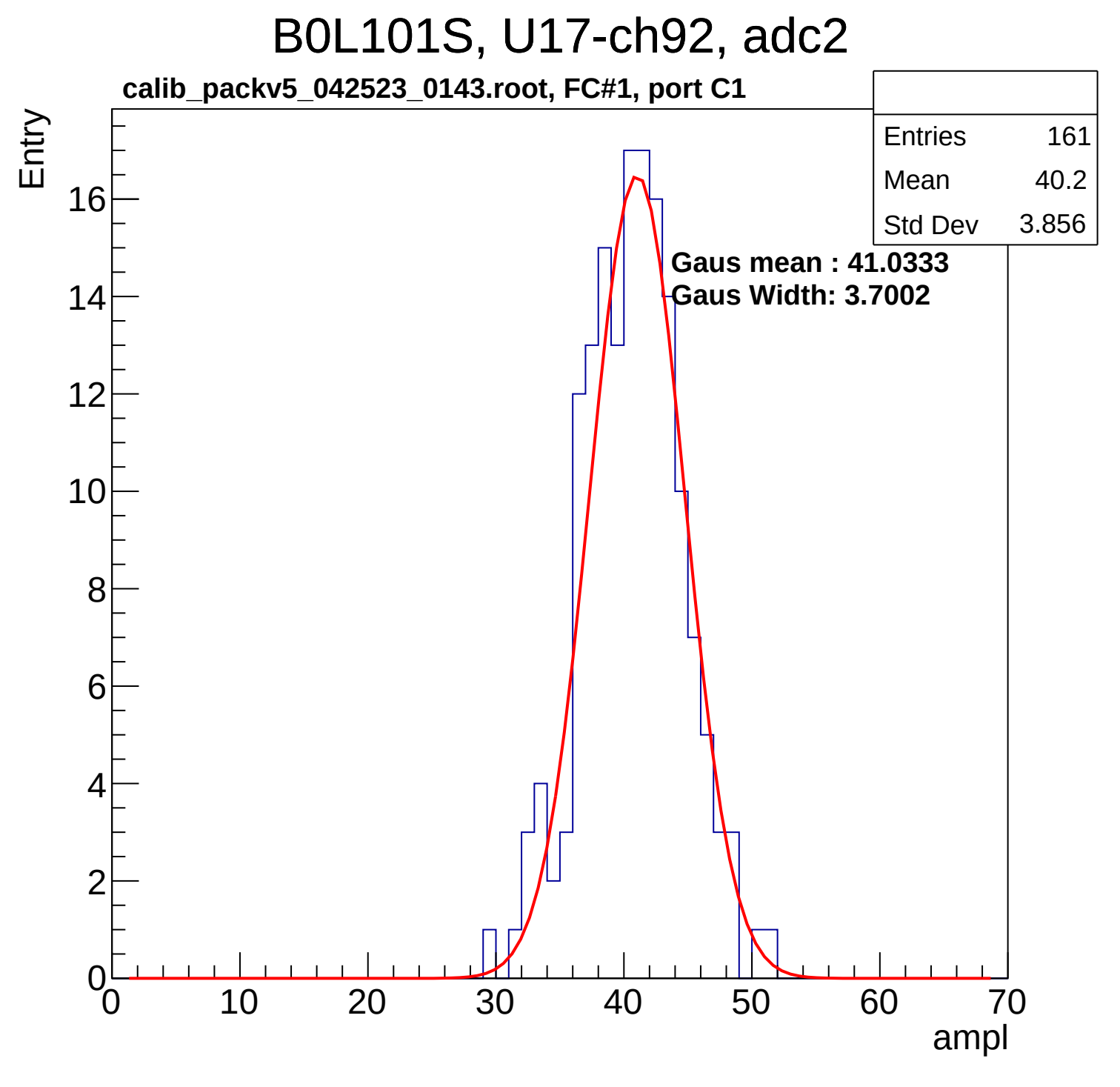

B0L101S, U17-ch91, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 61
Mean 58.03
Std Dev 11.01
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

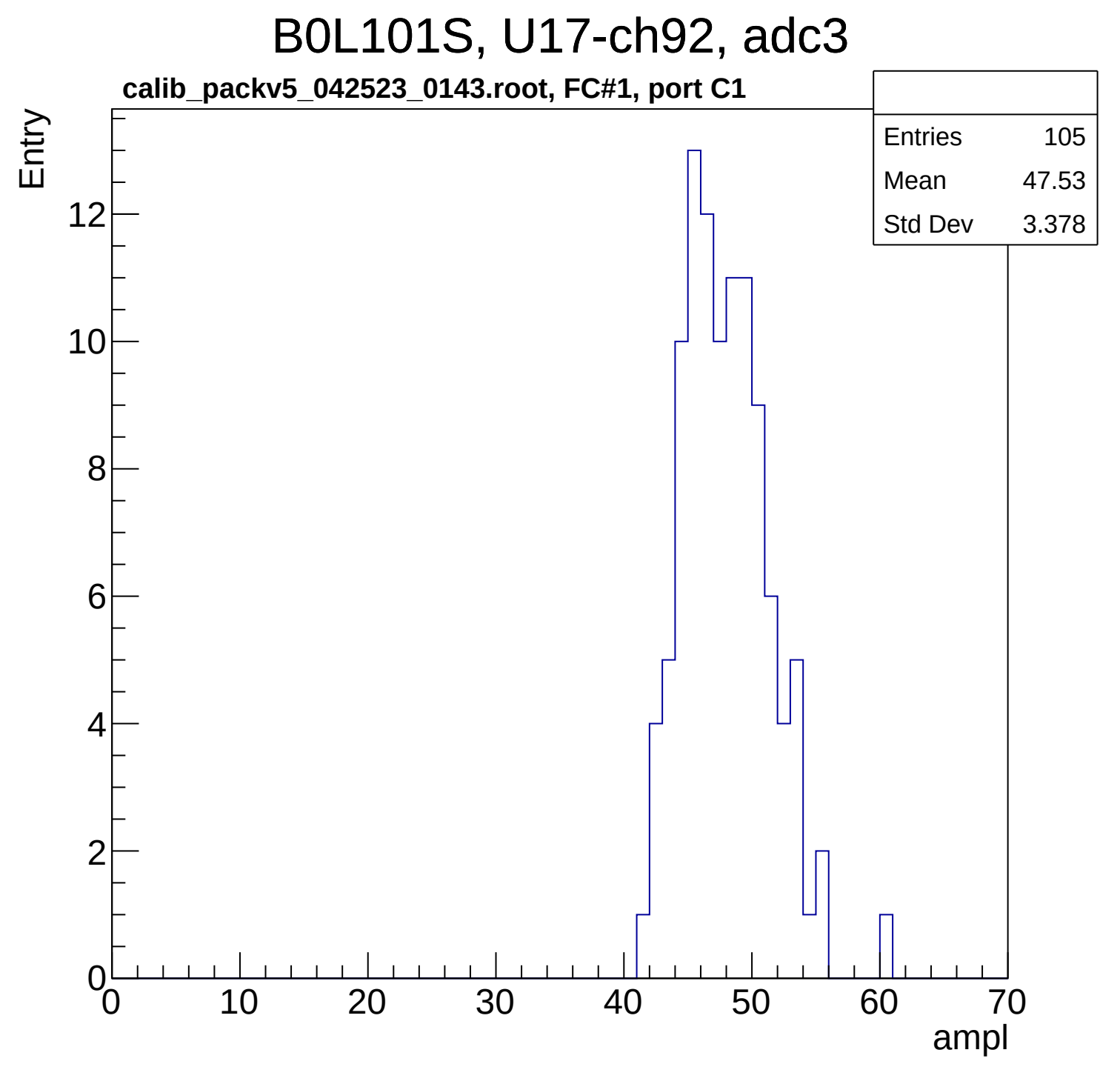

B0L101S, U17-ch91, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 62
Std Dev 0.9258
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

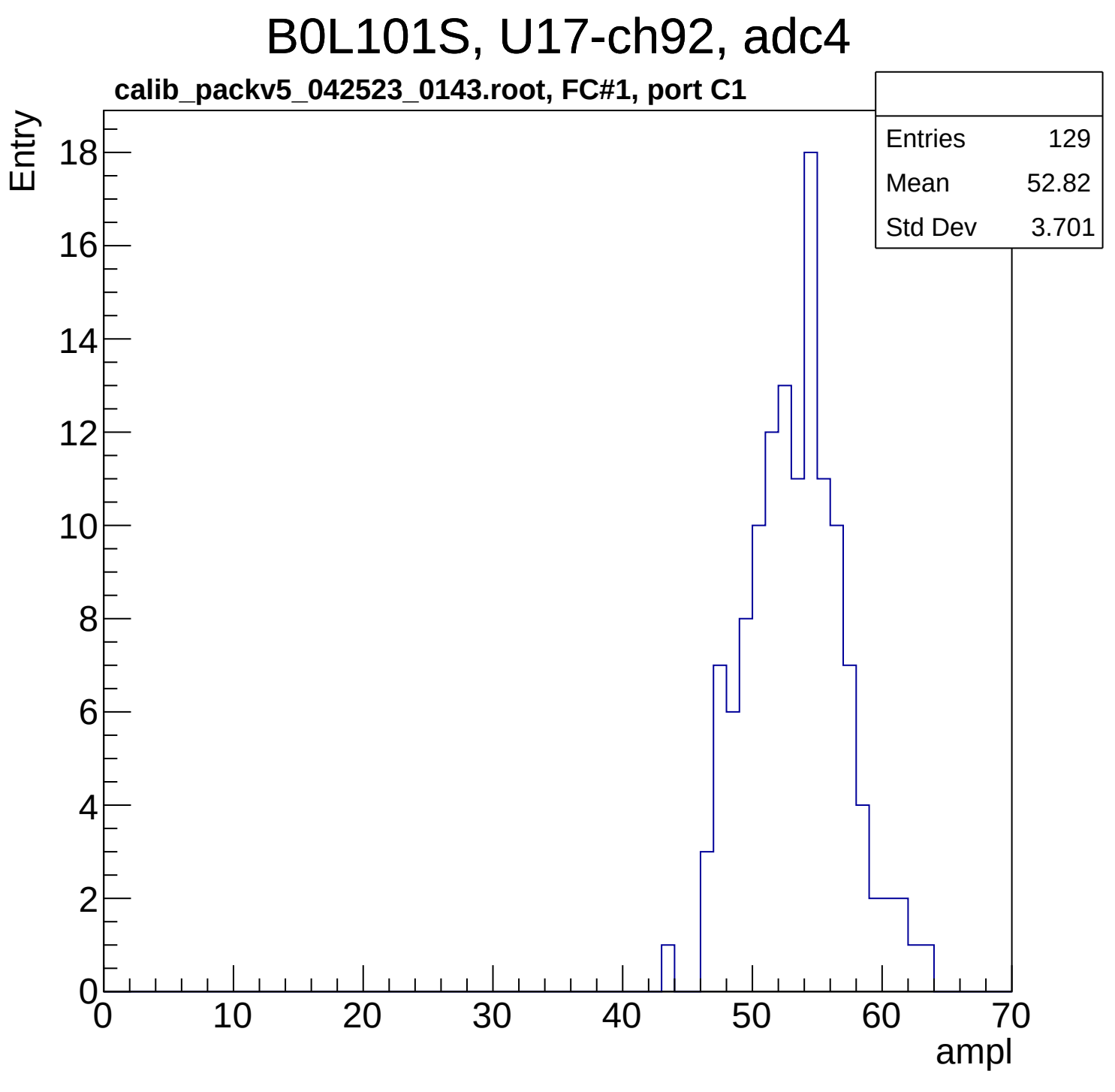

B0L101S, U17-ch91, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

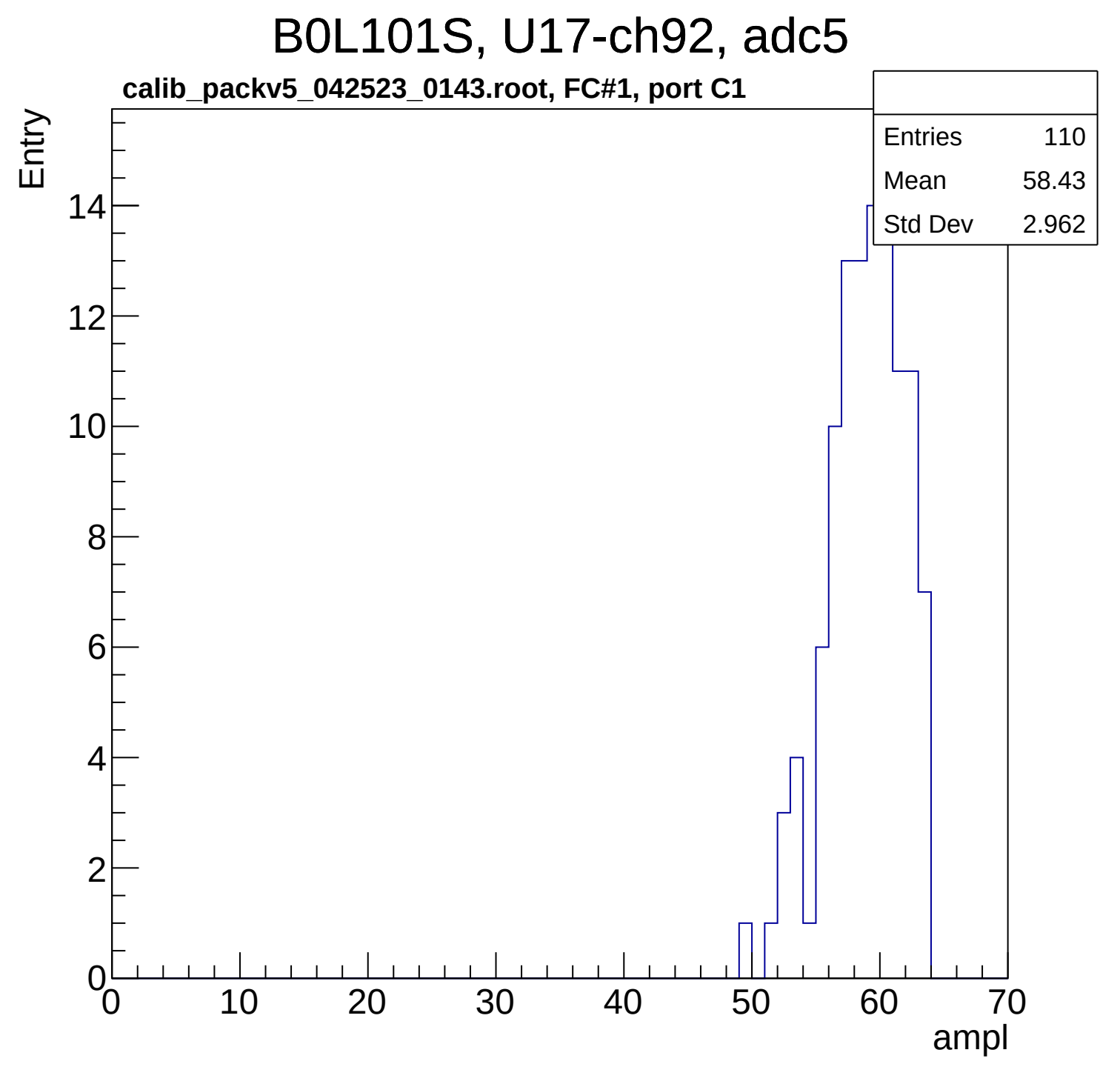

B0L101S, U17-ch92, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 26.2
Std Dev 4.53
Gaus mean : 27.4290
Gaus Width: 3.5268
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

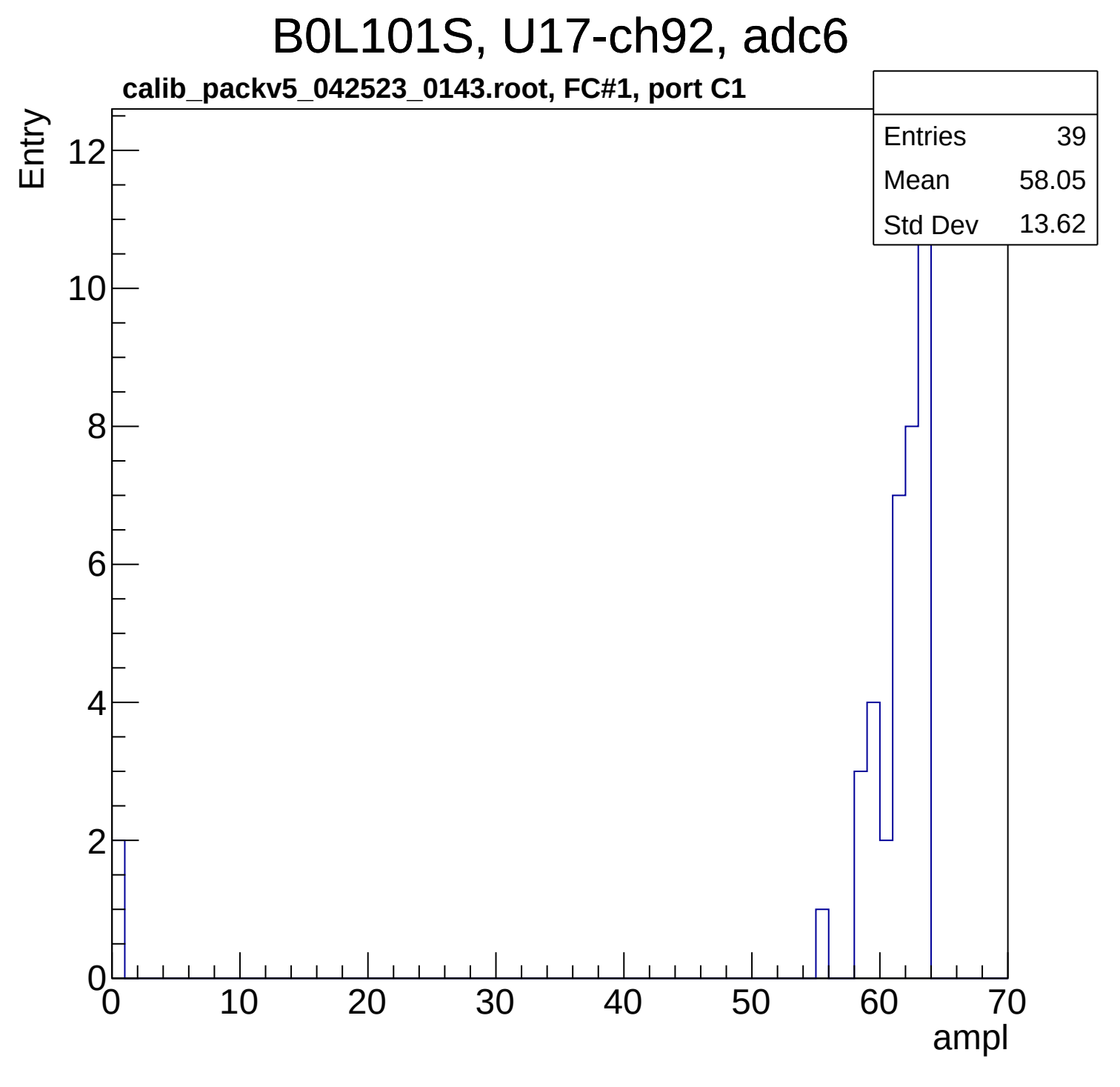

B0L101S, U17-ch92, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 33.21
Std Dev 3.636
Gaus mean : 33.9220
Gaus Width: 3.6325
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

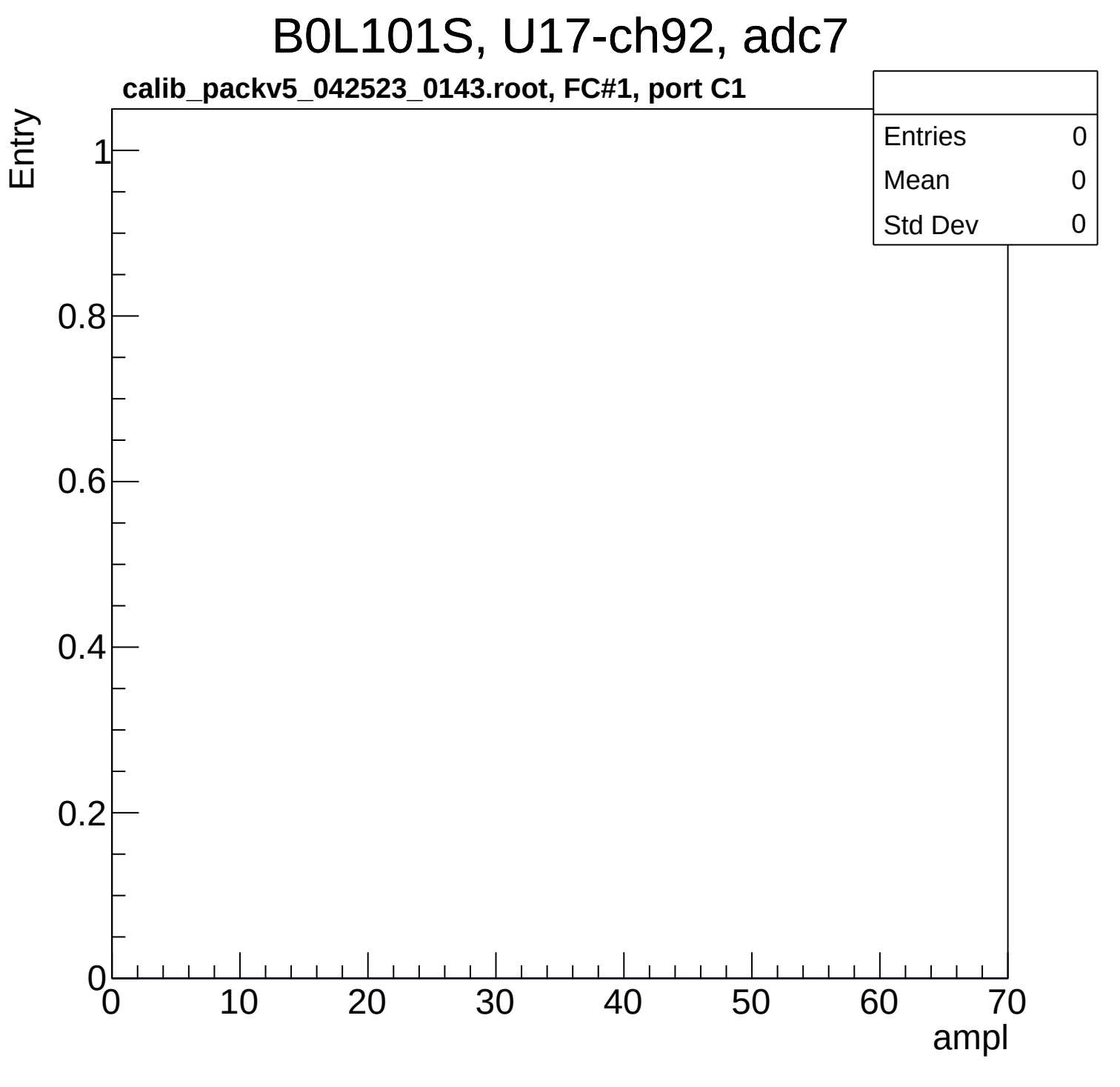

B0L101S, U17-ch92, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 161
Mean 40.2
Std Dev 3.856
Gaus mean : 41.0333
Gaus Width: 3.7002
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

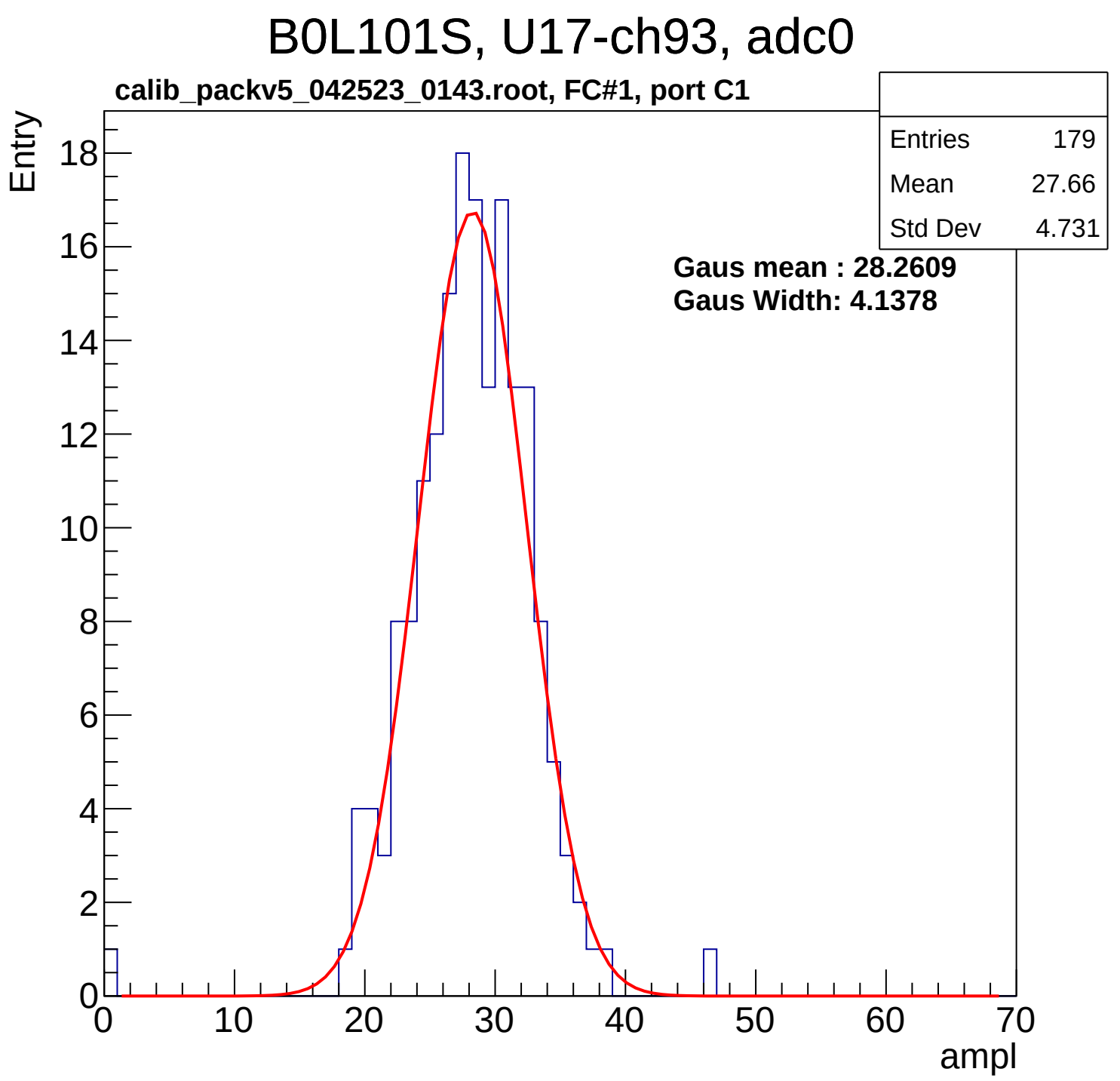

B0L101S, U17-ch92, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 105
Mean 47.53
Std Dev 3.378
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

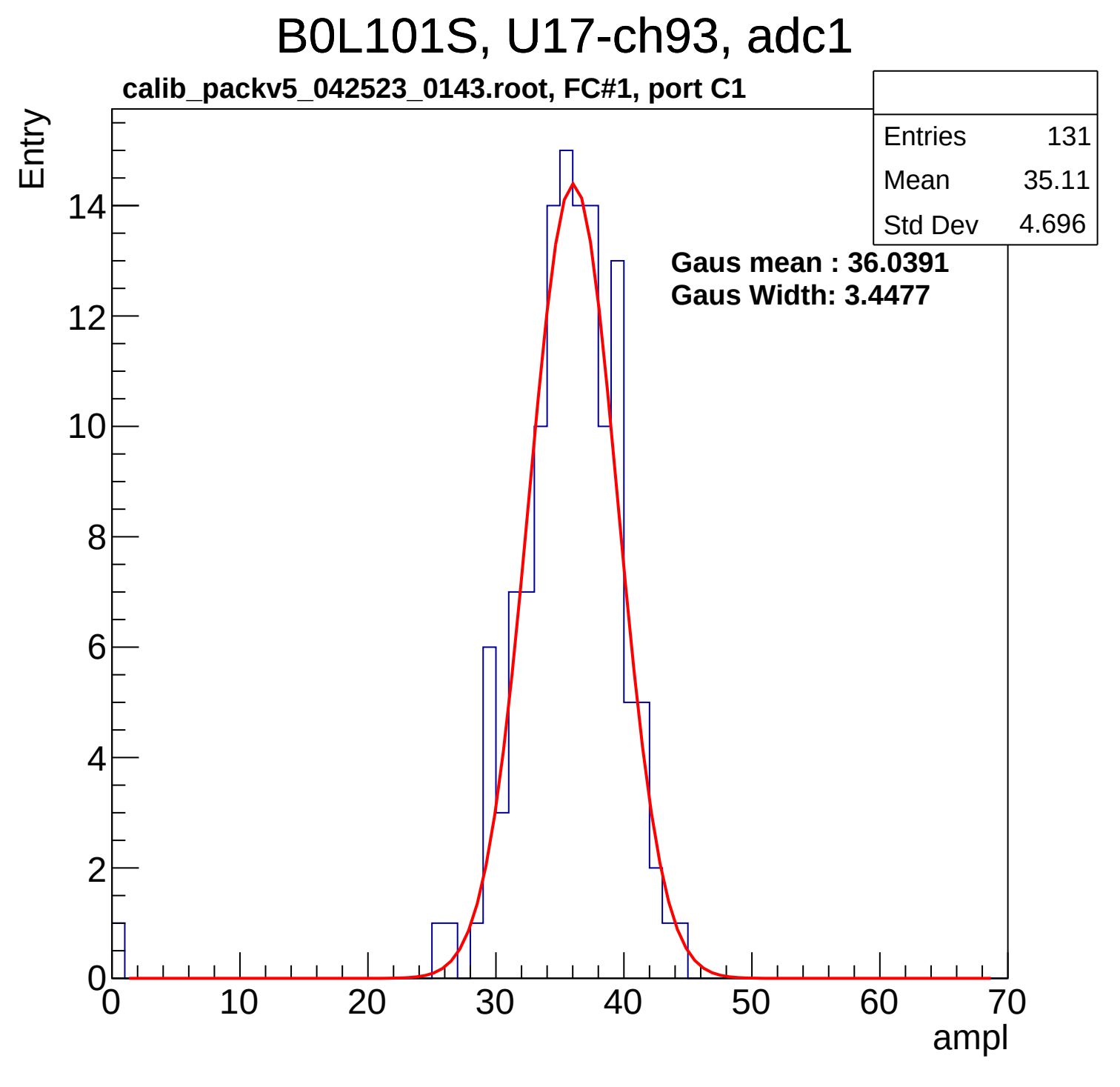

B0L101S, U17-ch92, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 129
Mean 52.82
Std Dev 3.701
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

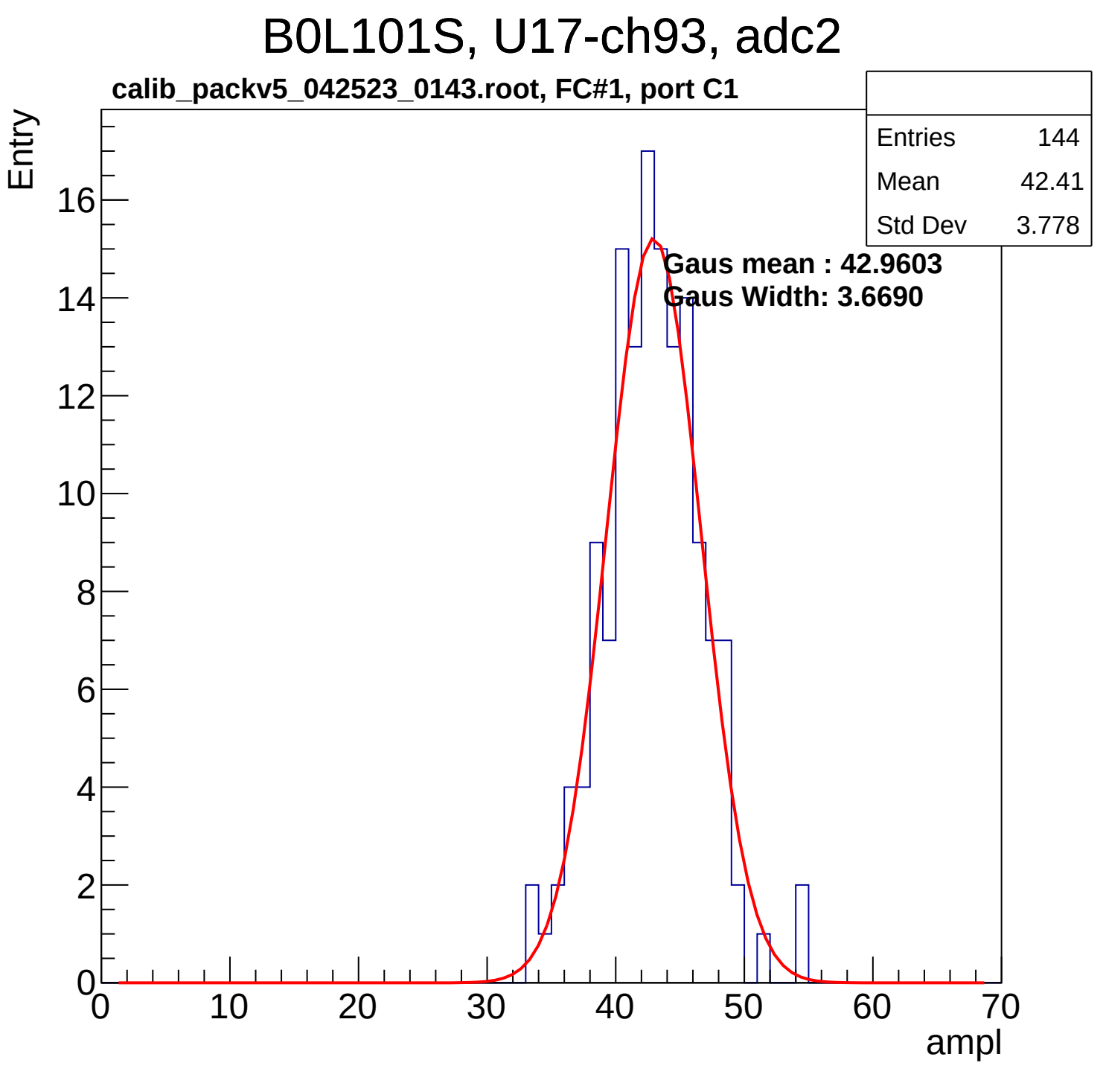

B0L101S, U17-ch92, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 58.43
Std Dev 2.962
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

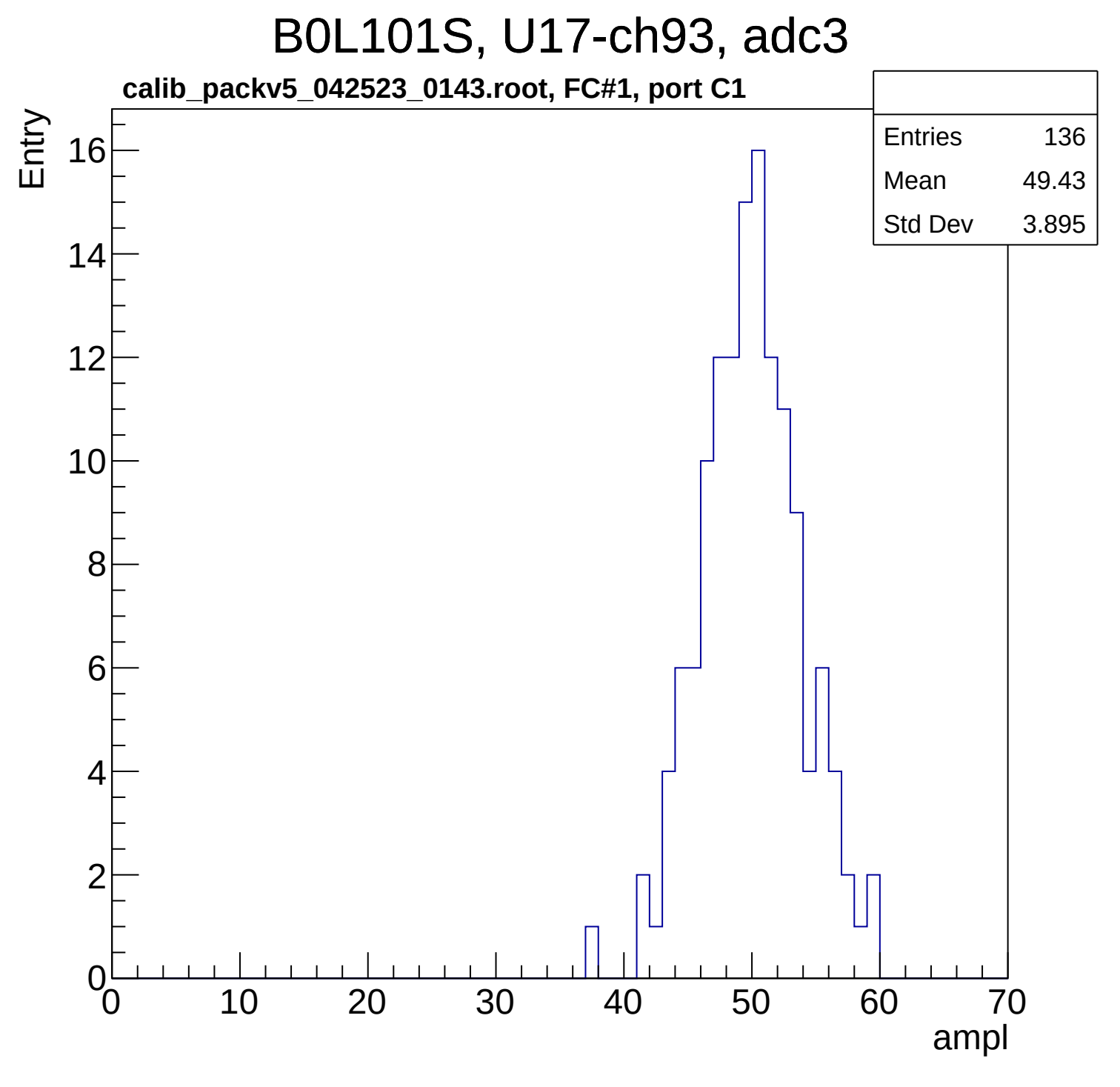

B0L101S, U17-ch92, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 39
Mean 58.05
Std Dev 13.62
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

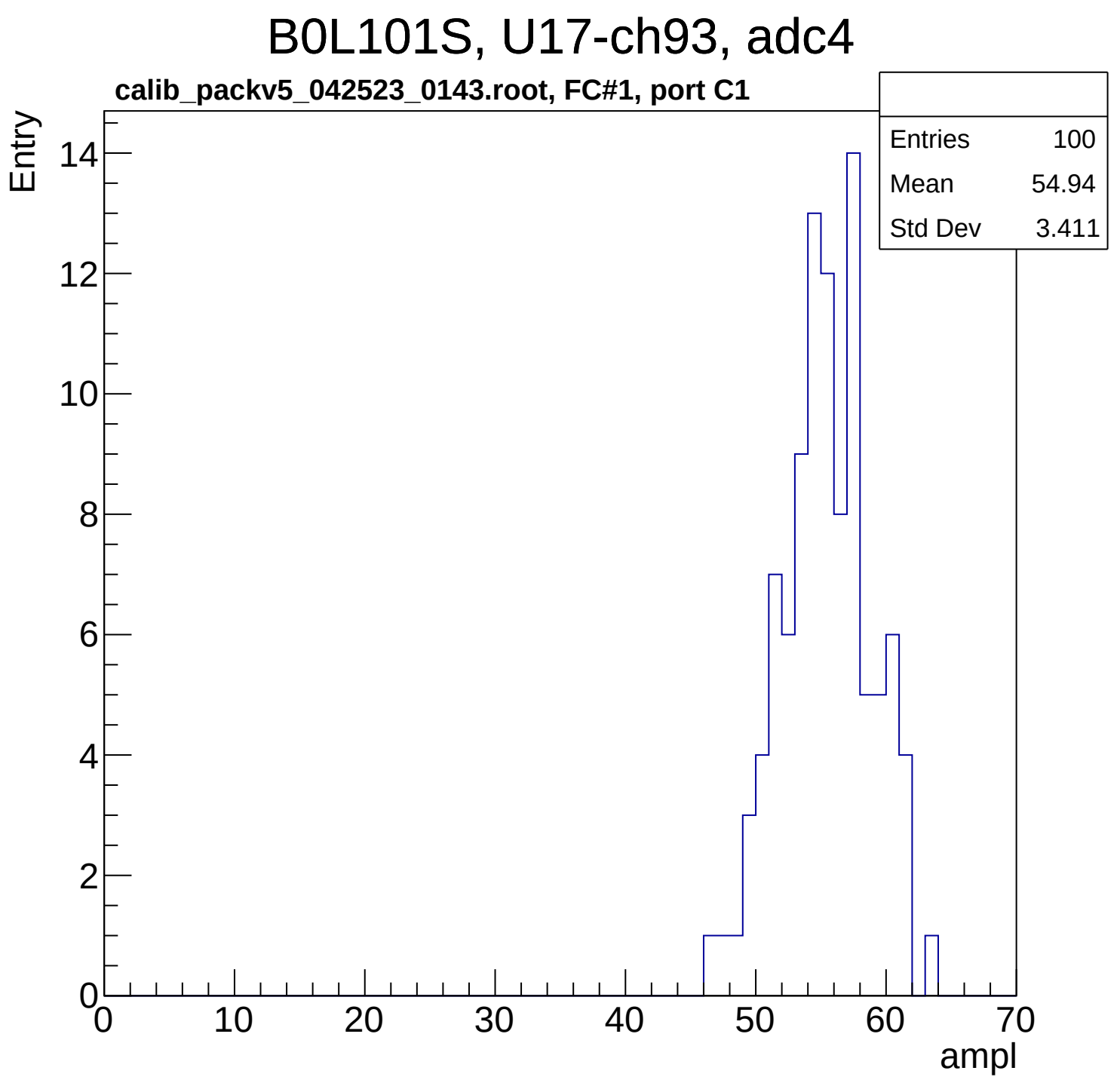

B0L101S, U17-ch92, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

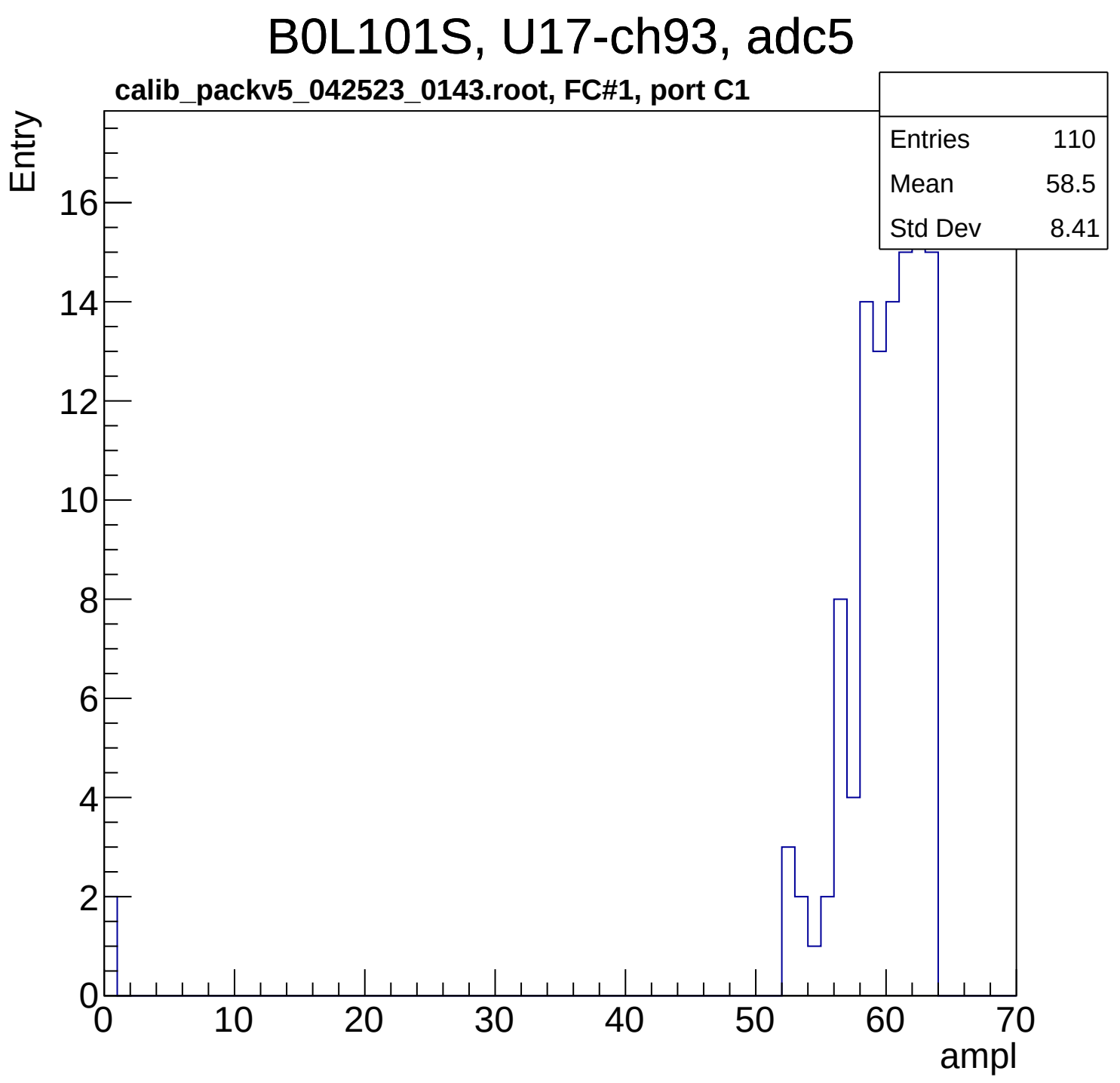

B0L101S, U17-ch93, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 179
Mean 27.66
Std Dev 4.731
Gaus mean : 28.2609
Gaus Width: 4.1378
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

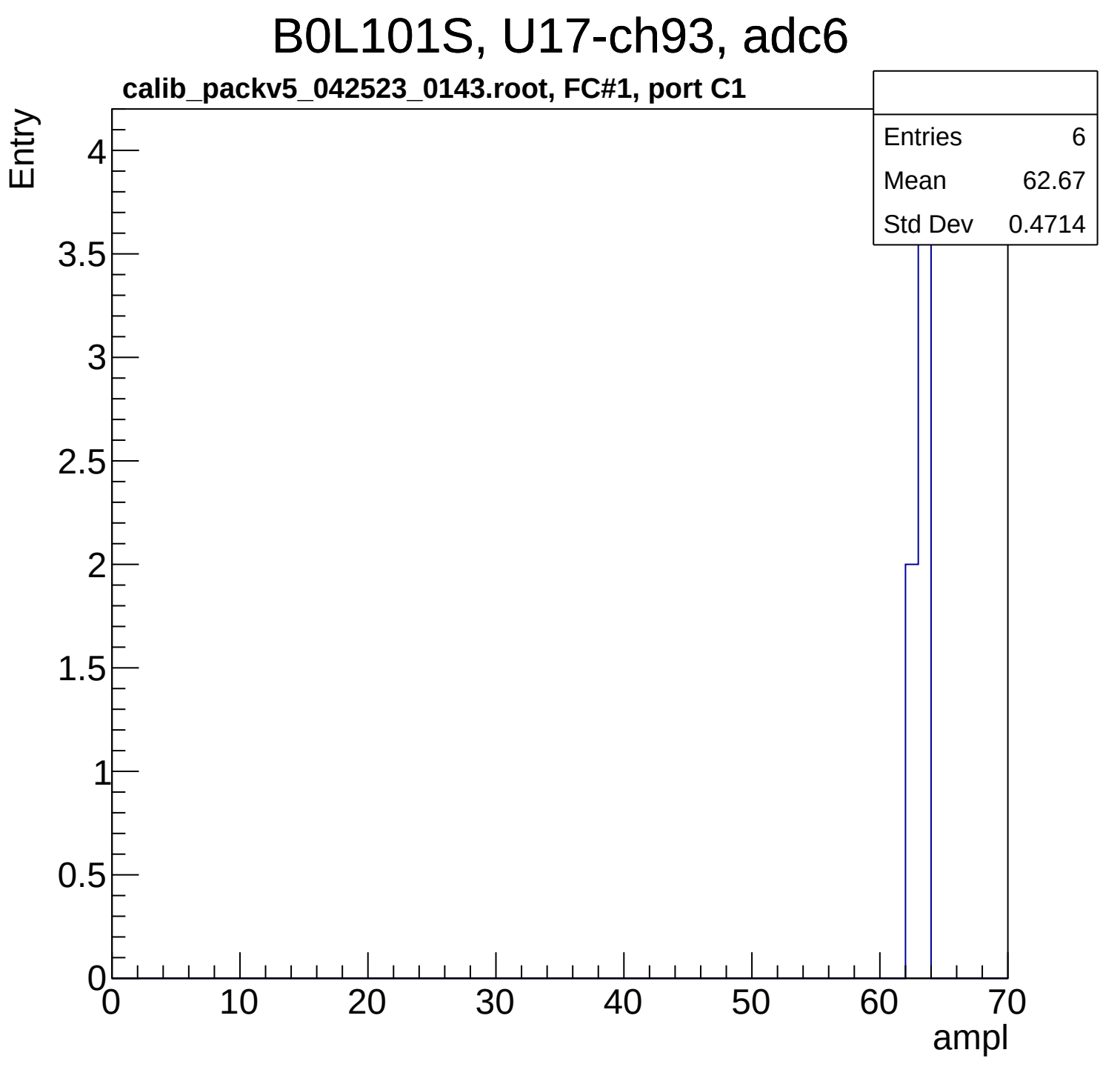

B0L101S, U17-ch93, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 35.11
Std Dev 4.696
Gaus mean : 36.0391
Gaus Width: 3.4477
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

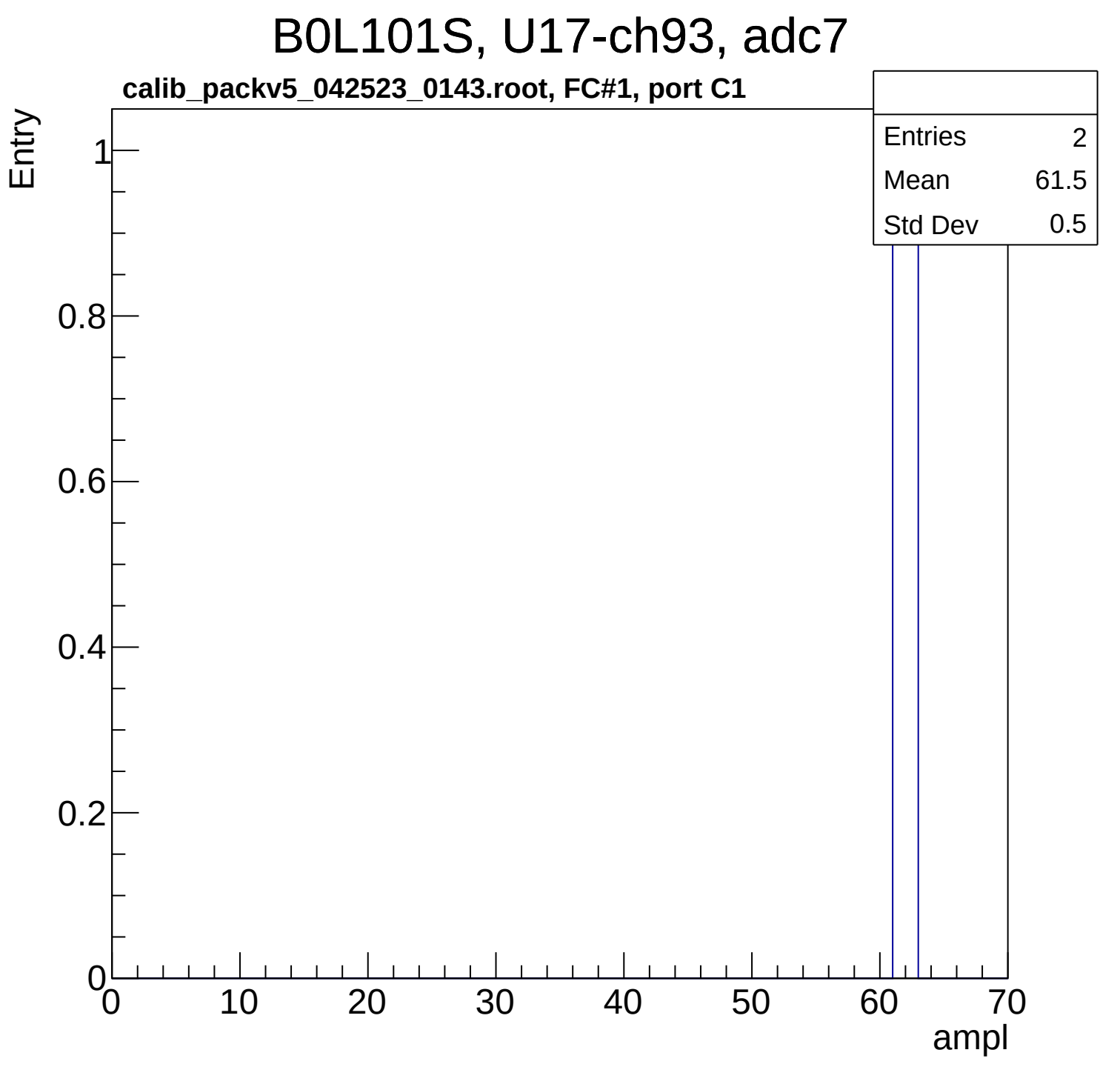

B0L101S, U17-ch93, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 42.41
Std Dev 3.778
Gaus mean : 42.9603
Gaus Width: 3.6690
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

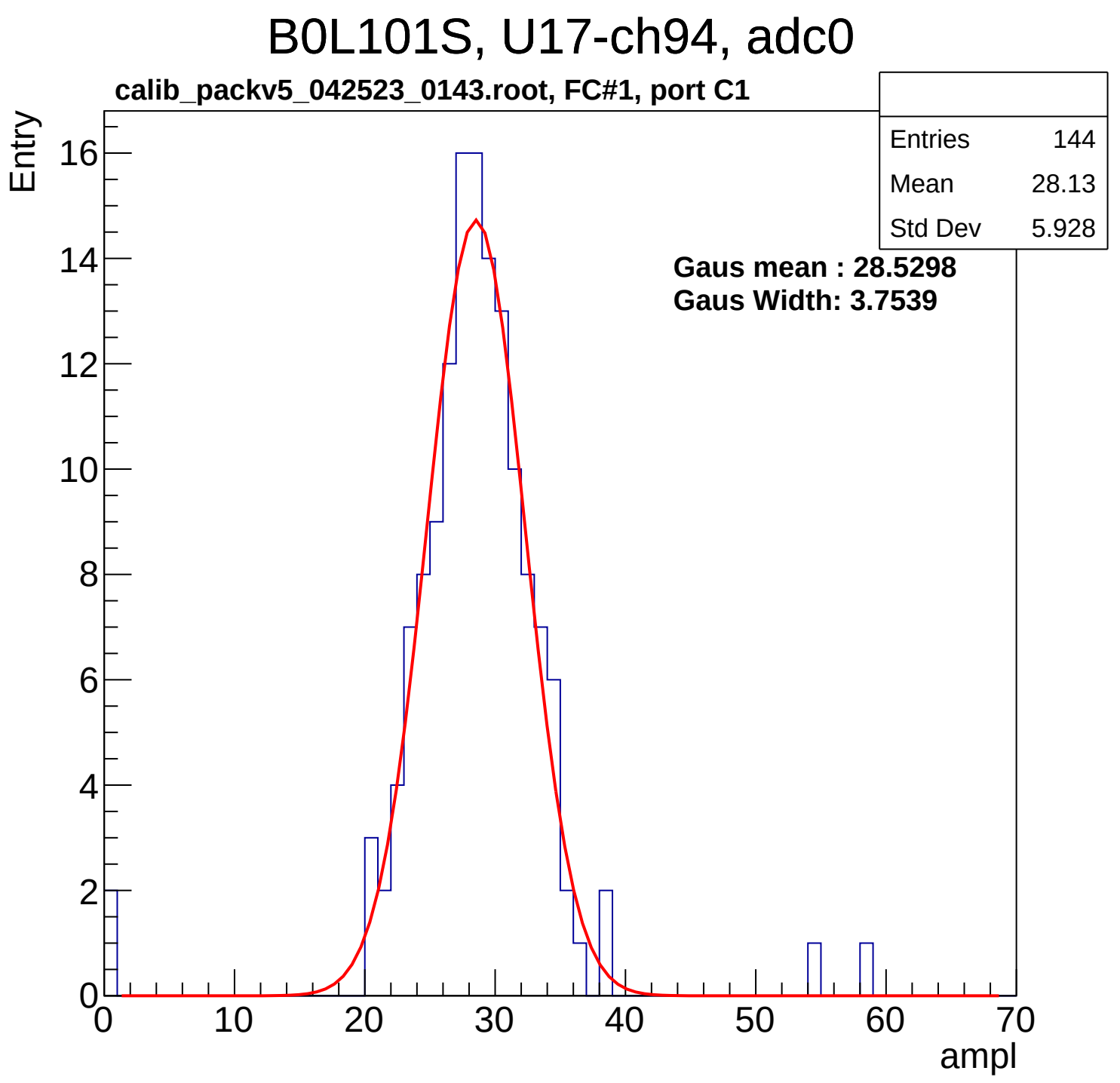

B0L101S, U17-ch93, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 49.43
Std Dev 3.895
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

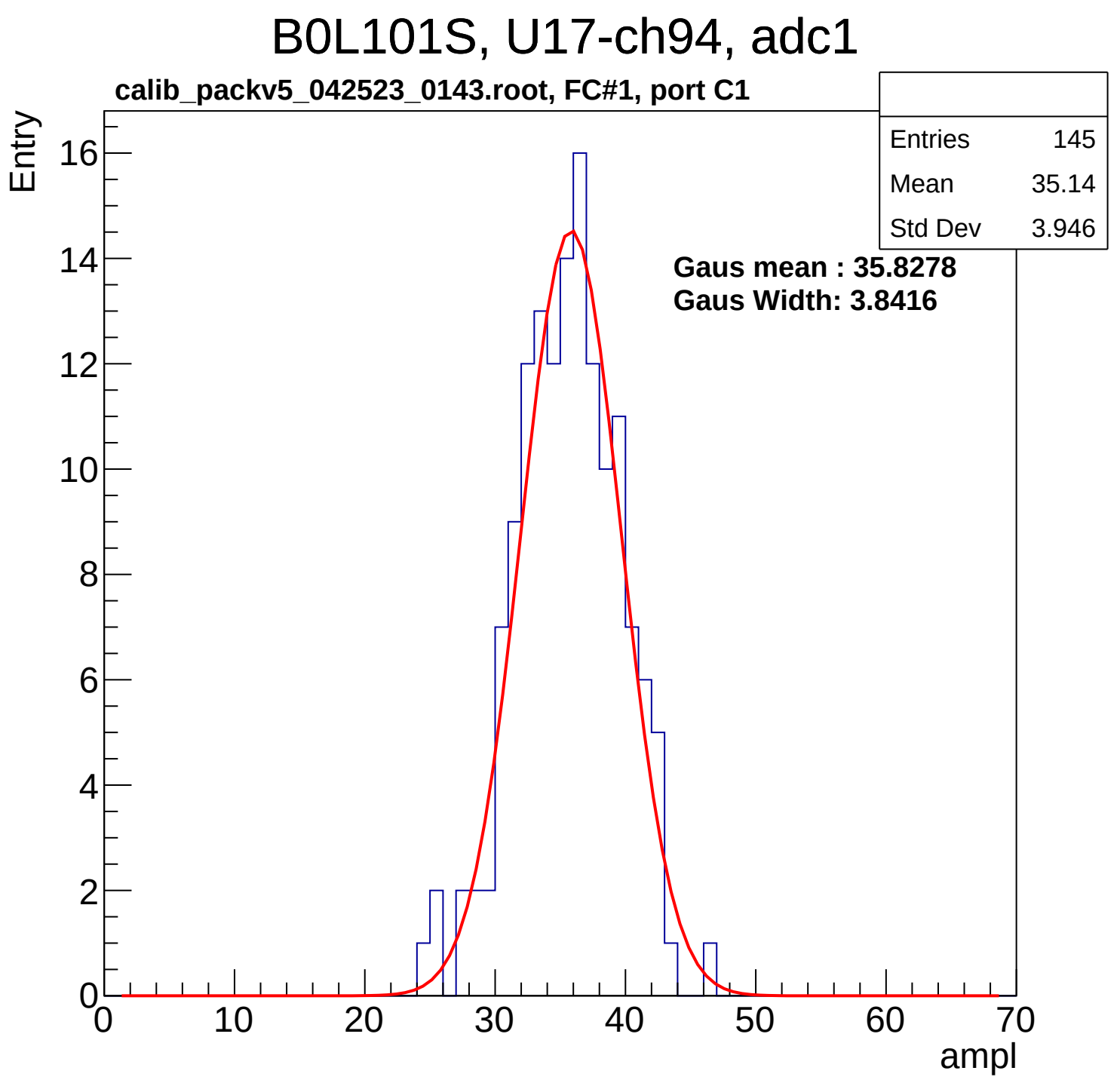

B0L101S, U17-ch93, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 100
Mean 54.94
Std Dev 3.411
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

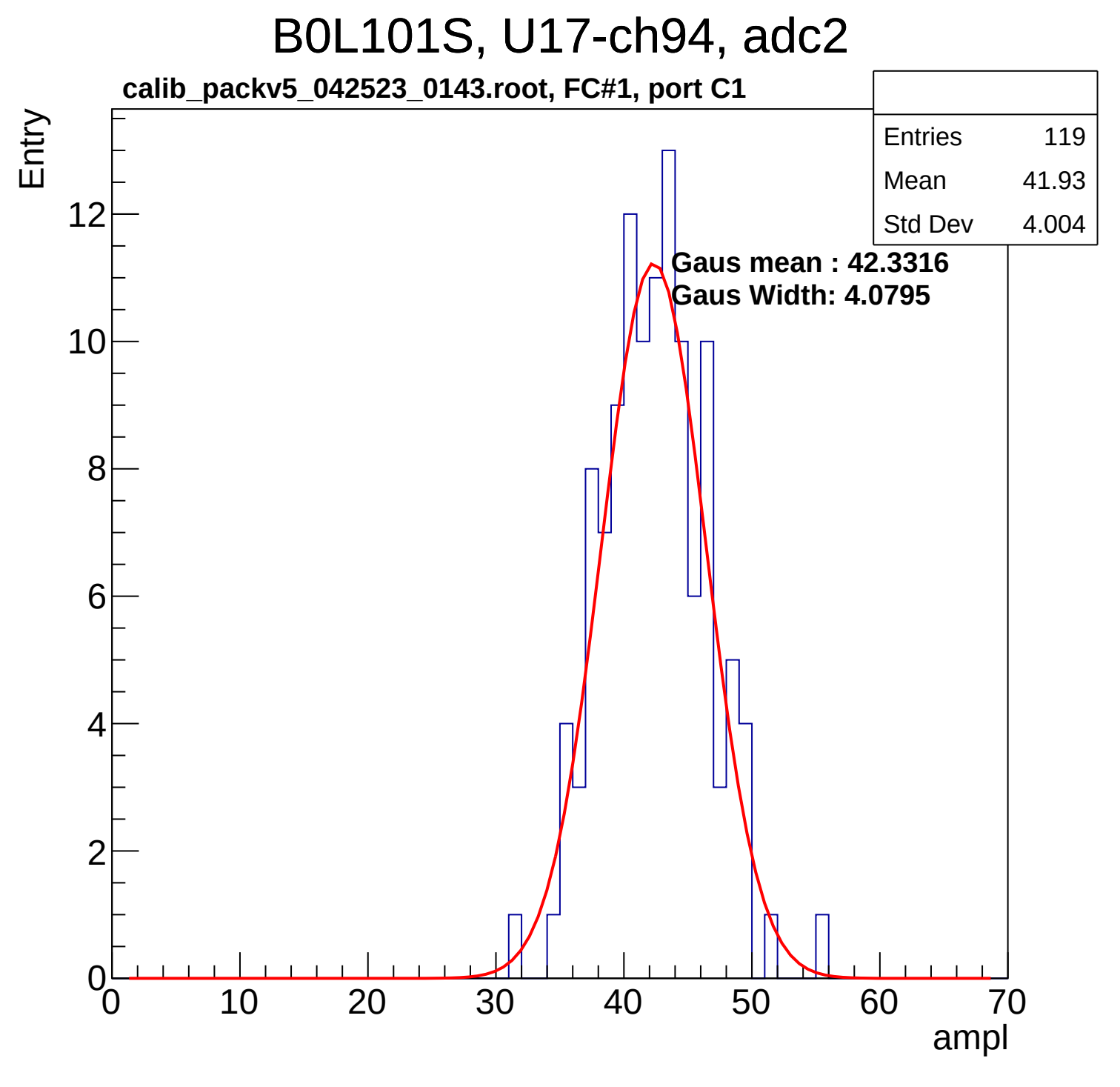

B0L101S, U17-ch93, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 58.5
Std Dev 8.41
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

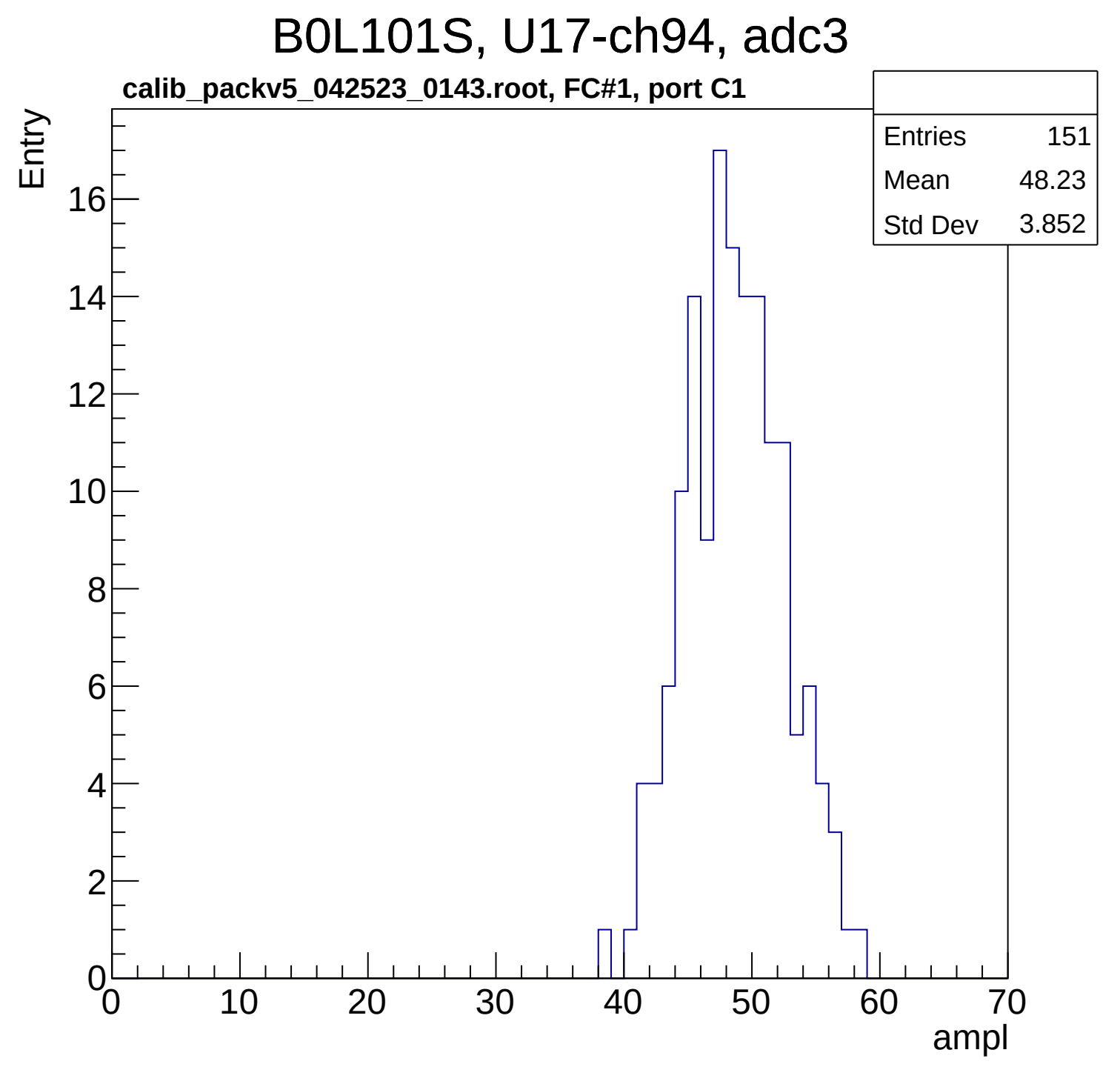

B0L101S, U17-ch93, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

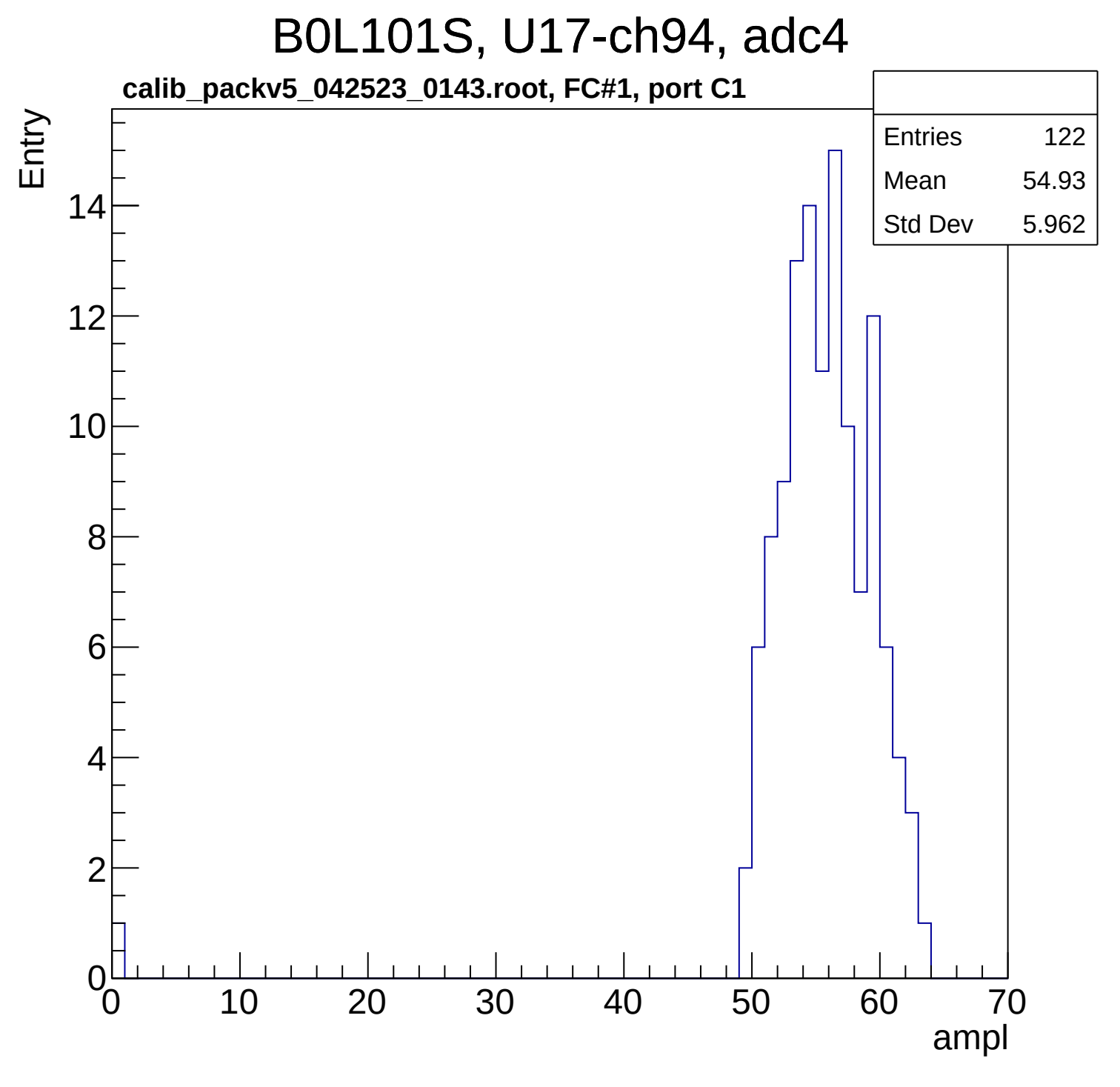

B0L101S, U17-ch93, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

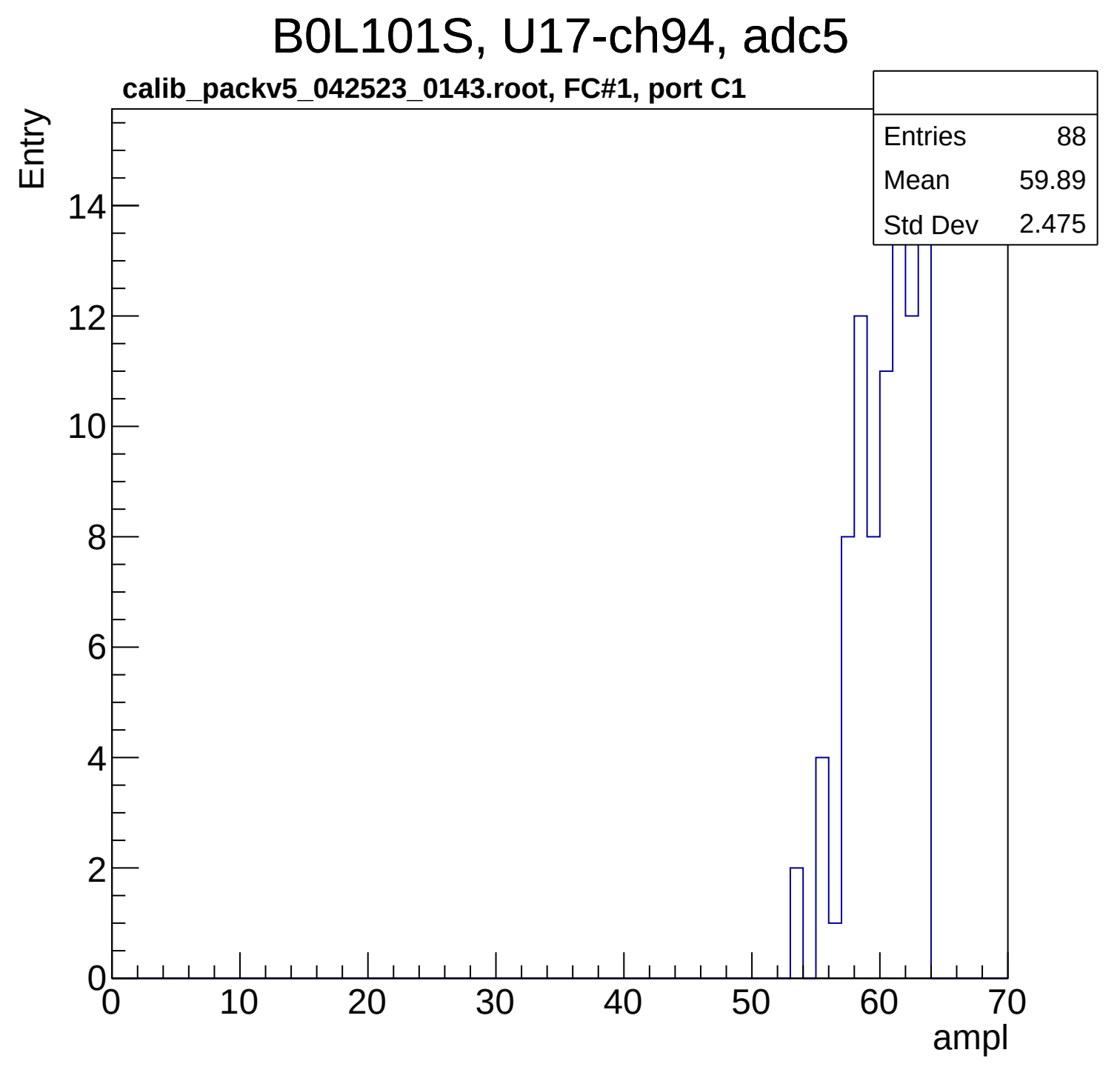

B0L101S, U17-ch94, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 28.13
Std Dev 5.928
Gaus mean : 28.5298
Gaus Width: 3.7539
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

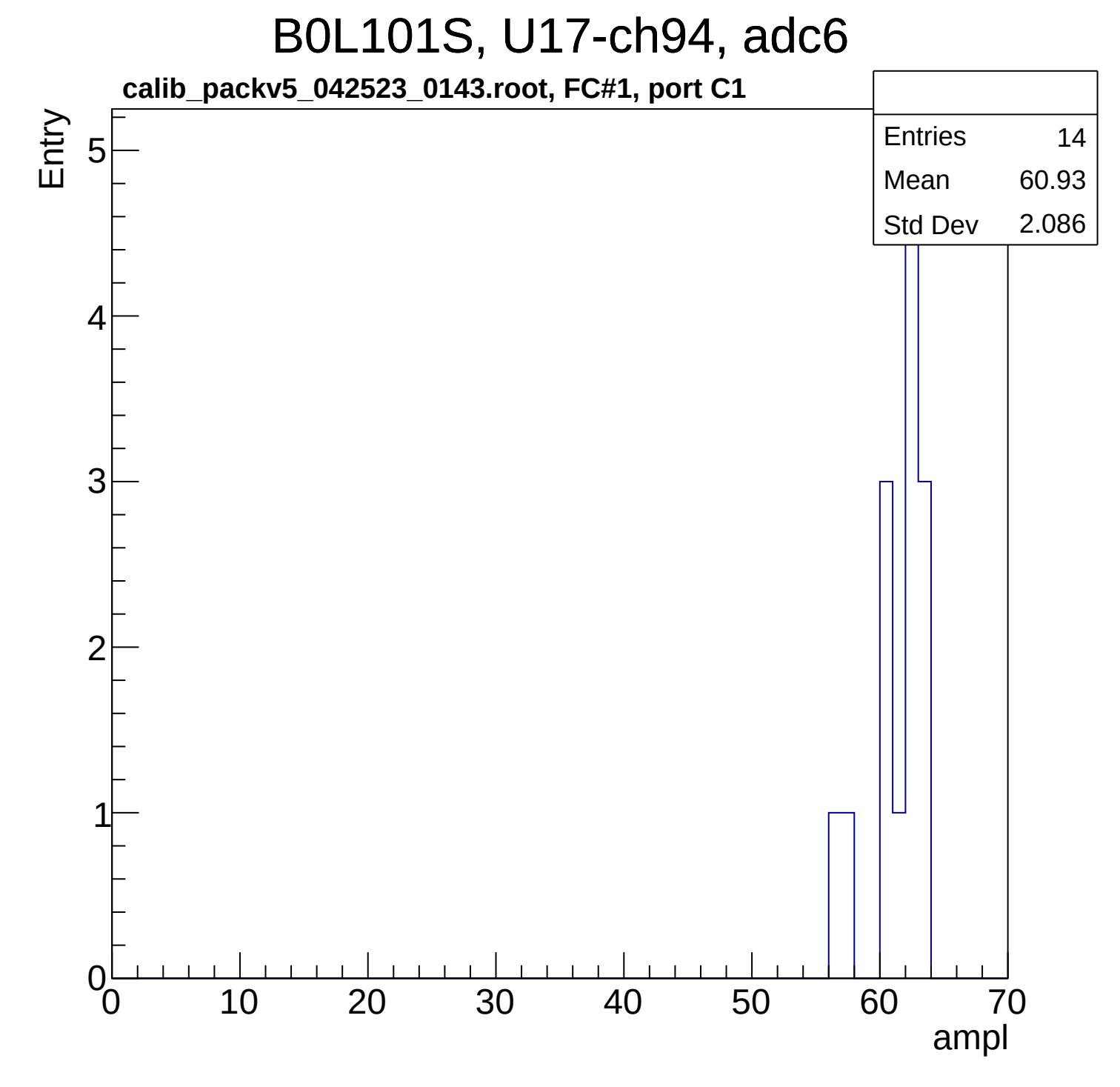

B0L101S, U17-ch94, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 35.14
Std Dev 3.946
Gaus mean : 35.8278
Gaus Width: 3.8416
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

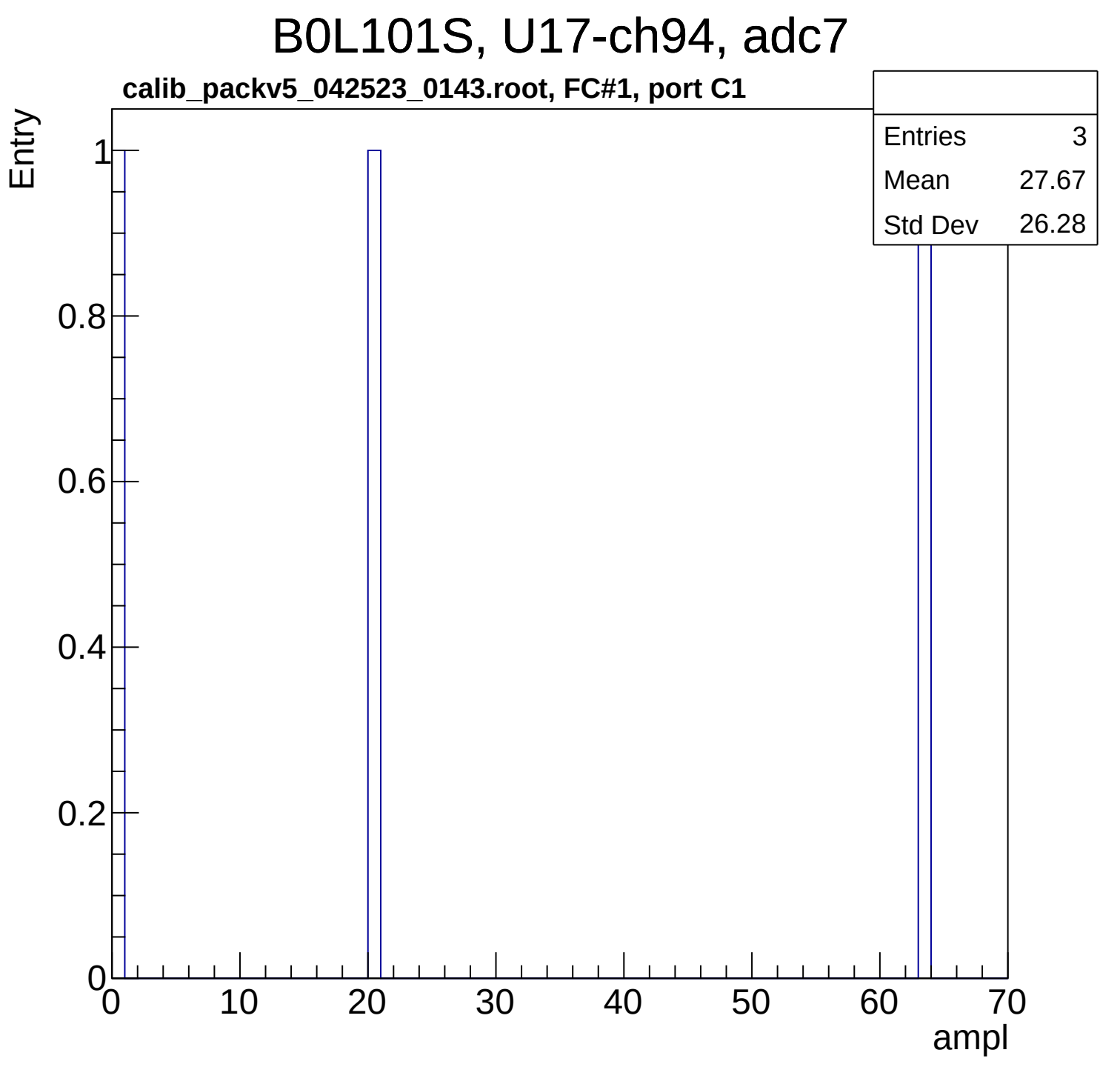

B0L101S, U17-ch94, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 41.93
Std Dev 4.004
Gaus mean : 42.3316
Gaus Width: 4.0795
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

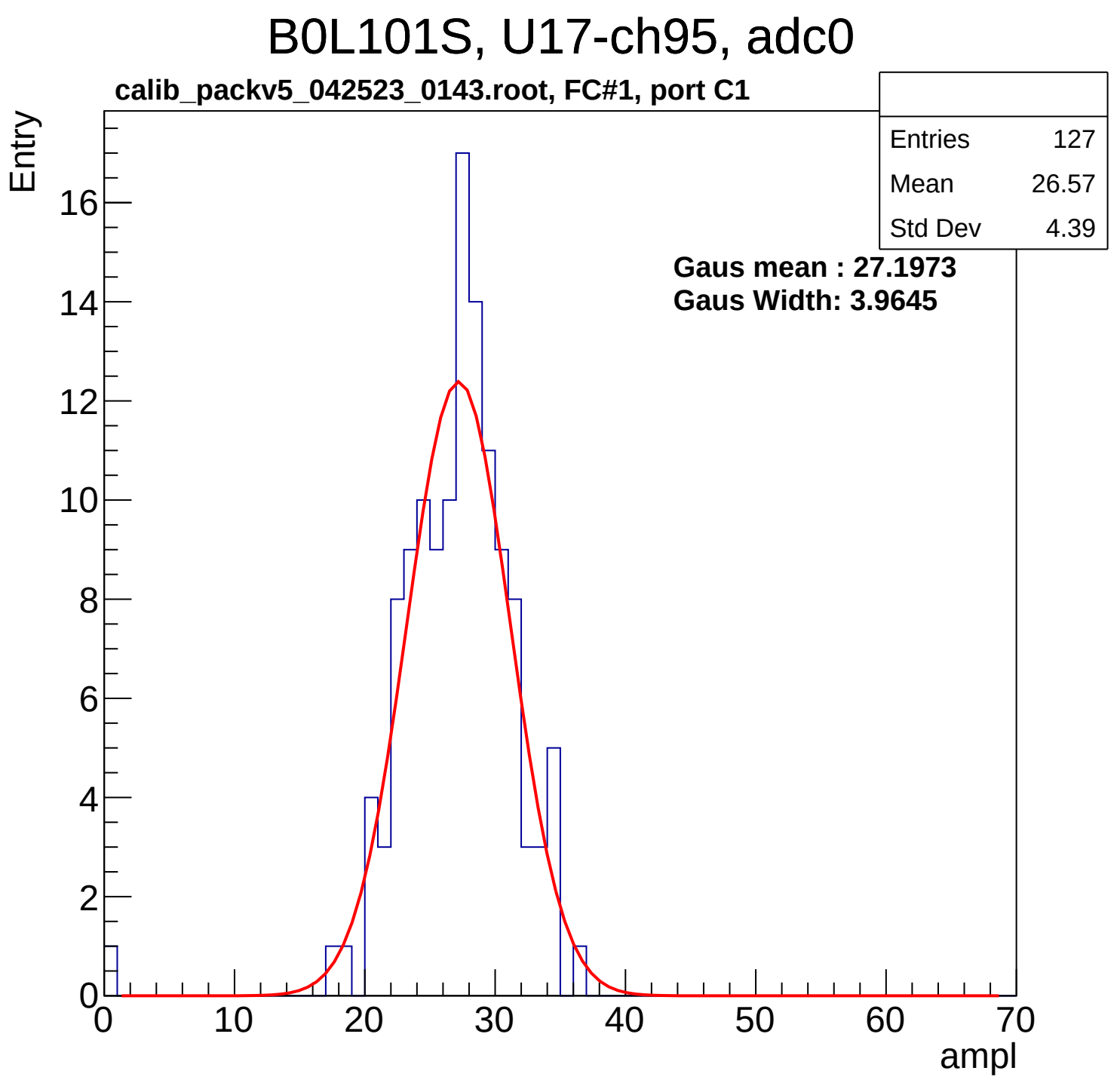

B0L101S, U17-ch94, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 48.23
Std Dev 3.852
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

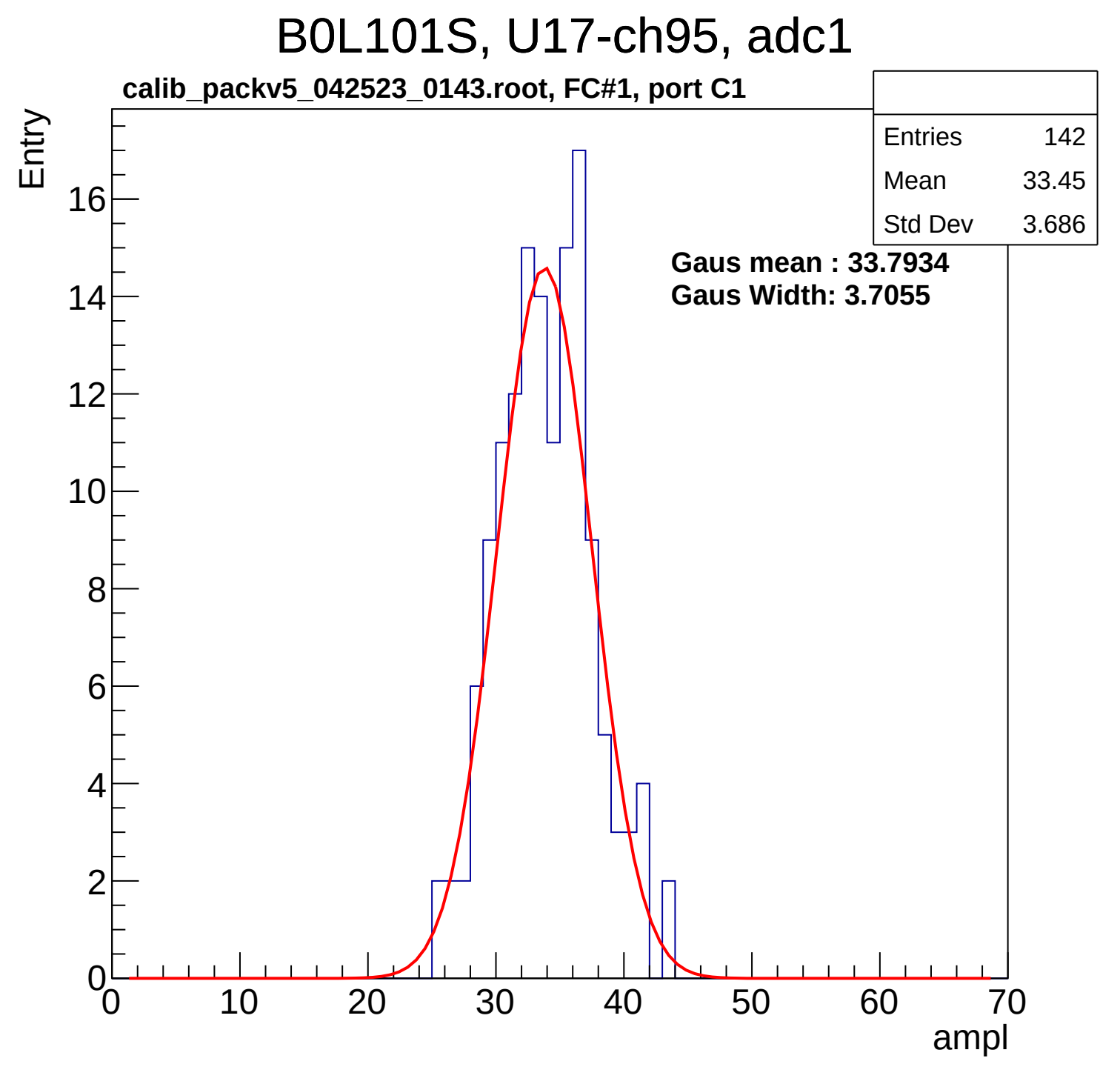

B0L101S, U17-ch94, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 54.93
Std Dev 5.962
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

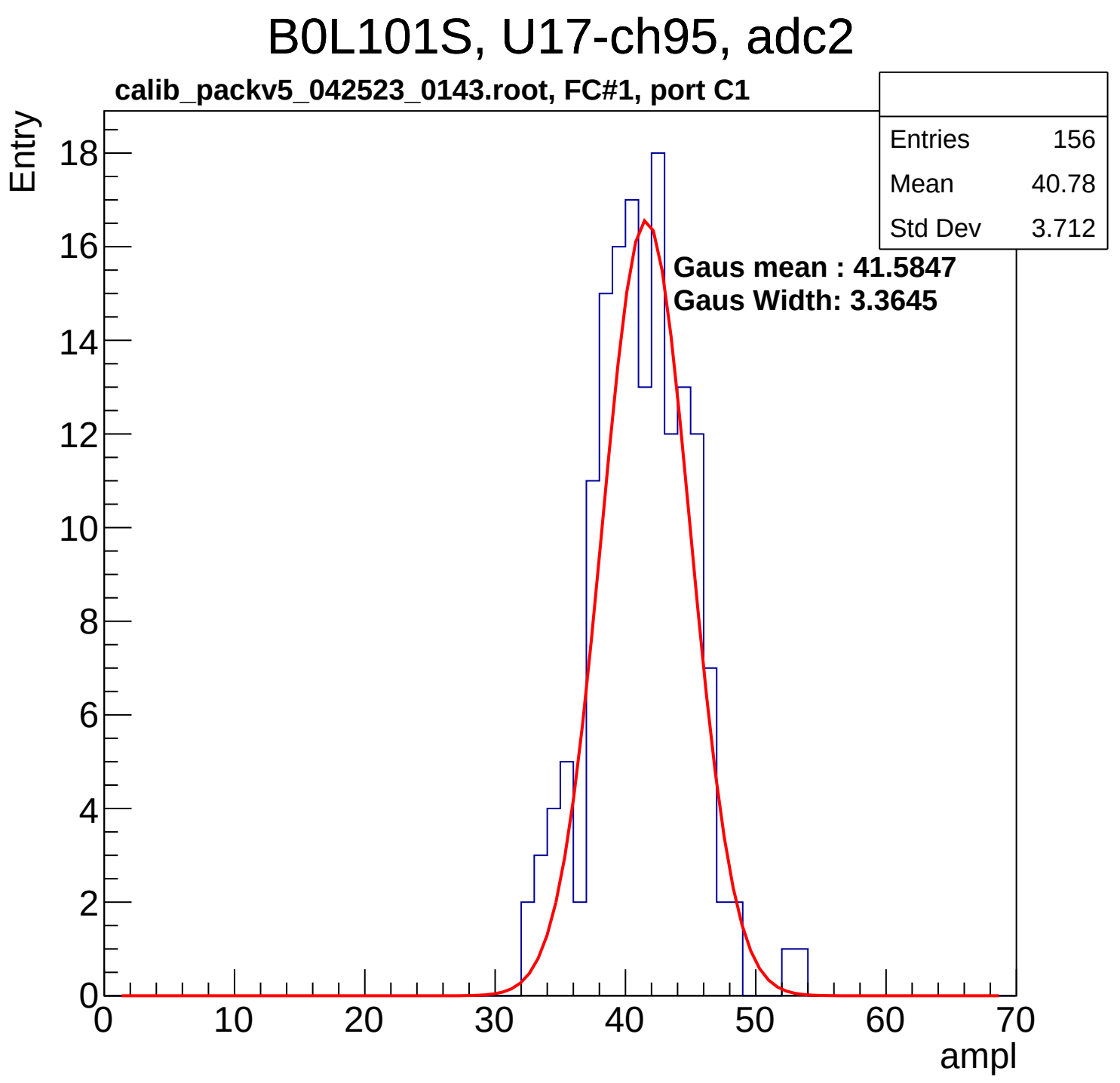

B0L101S, U17-ch94, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 88
Mean 59.89
Std Dev 2.475
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

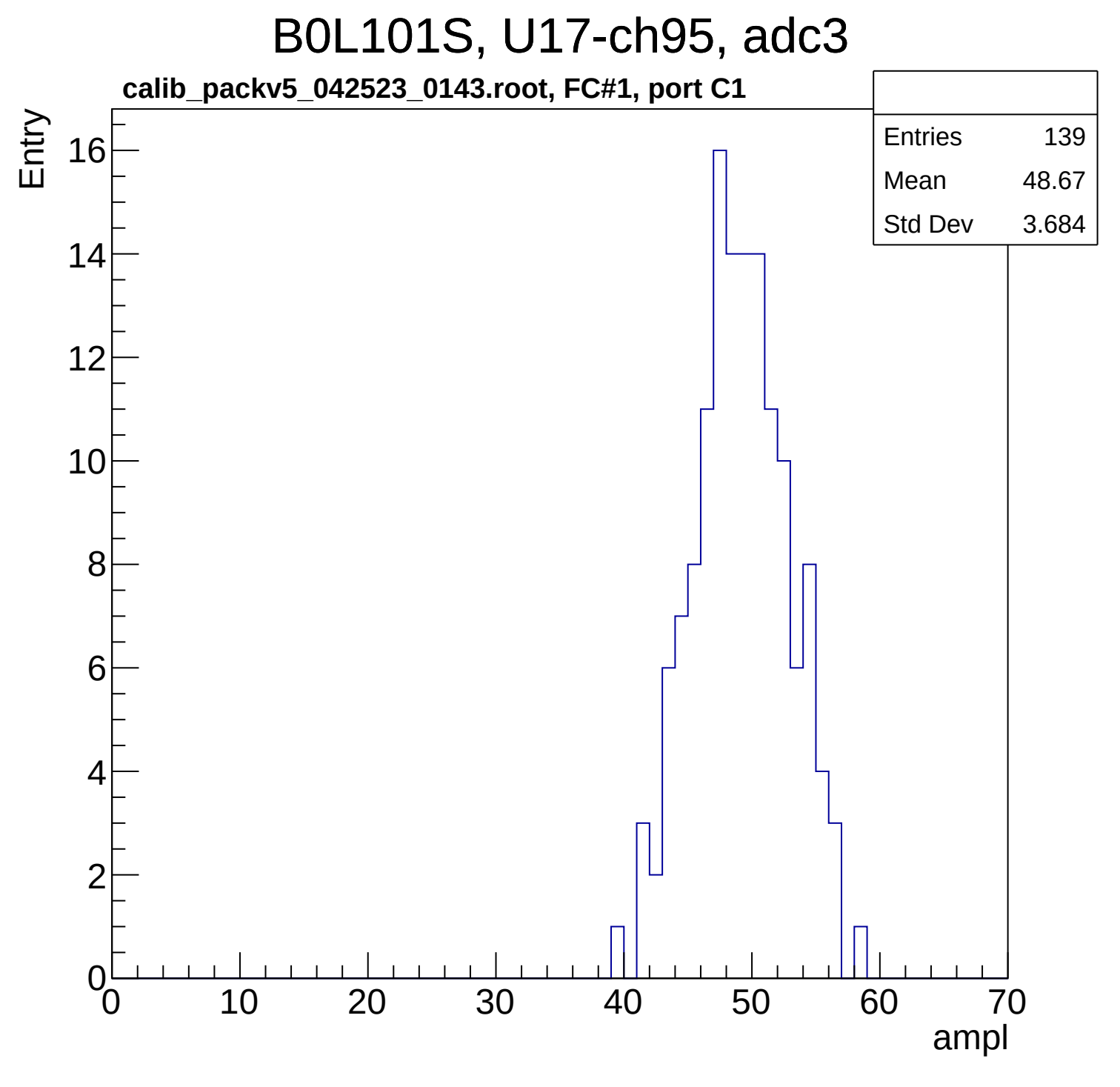

B0L101S, U17-ch94, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 60.93
Std Dev 2.086
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

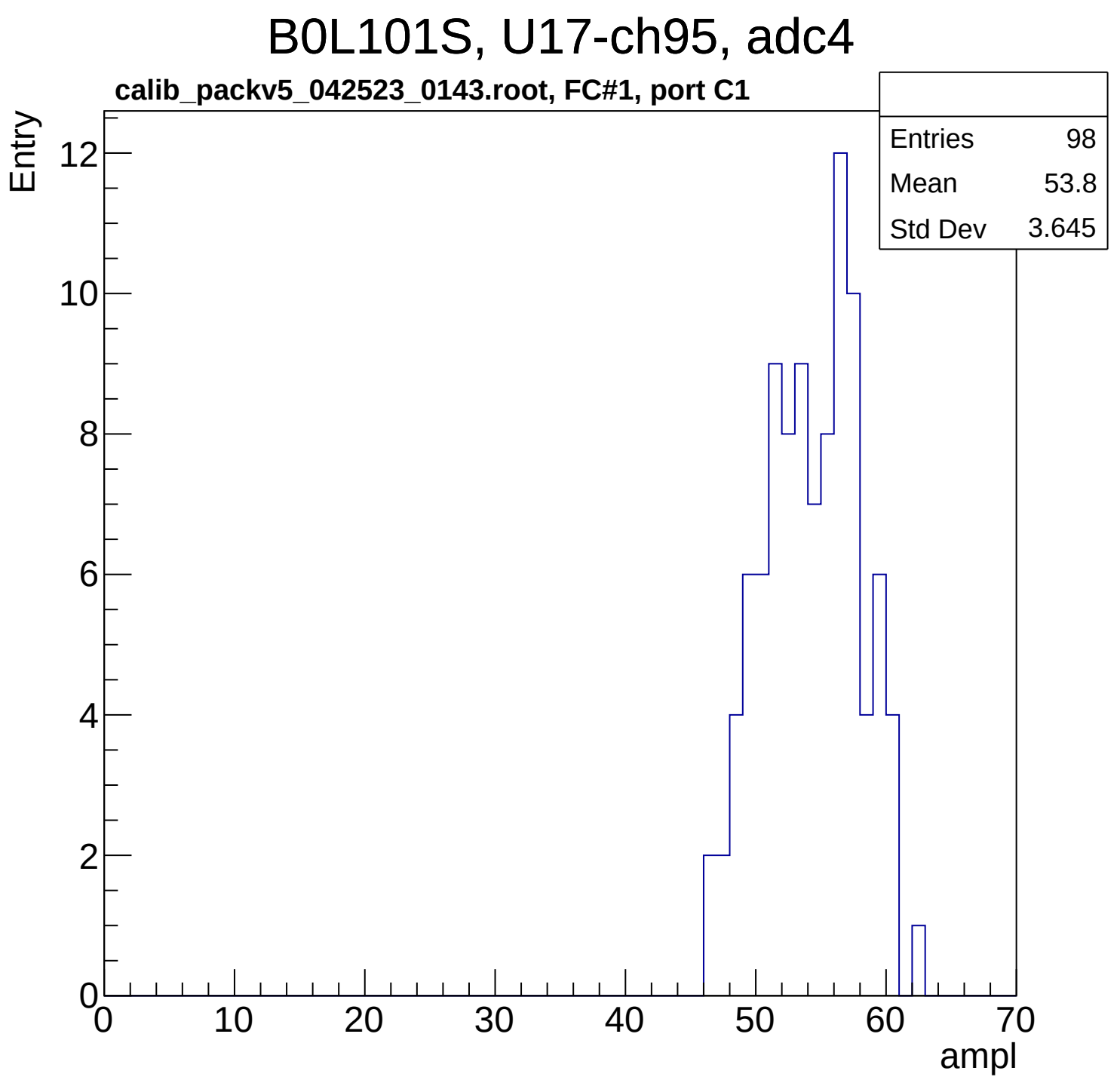

B0L101S, U17-ch94, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 27.67
Std Dev 26.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

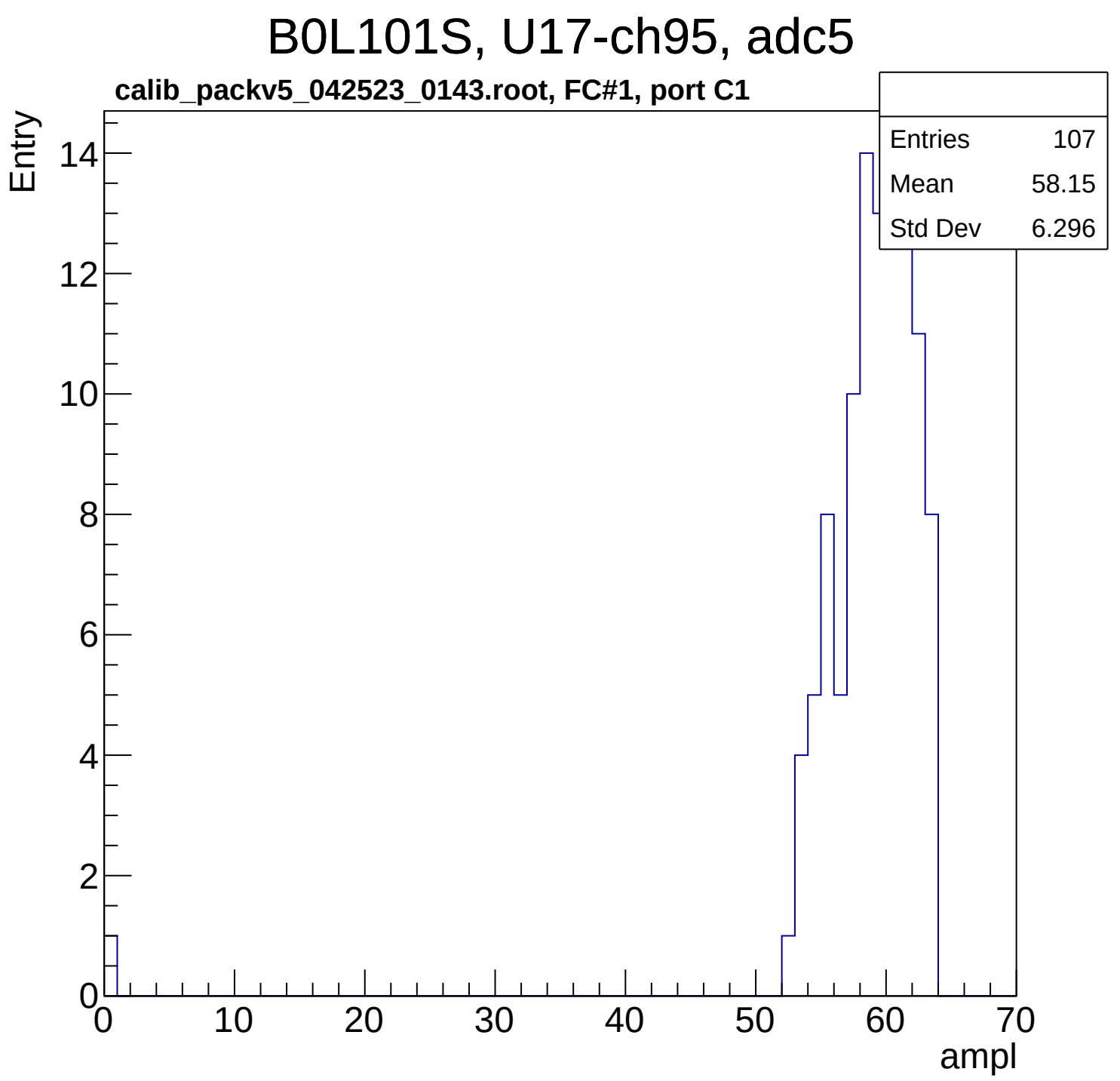

B0L101S, U17-ch95, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 127
Mean 26.57
Std Dev 4.39
Gaus mean : 27.1973
Gaus Width: 3.9645
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

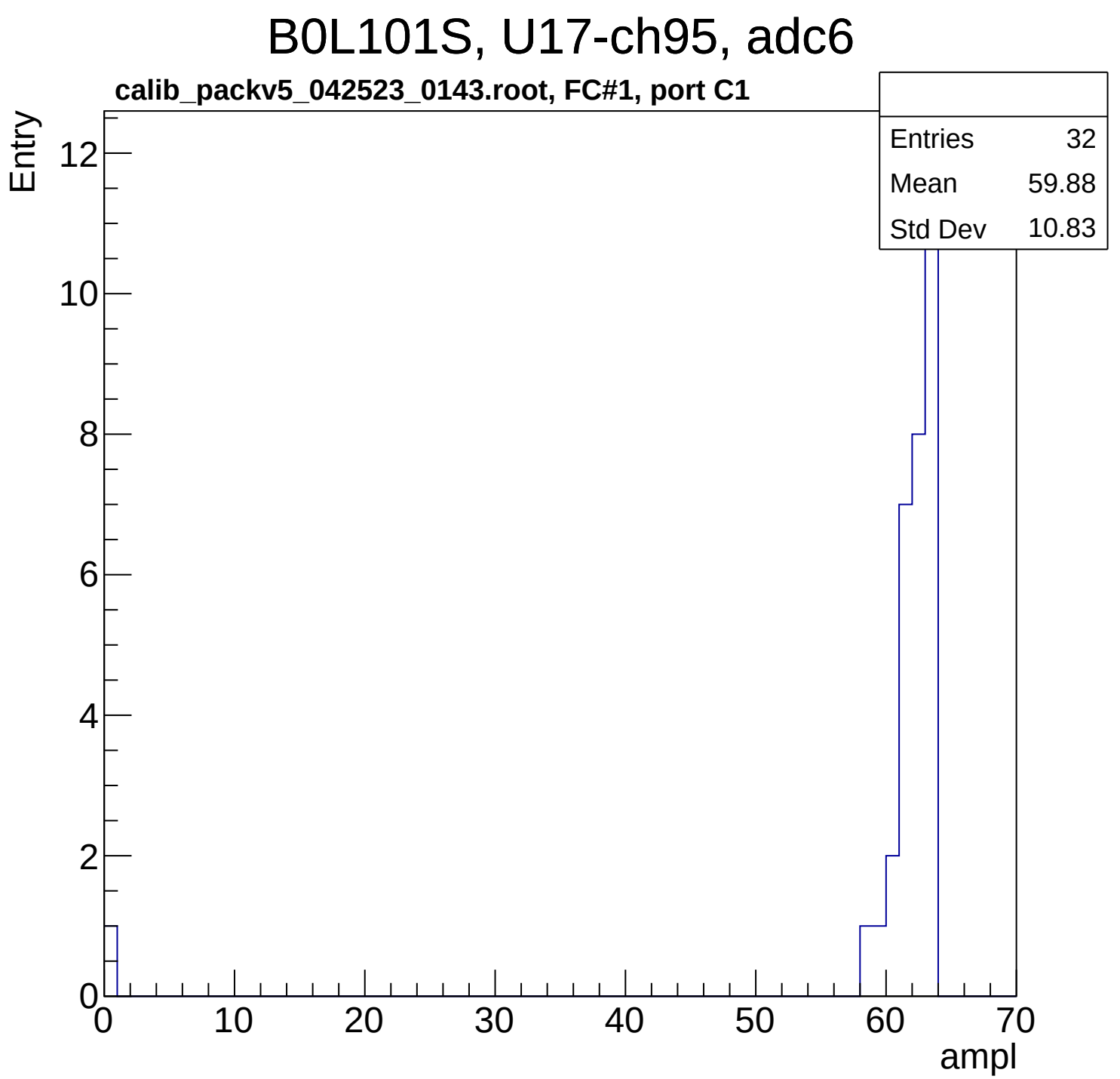

B0L101S, U17-ch95, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 142
Mean 33.45
Std Dev 3.686
Gaus mean : 33.7934
Gaus Width: 3.7055
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

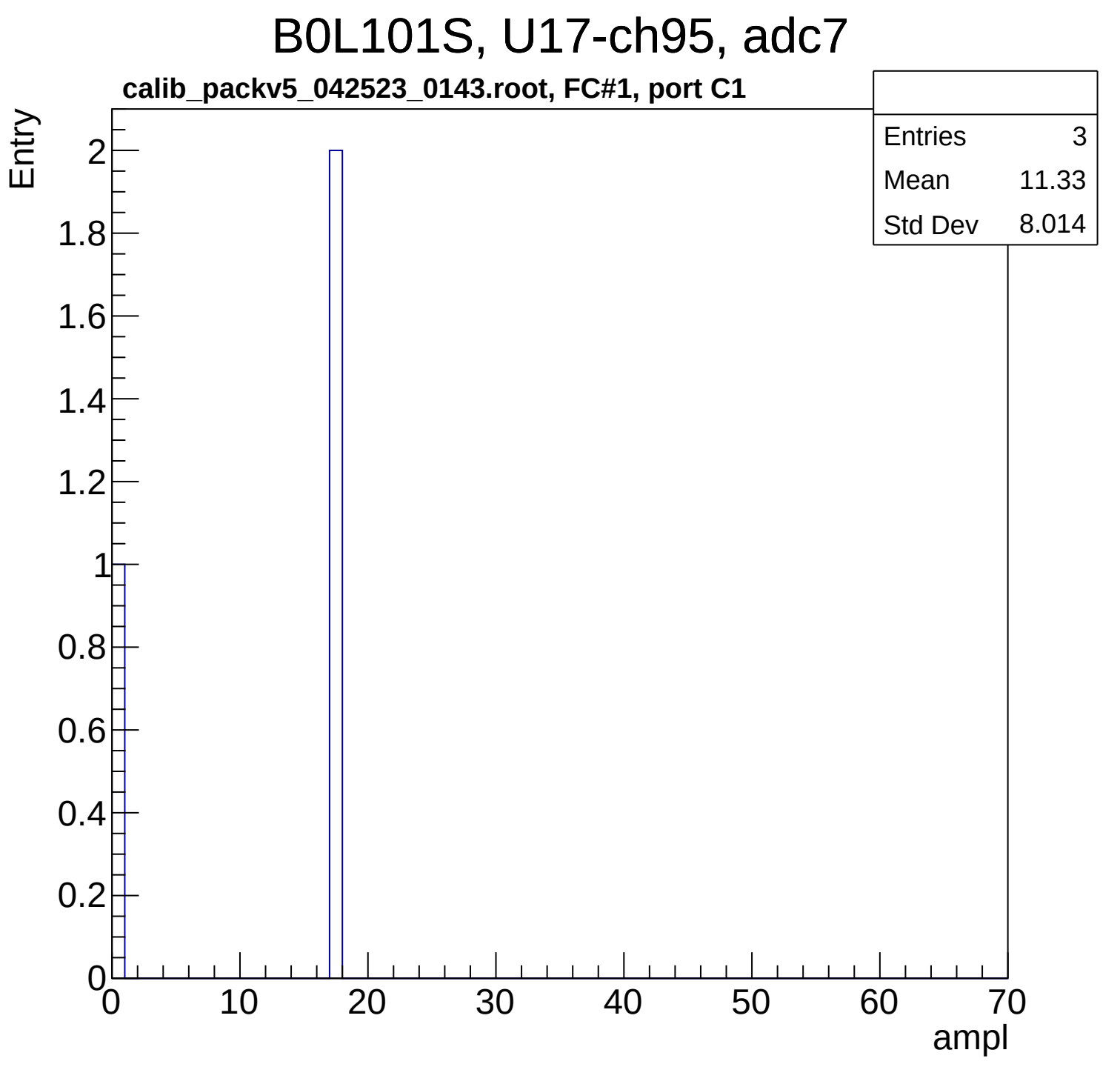

B0L101S, U17-ch95, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 40.78
Std Dev 3.712
Gaus mean : 41.5847
Gaus Width: 3.3645
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

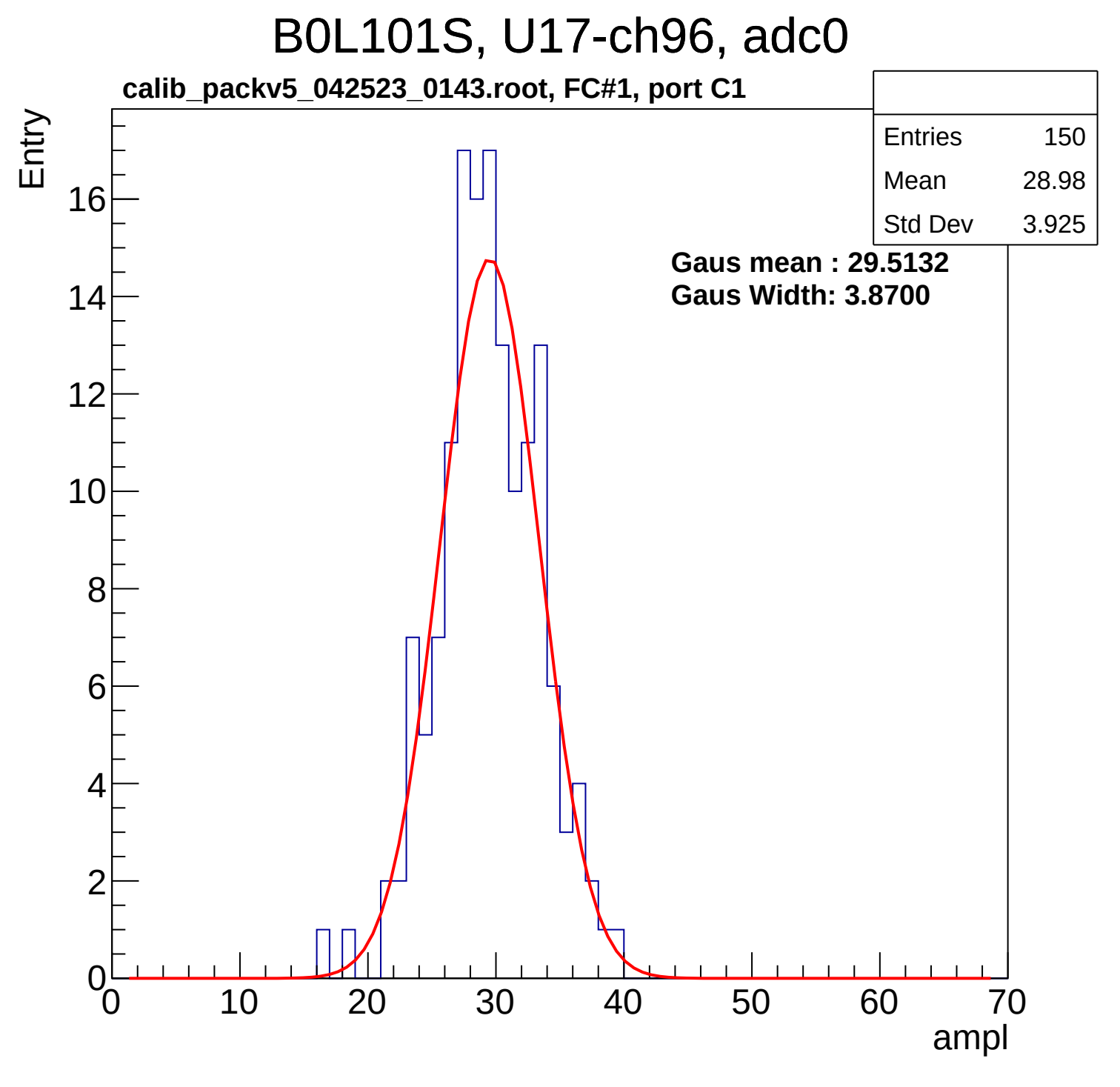

B0L101S, U17-ch95, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 48.67
Std Dev 3.684
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

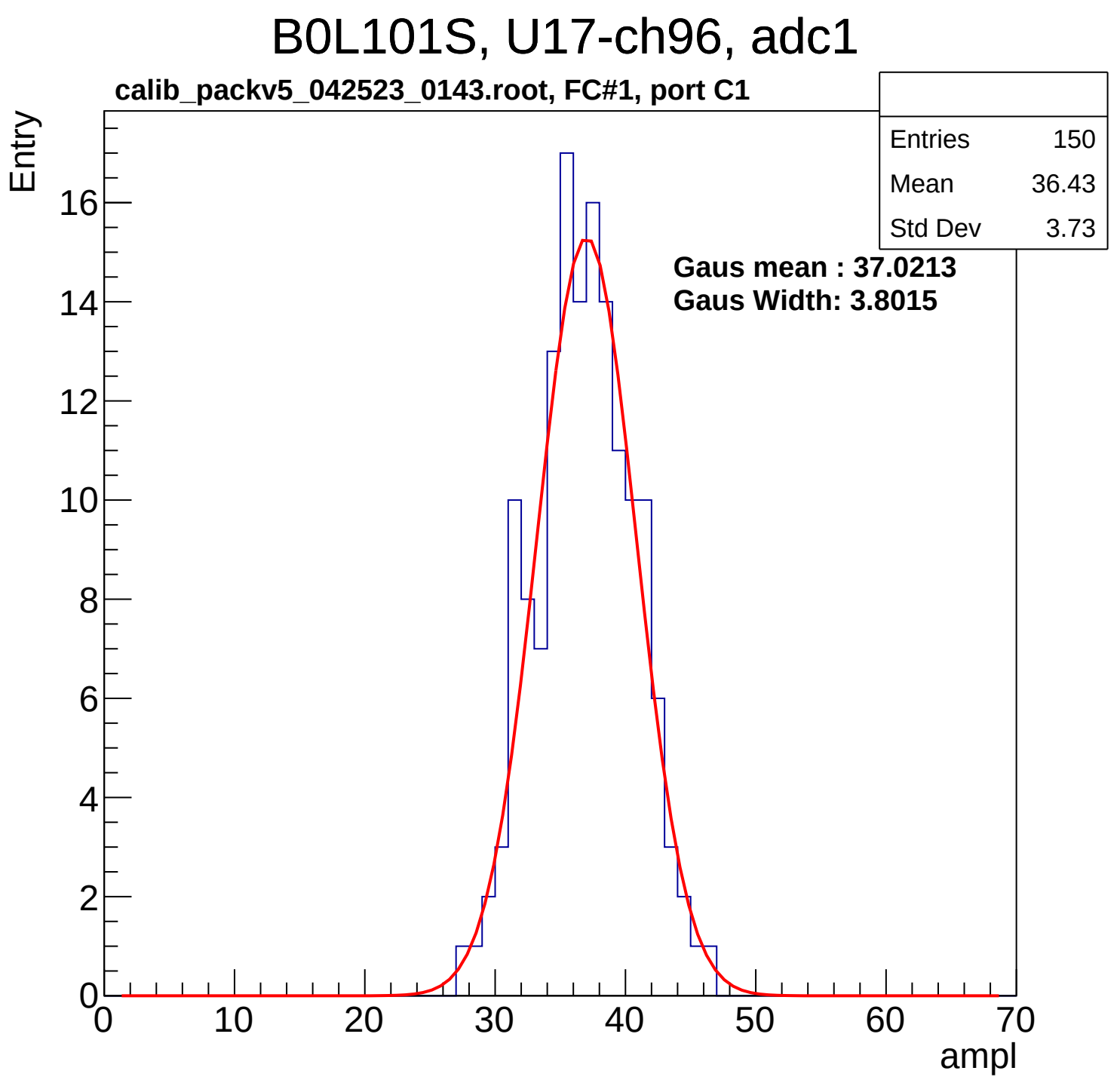

B0L101S, U17-ch95, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 98
Mean 53.8
Std Dev 3.645
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

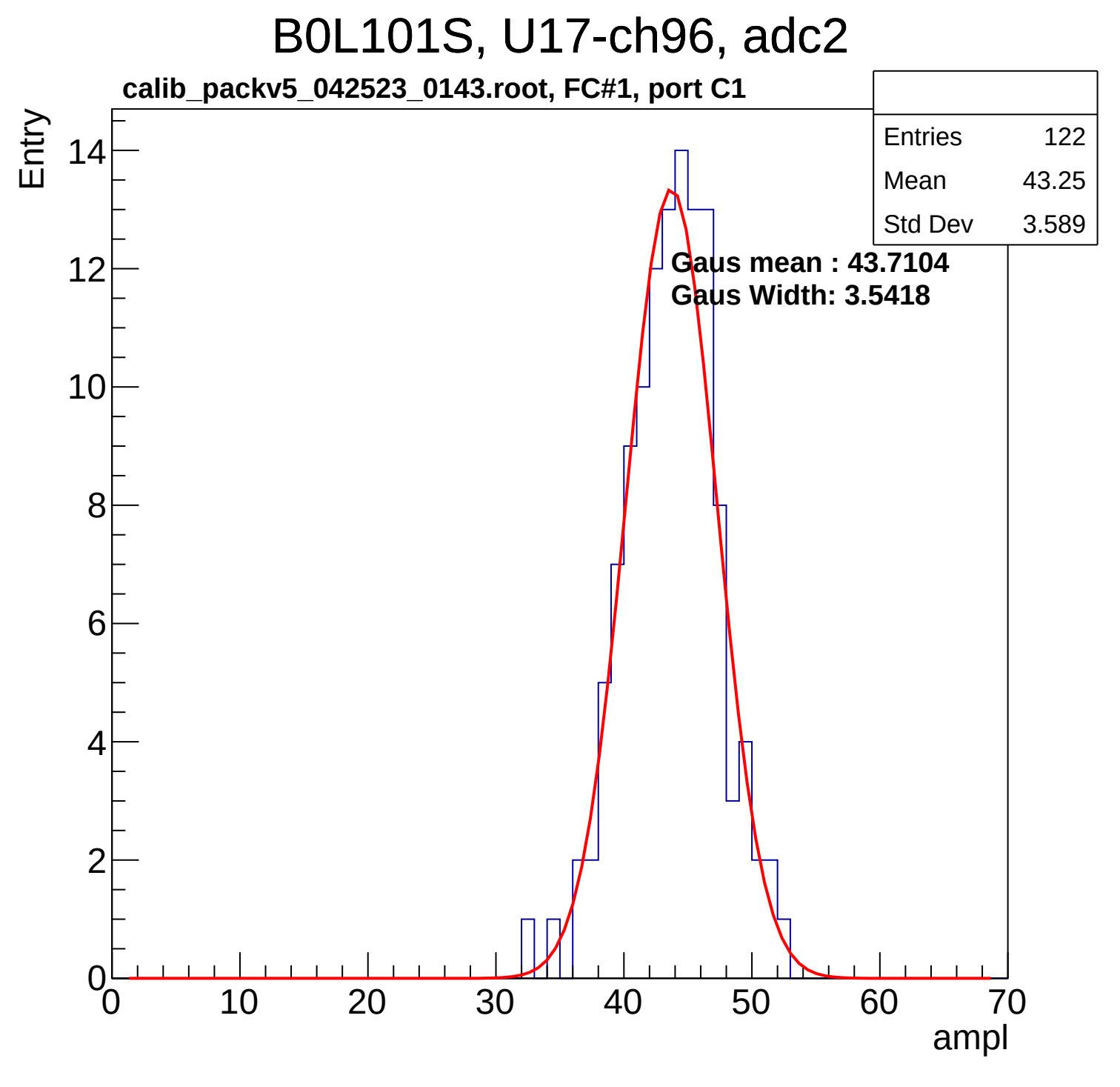

B0L101S, U17-ch95, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 107
Mean 58.15
Std Dev 6.296
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

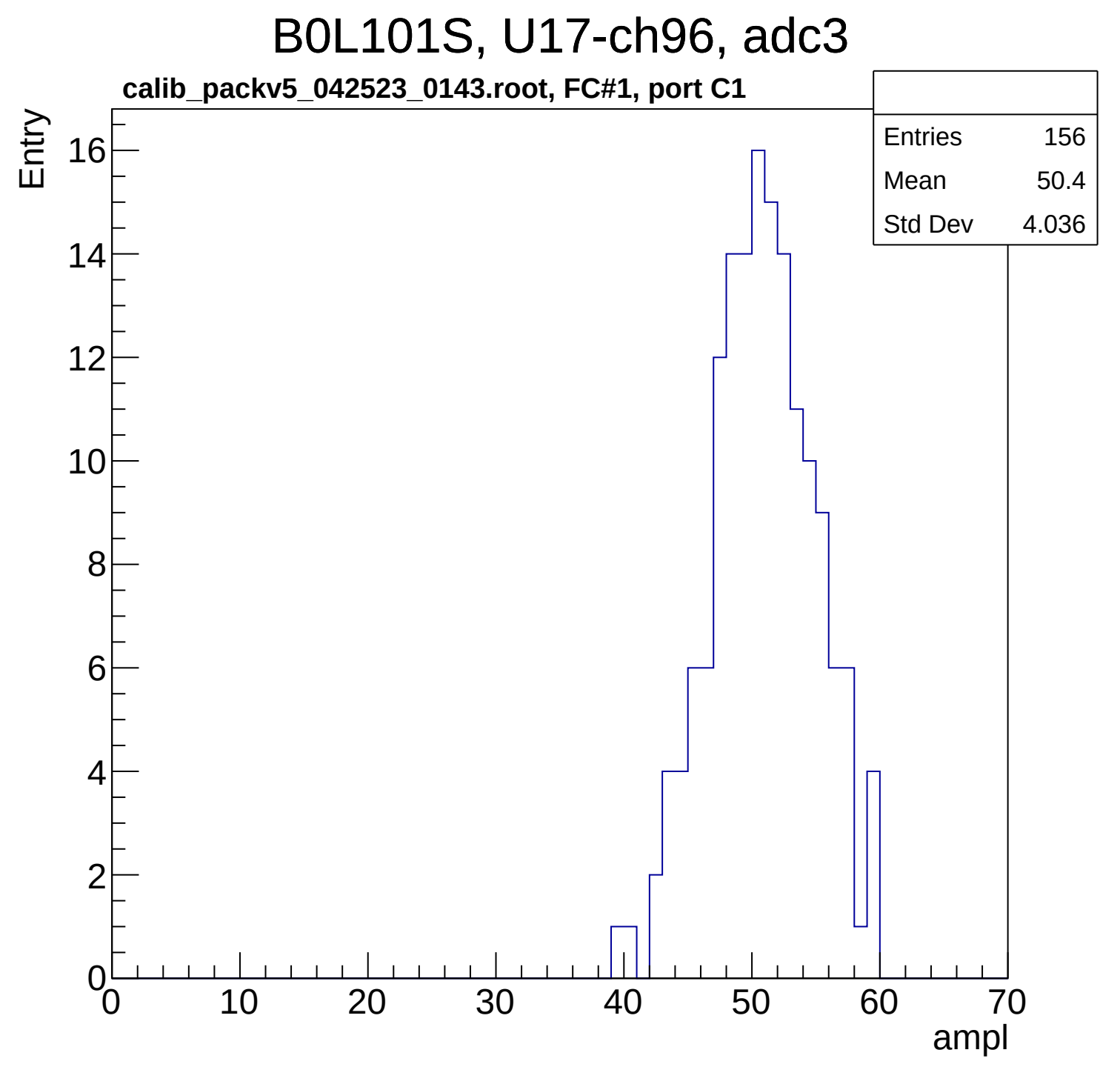

B0L101S, U17-ch95, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 32
Mean 59.88
Std Dev 10.83
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

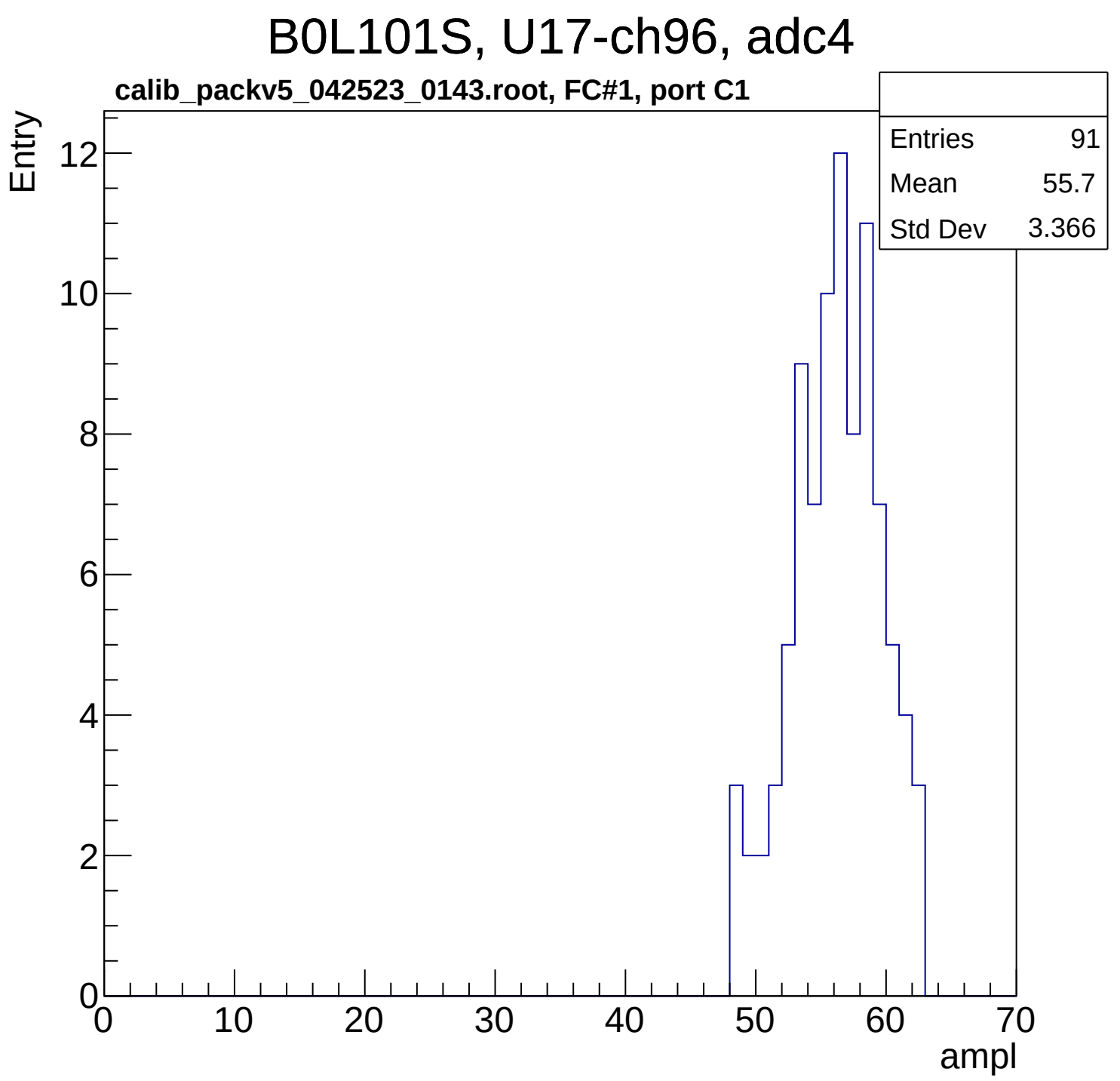

B0L101S, U17-ch95, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 11.33
Std Dev 8.014
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

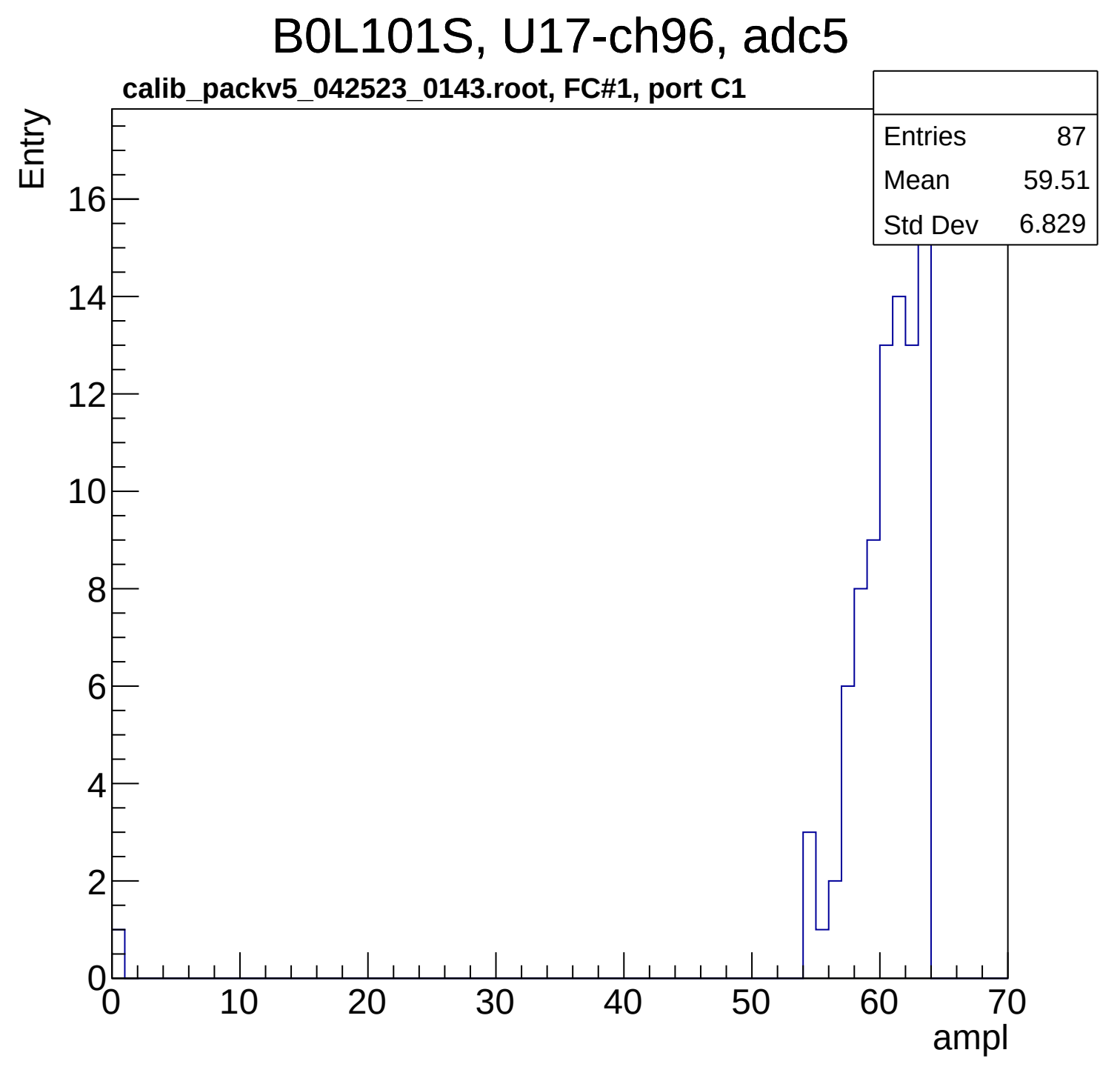

B0L101S, U17-ch96, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 28.98
Std Dev 3.925
Gaus mean : 29.5132
Gaus Width: 3.8700
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

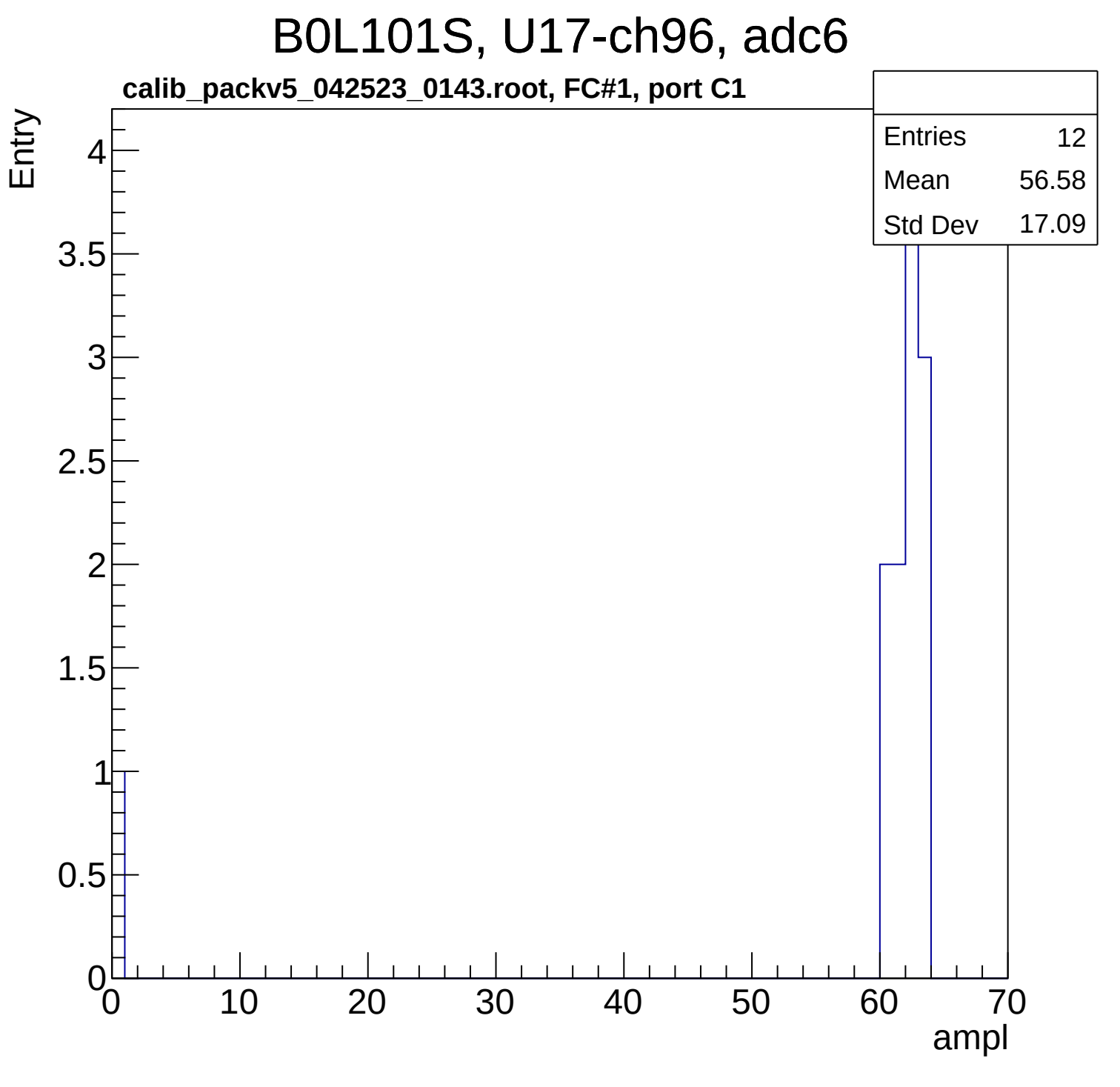

B0L101S, U17-ch96, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 36.43
Std Dev 3.73
Gaus mean : 37.0213
Gaus Width: 3.8015
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

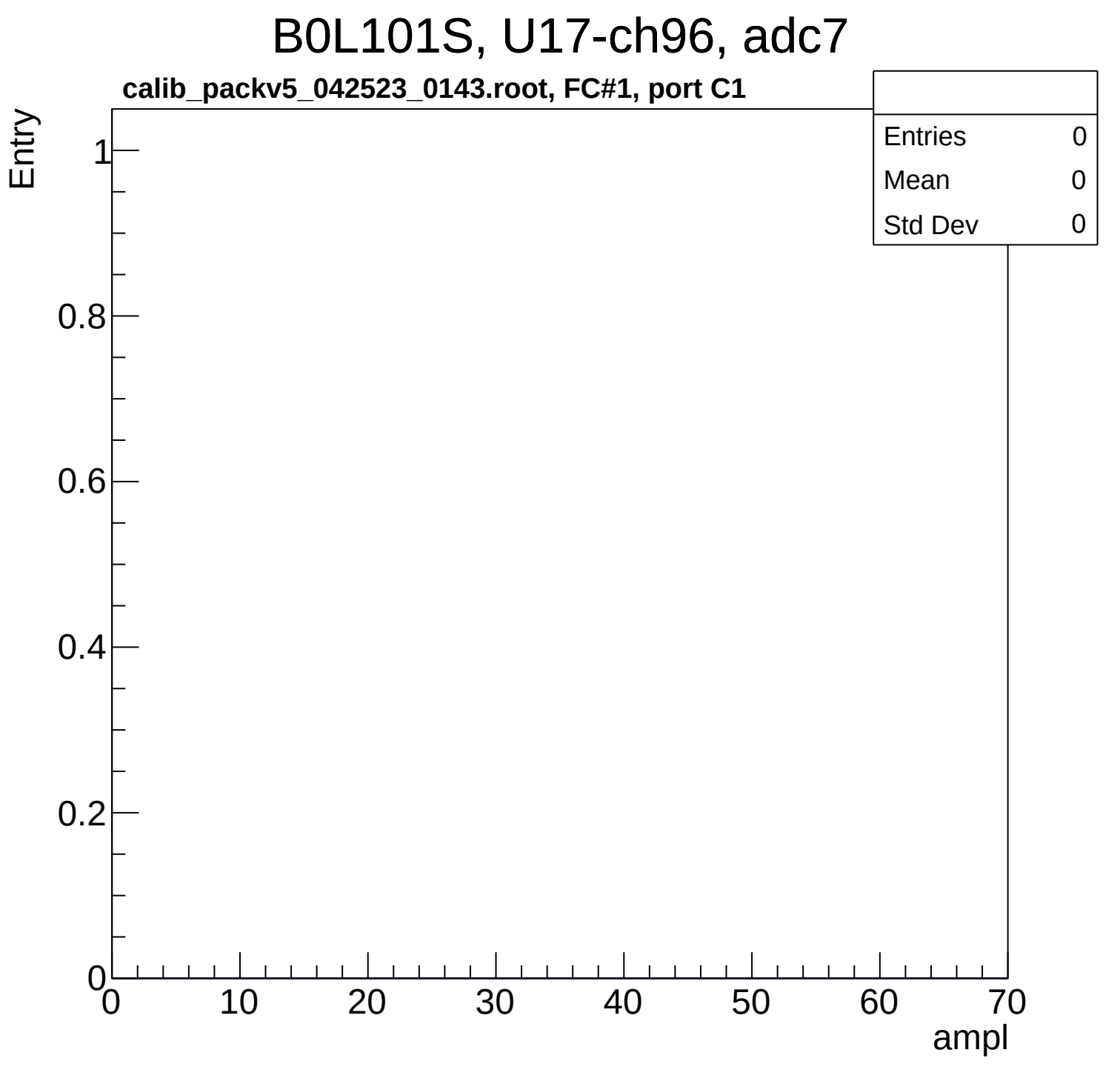

B0L101S, U17-ch96, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 43.25
Std Dev 3.589
Gaus mean : 43.7104
Gaus Width: 3.5418
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

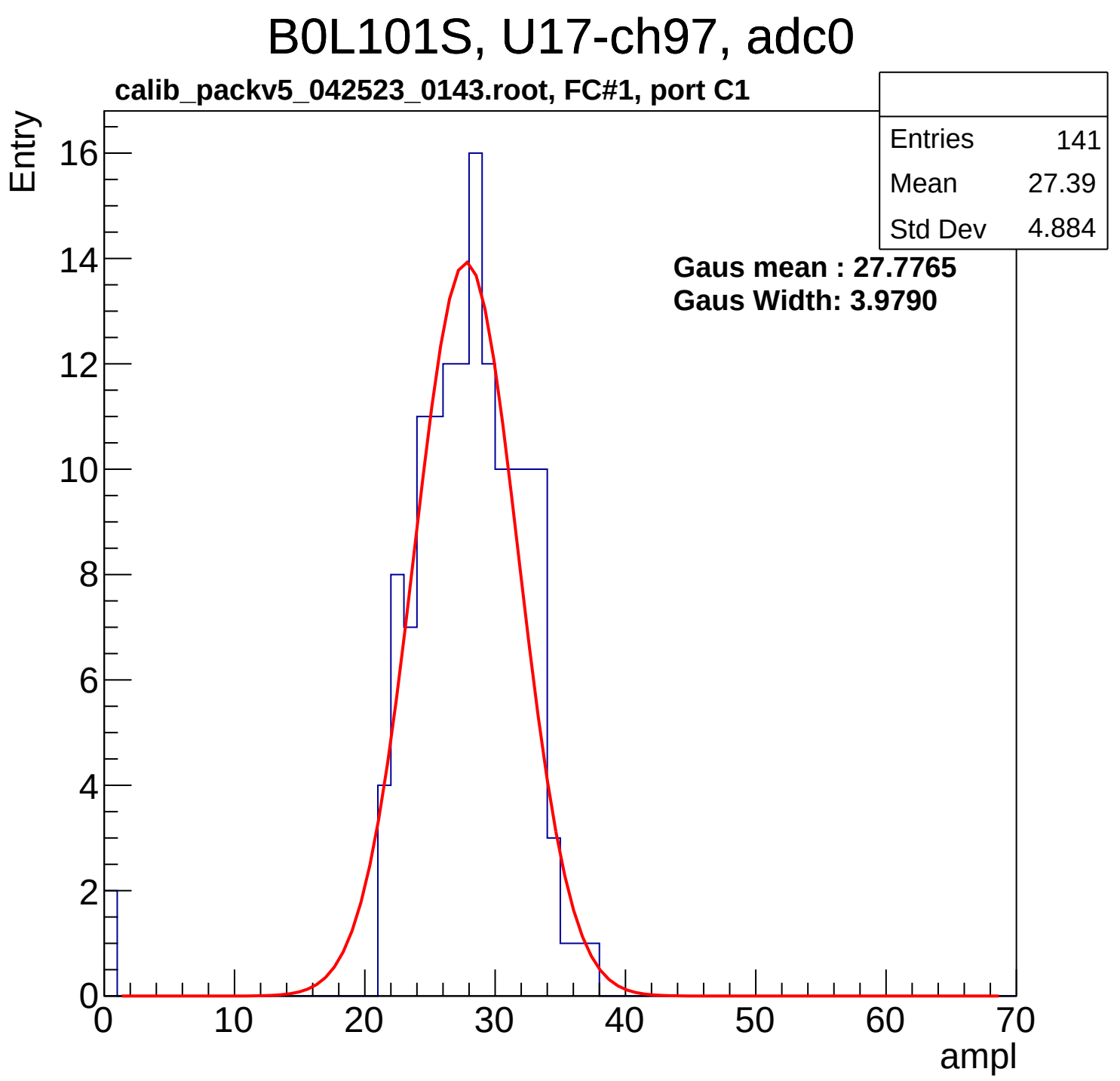

B0L101S, U17-ch96, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 50.4
Std Dev 4.036
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

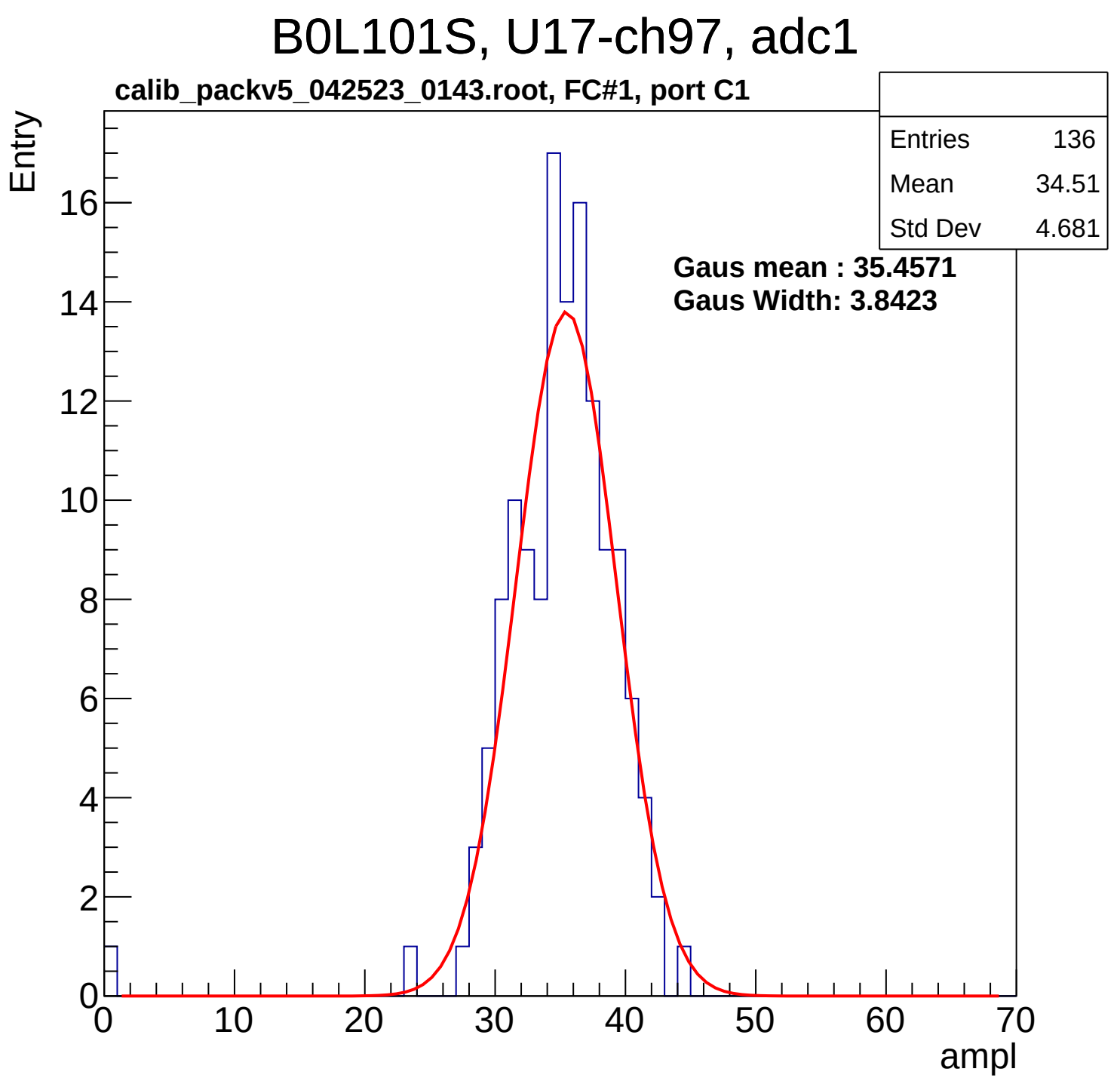

B0L101S, U17-ch96, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 91
Mean 55.7
Std Dev 3.366
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

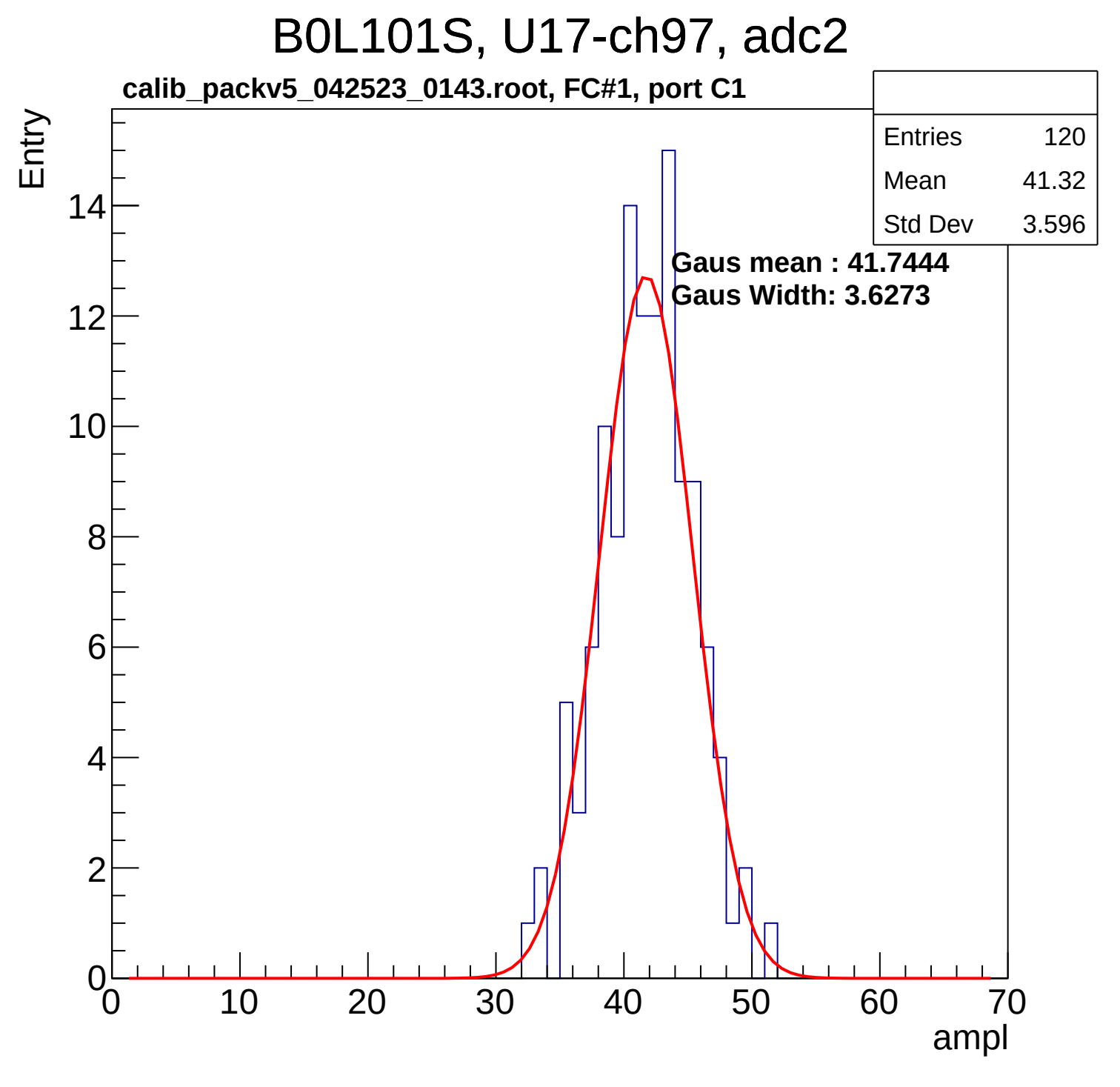

B0L101S, U17-ch96, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 87
Mean 59.51
Std Dev 6.829
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

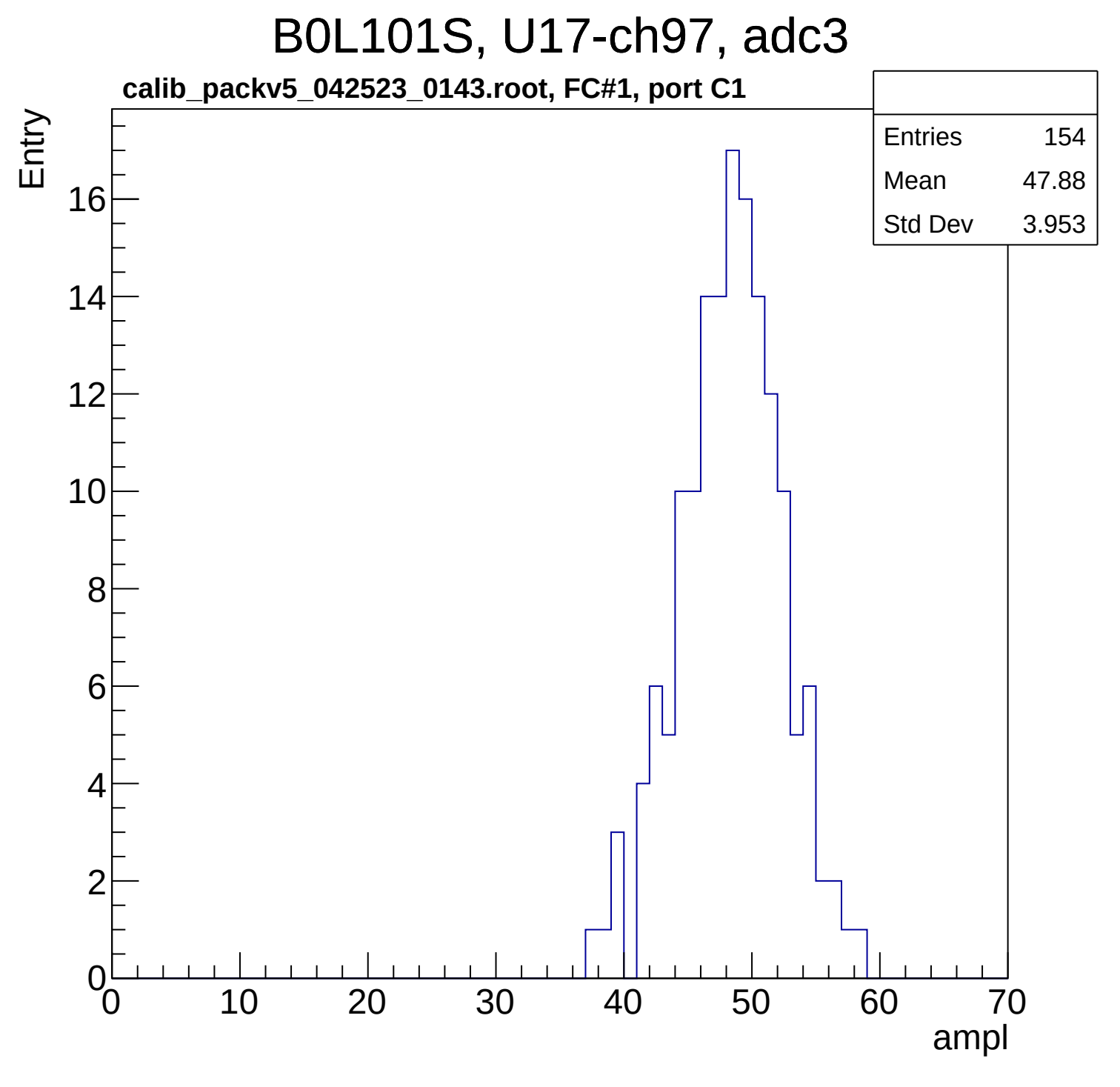

B0L101S, U17-ch96, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 56.58
Std Dev 17.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

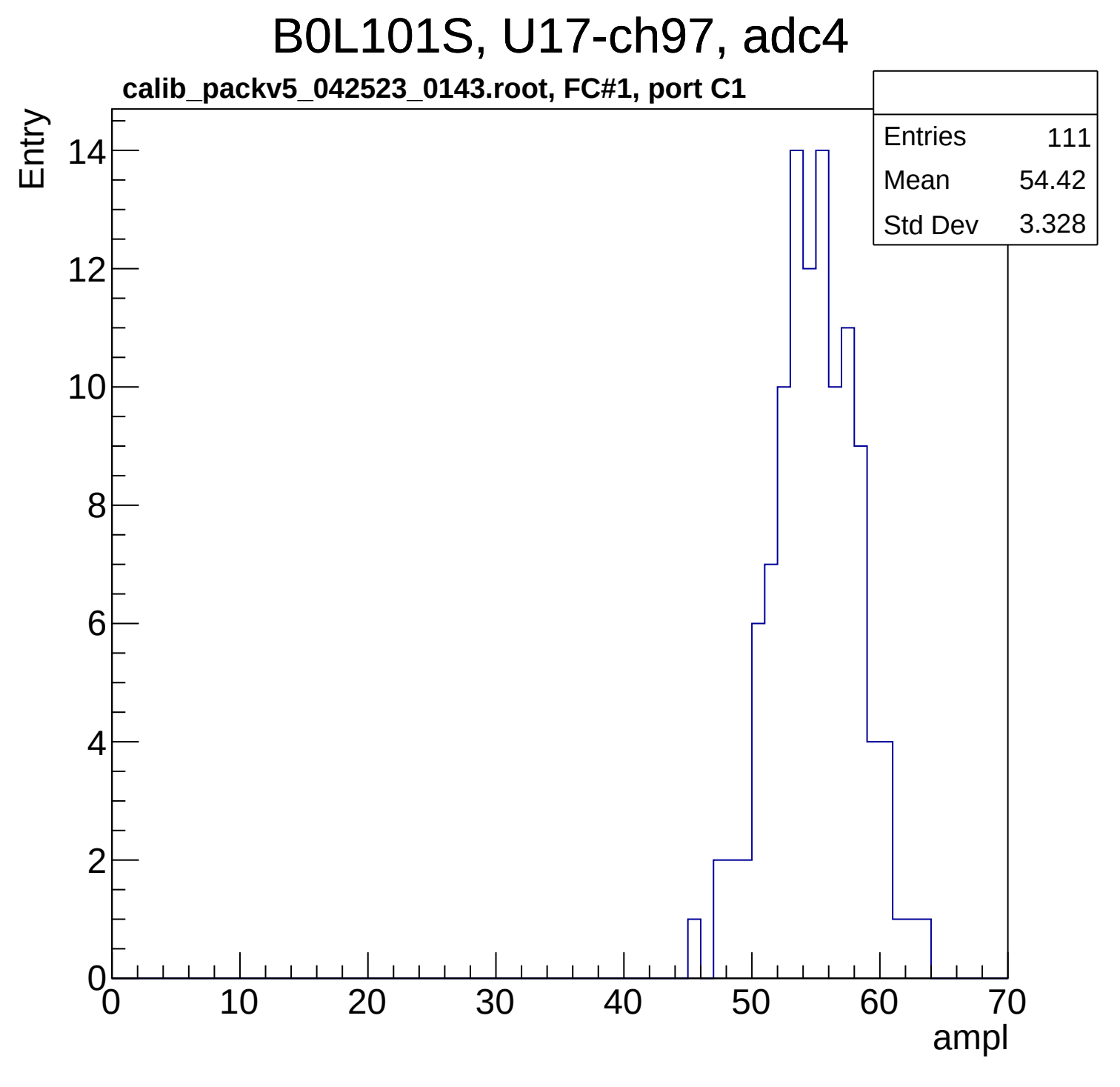

B0L101S, U17-ch96, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

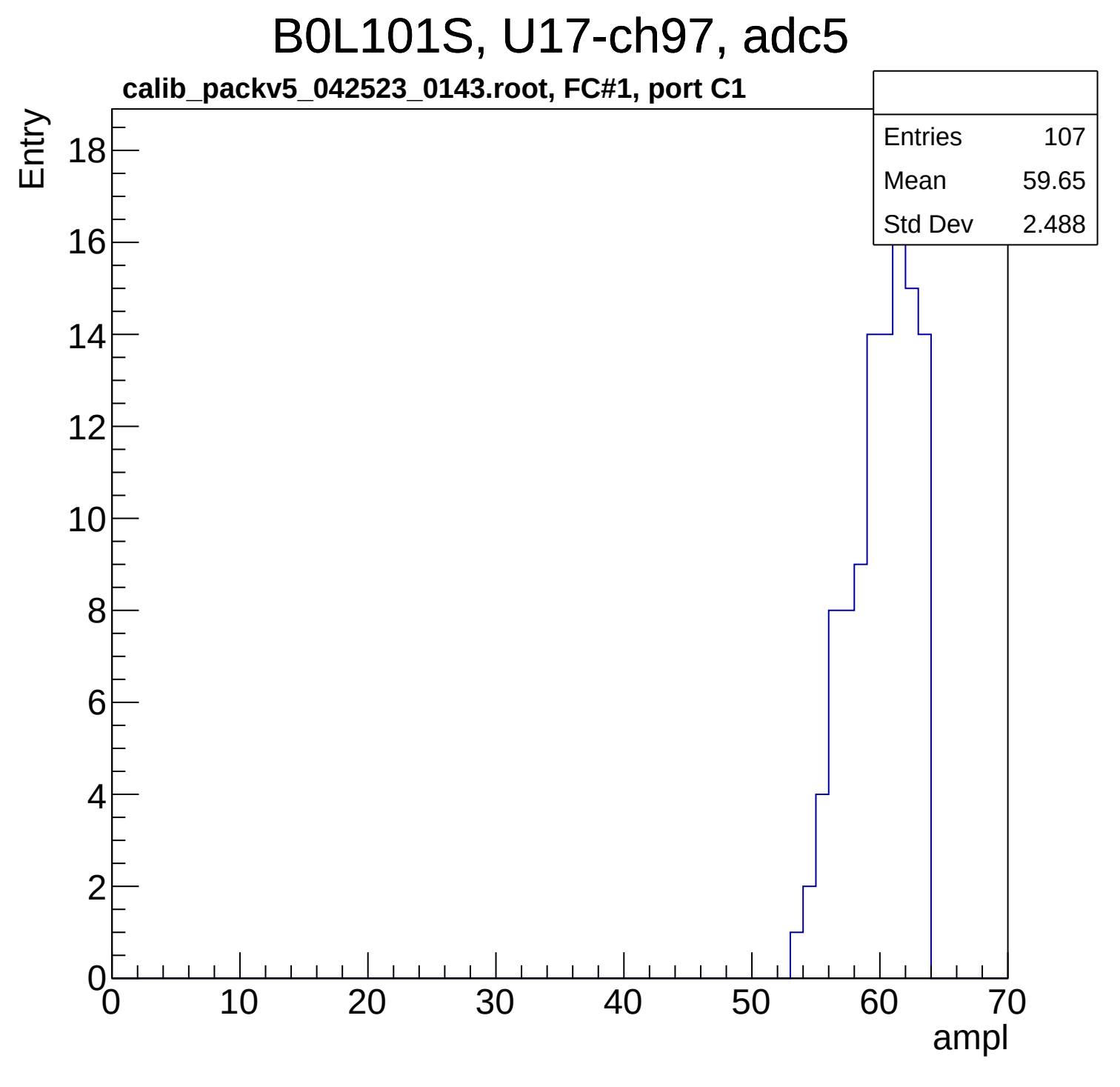

B0L101S, U17-ch97, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 27.39
Std Dev 4.884
Gaus mean : 27.7765
Gaus Width: 3.9790
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

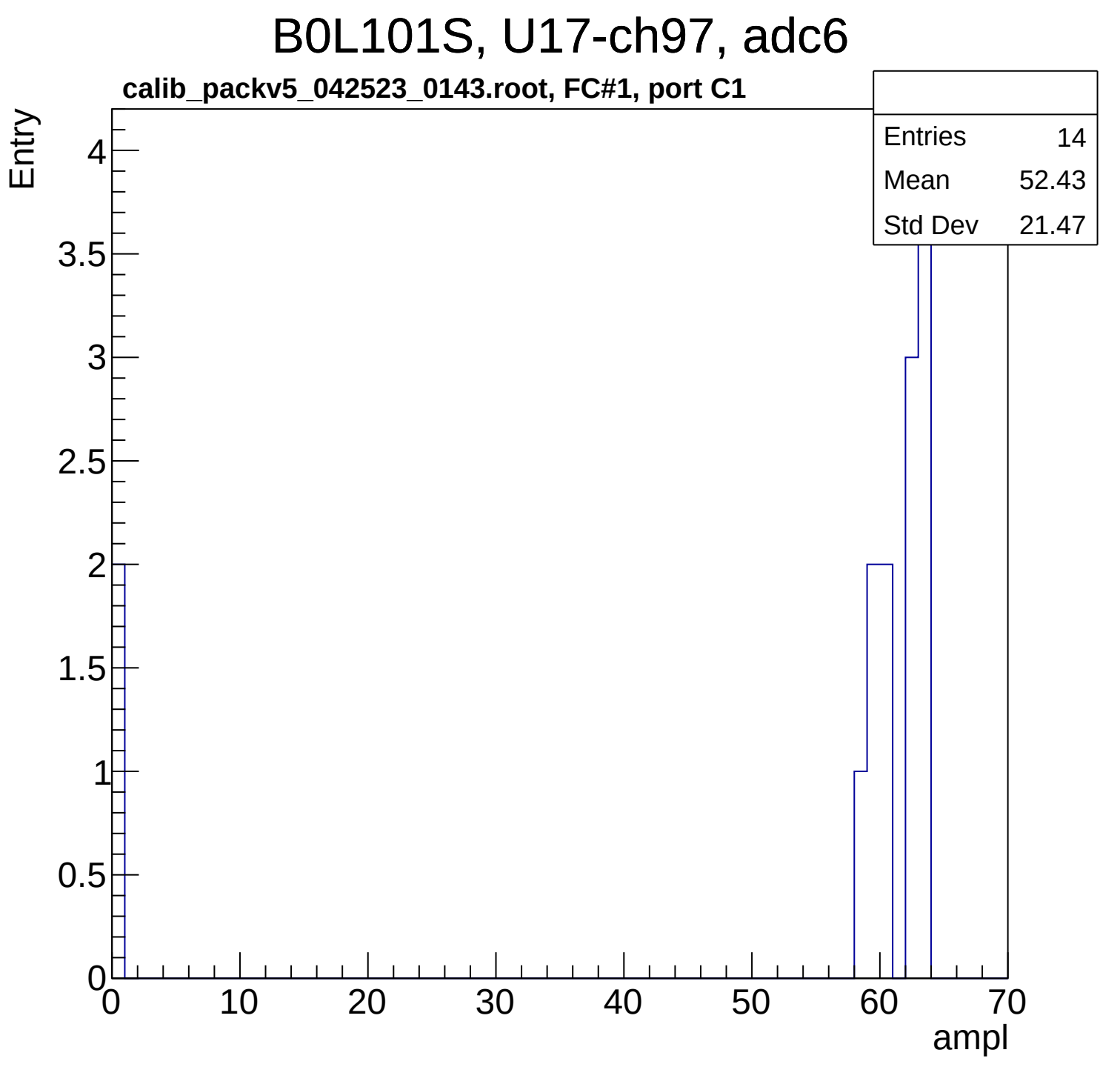

B0L101S, U17-ch97, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 34.51
Std Dev 4.681
Gaus mean : 35.4571
Gaus Width: 3.8423
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

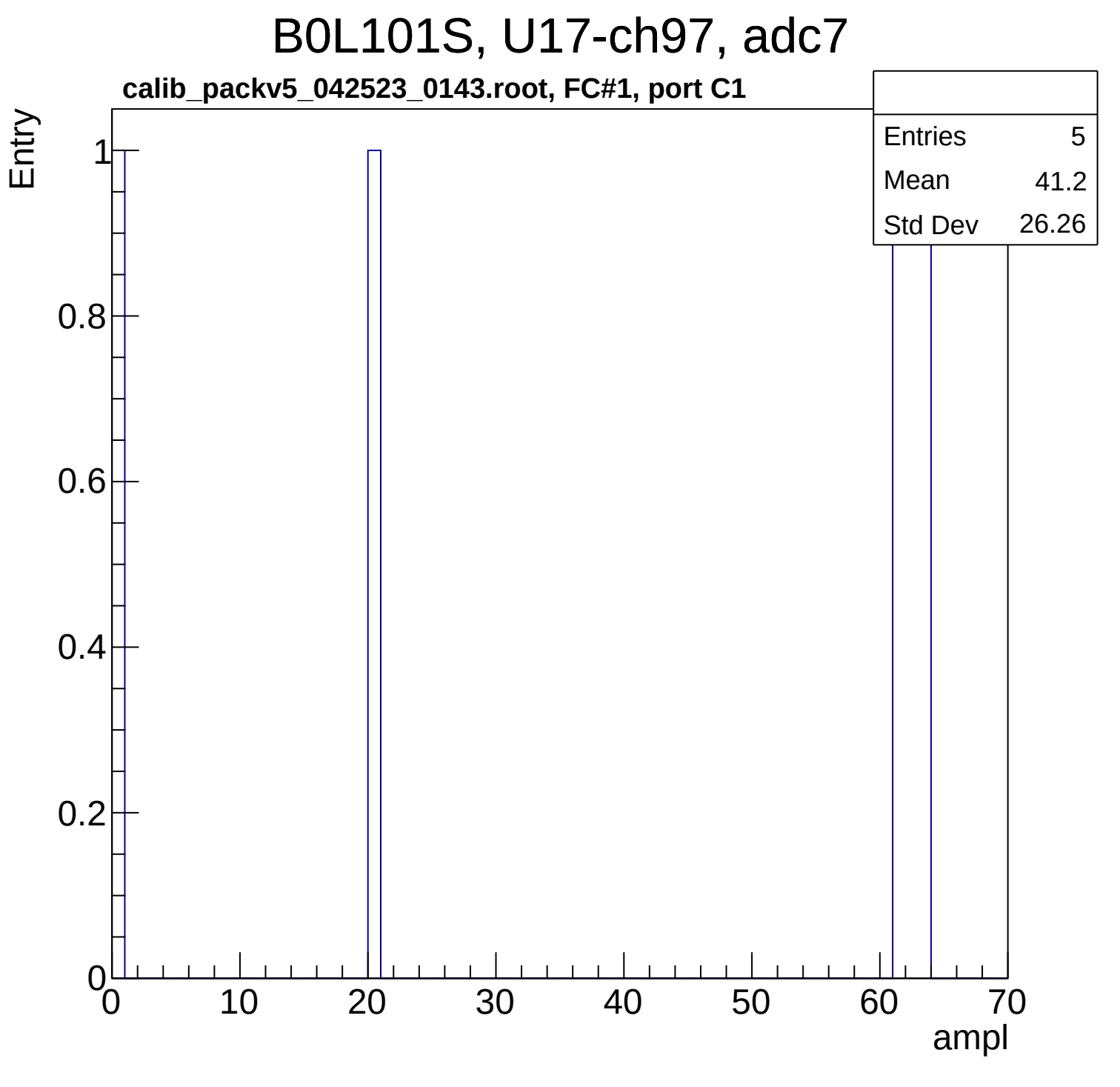

B0L101S, U17-ch97, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 41.32
Std Dev 3.596
Gaus mean : 41.7444
Gaus Width: 3.6273
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

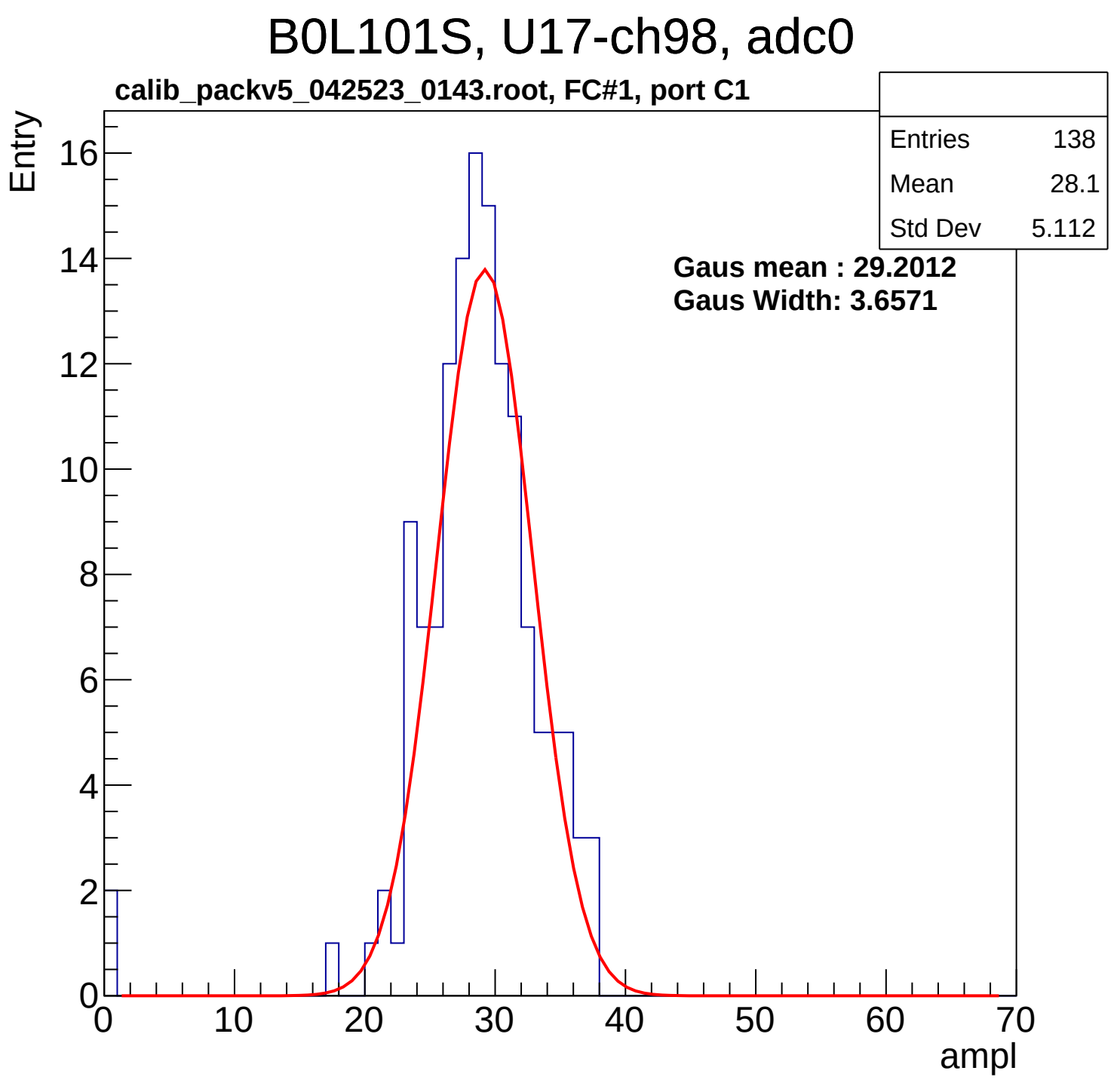

B0L101S, U17-ch97, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 47.88
Std Dev 3.953
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

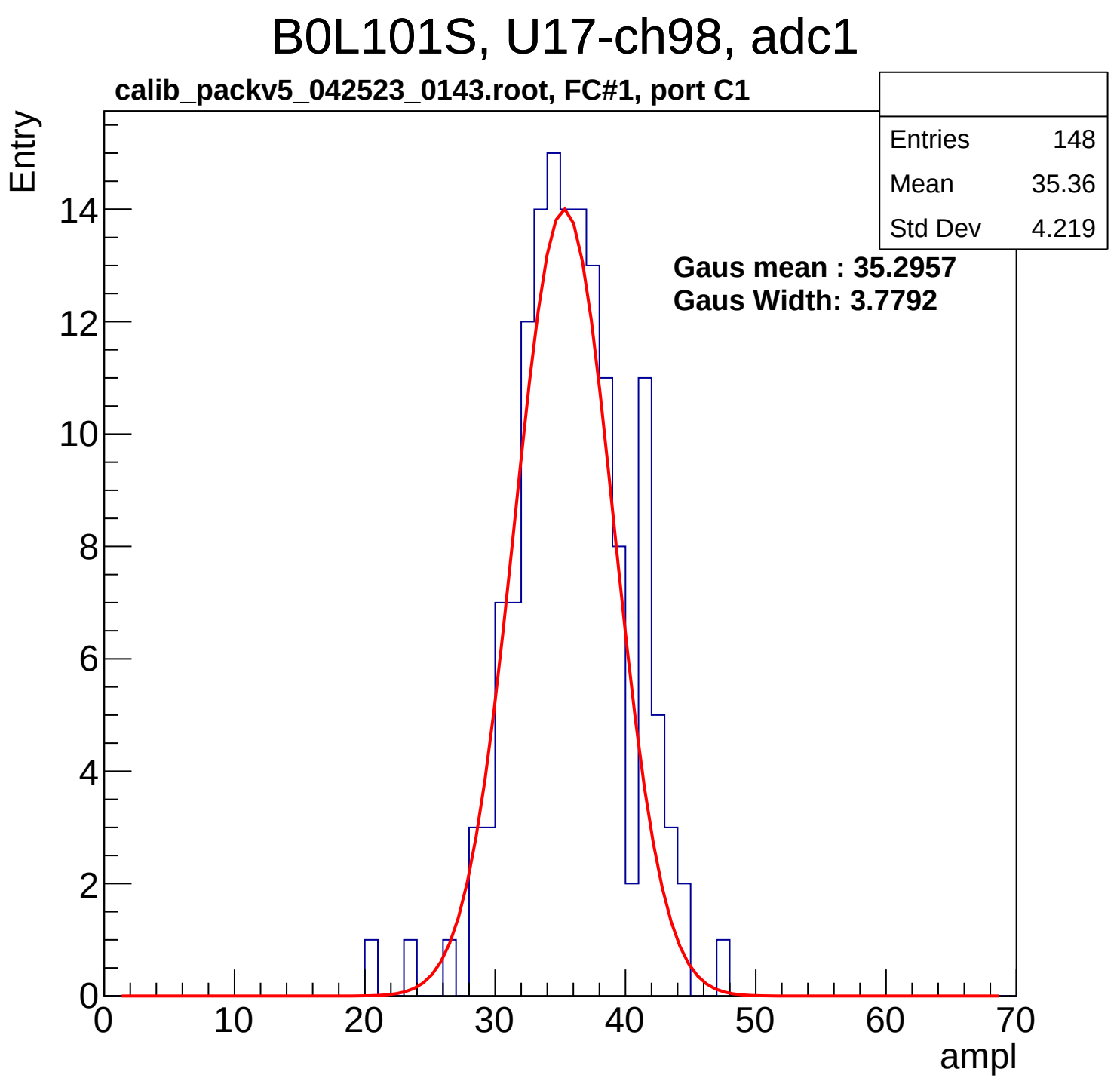

B0L101S, U17-ch97, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 54.42
Std Dev 3.328
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

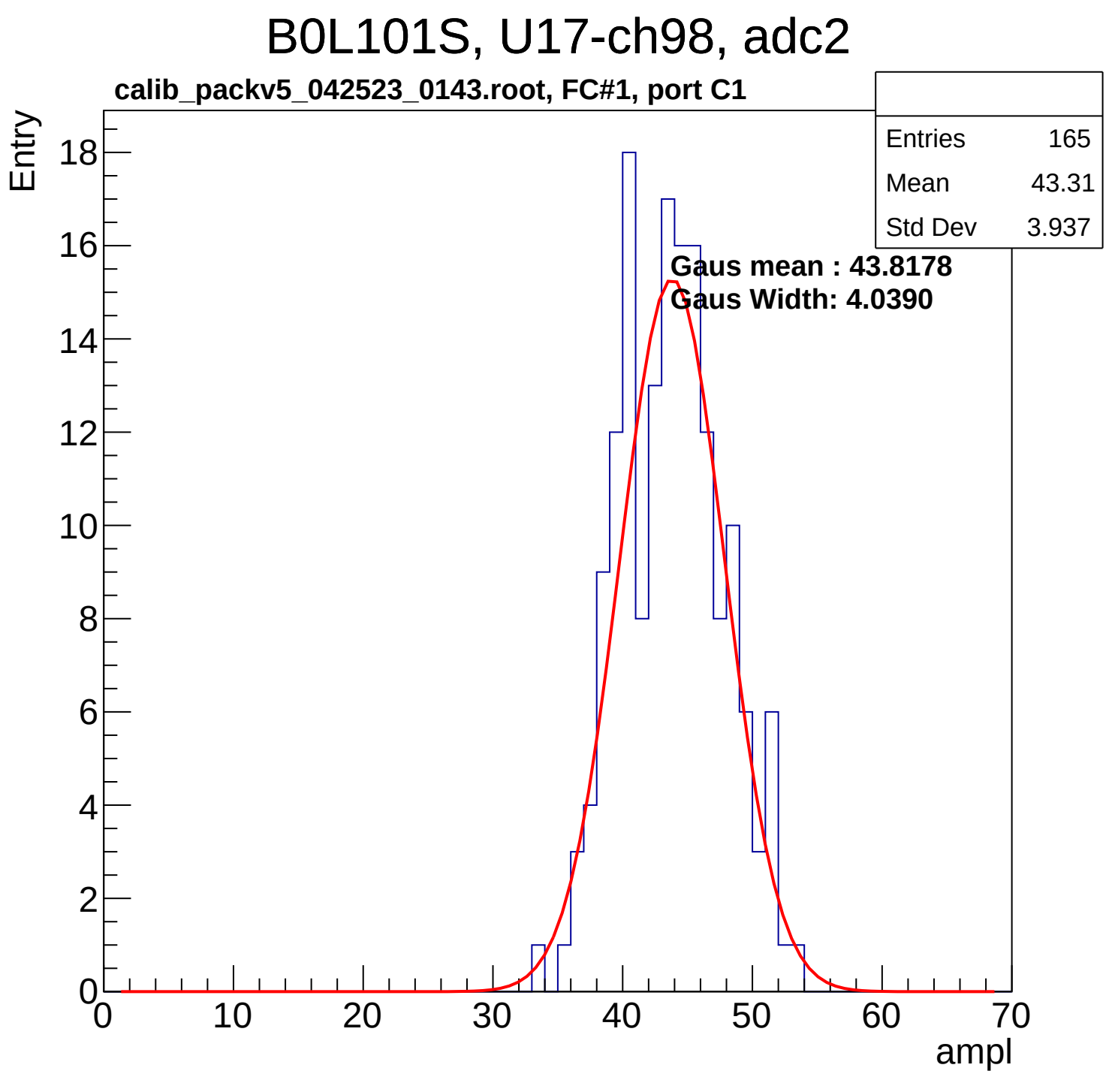

B0L101S, U17-ch97, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 107
Mean 59.65
Std Dev 2.488
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

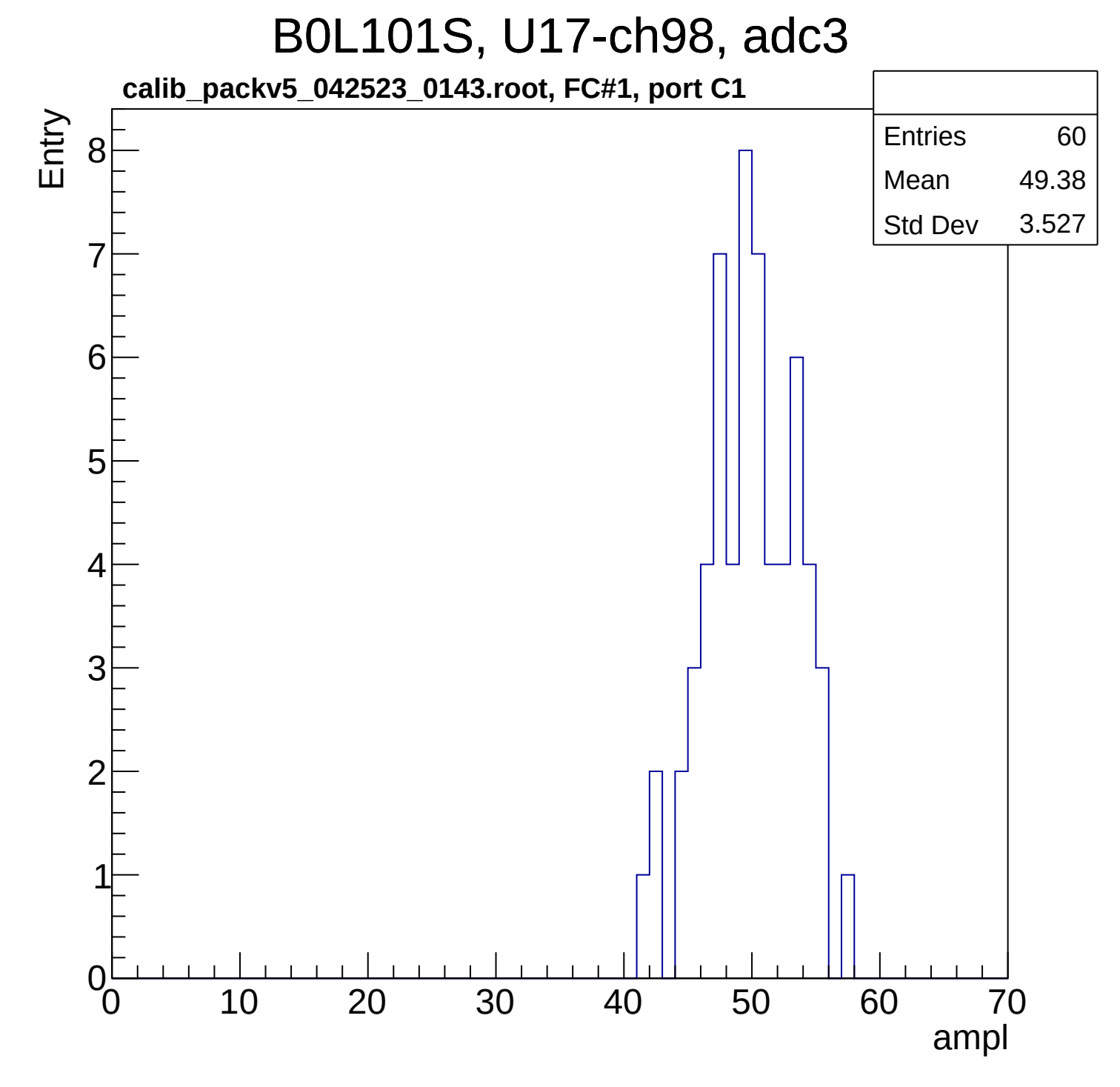

B0L101S, U17-ch97, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 52.43
Std Dev 21.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

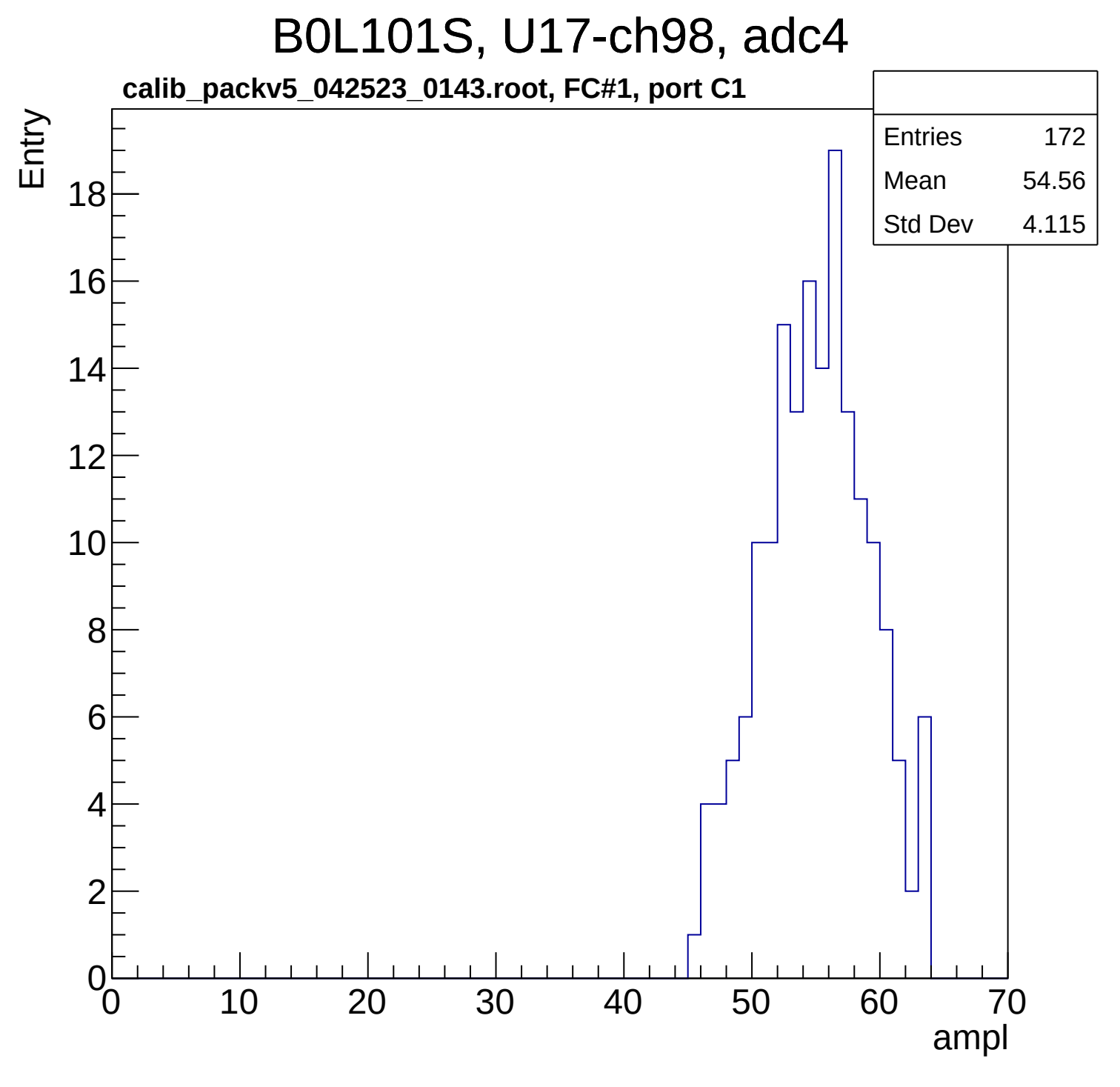

B0L101S, U17-ch97, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 41.2
Std Dev 26.26
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

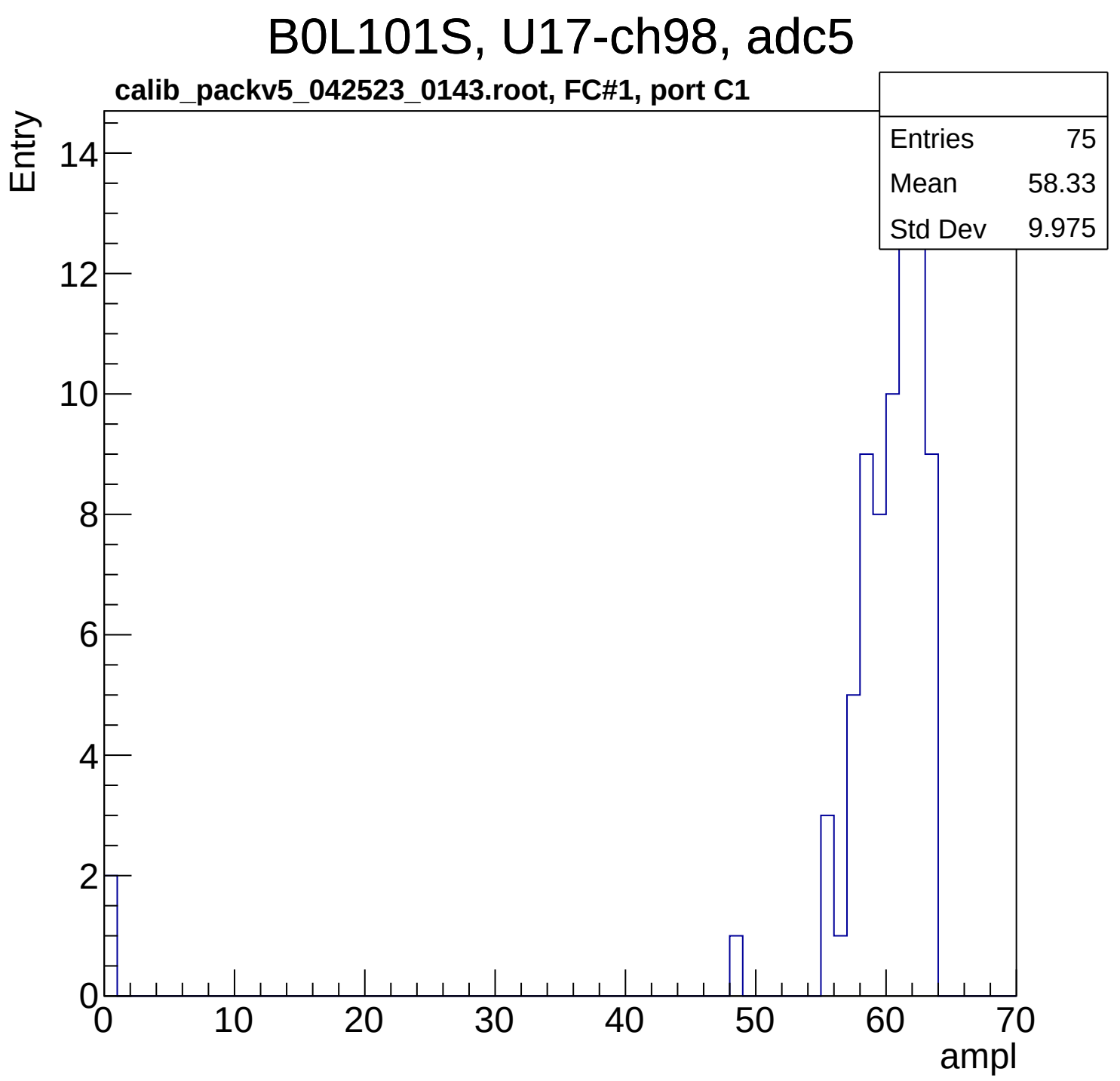

B0L101S, U17-ch98, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 28.1
Std Dev 5.112
Gaus mean : 29.2012
Gaus Width: 3.6571
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

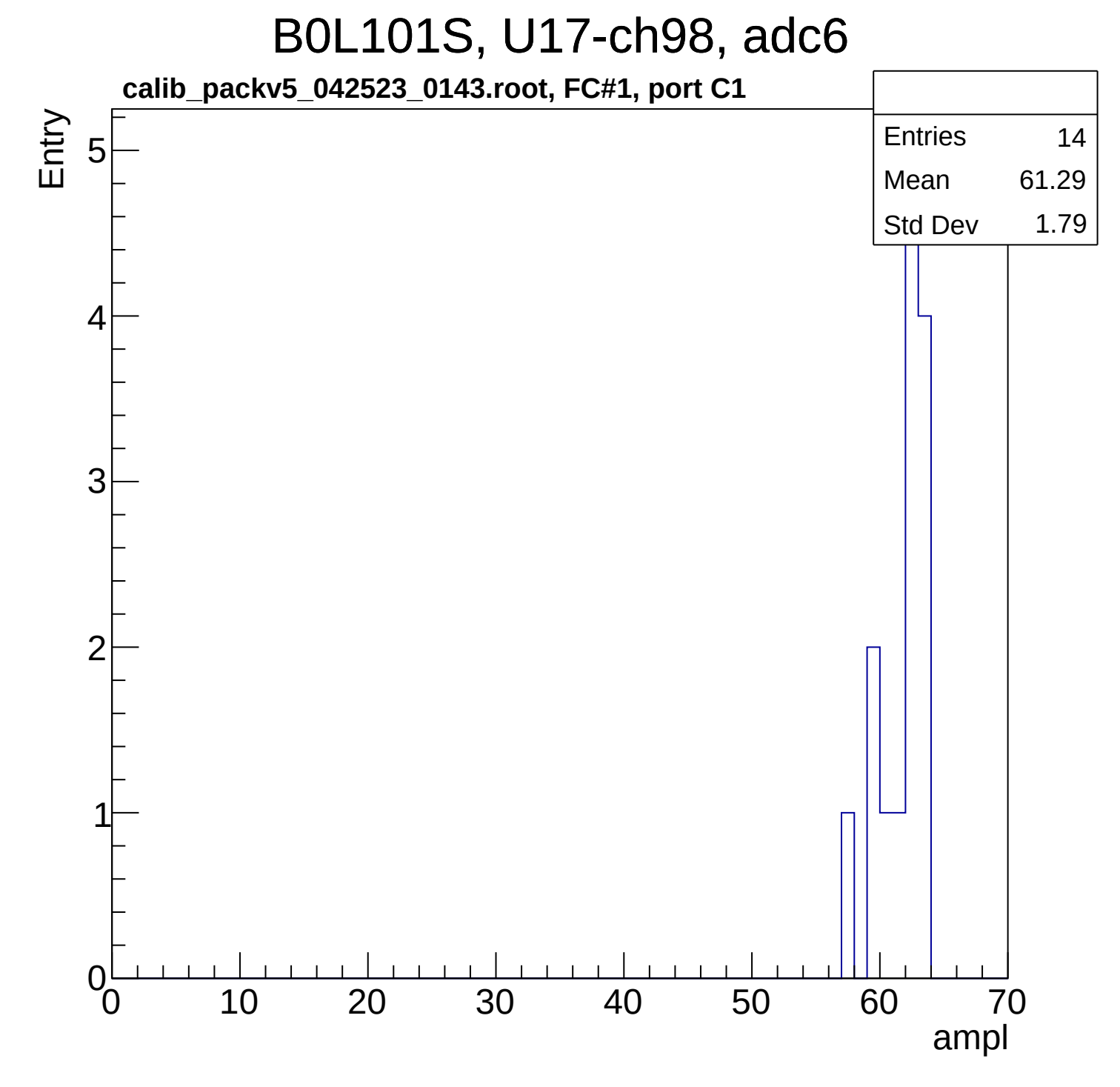

B0L101S, U17-ch98, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 35.36
Std Dev 4.219
Gaus mean : 35.2957
Gaus Width: 3.7792
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

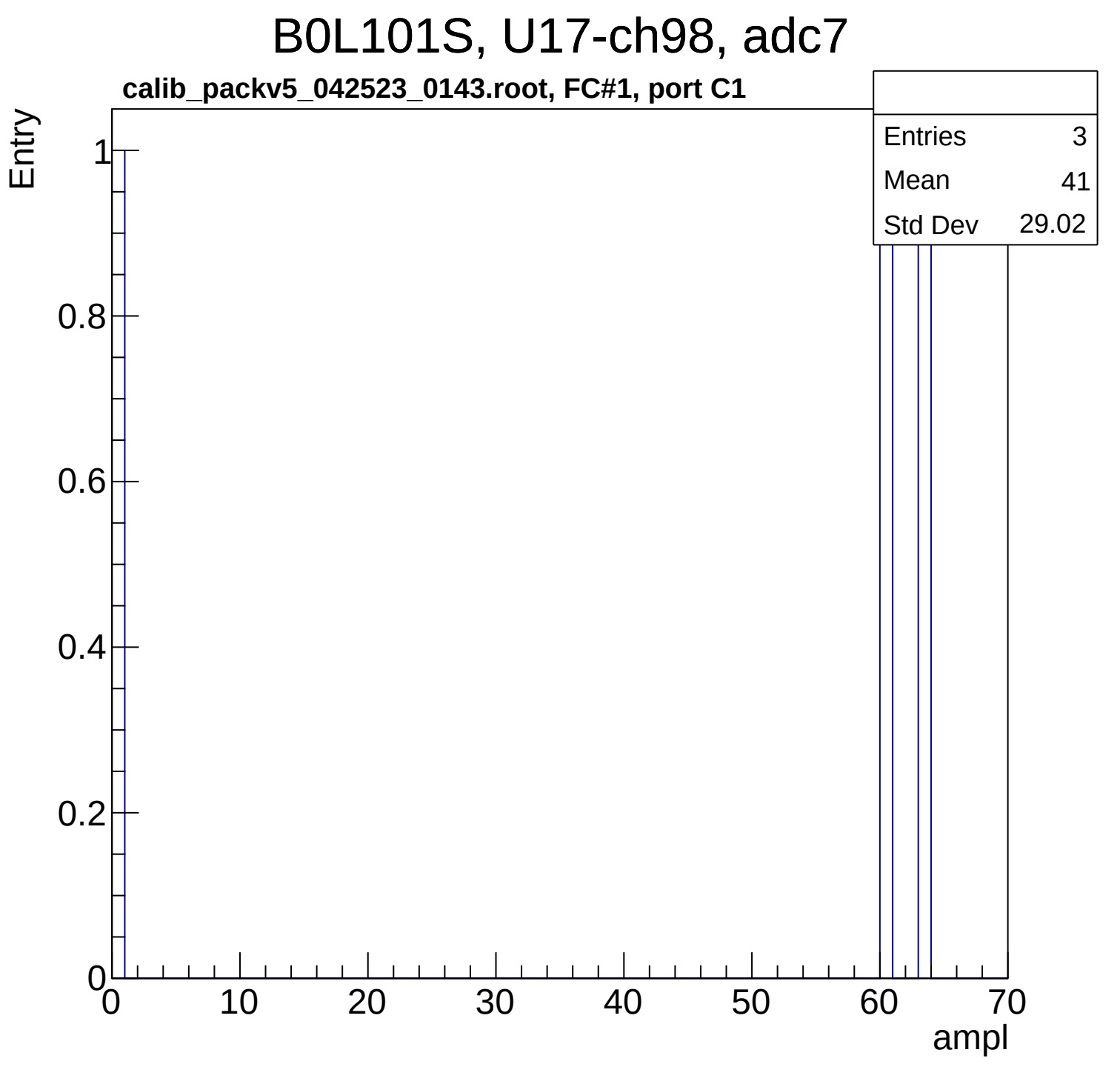

B0L101S, U17-ch98, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 165
Mean 43.31
Std Dev 3.937
Gaus mean : 43.8178
Gaus Width: 4.0390
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

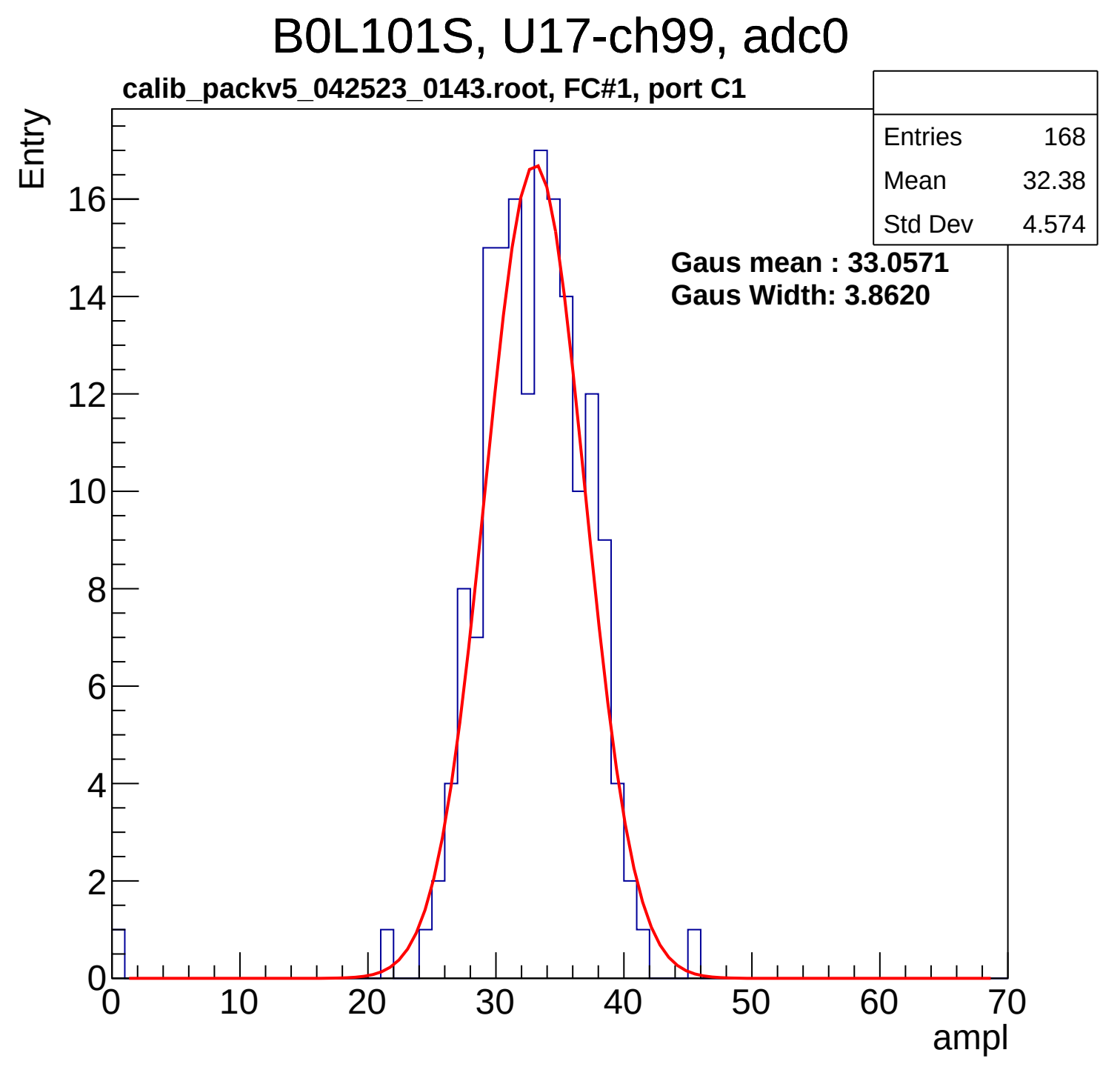

B0L101S, U17-ch98, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 60
Mean 49.38
Std Dev 3.527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

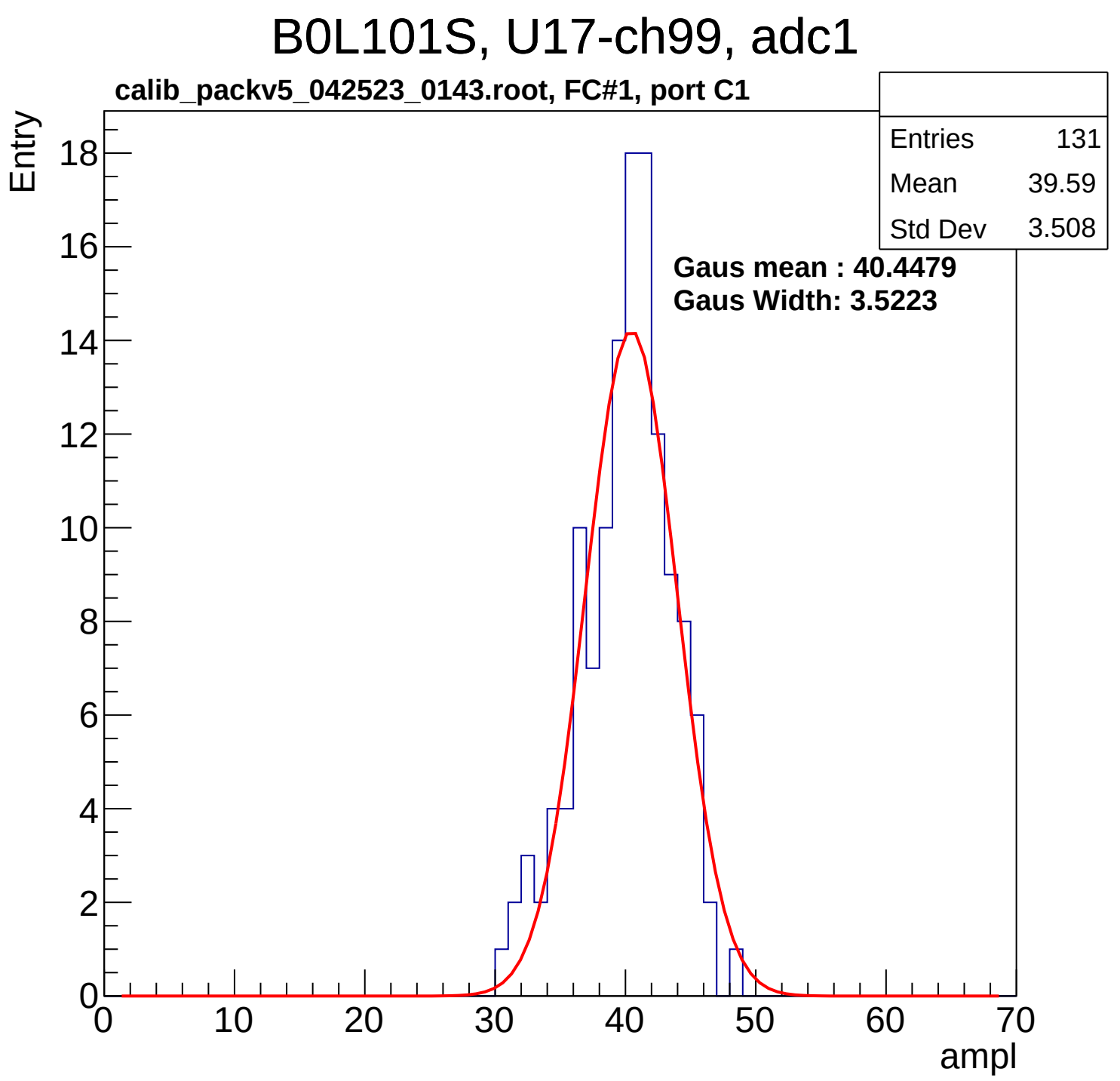

B0L101S, U17-ch98, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 172
Mean 54.56
Std Dev 4.115
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

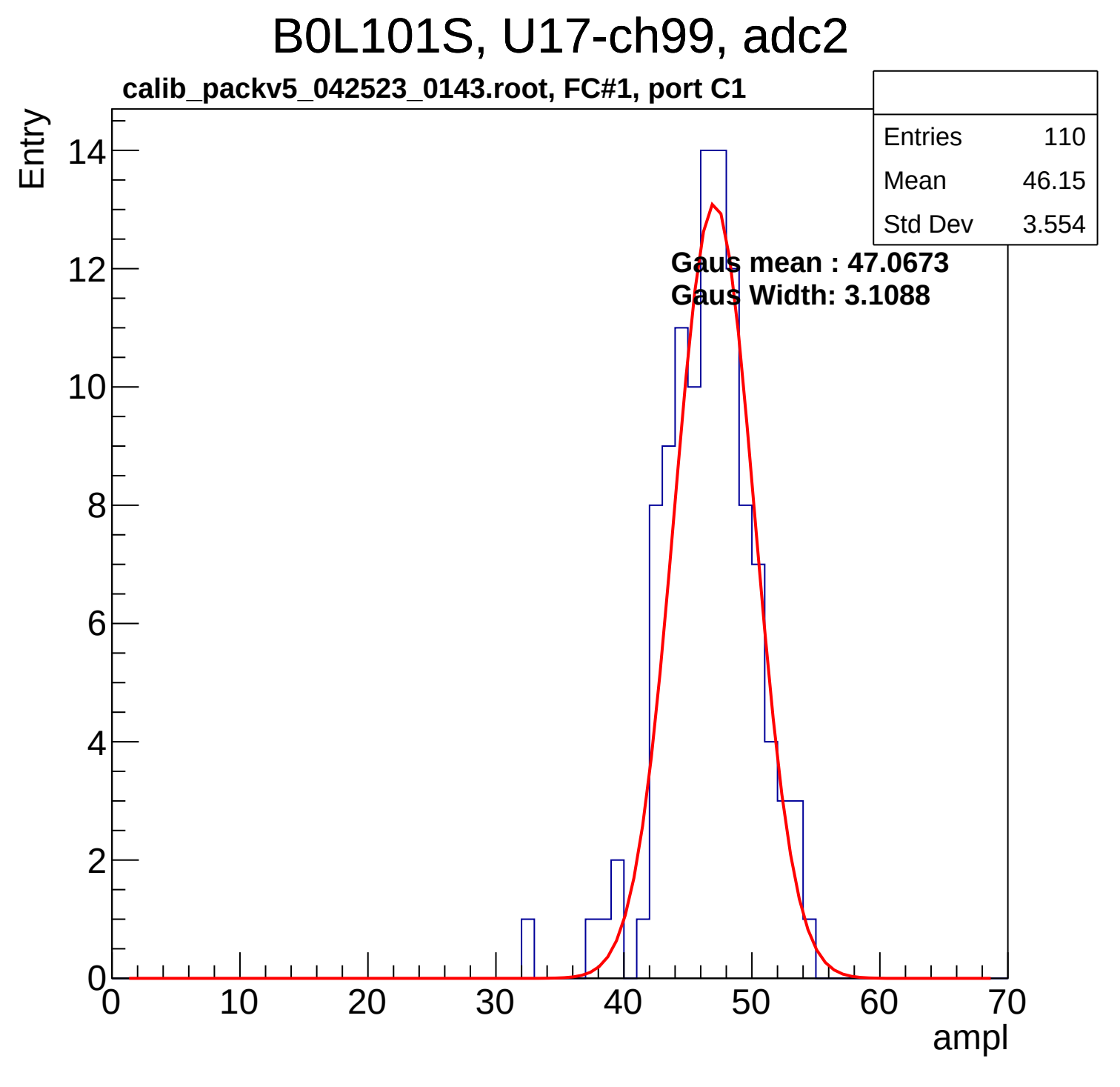

B0L101S, U17-ch98, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 75
Mean 58.33
Std Dev 9.975
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

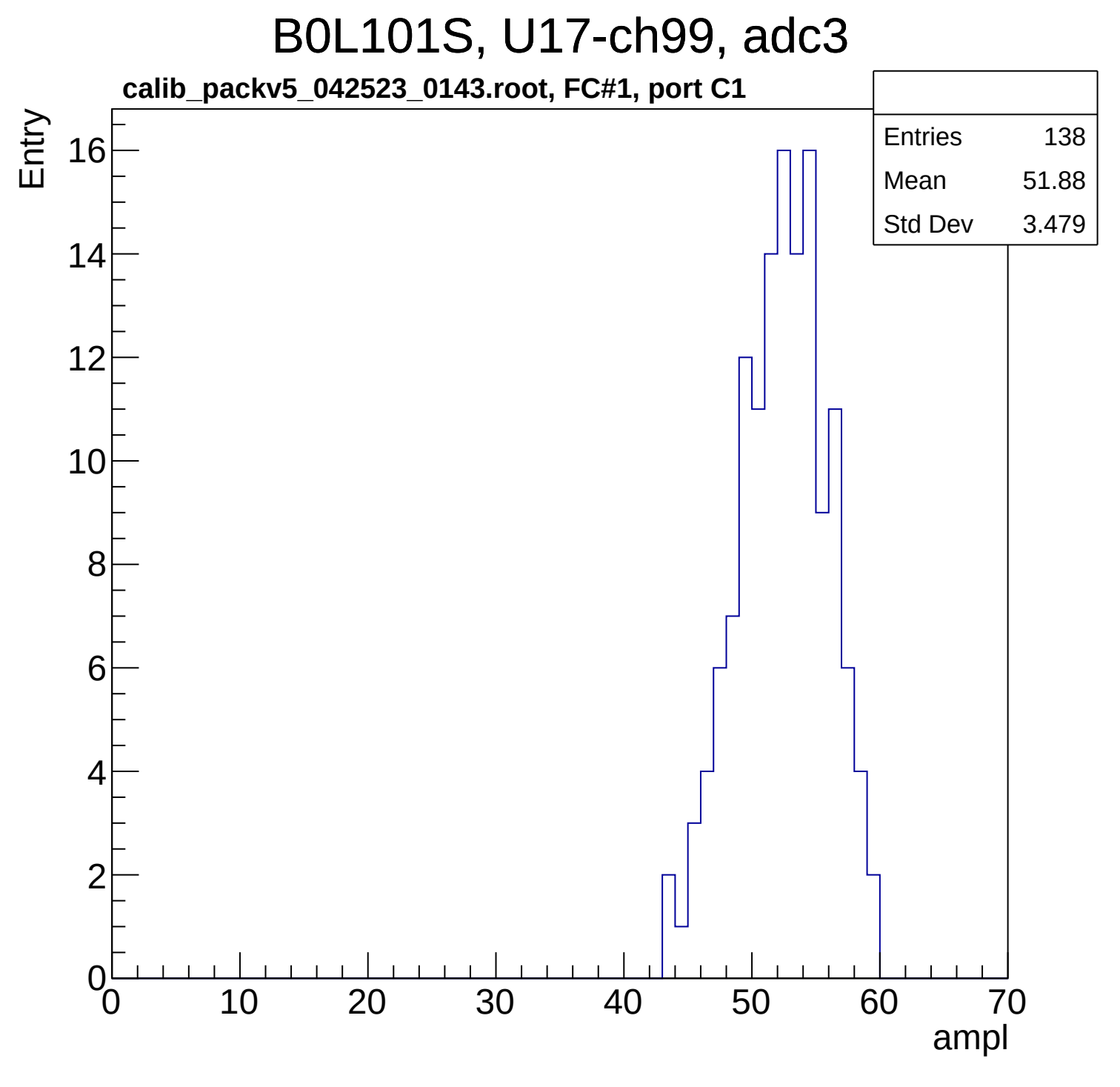

B0L101S, U17-ch98, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 14
Mean 61.29
Std Dev 1.79
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

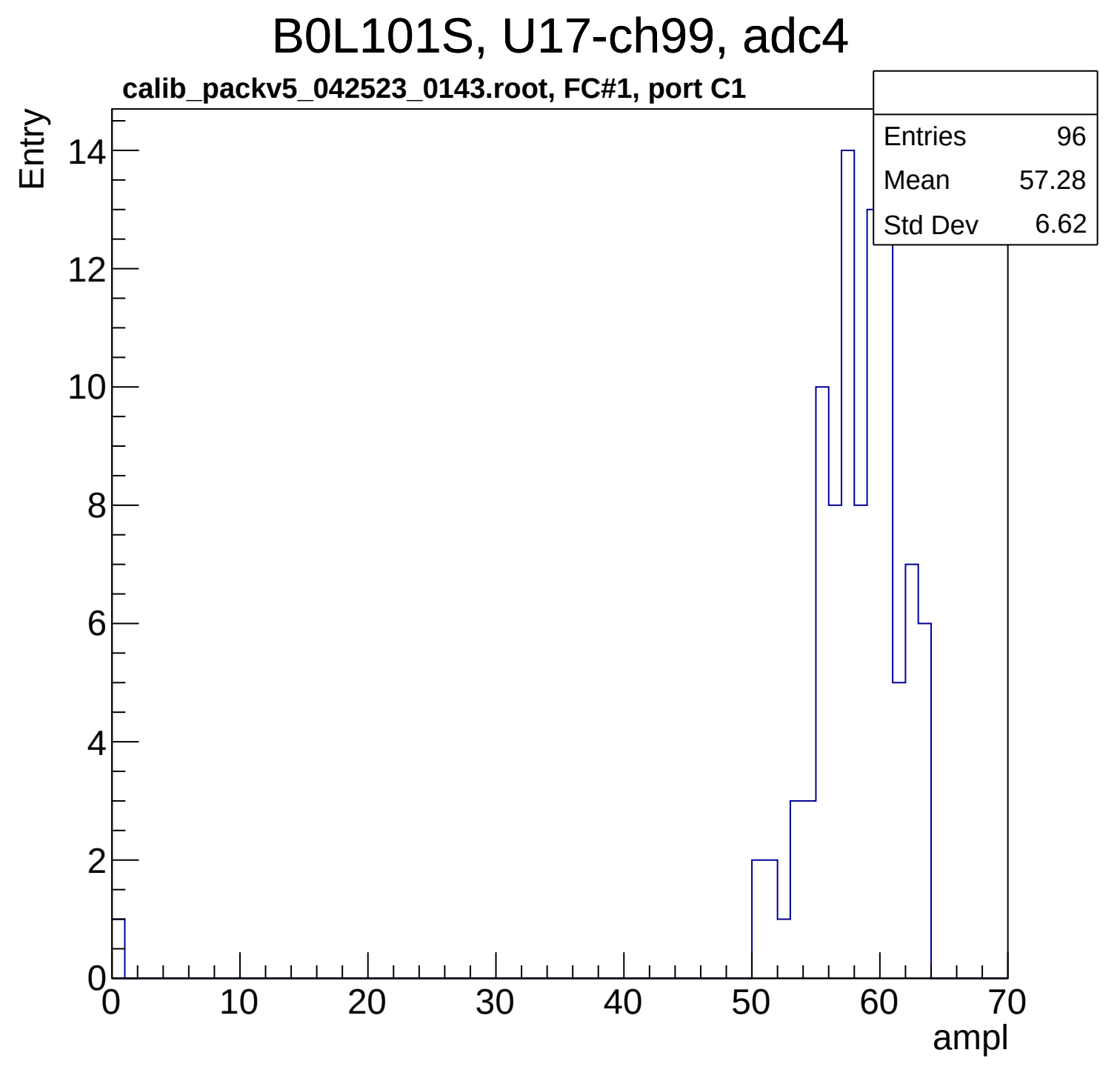

B0L101S, U17-ch98, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

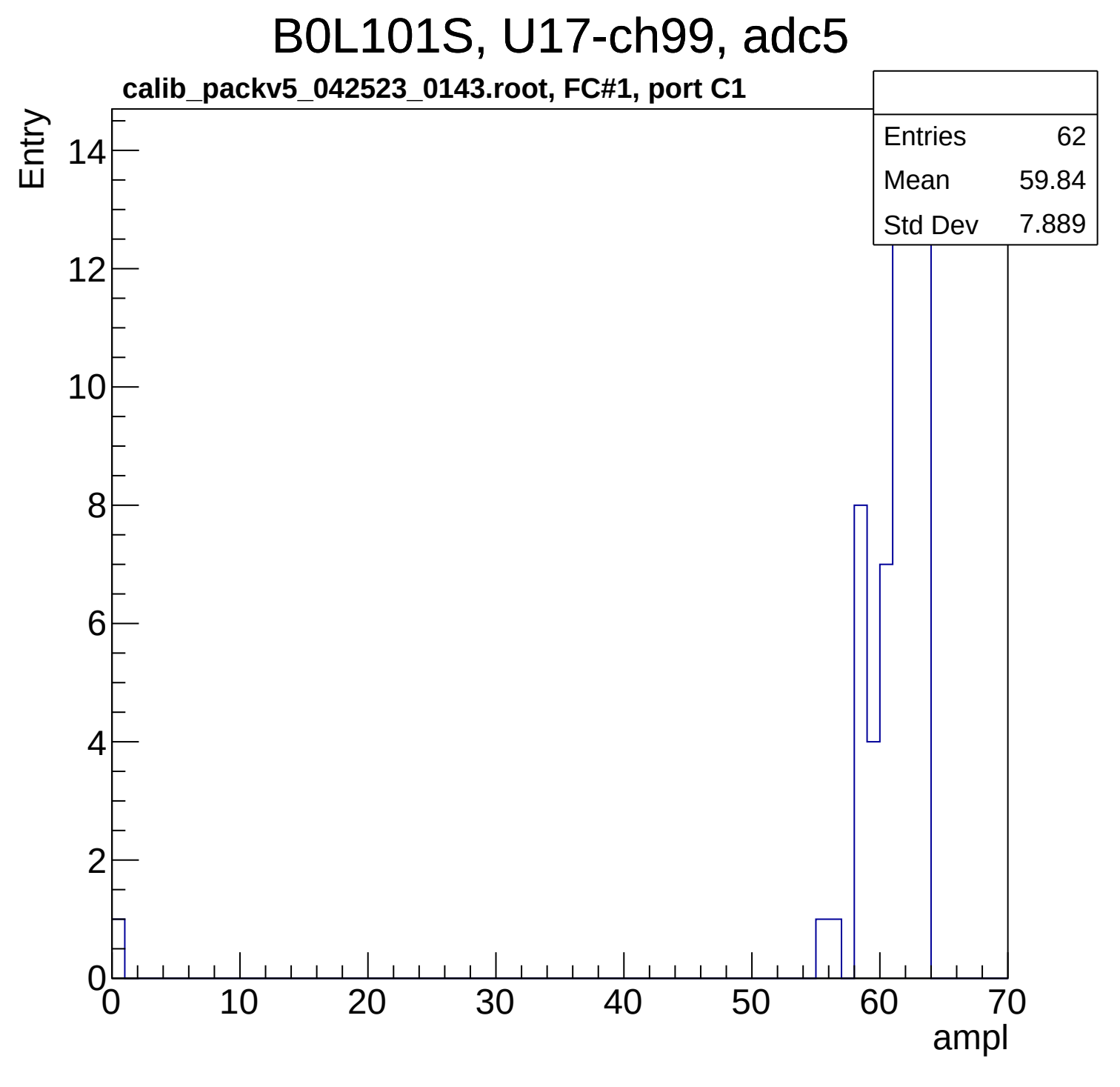

B0L101S, U17-ch99, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 168
Mean 32.38
Std Dev 4.574
Gaus mean : 33.0571
Gaus Width: 3.8620
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

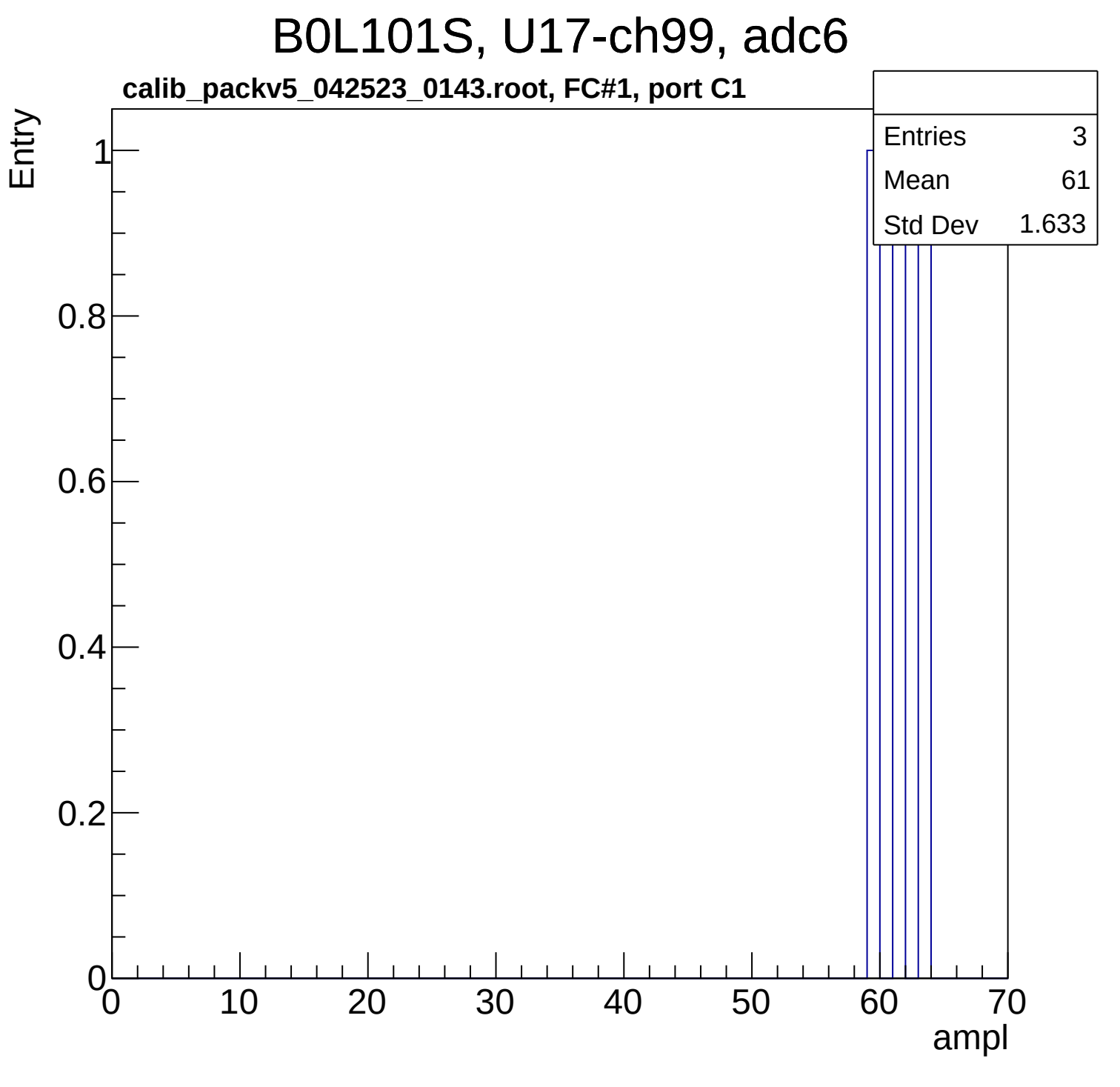

B0L101S, U17-ch99, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 39.59
Std Dev 3.508
Gaus mean : 40.4479
Gaus Width: 3.5223
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

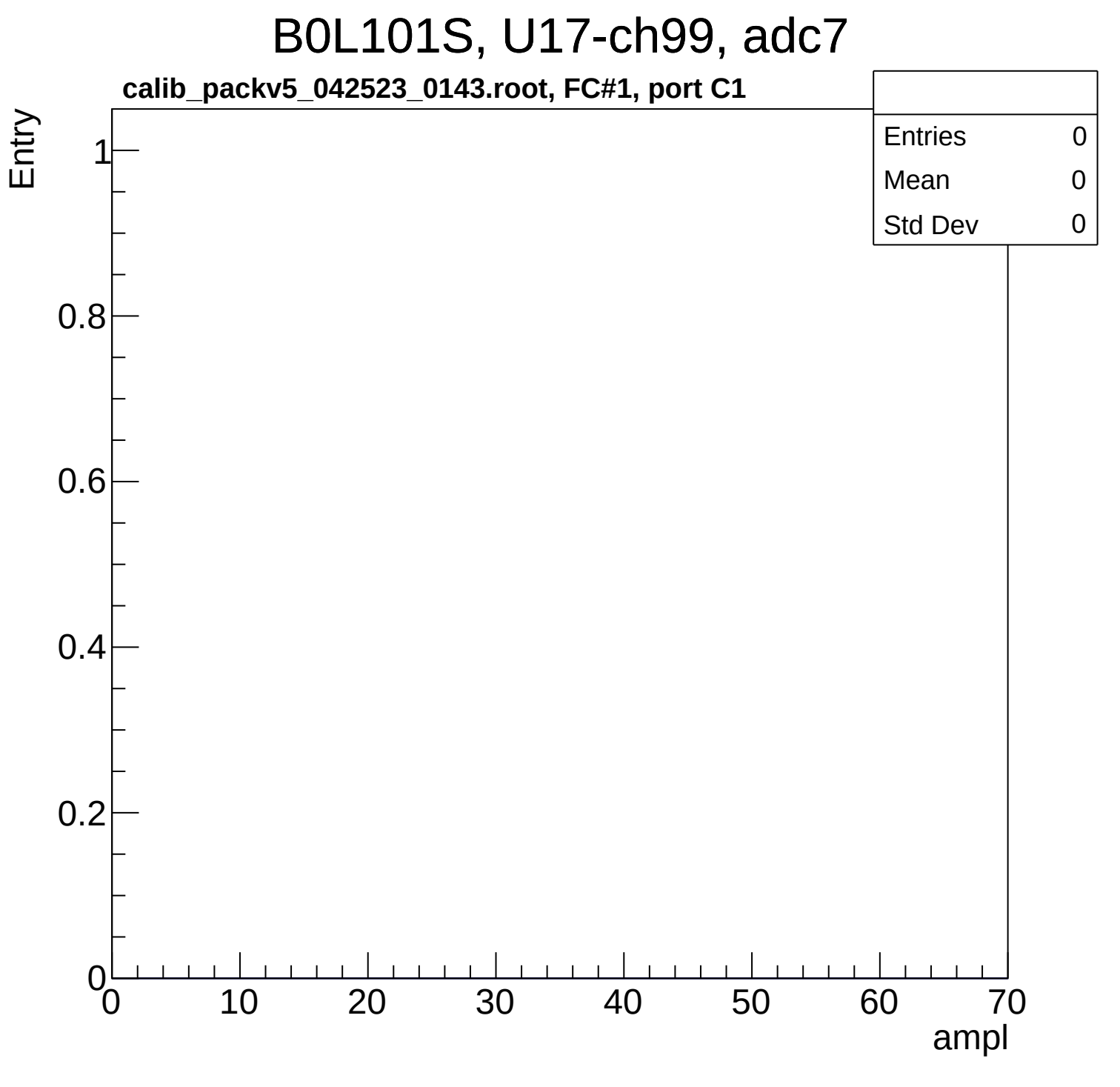

B0L101S, U17-ch99, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 46.15
Std Dev 3.554
Gaus mean : 47.0673
Gaus Width: 3.1088
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

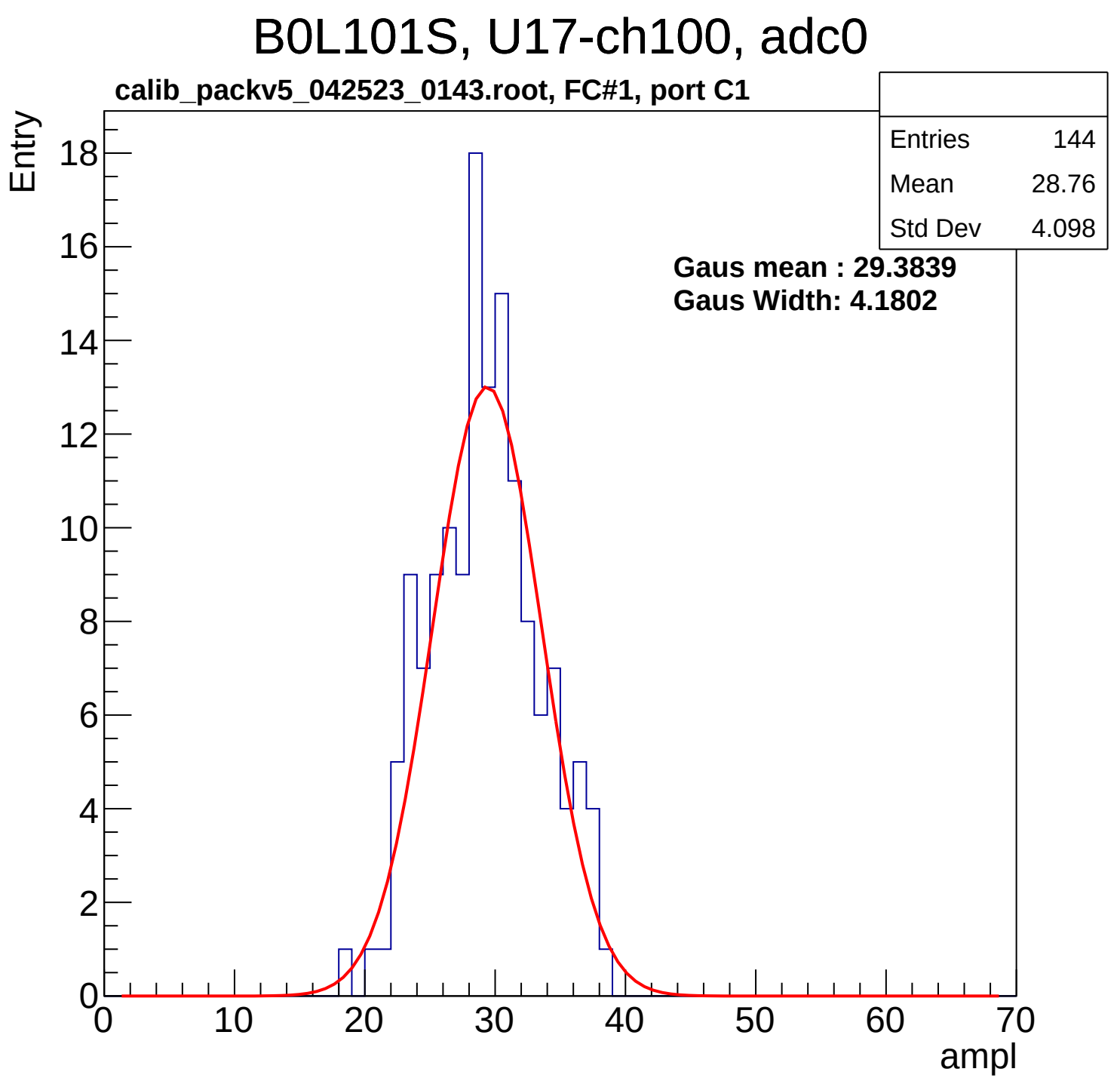

B0L101S, U17-ch99, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 51.88
Std Dev 3.479
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

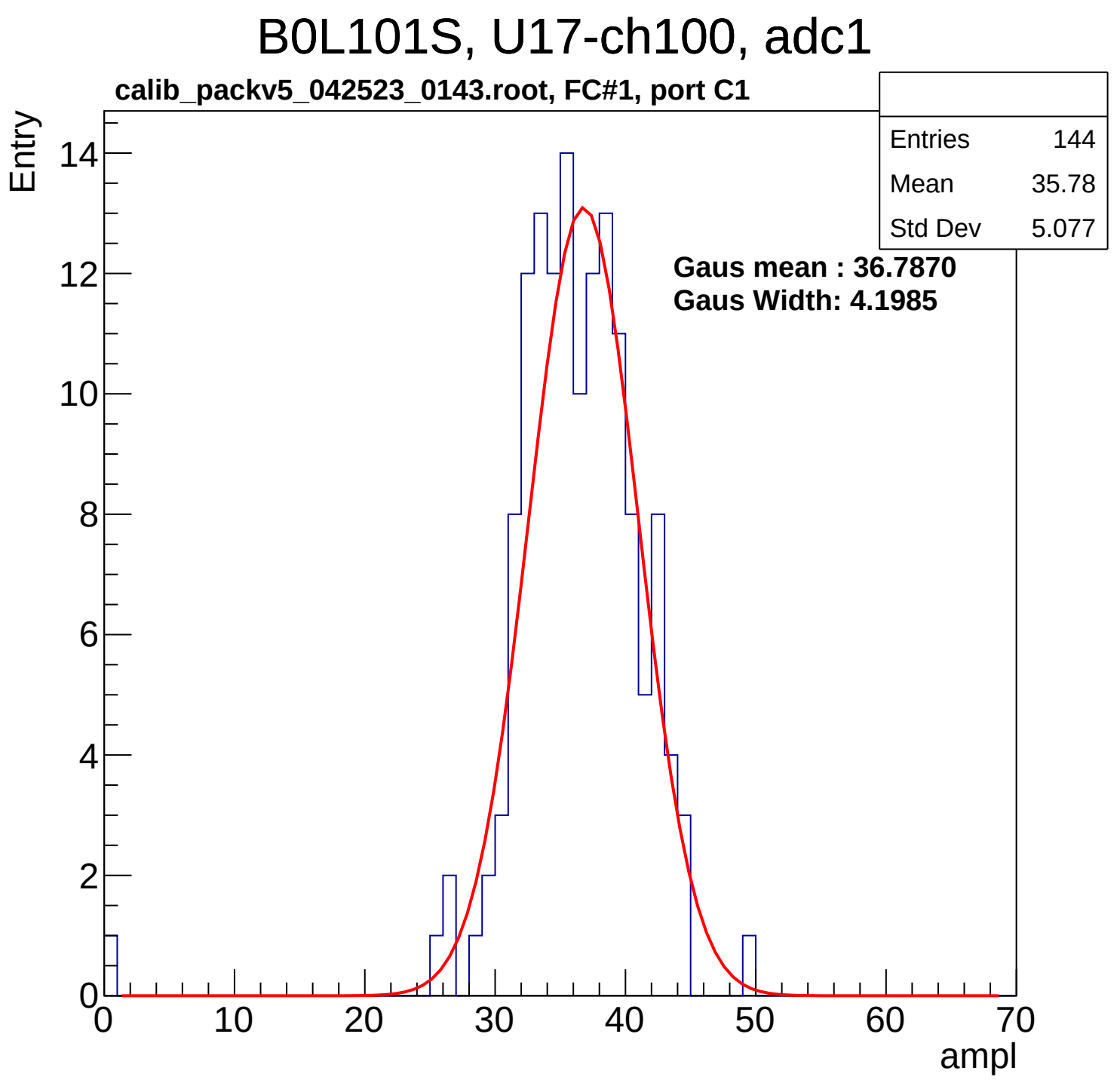

B0L101S, U17-ch99, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 57.28
Std Dev 6.62
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

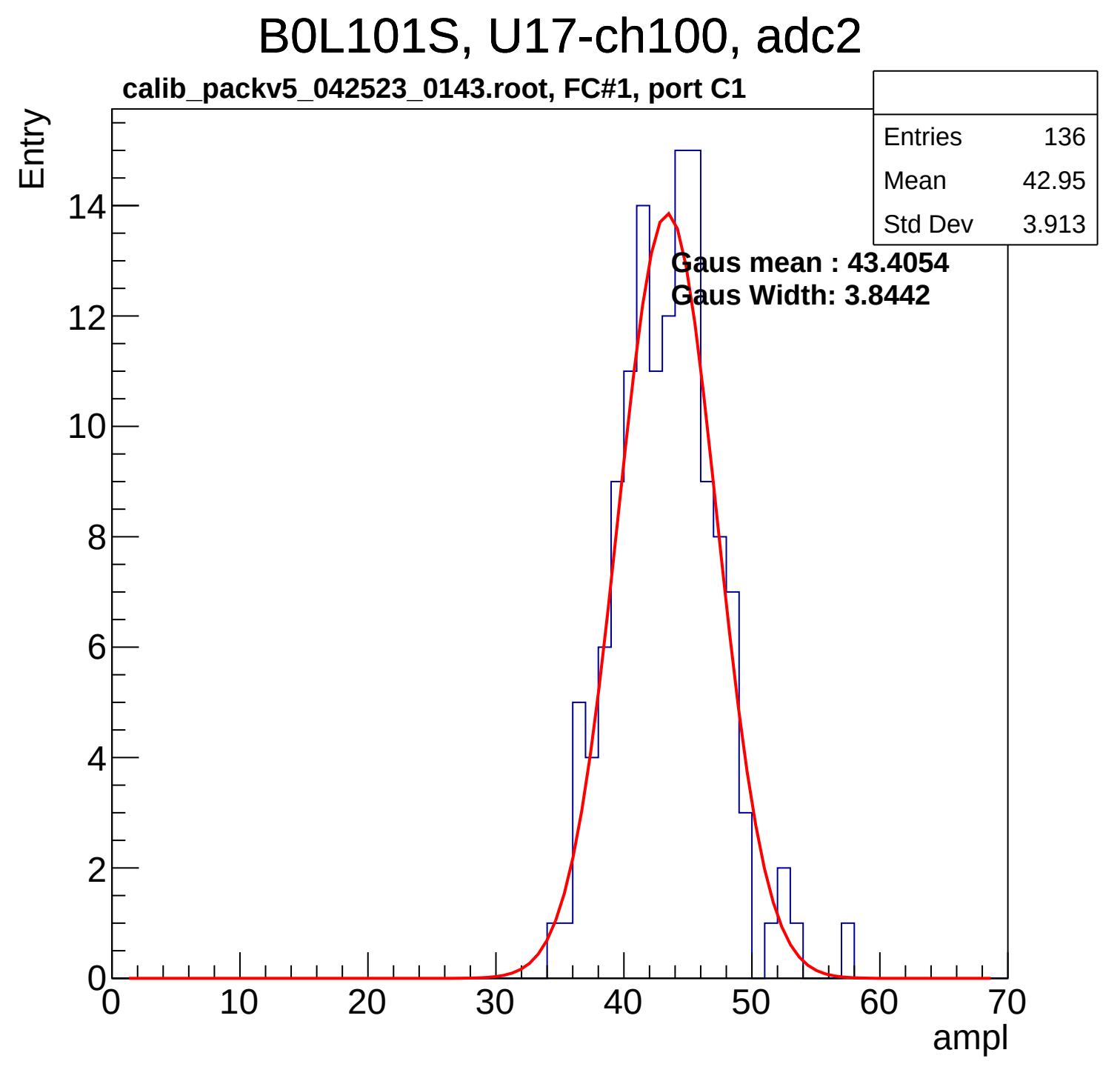

B0L101S, U17-ch99, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 62
Mean 59.84
Std Dev 7.889
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

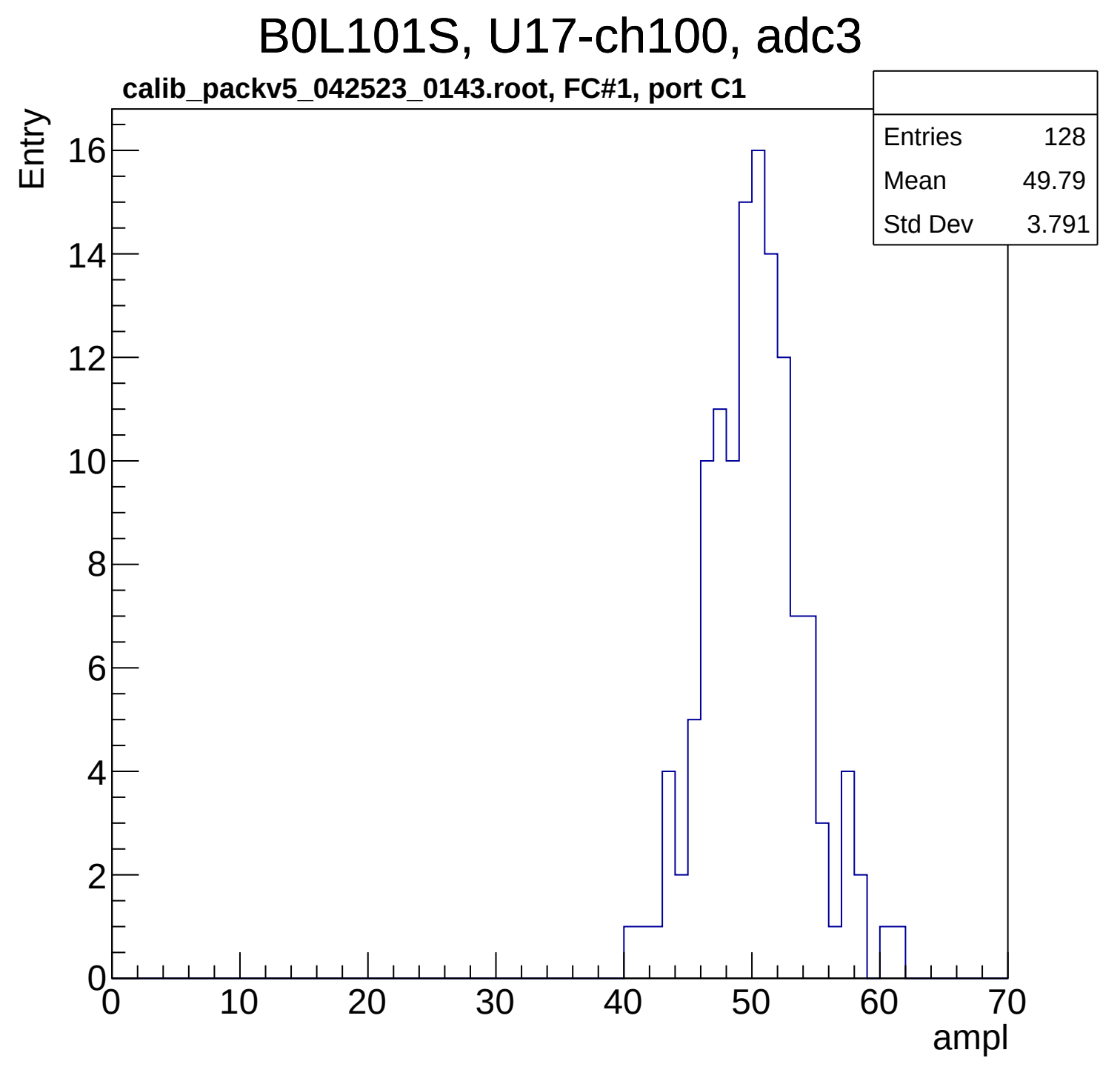

B0L101S, U17-ch99, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 61
Std Dev 1.633
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

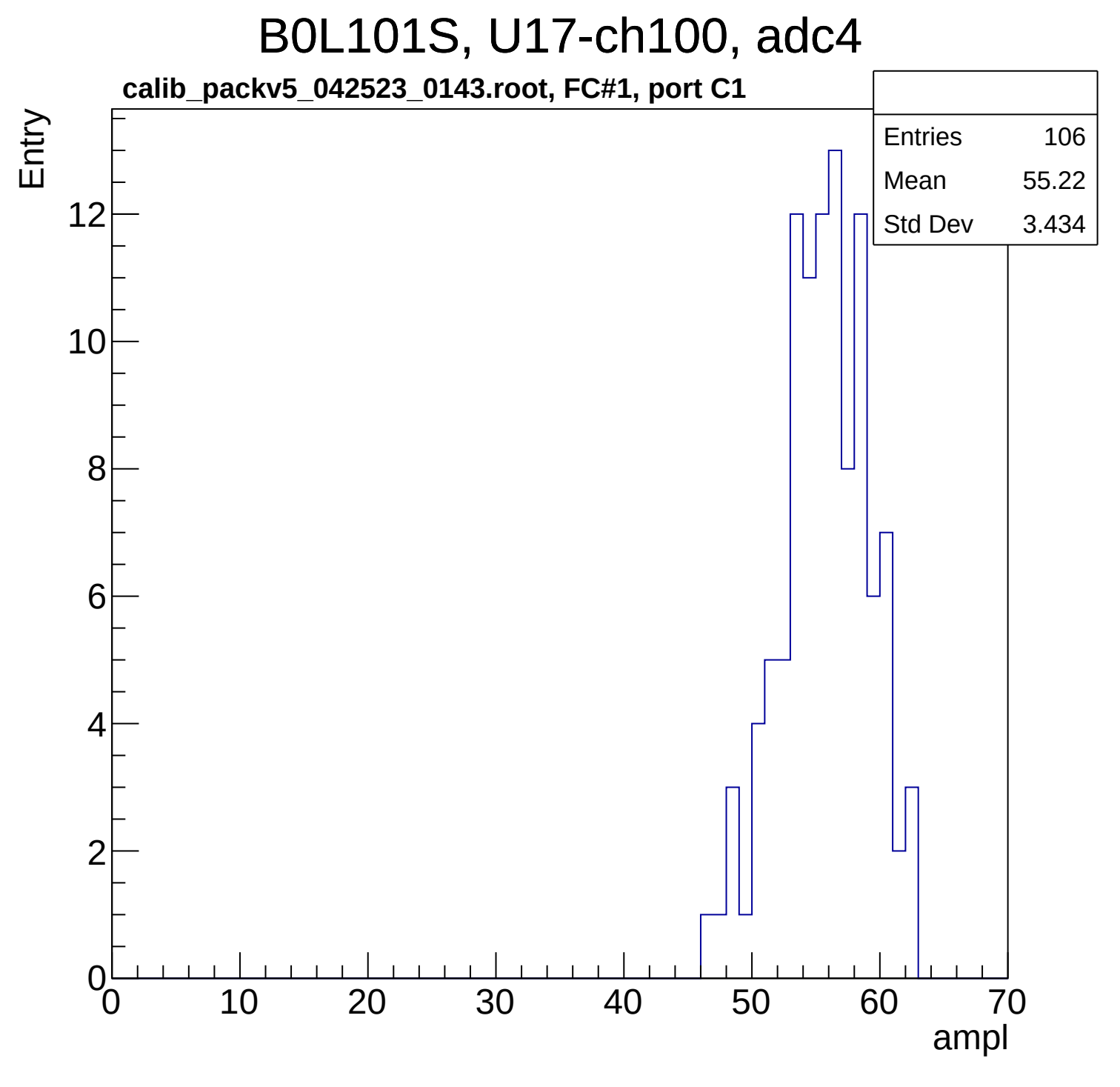

B0L101S, U17-ch99, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

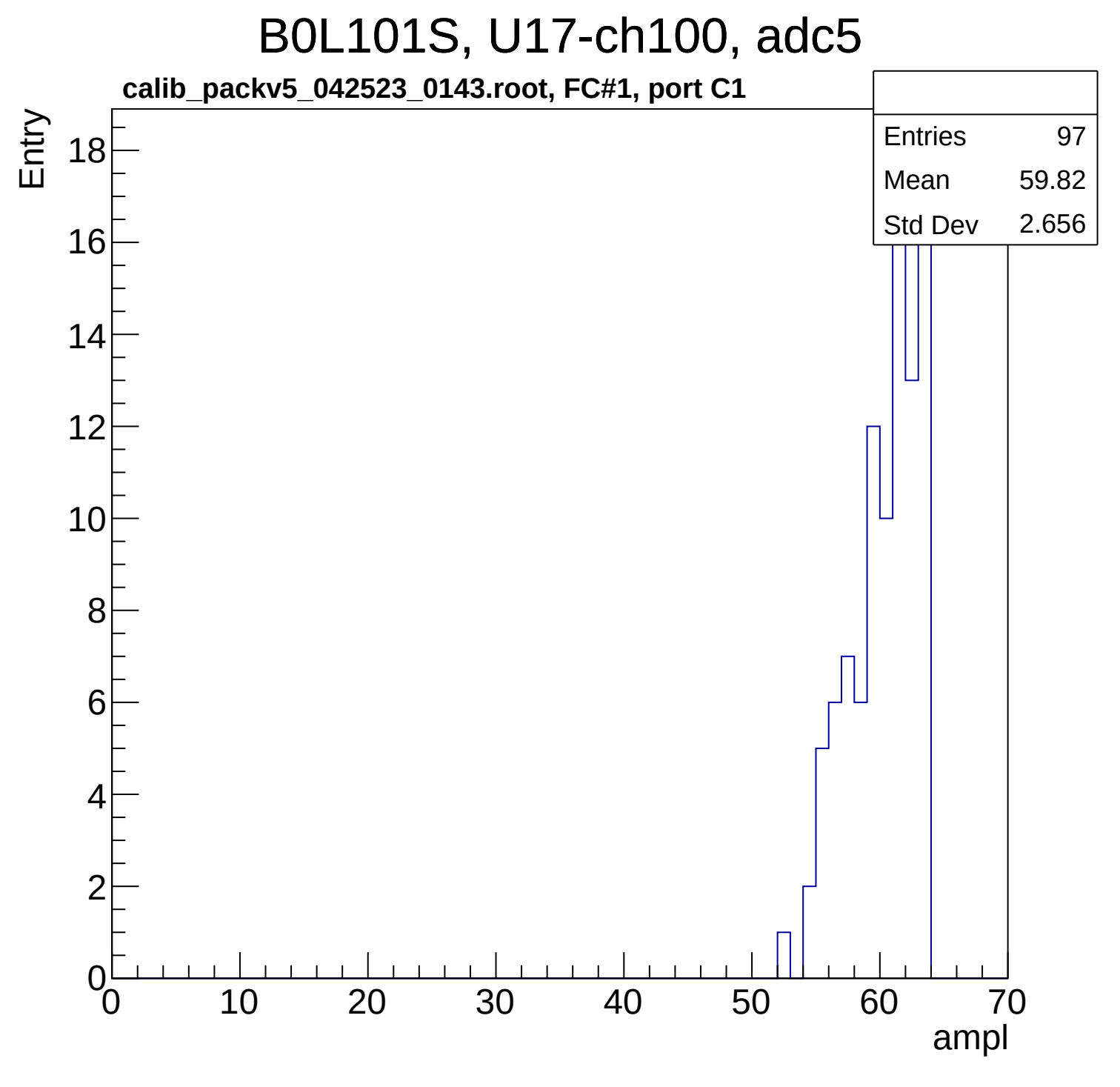

B0L101S, U17-ch100, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 28.76
Std Dev 4.098
Gaus mean : 29.3839
Gaus Width: 4.1802
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

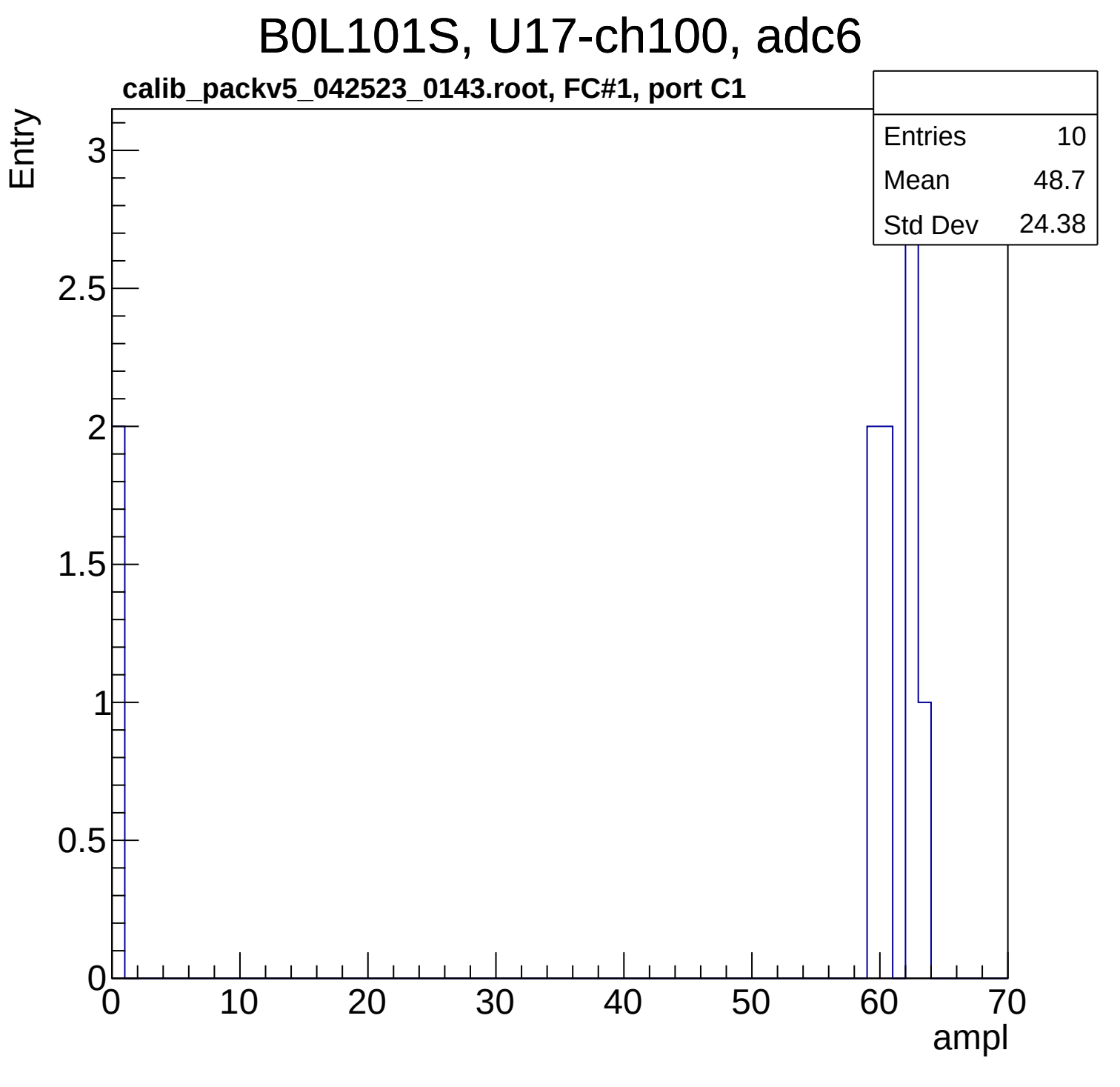

B0L101S, U17-ch100, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 35.78
Std Dev 5.077
Gaus mean : 36.7870
Gaus Width: 4.1985
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

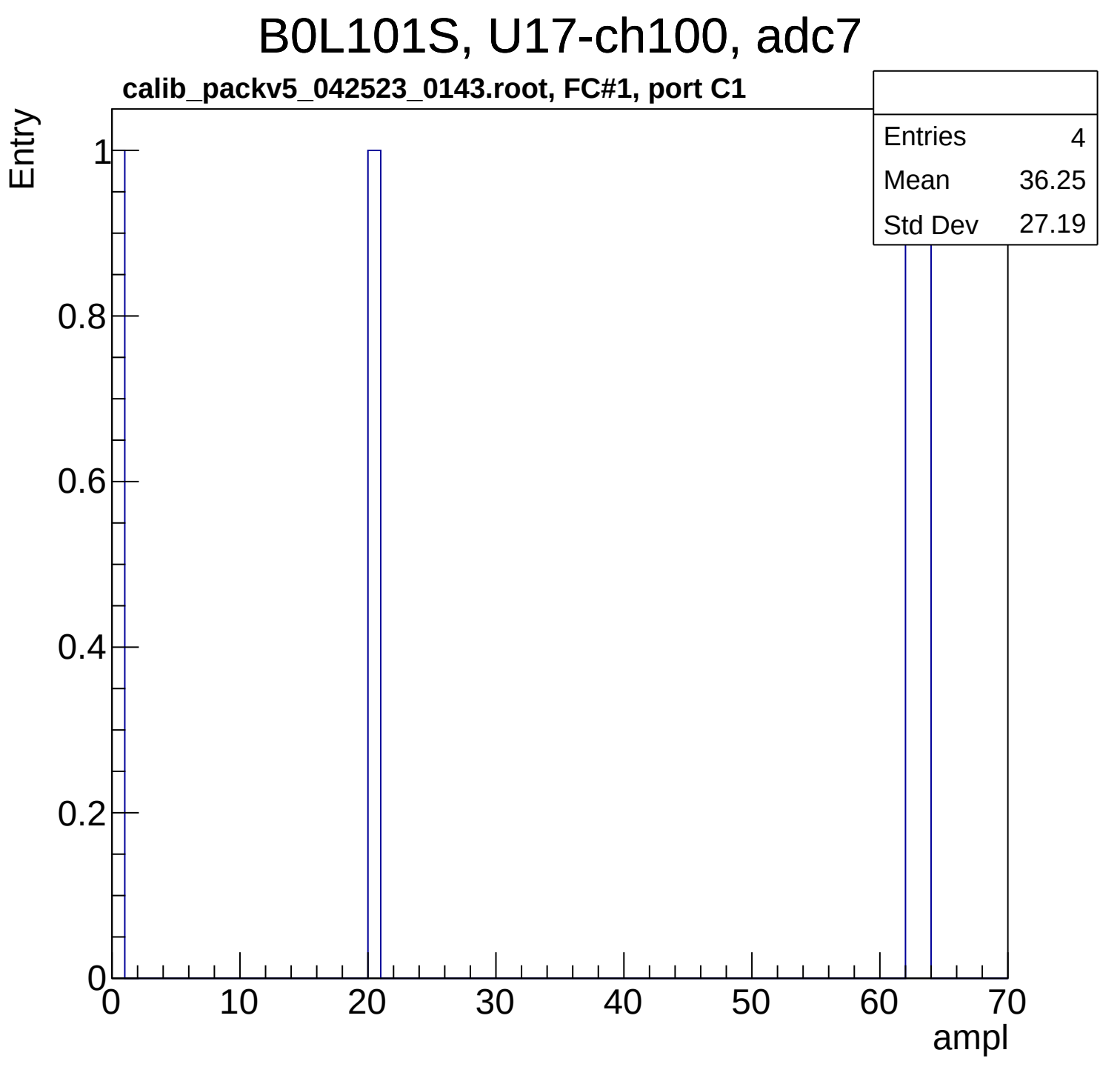

B0L101S, U17-ch100, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 42.95
Std Dev 3.913
Gaus mean : 43.4054
Gaus Width: 3.8442
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

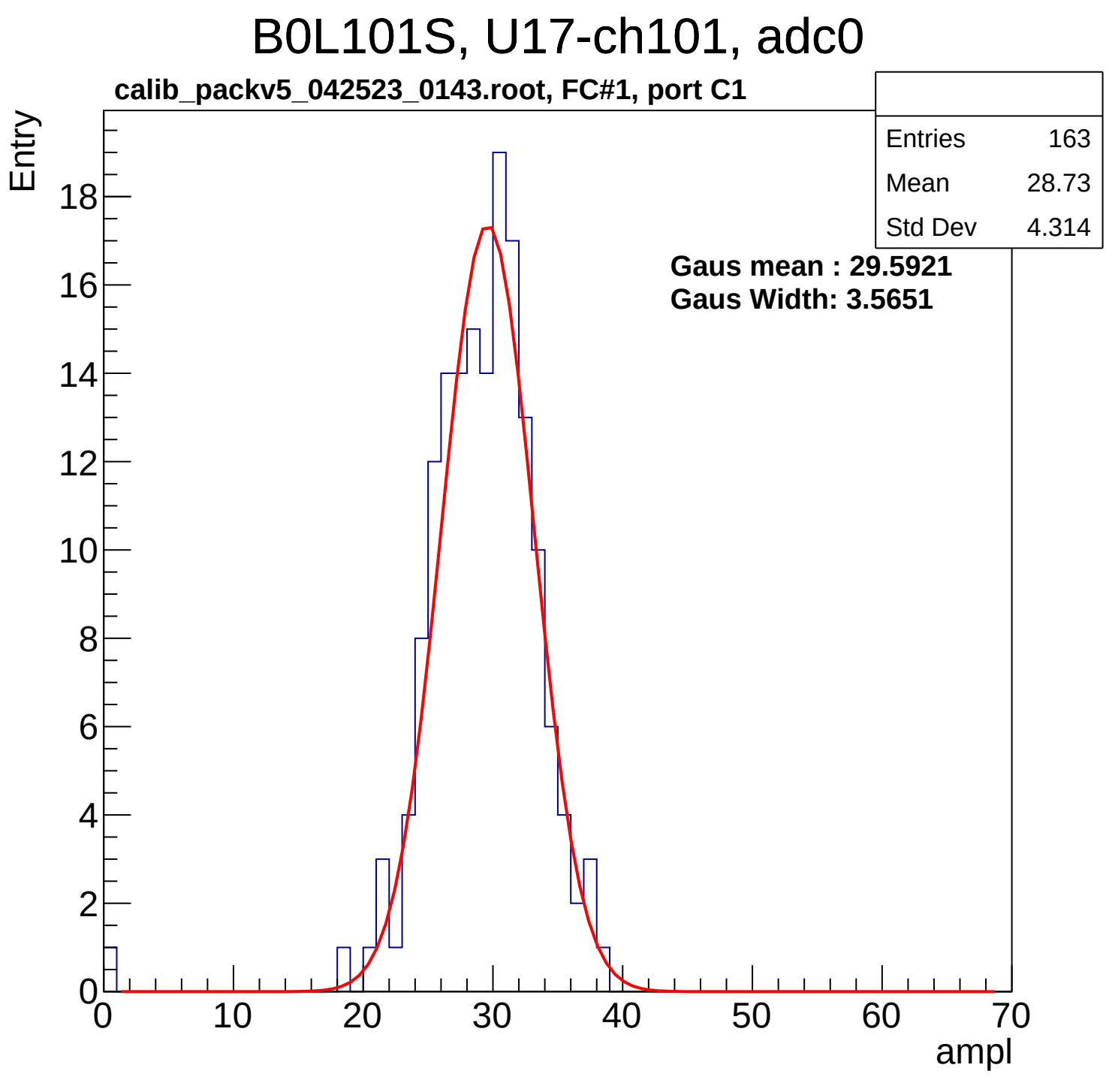

B0L101S, U17-ch100, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 49.79
Std Dev 3.791
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

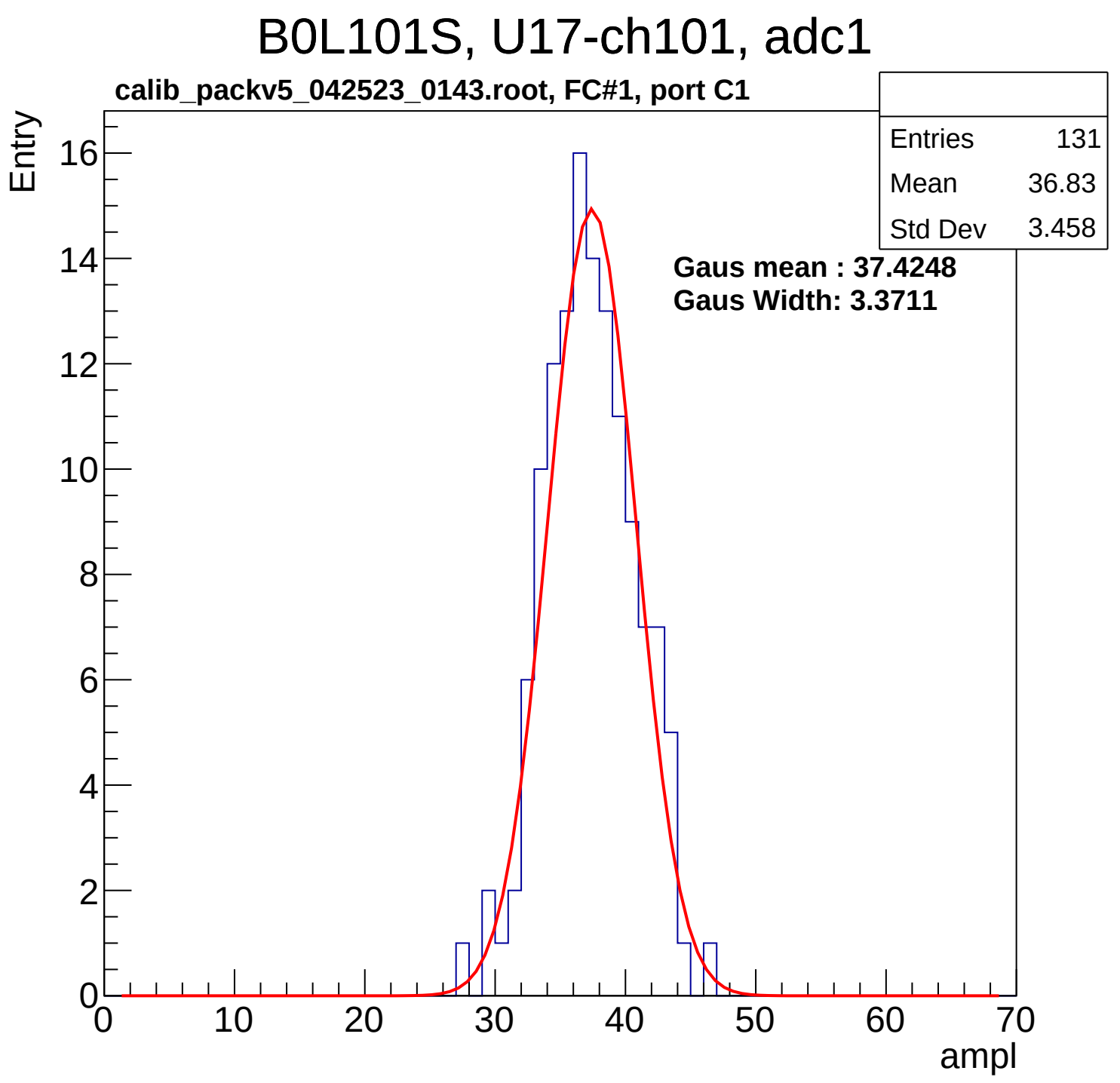

B0L101S, U17-ch100, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 106
Mean 55.22
Std Dev 3.434
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

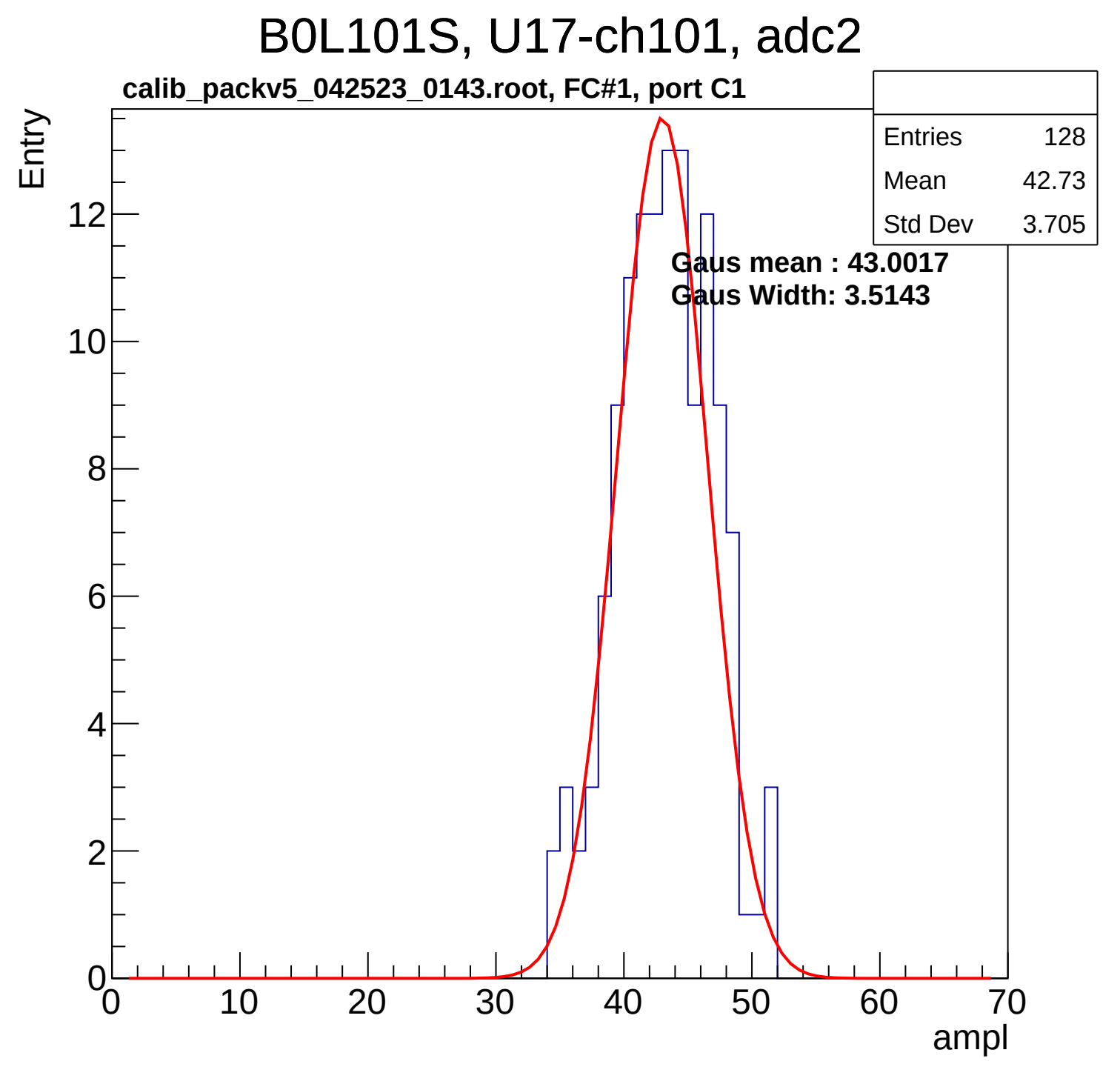

B0L101S, U17-ch100, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 97
Mean 59.82
Std Dev 2.656
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

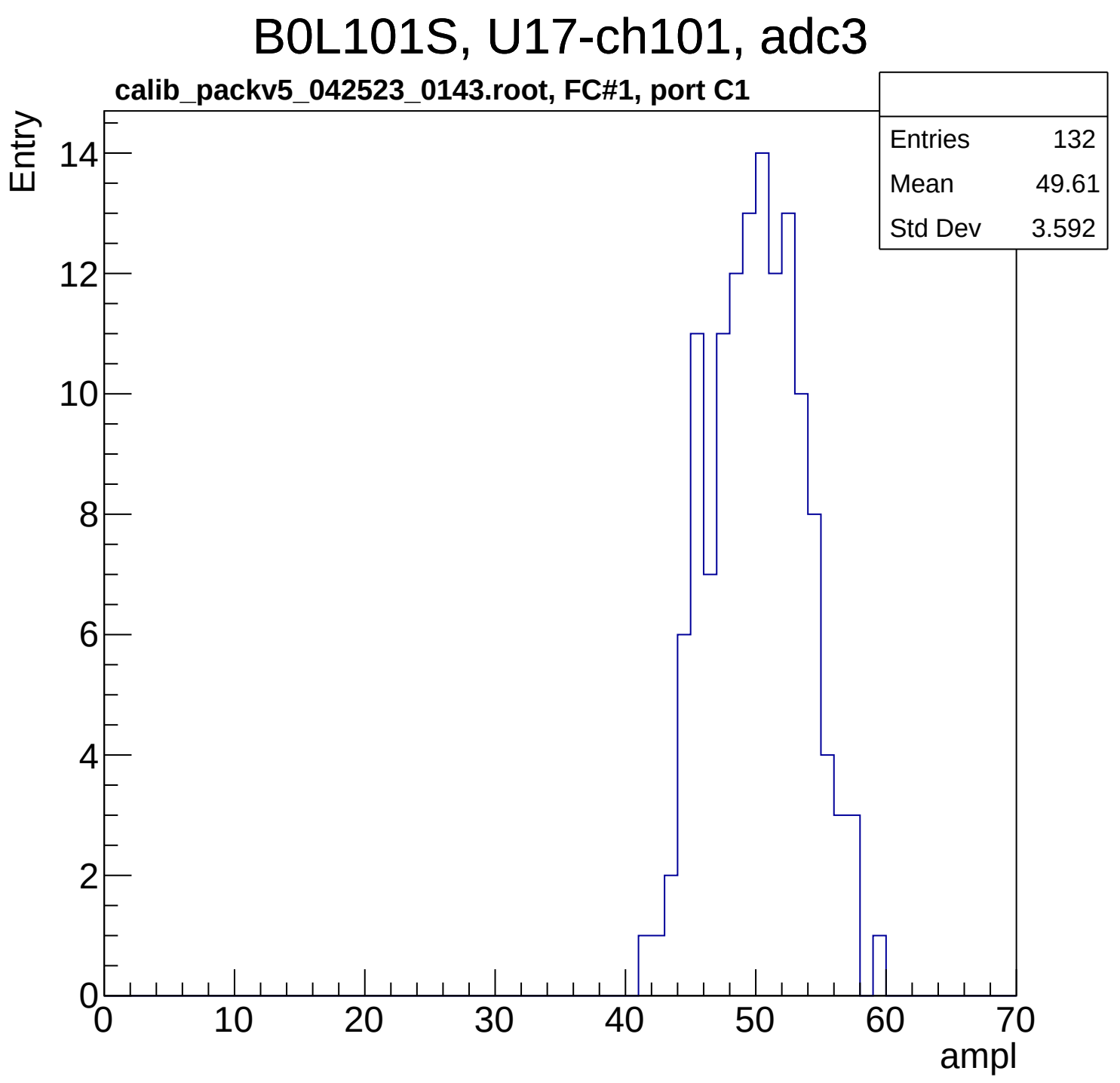

B0L101S, U17-ch100, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 48.7
Std Dev 24.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

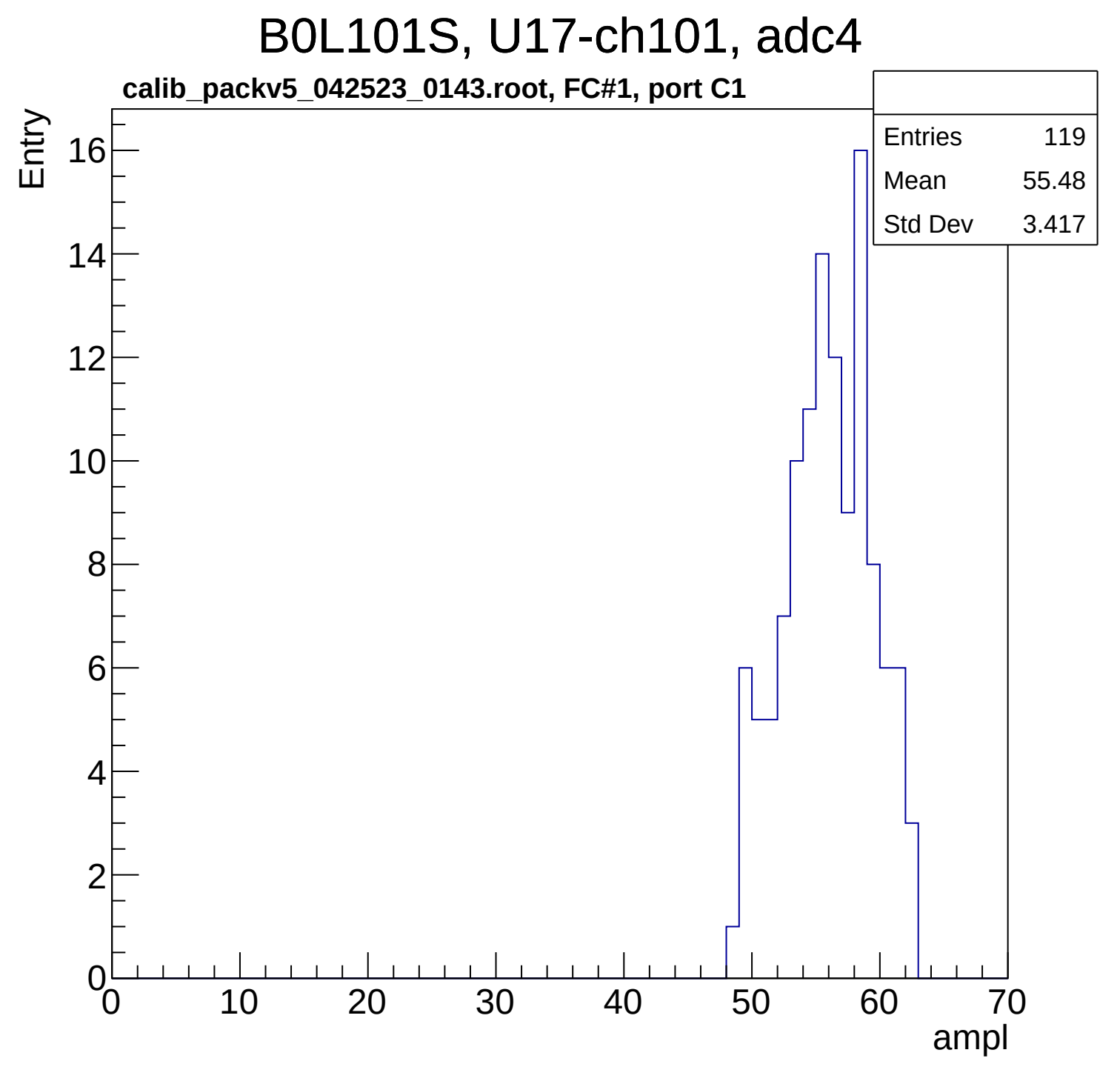

B0L101S, U17-ch100, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 36.25
Std Dev 27.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

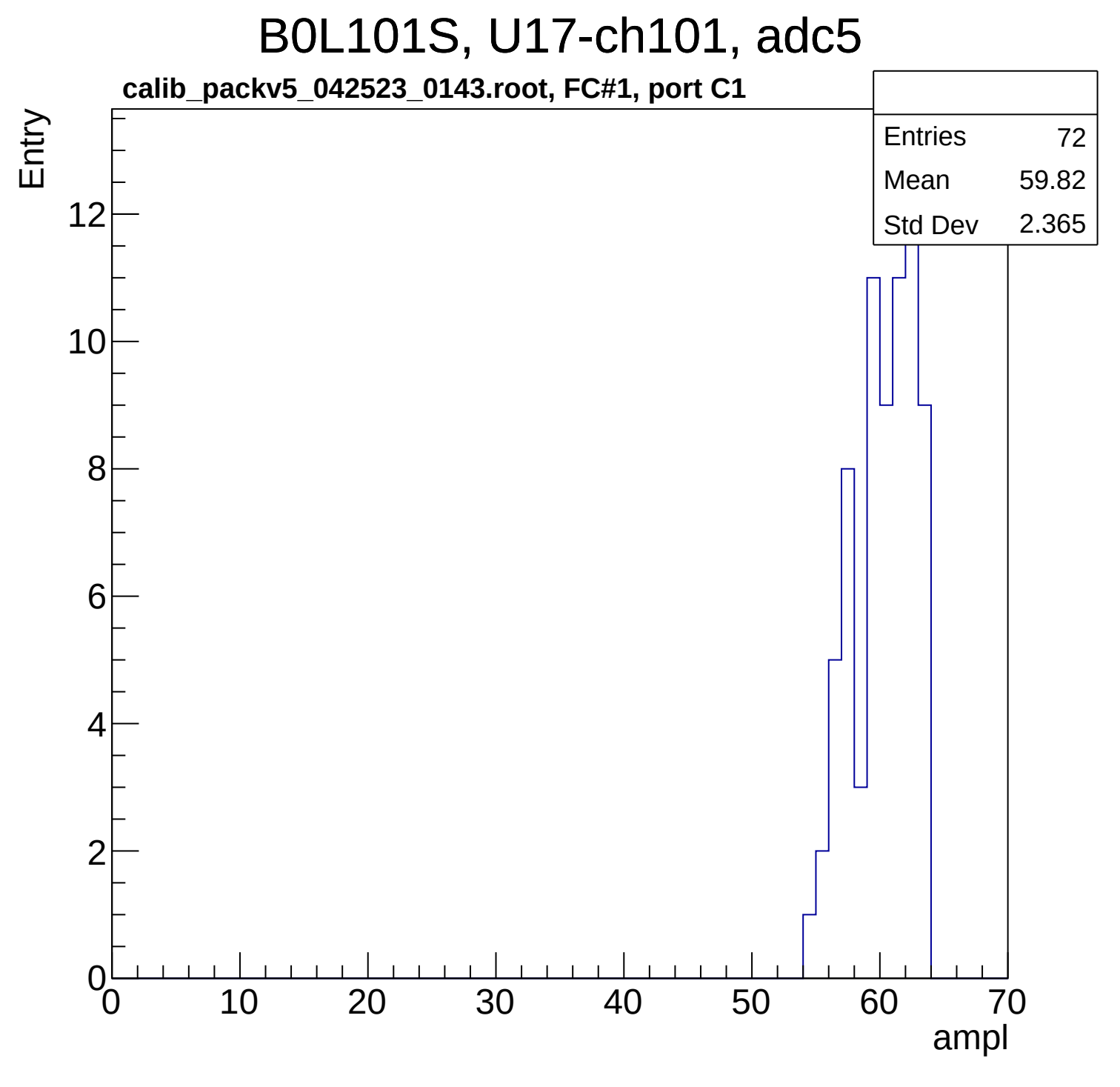

B0L101S, U17-ch101, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 28.73
Std Dev 4.314
Gaus mean : 29.5921
Gaus Width: 3.5651
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

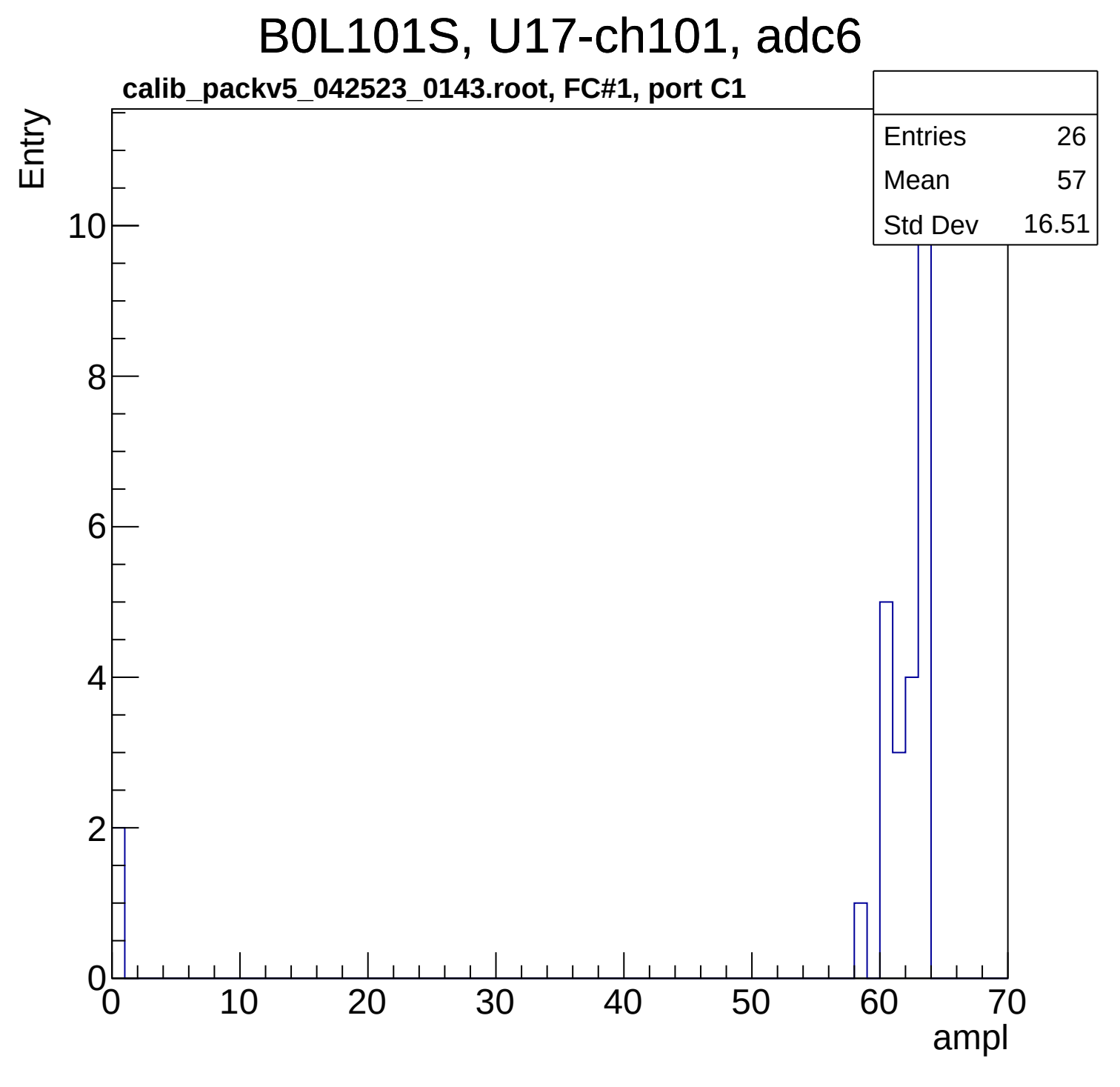

B0L101S, U17-ch101, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 36.83
Std Dev 3.458
Gaus mean : 37.4248
Gaus Width: 3.3711
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

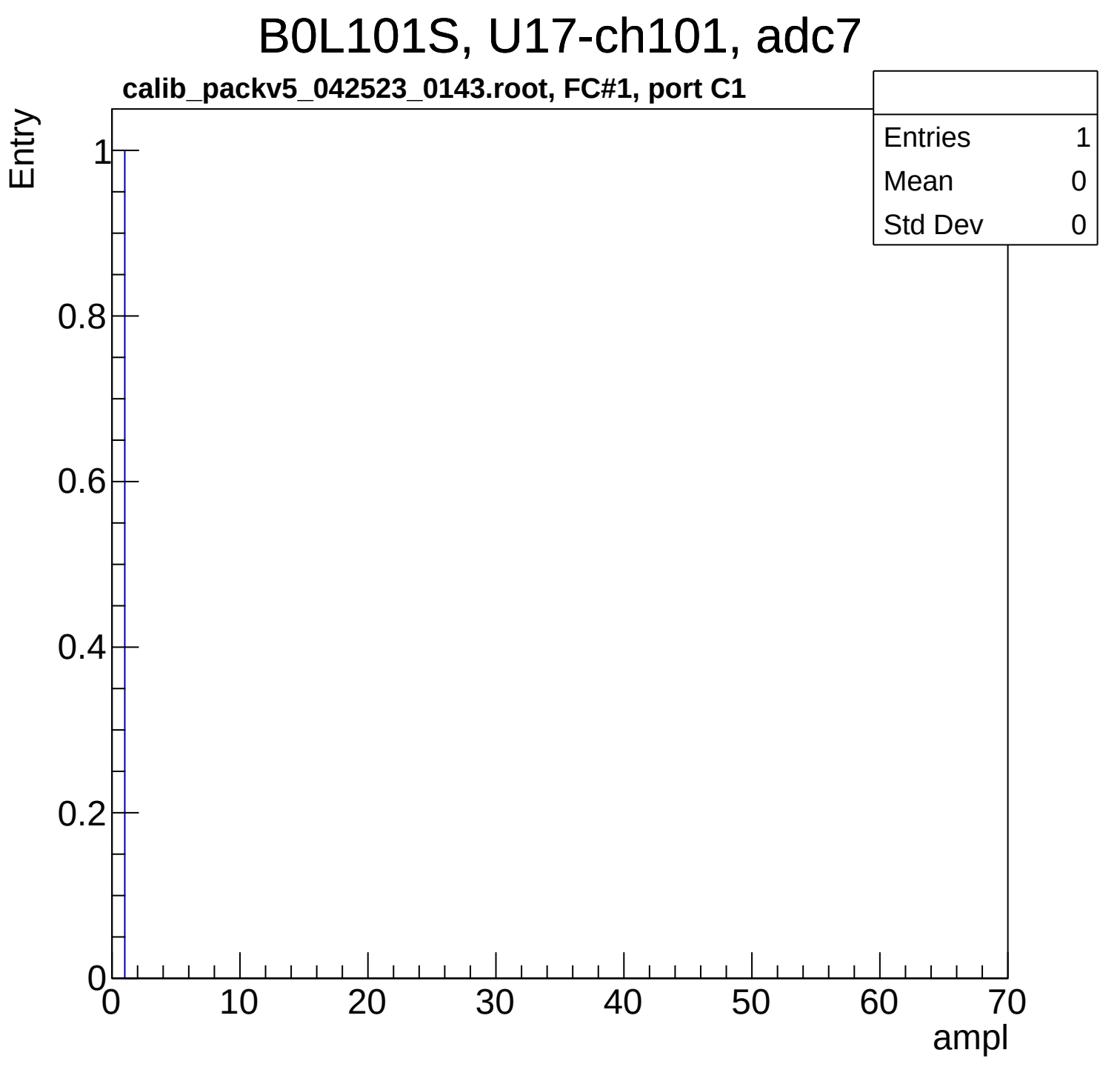

B0L101S, U17-ch101, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 42.73
Std Dev 3.705
Gaus mean : 43.0017
Gaus Width: 3.5143
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

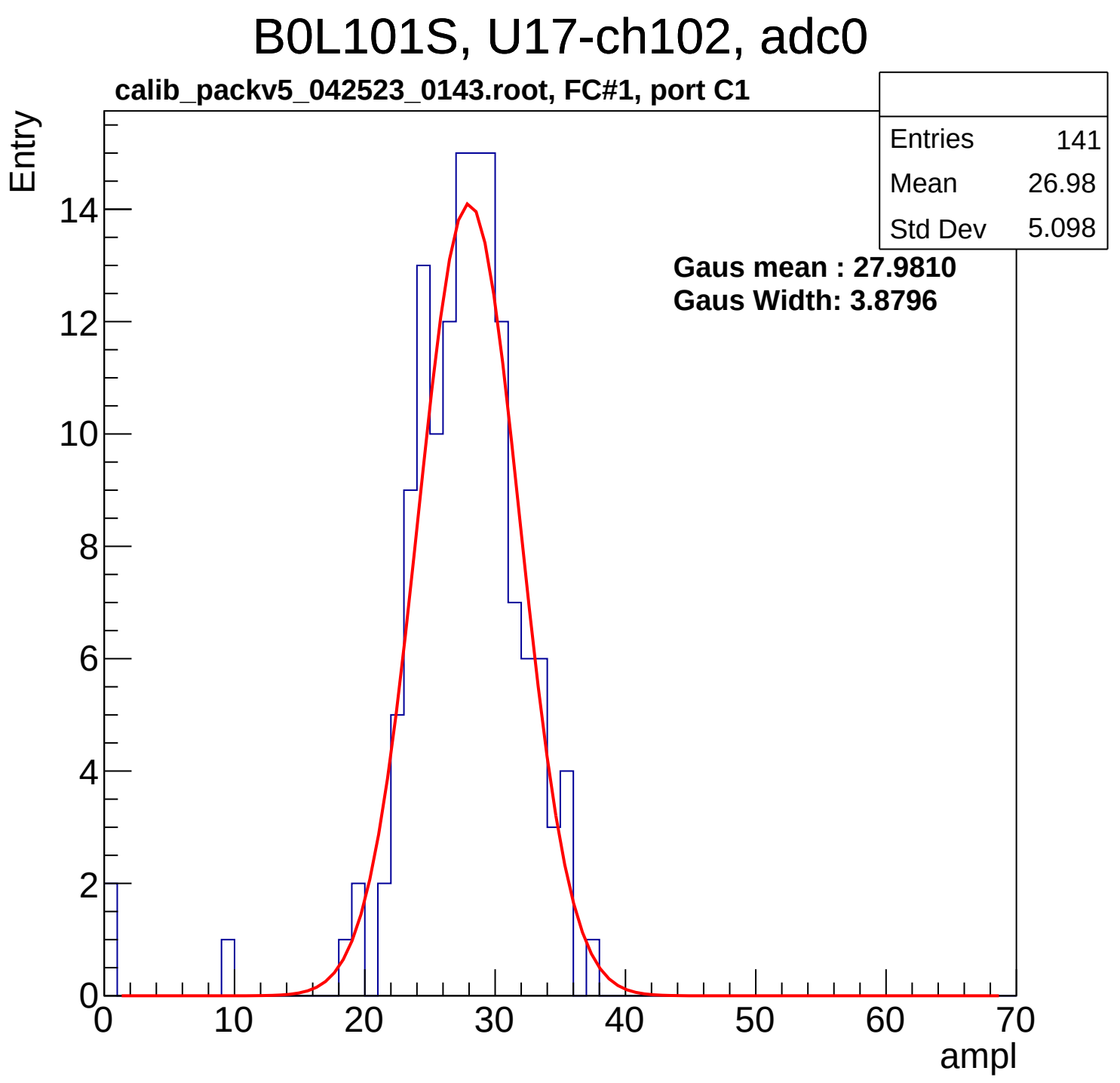

B0L101S, U17-ch101, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 132
Mean 49.61
Std Dev 3.592
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

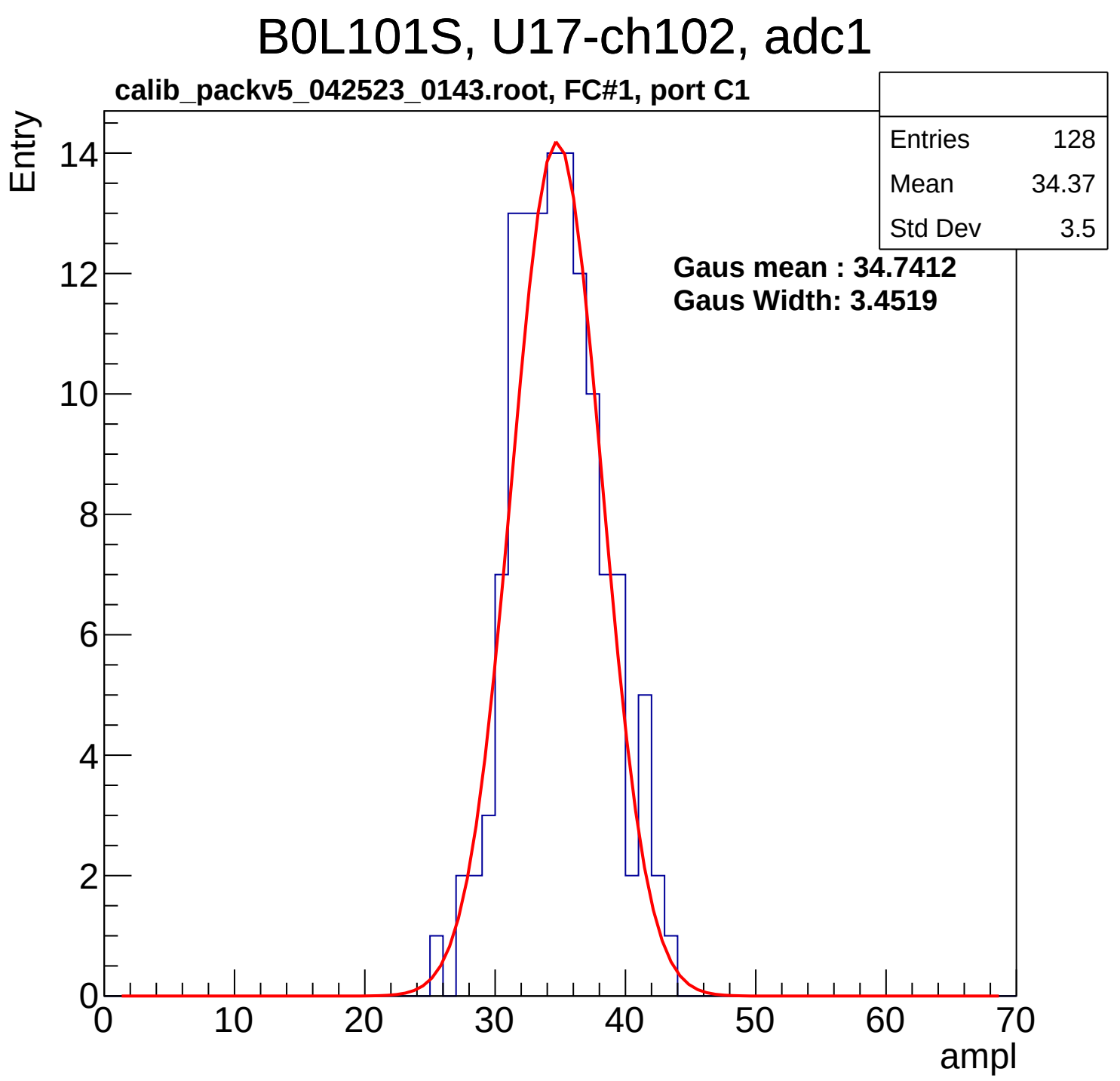

B0L101S, U17-ch101, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 119
Mean 55.48
Std Dev 3.417
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

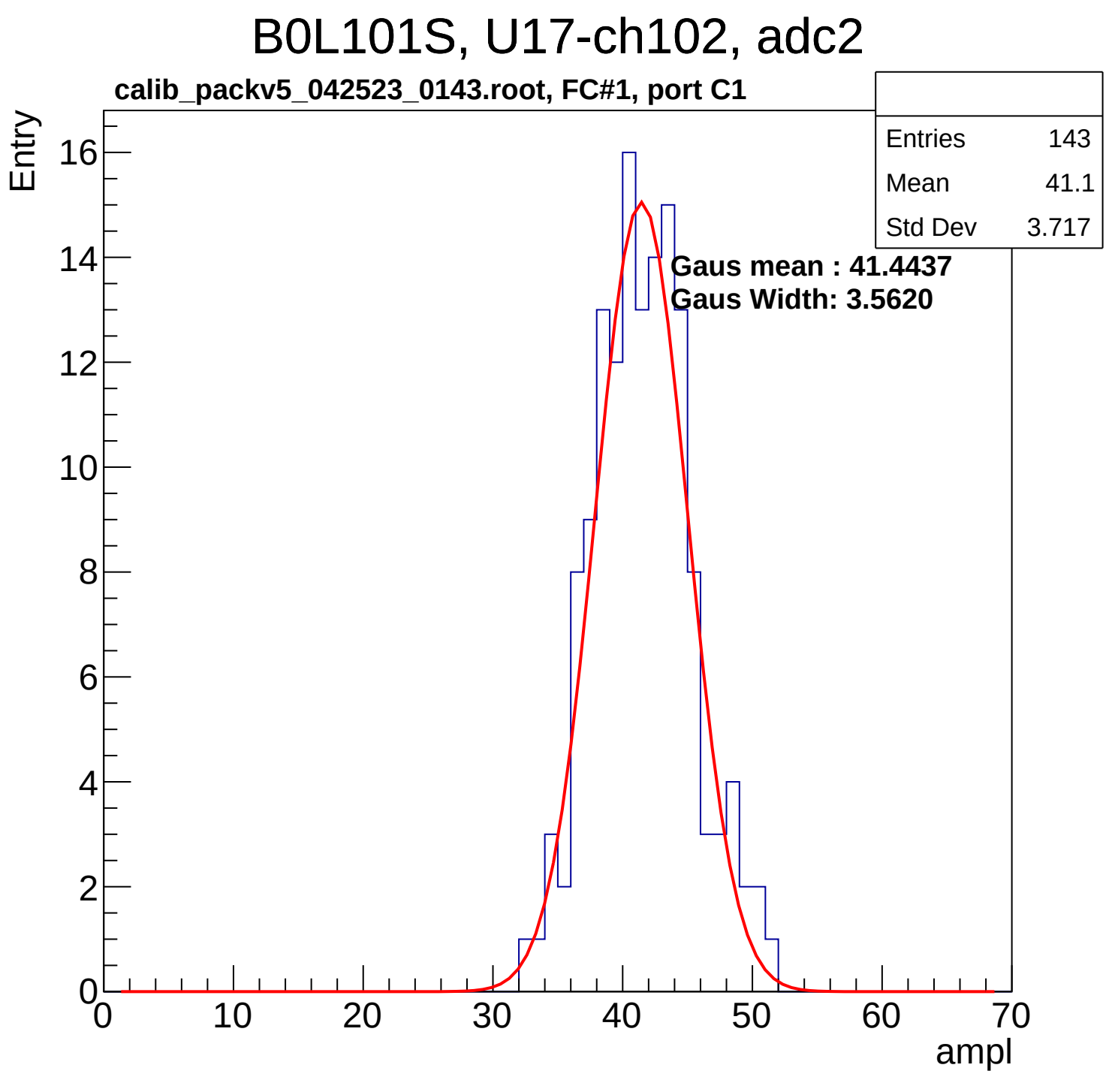

B0L101S, U17-ch101, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 72
Mean 59.82
Std Dev 2.365
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

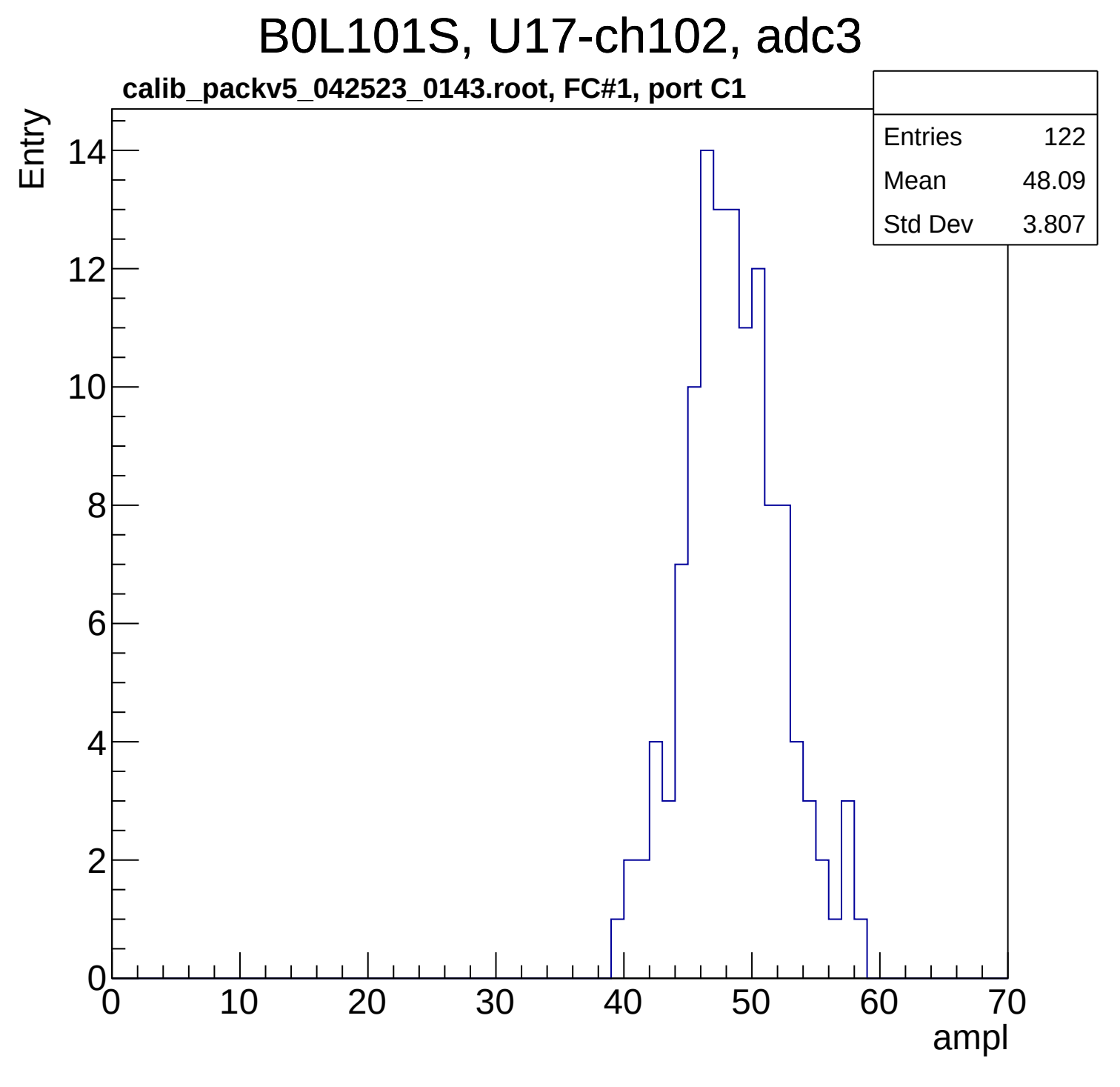

B0L101S, U17-ch101, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 26
Mean 57
Std Dev 16.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

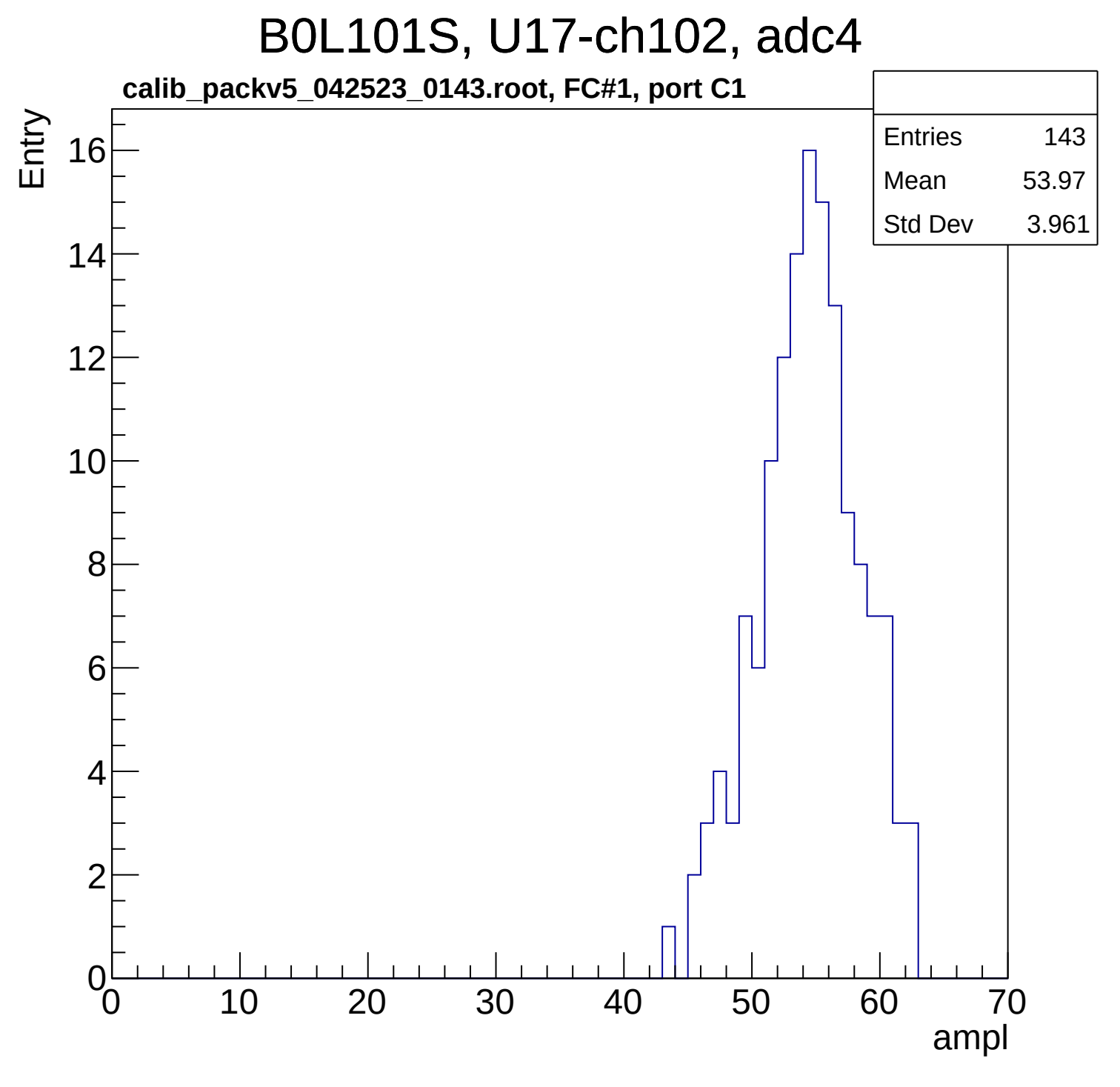

B0L101S, U17-ch101, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

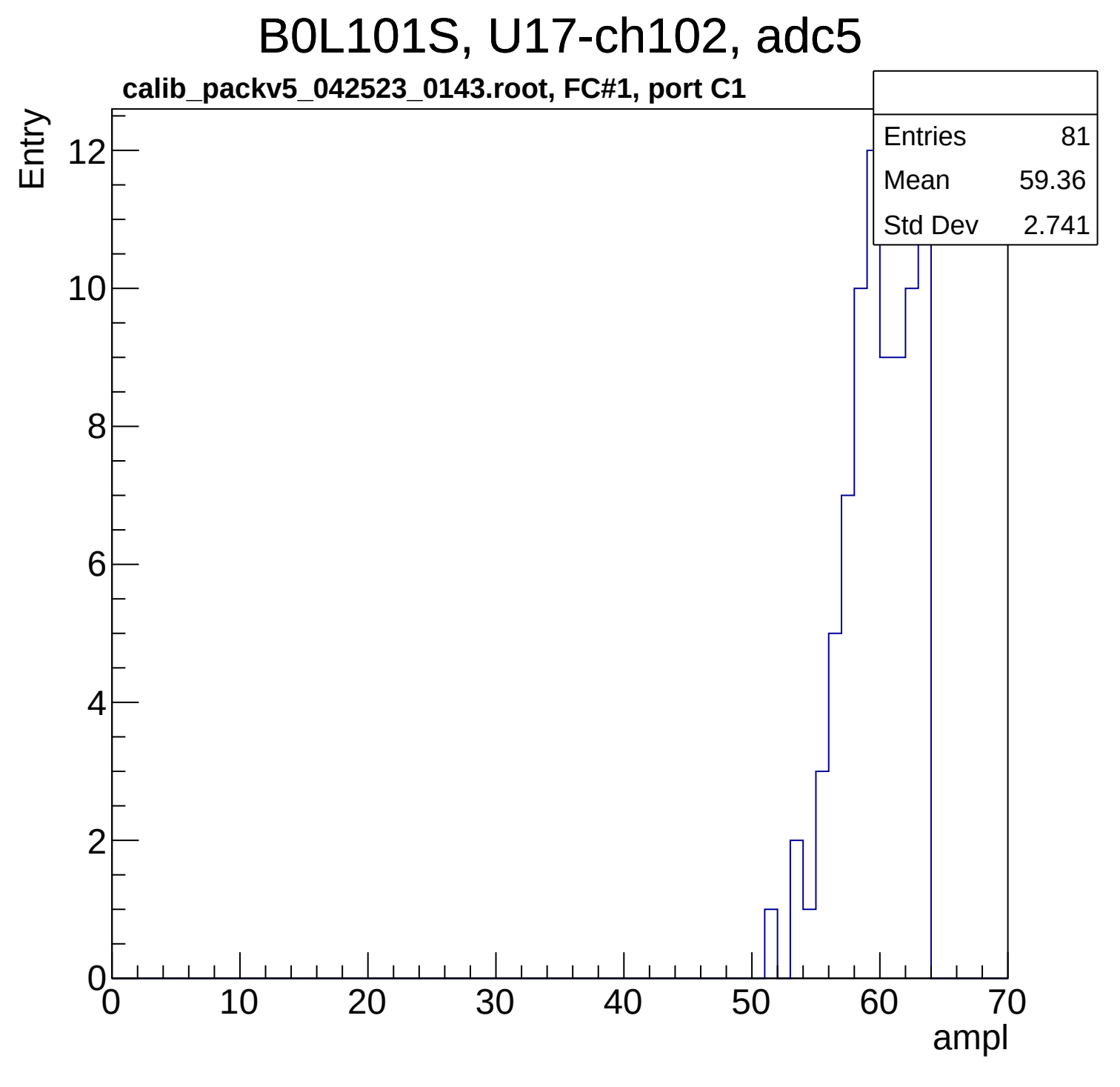

B0L101S, U17-ch102, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 26.98
Std Dev 5.098
Gaus mean : 27.9810
Gaus Width: 3.8796
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

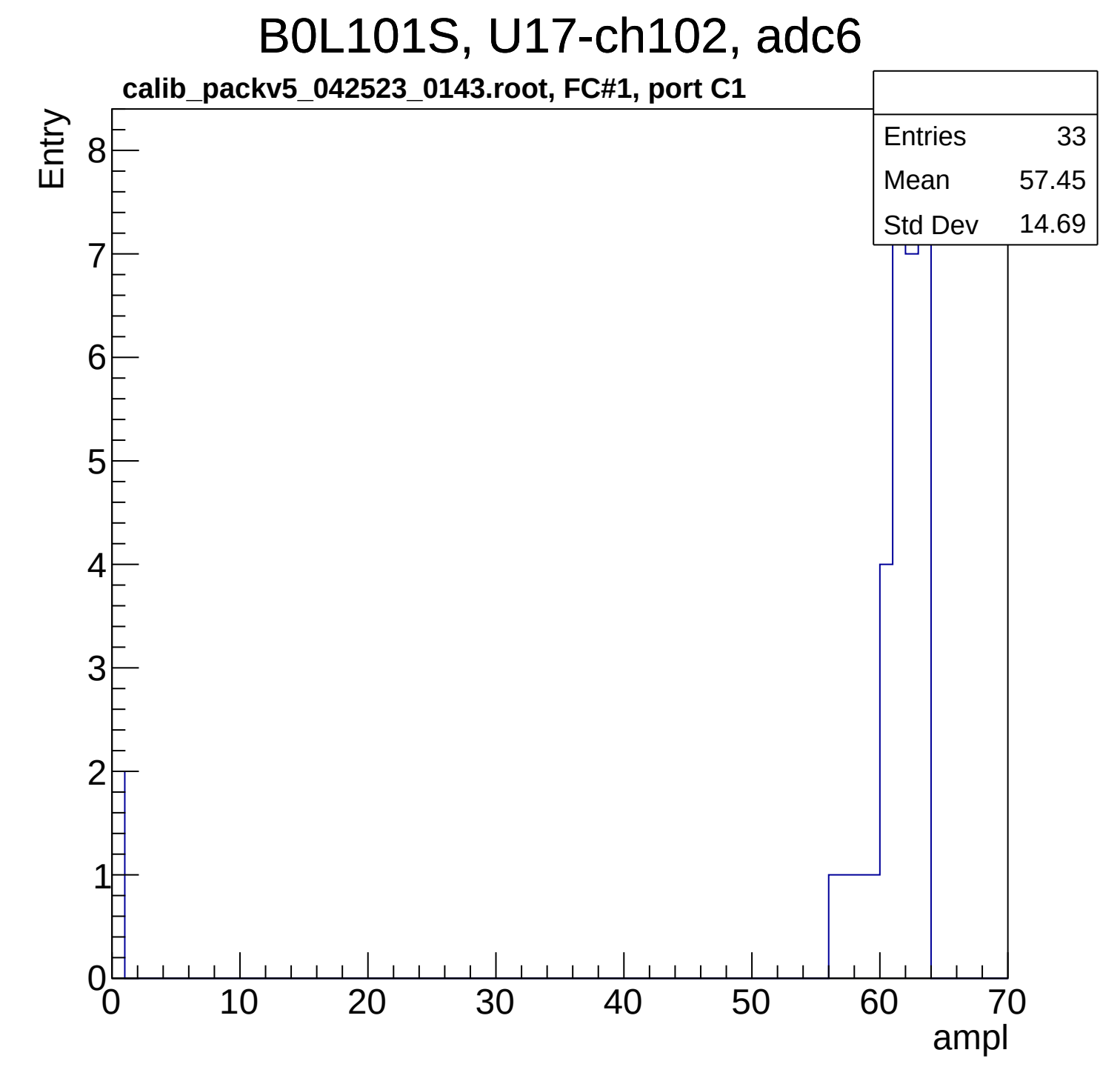

B0L101S, U17-ch102, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 34.37
Std Dev 3.5
Gaus mean : 34.7412
Gaus Width: 3.4519
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

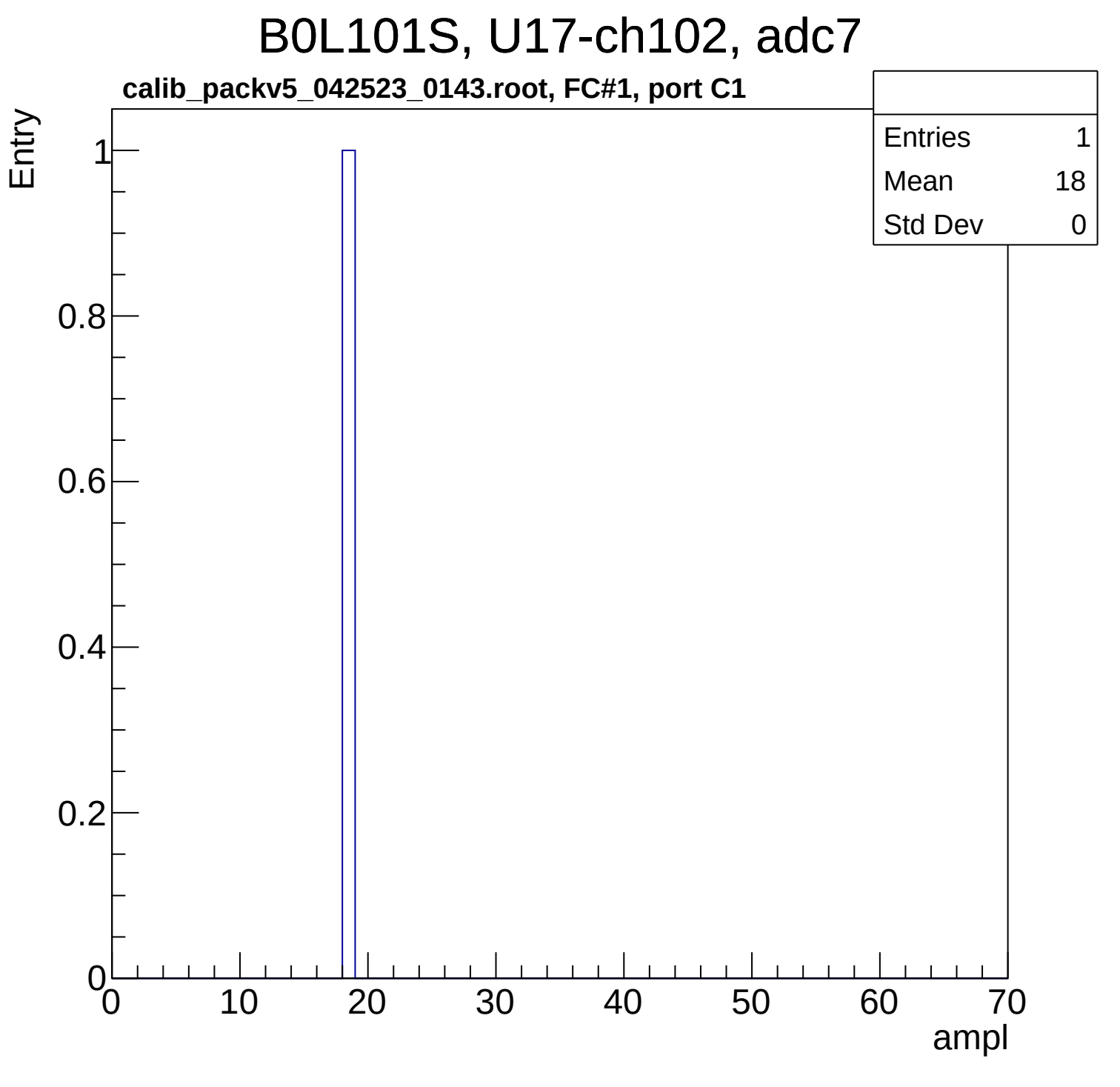

B0L101S, U17-ch102, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 41.1
Std Dev 3.717
Gaus mean : 41.4437
Gaus Width: 3.5620
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

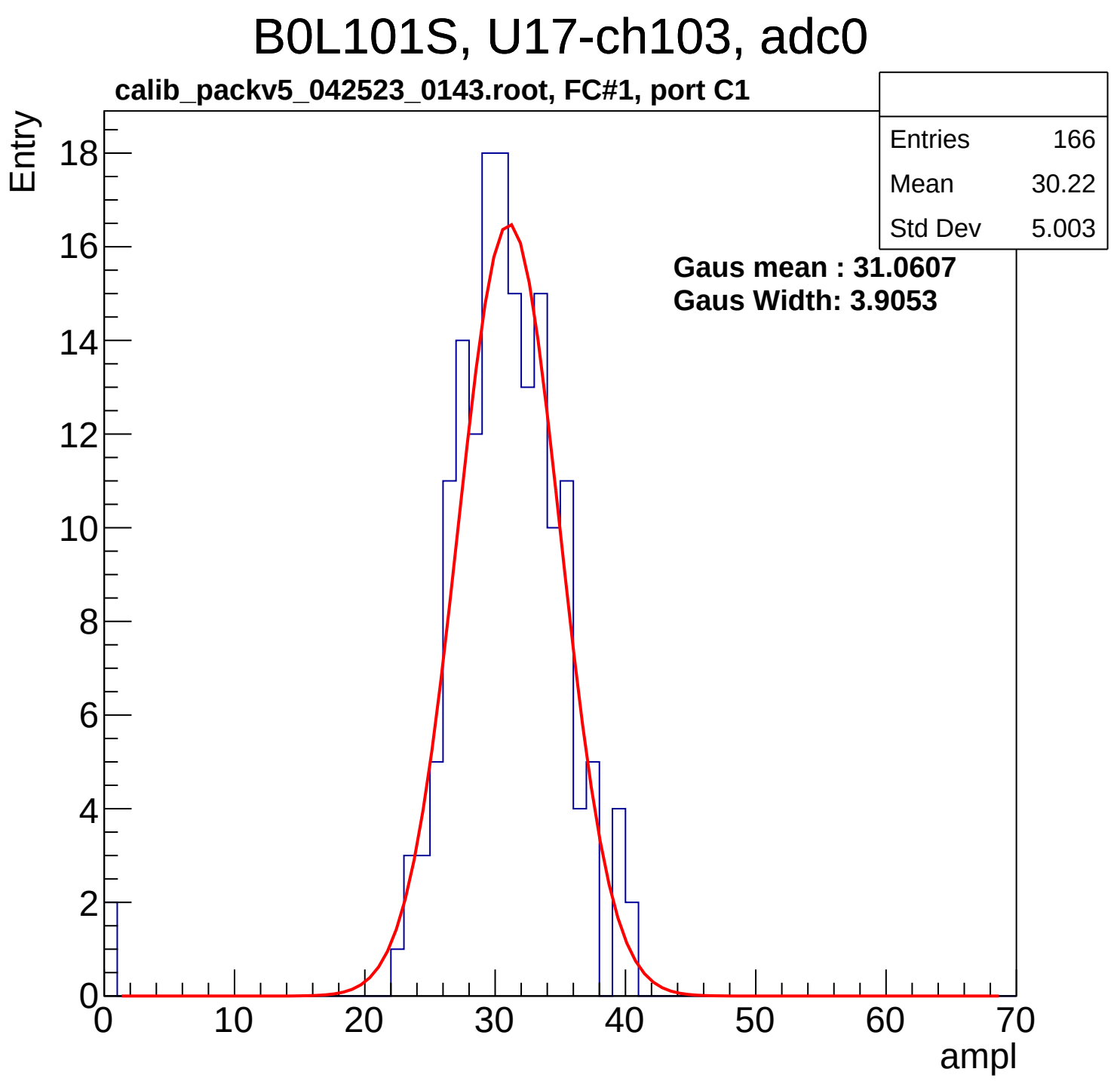

B0L101S, U17-ch102, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 48.09
Std Dev 3.807
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

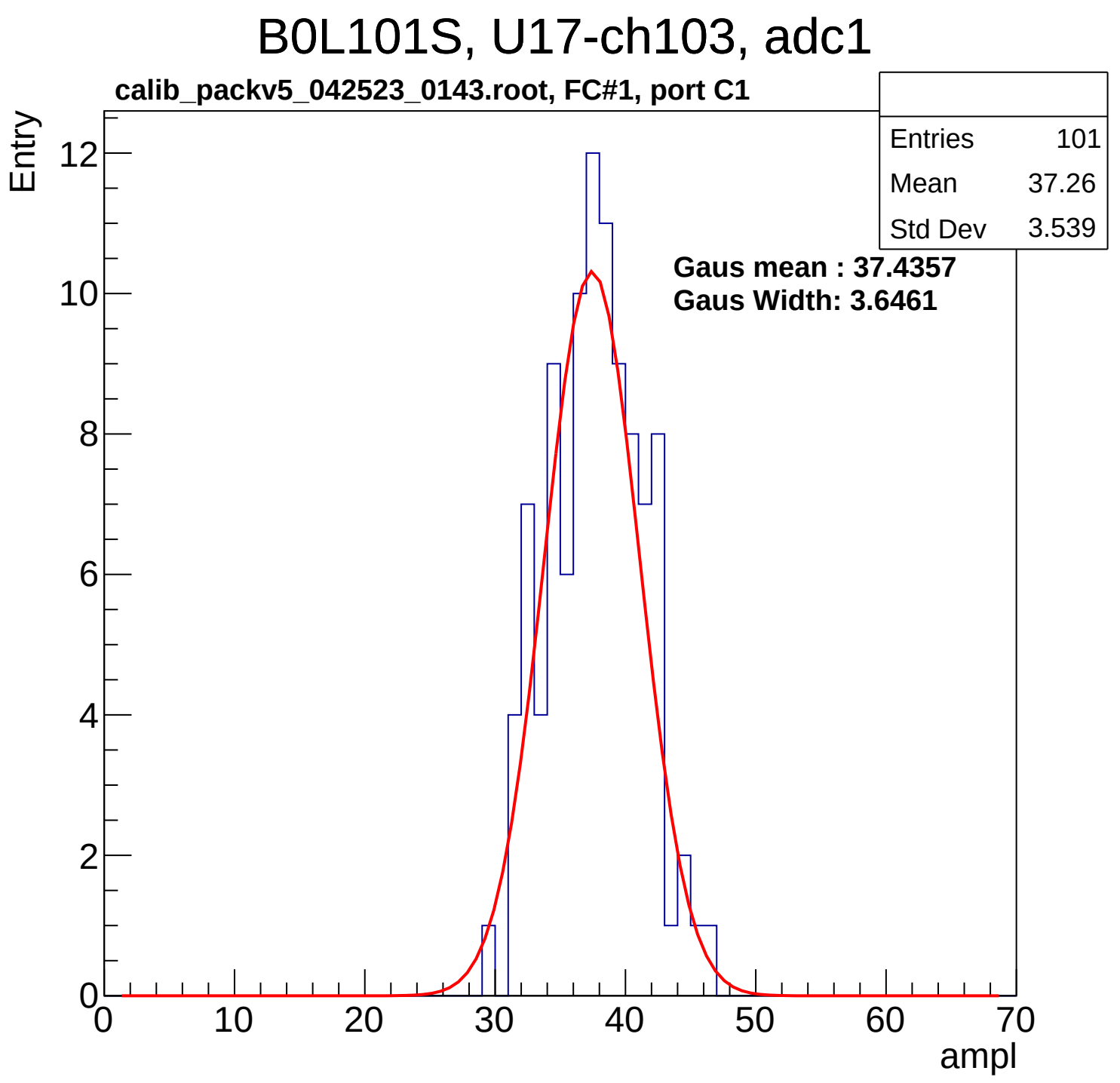

B0L101S, U17-ch102, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 143
Mean 53.97
Std Dev 3.961
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

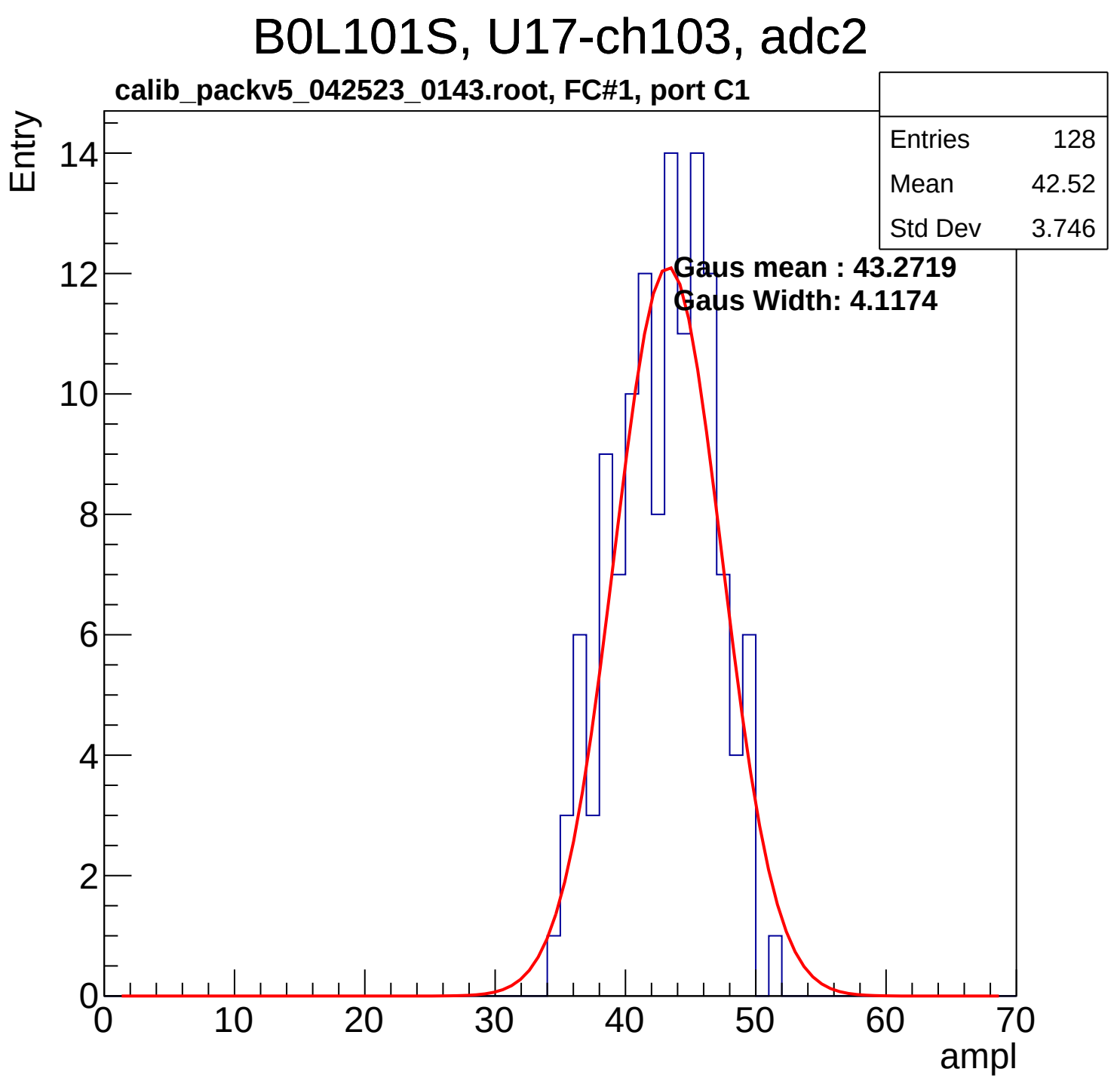

B0L101S, U17-ch102, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.36
Std Dev 2.741
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

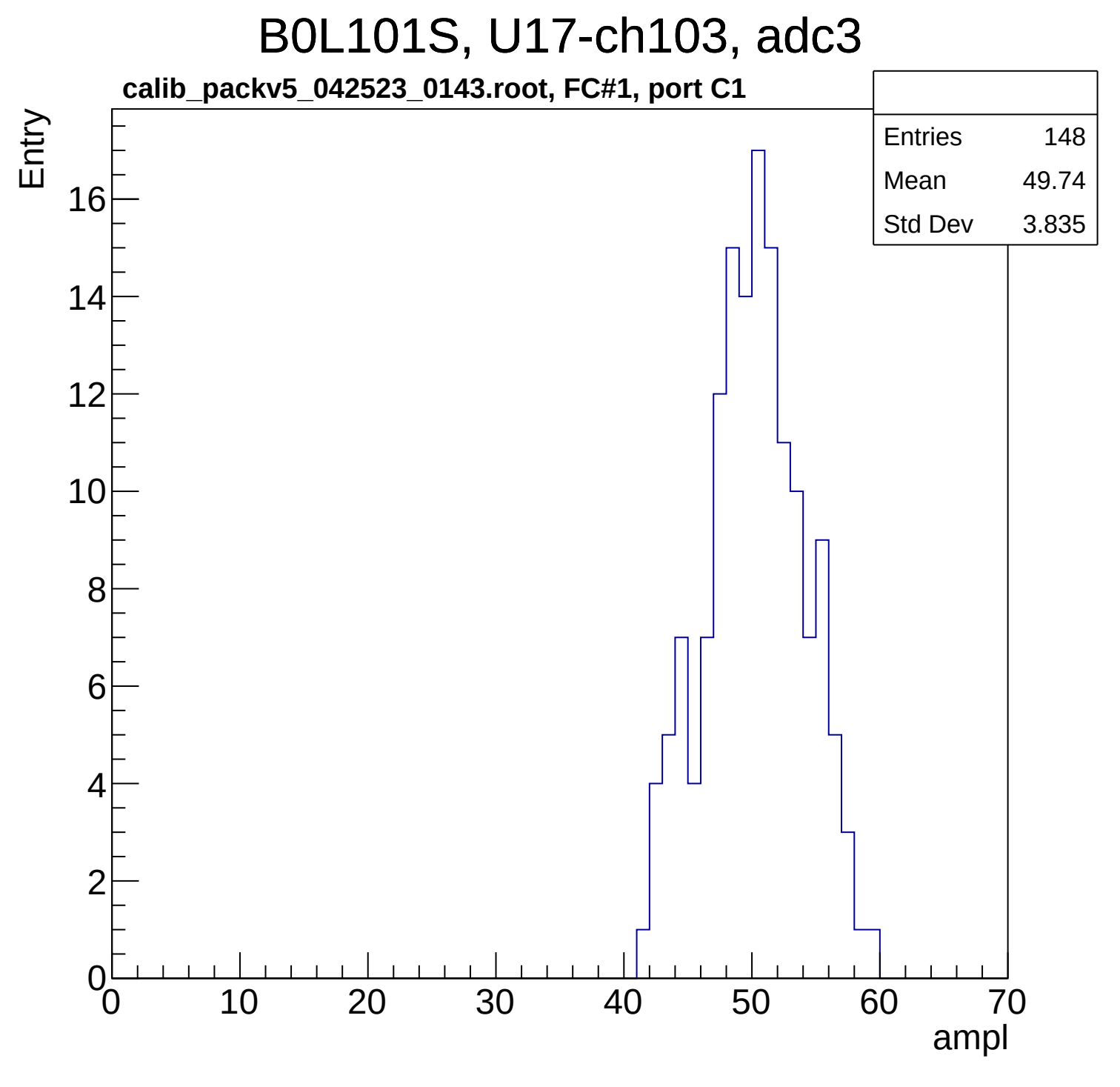

B0L101S, U17-ch102, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 33
Mean 57.45
Std Dev 14.69
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

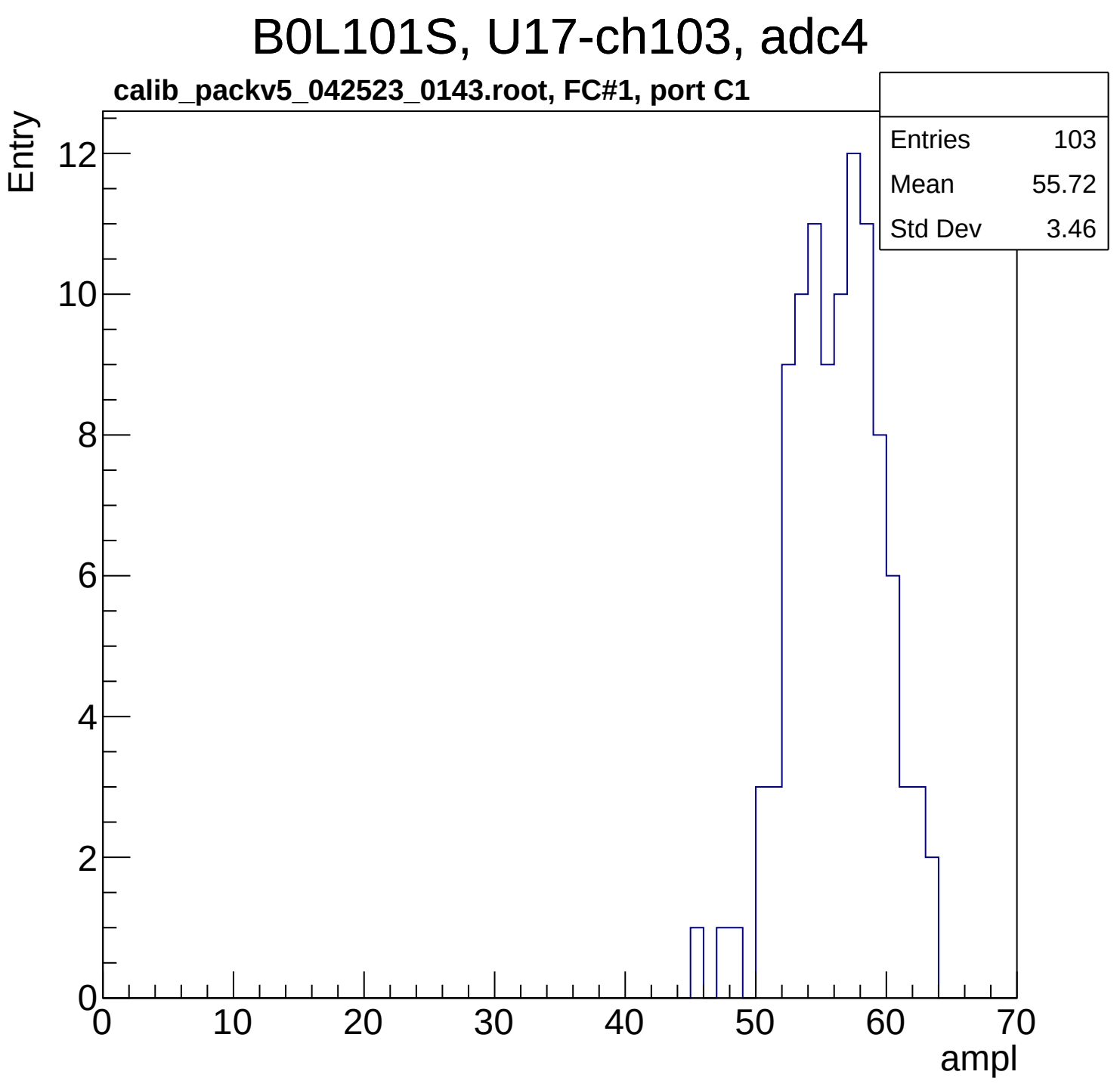

B0L101S, U17-ch102, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

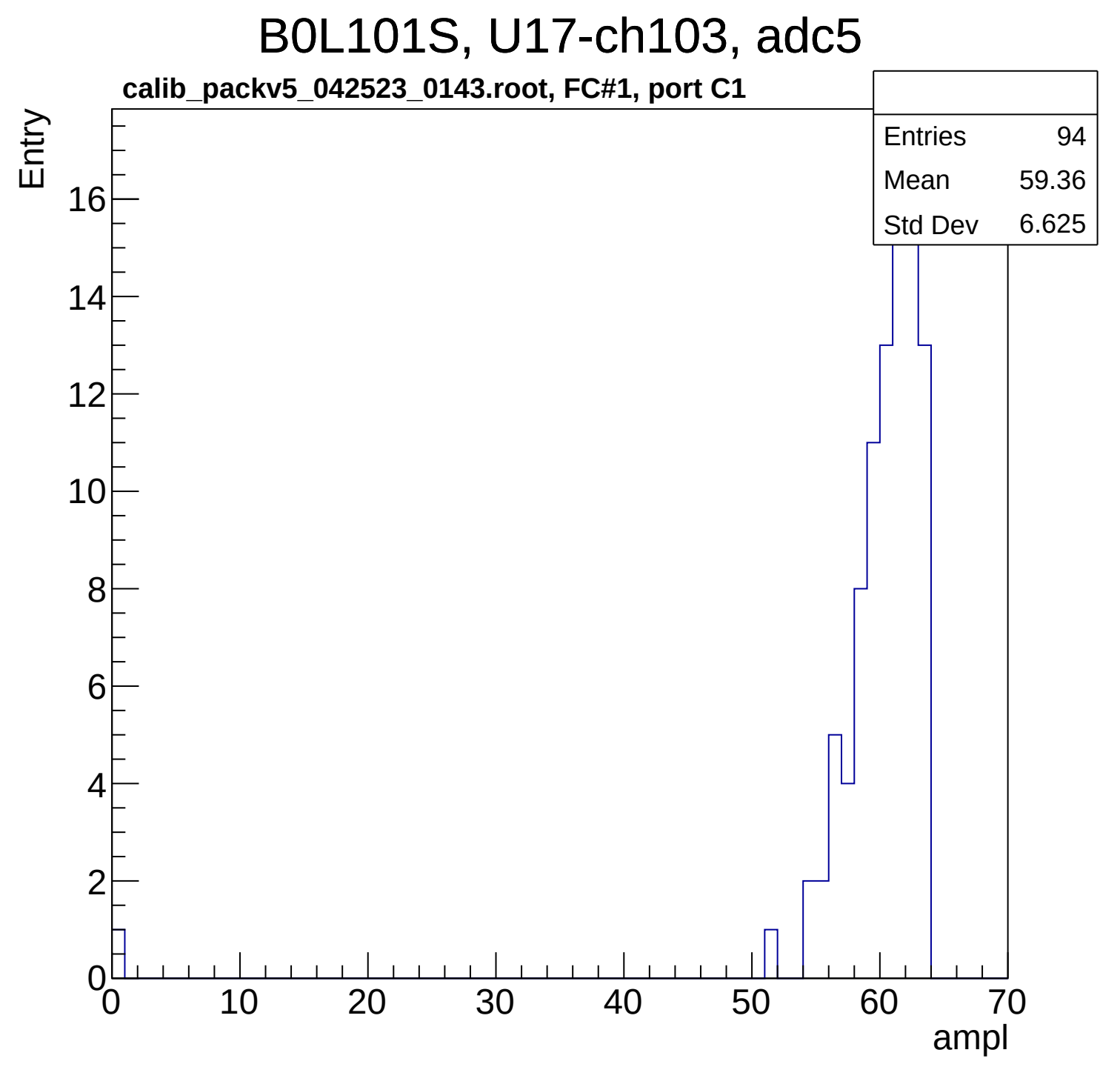

B0L101S, U17-ch103, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 166
Mean 30.22
Std Dev 5.003
Gaus mean : 31.0607
Gaus Width: 3.9053
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

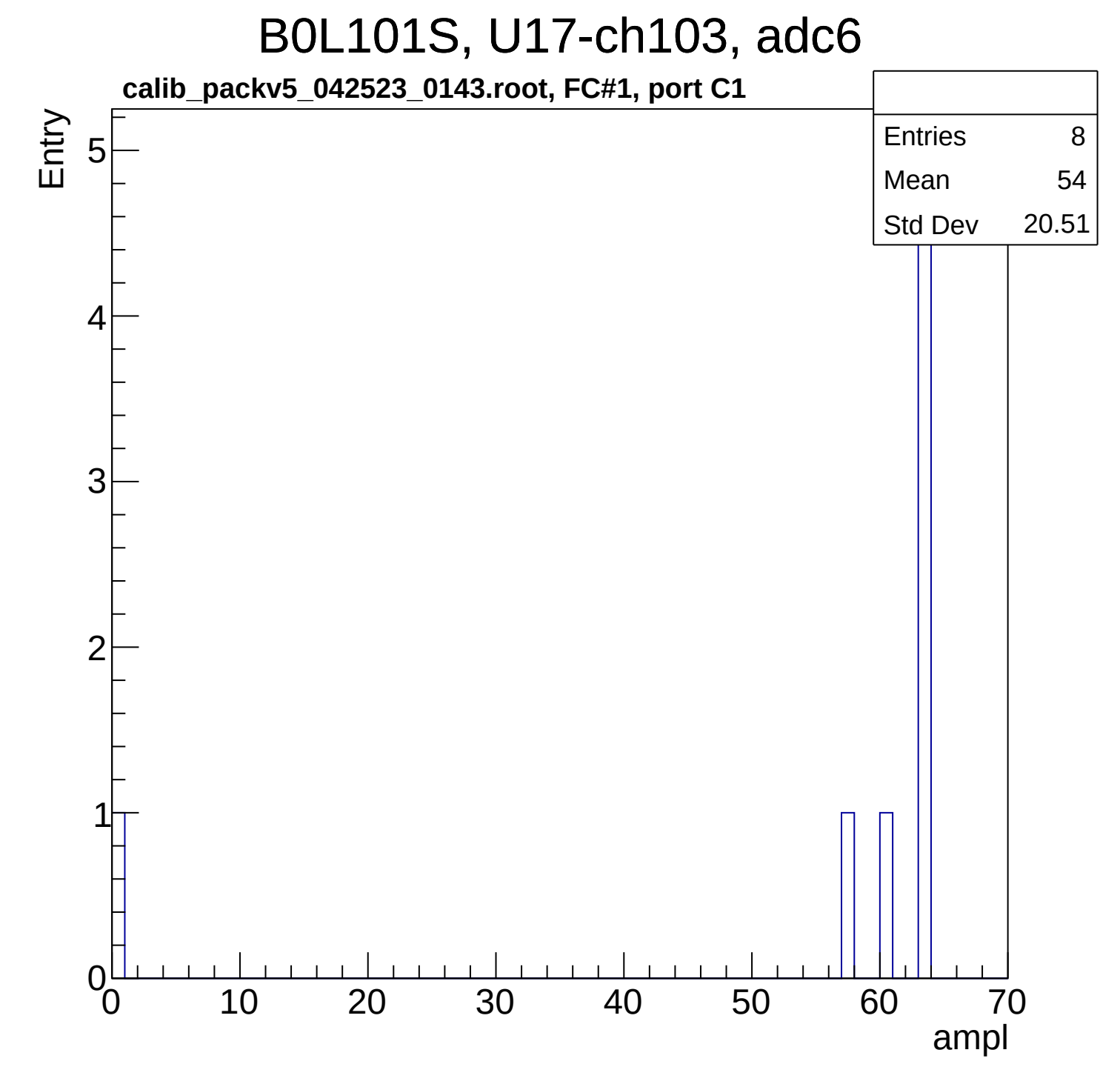

B0L101S, U17-ch103, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 101
Mean 37.26
Std Dev 3.539
Gaus mean : 37.4357
Gaus Width: 3.6461
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

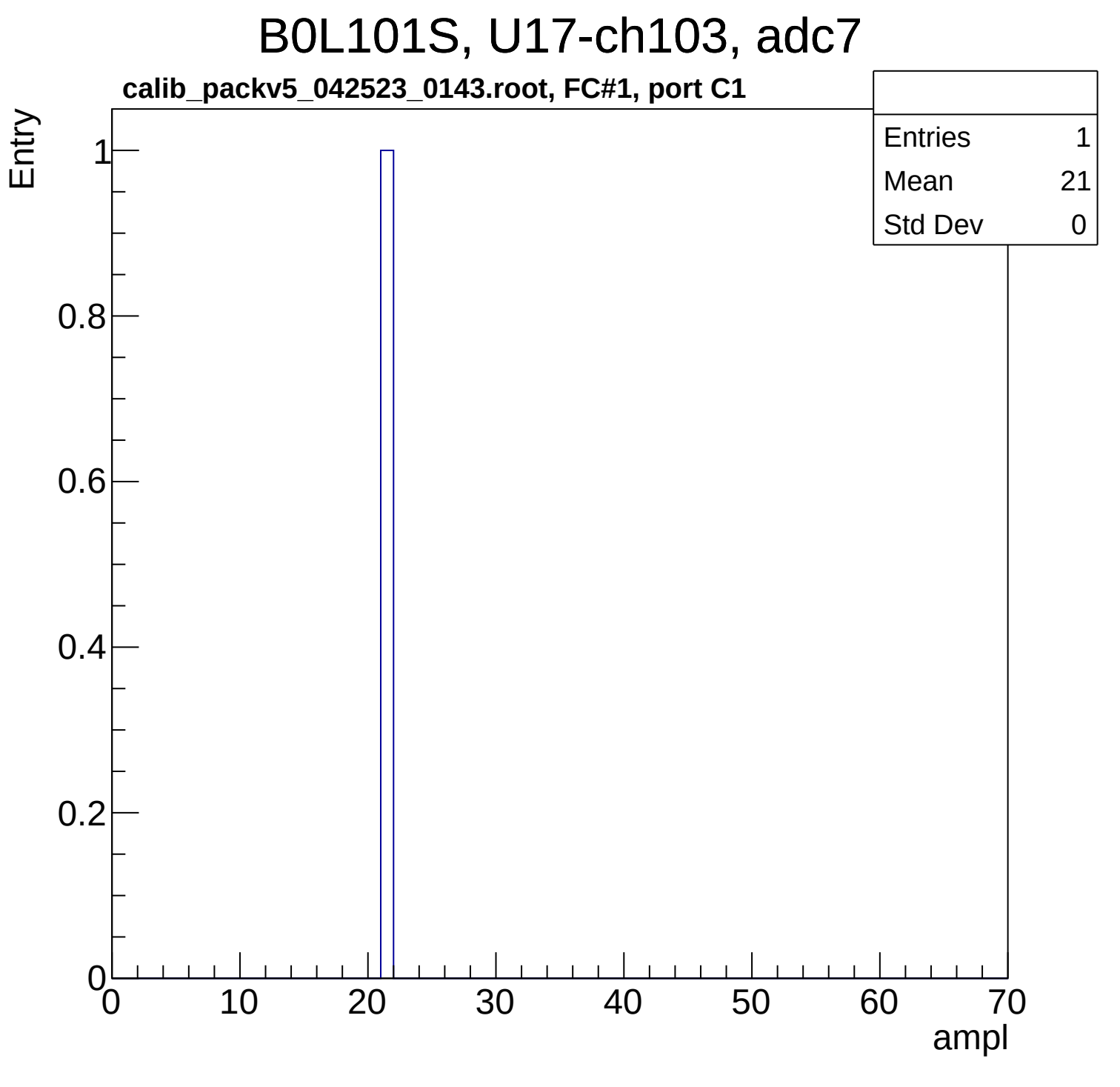

B0L101S, U17-ch103, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 42.52
Std Dev 3.746
Gaus mean : 43.2719
Gaus Width: 4.1174
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

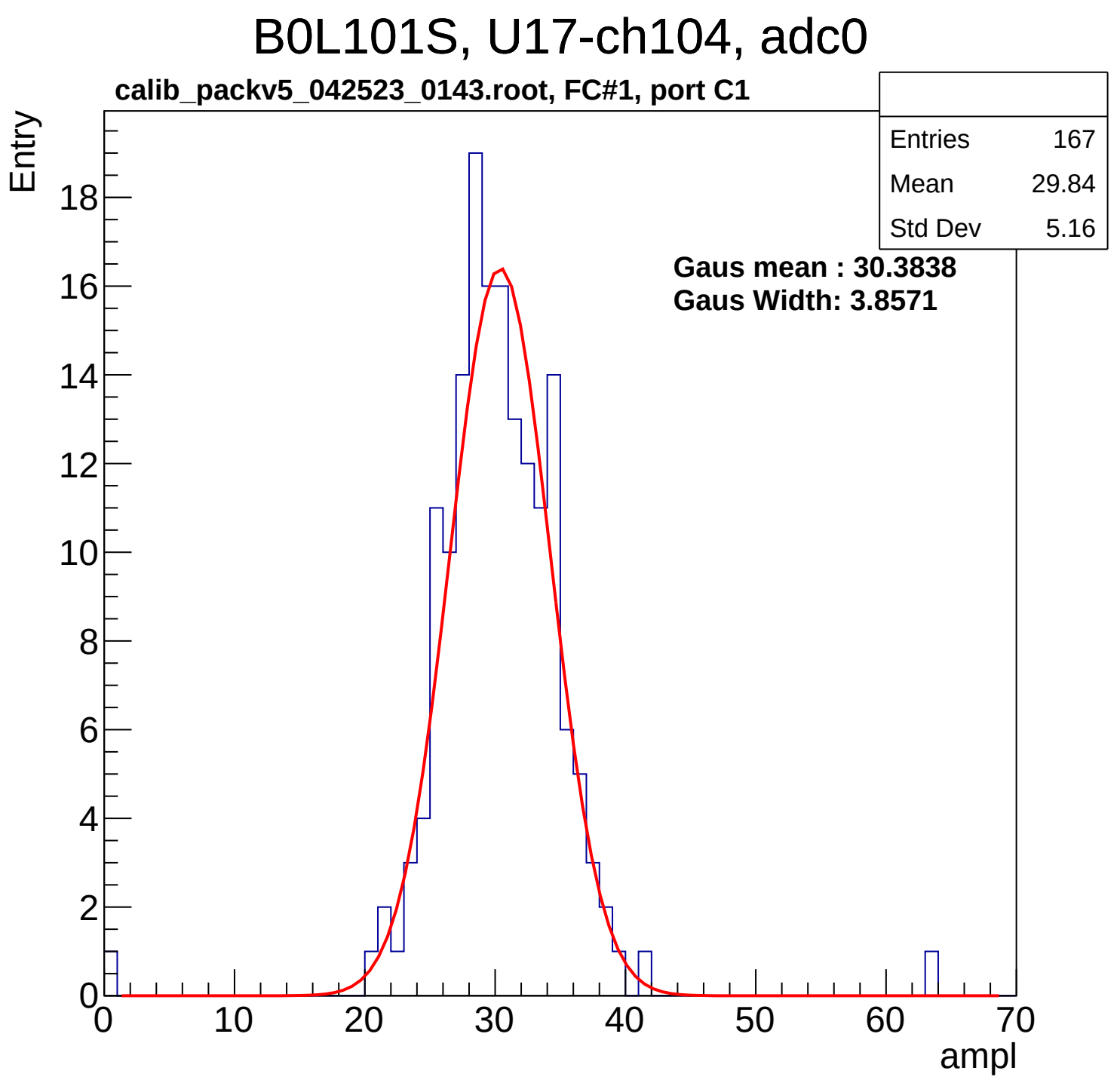

B0L101S, U17-ch103, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 49.74
Std Dev 3.835
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

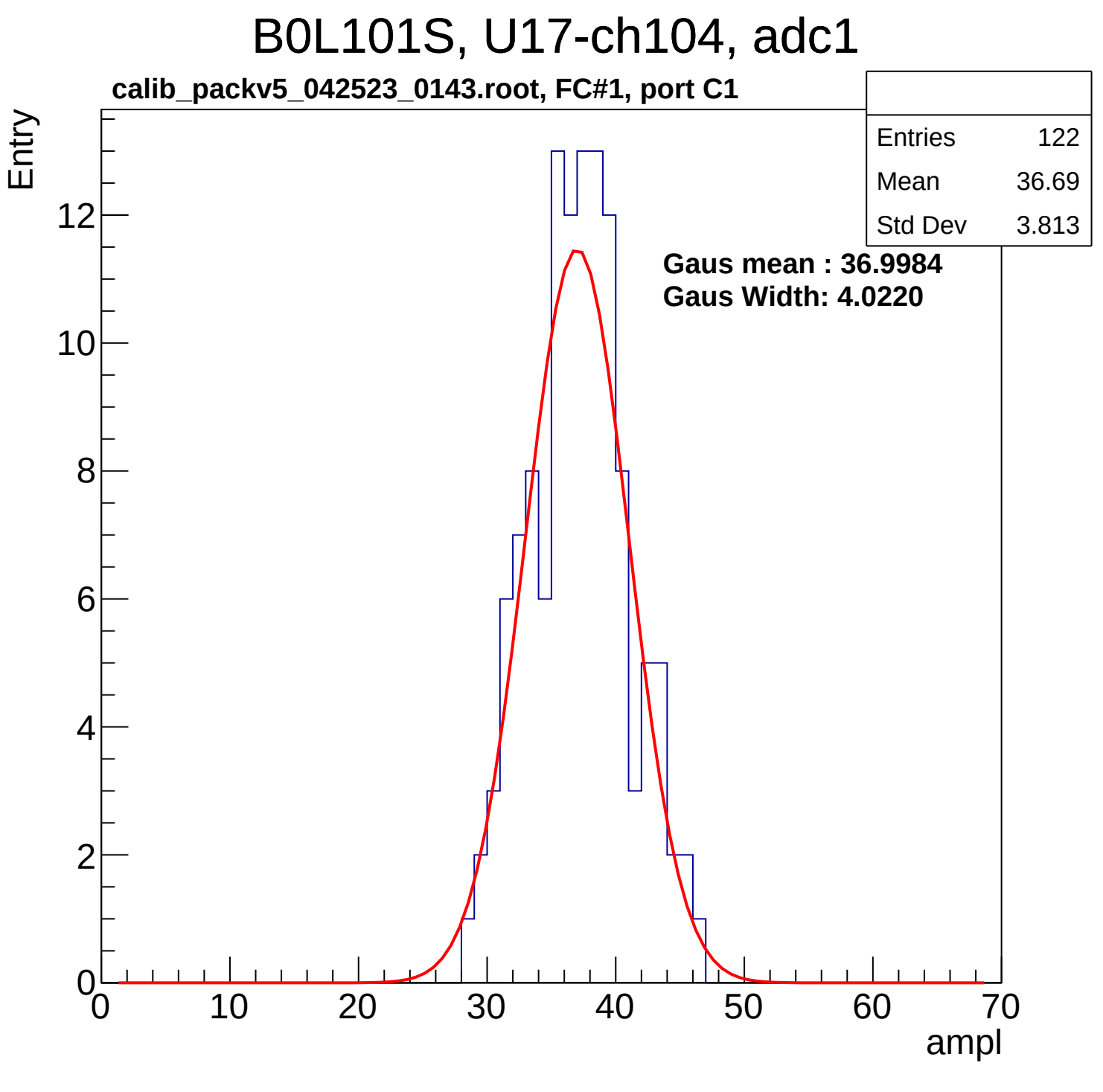

B0L101S, U17-ch103, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 55.72
Std Dev 3.46
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

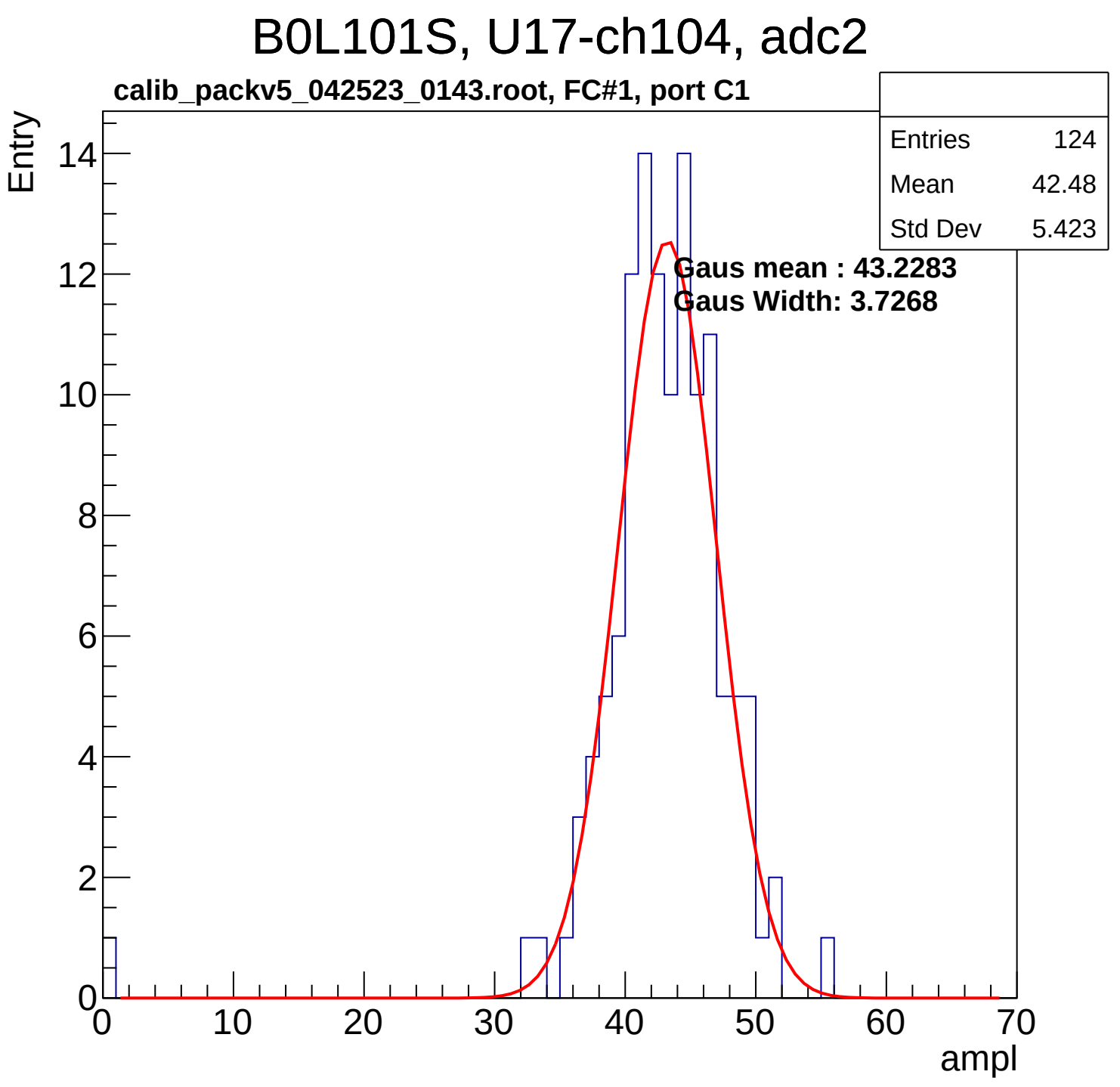

B0L101S, U17-ch103, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 94
Mean 59.36
Std Dev 6.625
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

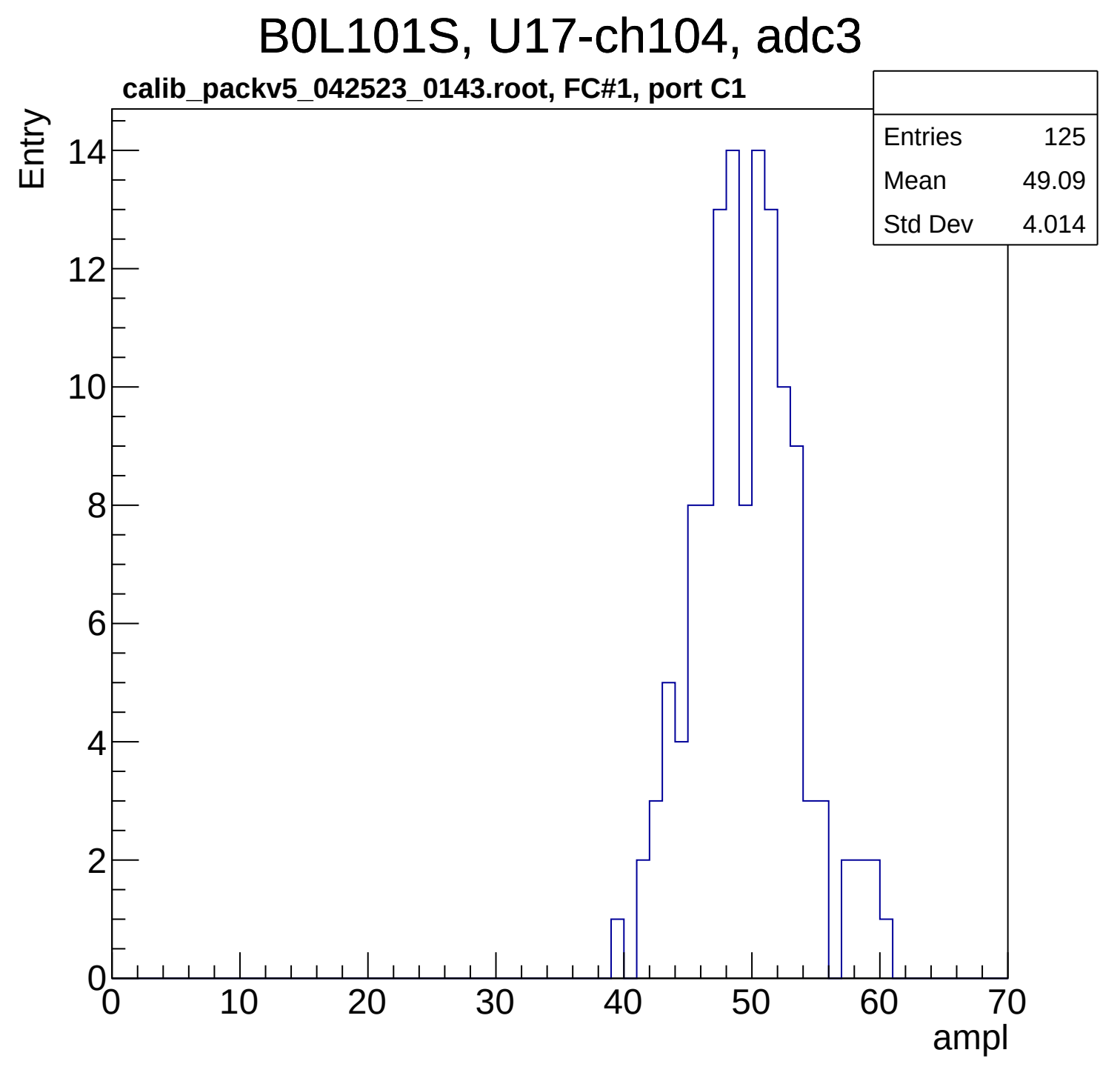

B0L101S, U17-ch103, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 8
Mean 54
Std Dev 20.51
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

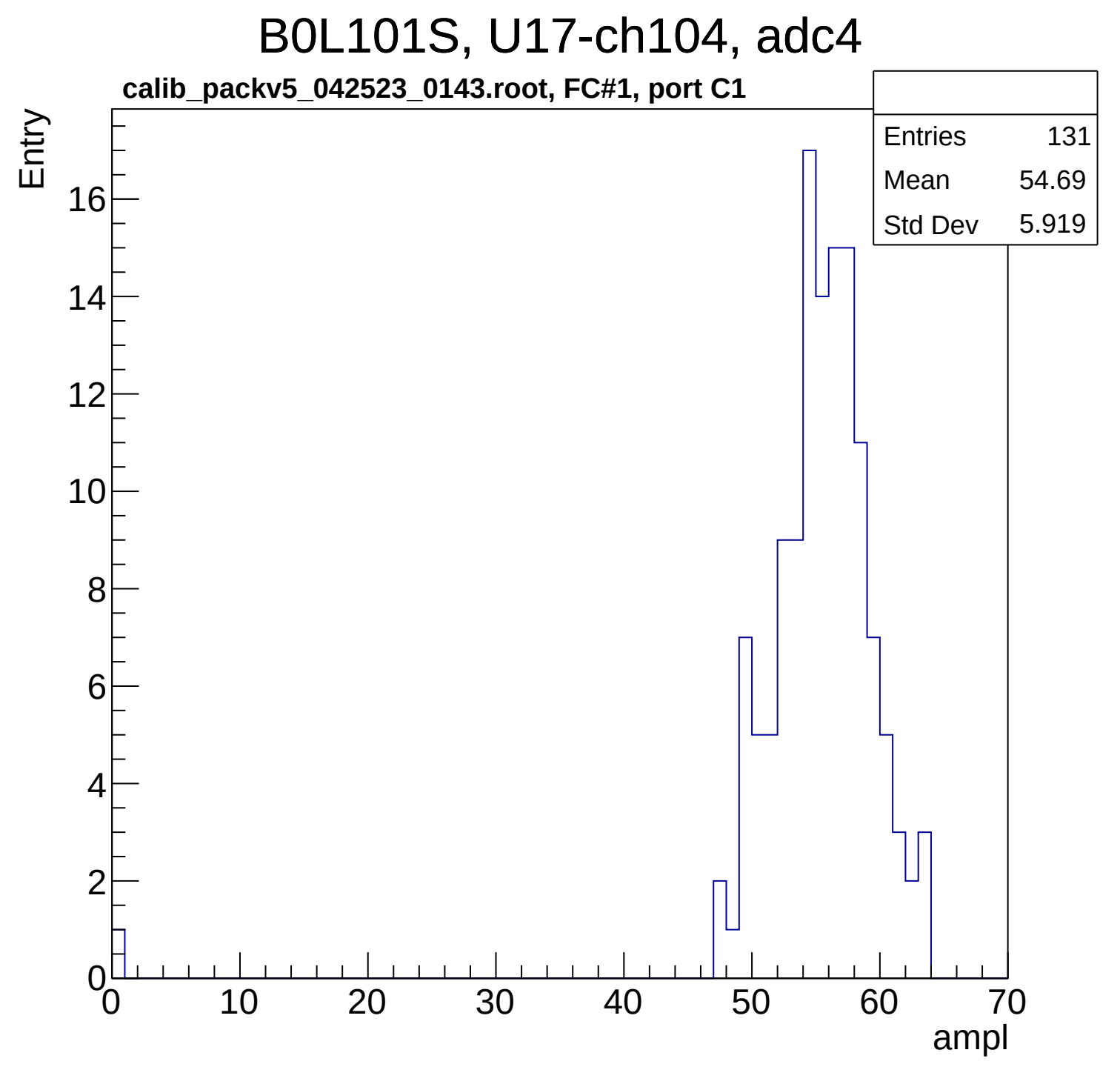

B0L101S, U17-ch103, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

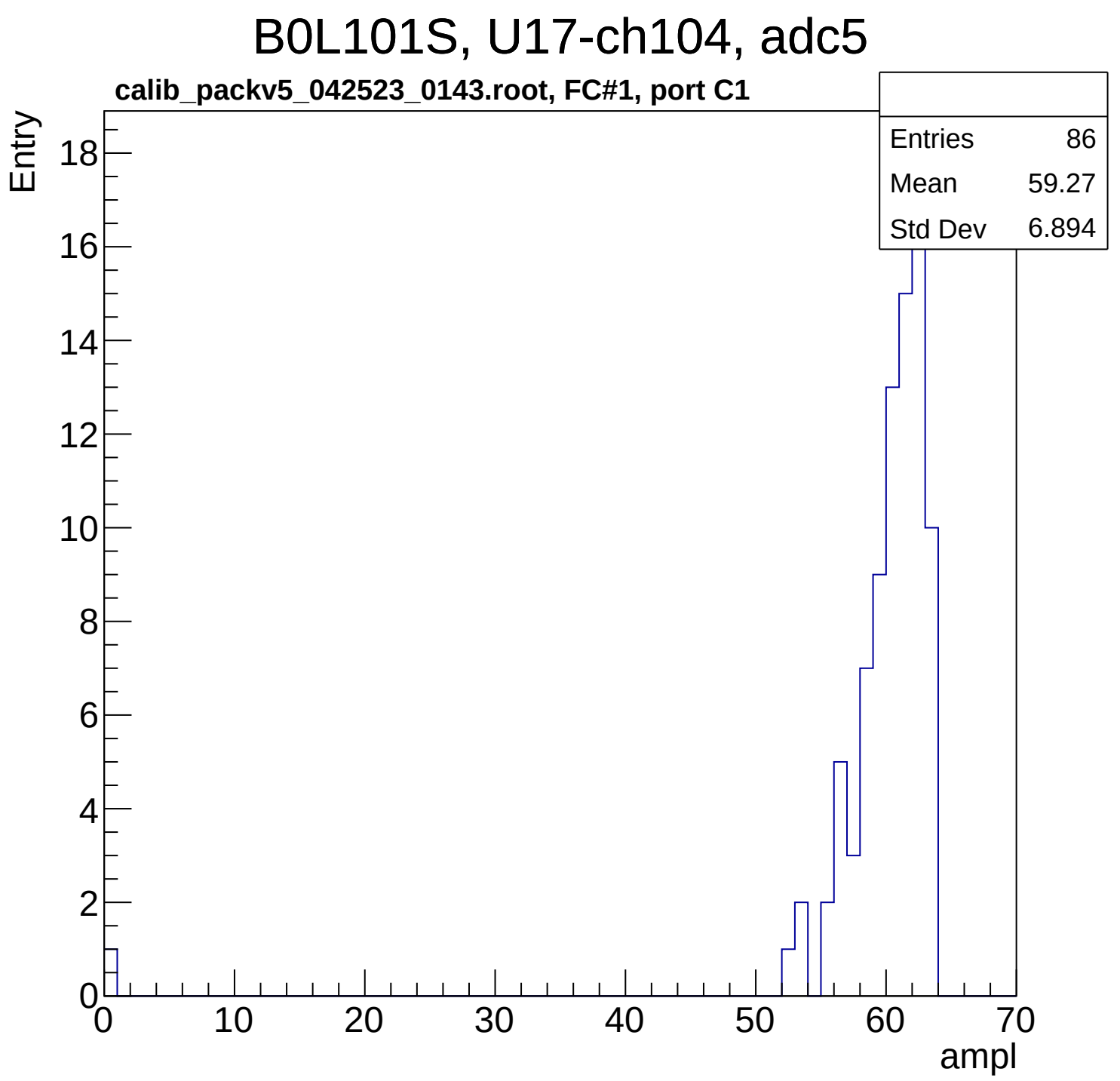

B0L101S, U17-ch104, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 167
Mean 29.84
Std Dev 5.16
Gaus mean : 30.3838
Gaus Width: 3.8571
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

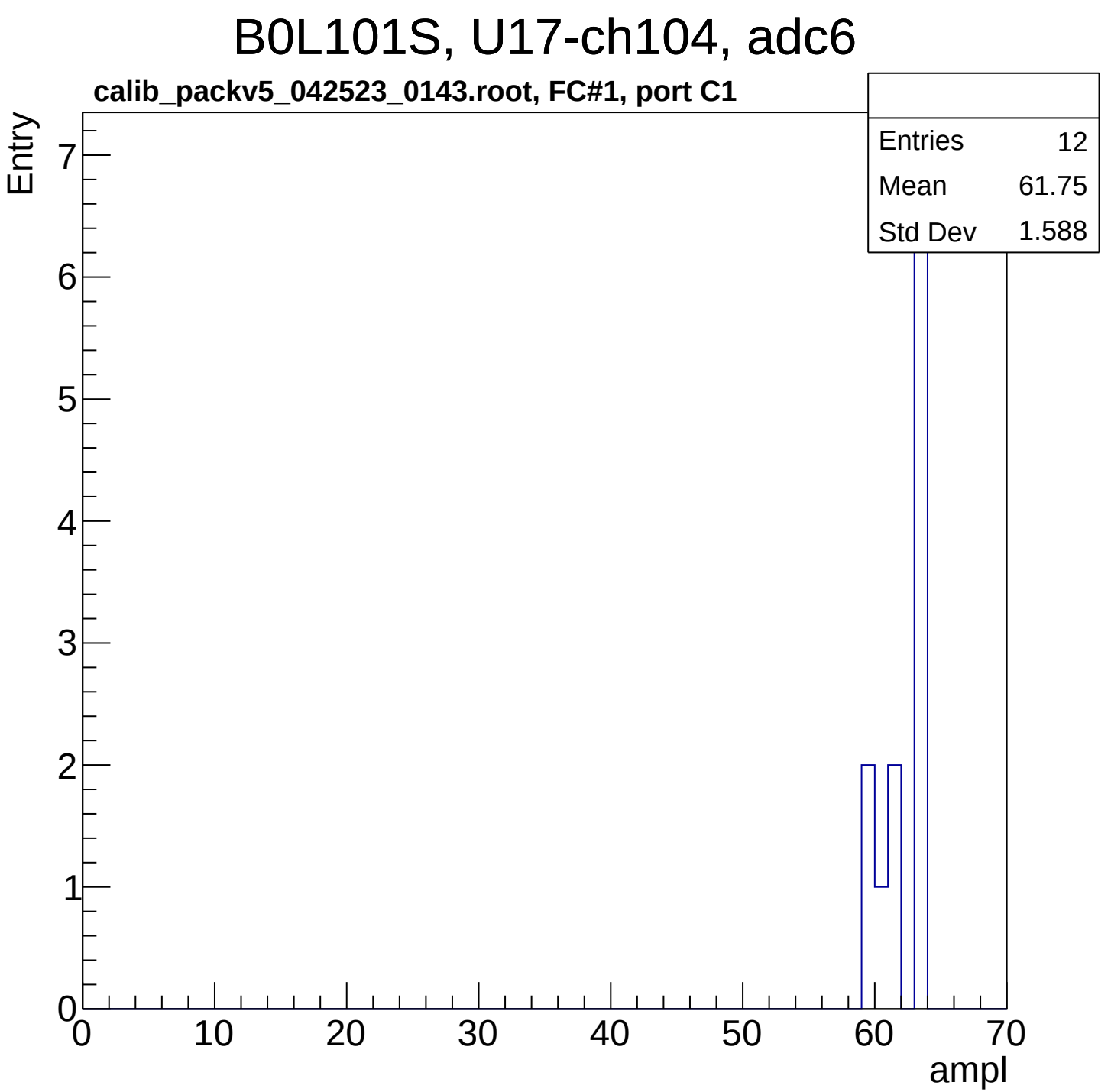

B0L101S, U17-ch104, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 36.69
Std Dev 3.813
Gaus mean : 36.9984
Gaus Width: 4.0220
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

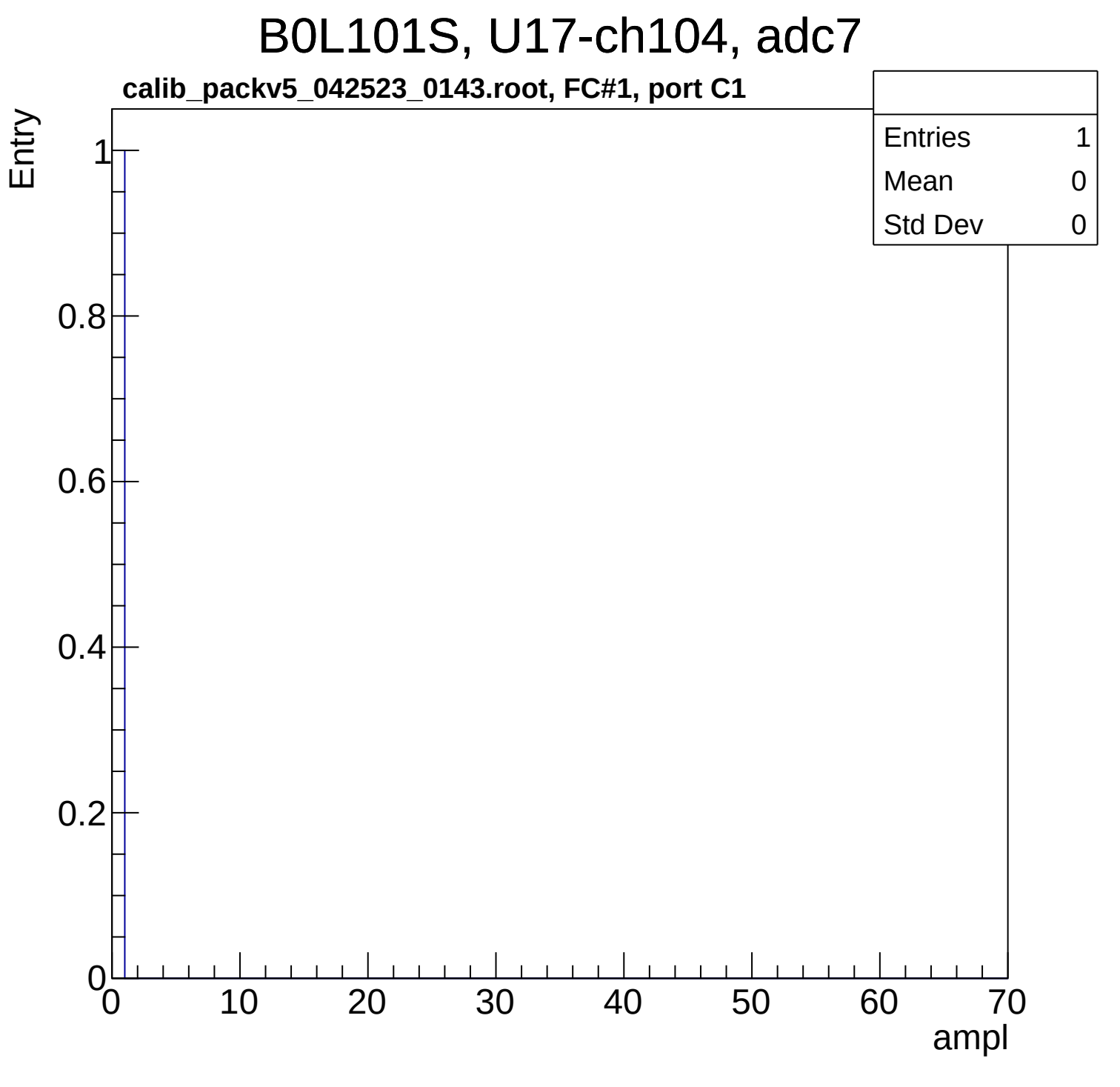

B0L101S, U17-ch104, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 42.48
Std Dev 5.423
Gaus mean : 43.2283
Gaus Width: 3.7268
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

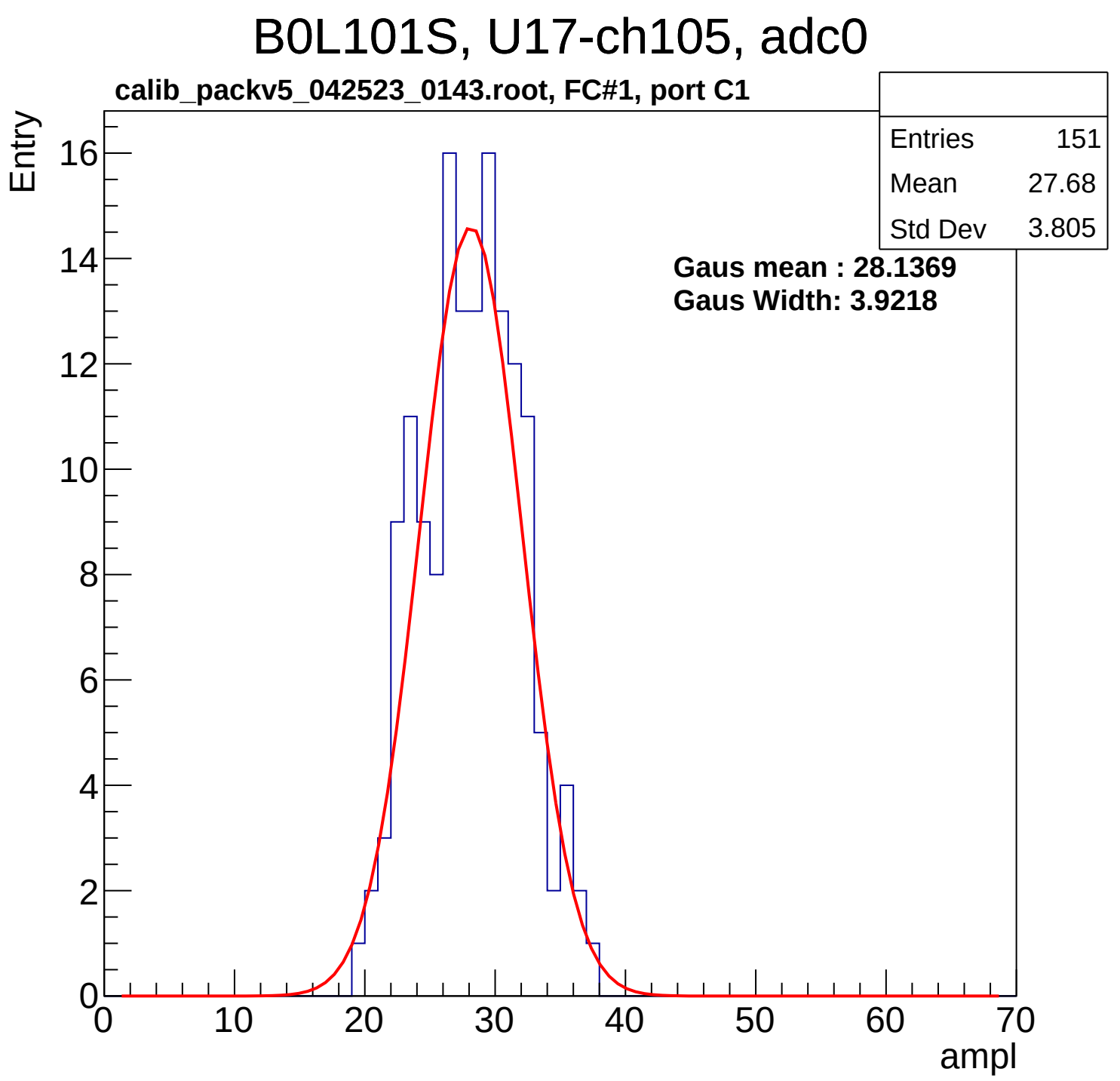

B0L101S, U17-ch104, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 125
Mean 49.09
Std Dev 4.014
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

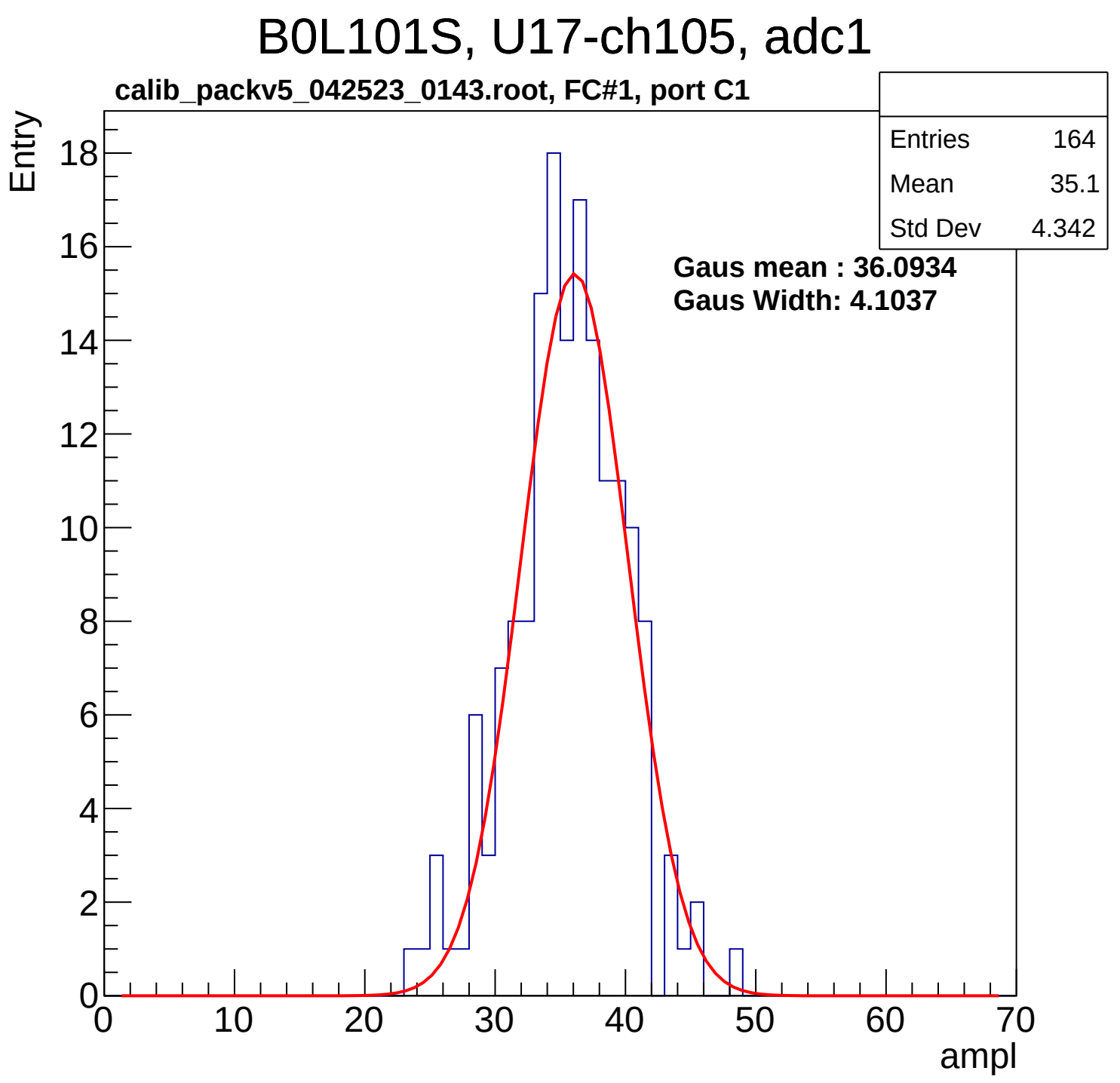

B0L101S, U17-ch104, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 54.69
Std Dev 5.919
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

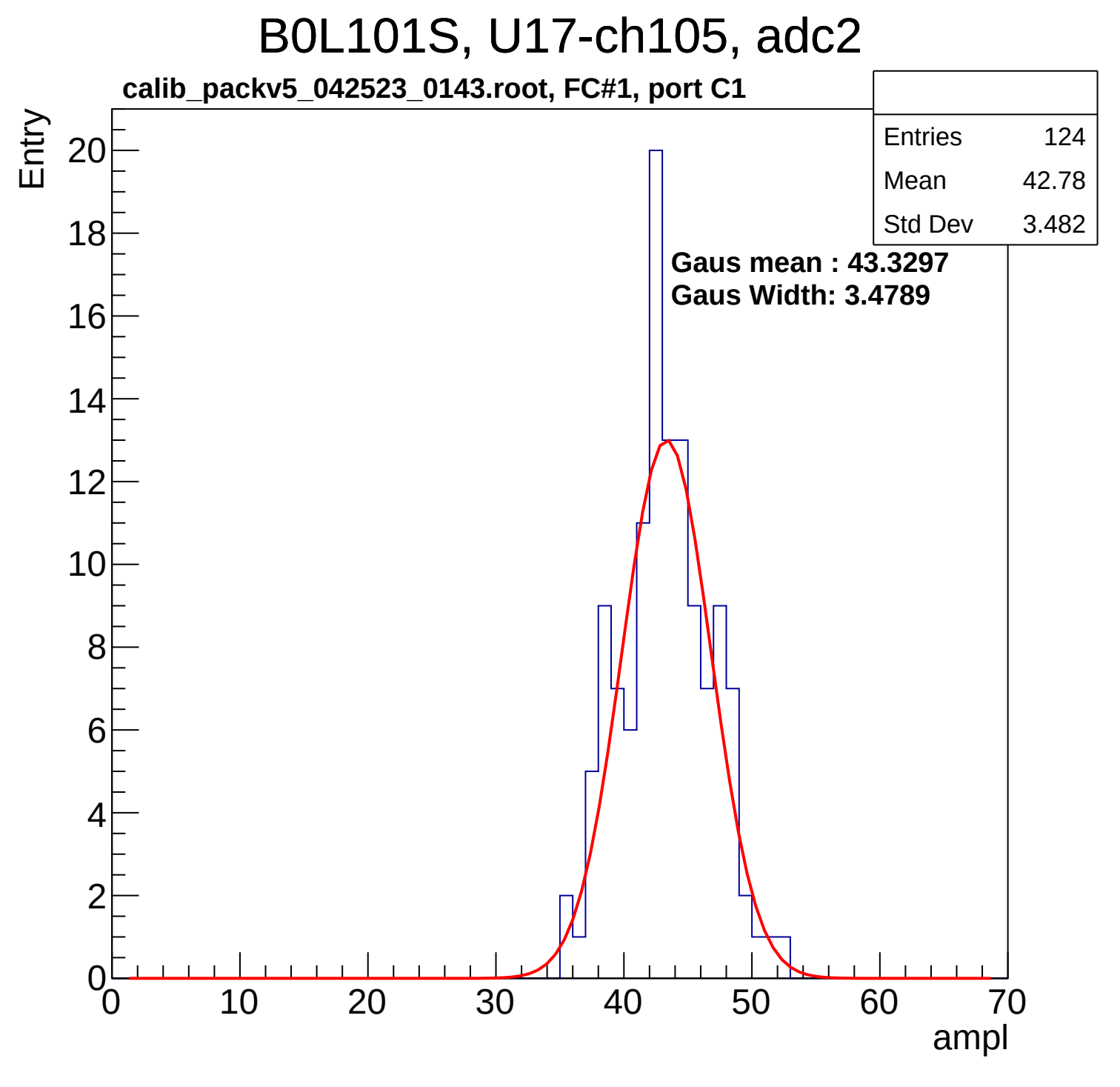

B0L101S, U17-ch104, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 86
Mean 59.27
Std Dev 6.894
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

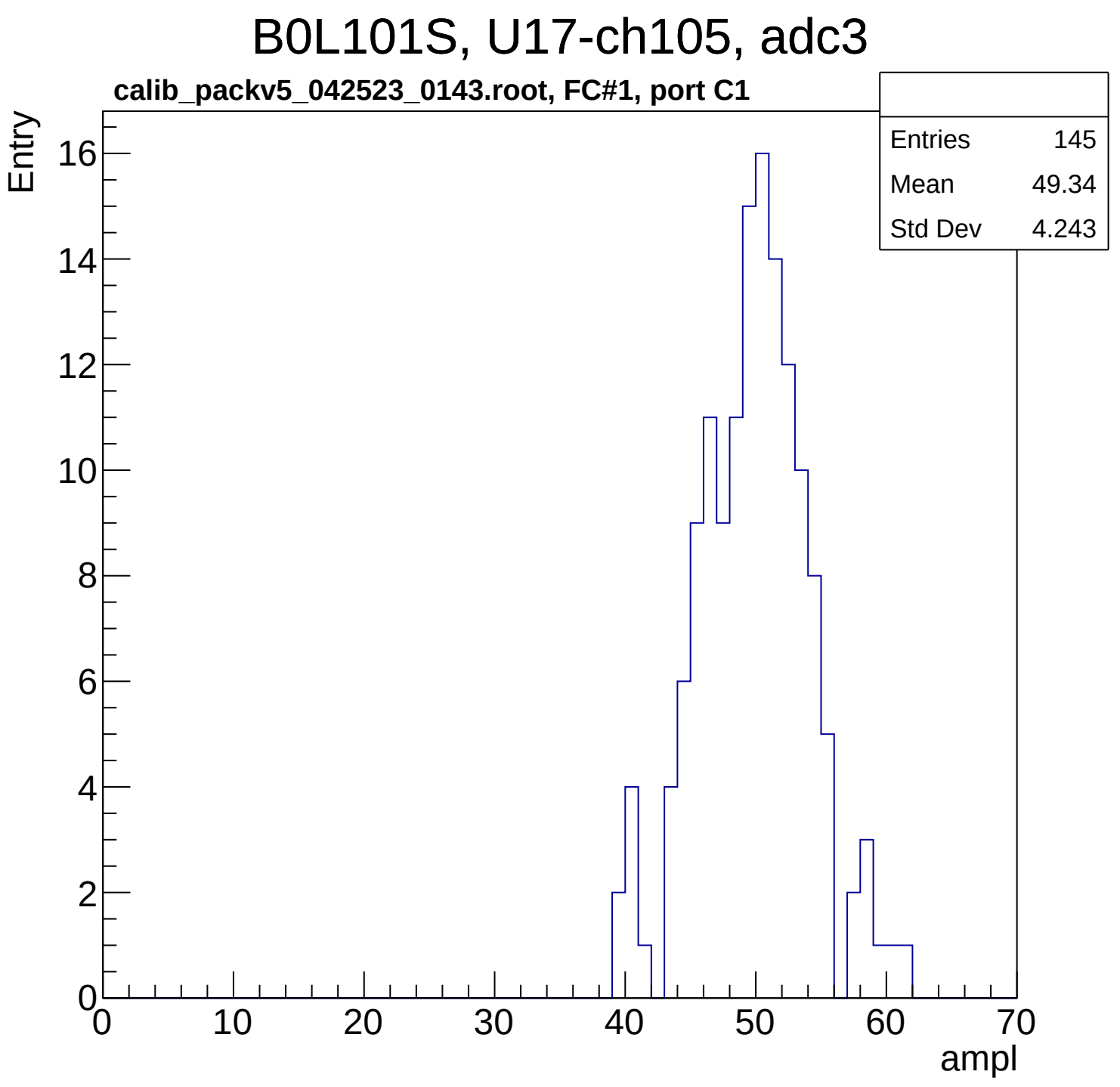

B0L101S, U17-ch104, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.75
Std Dev 1.588
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

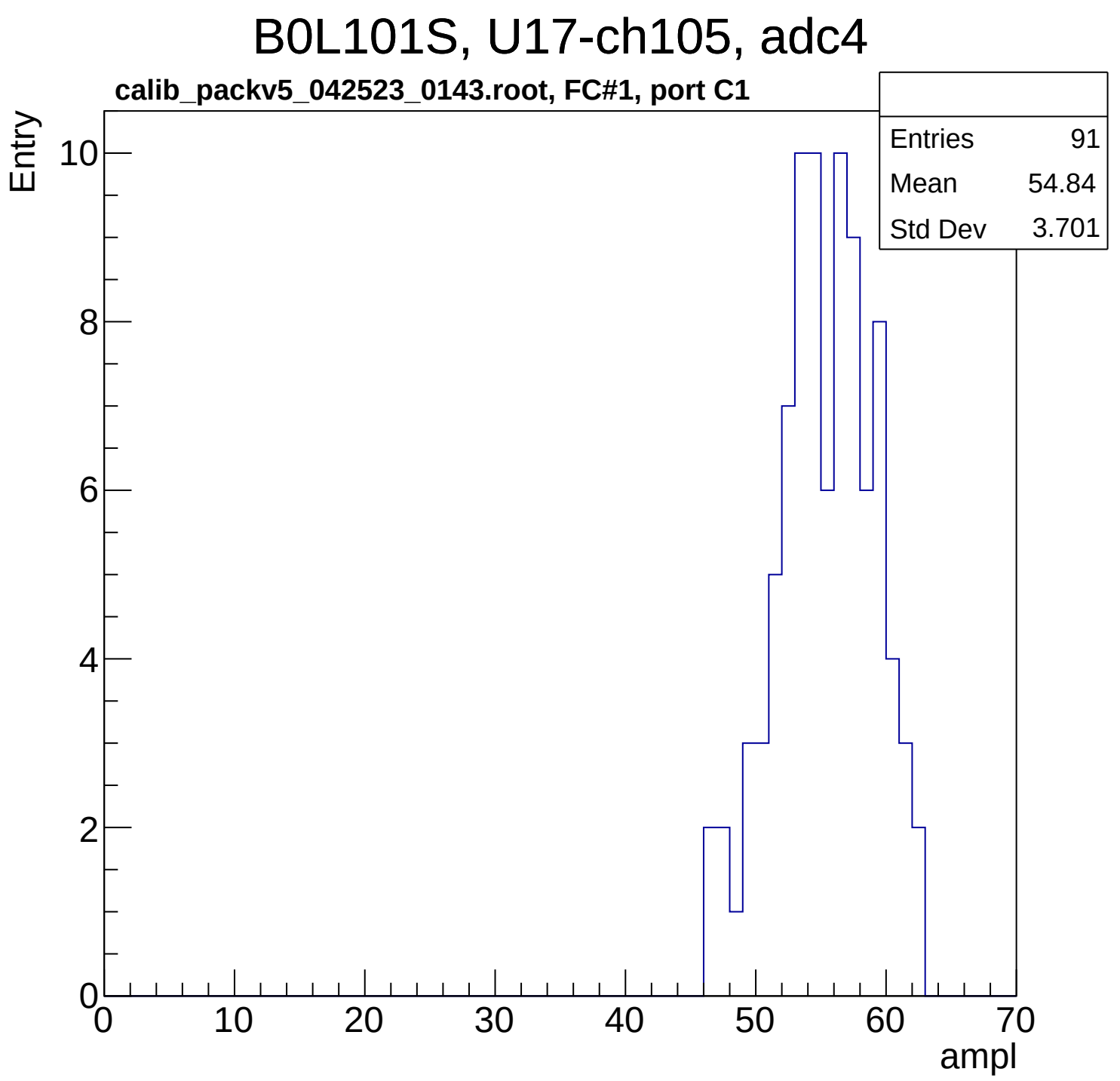

B0L101S, U17-ch104, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

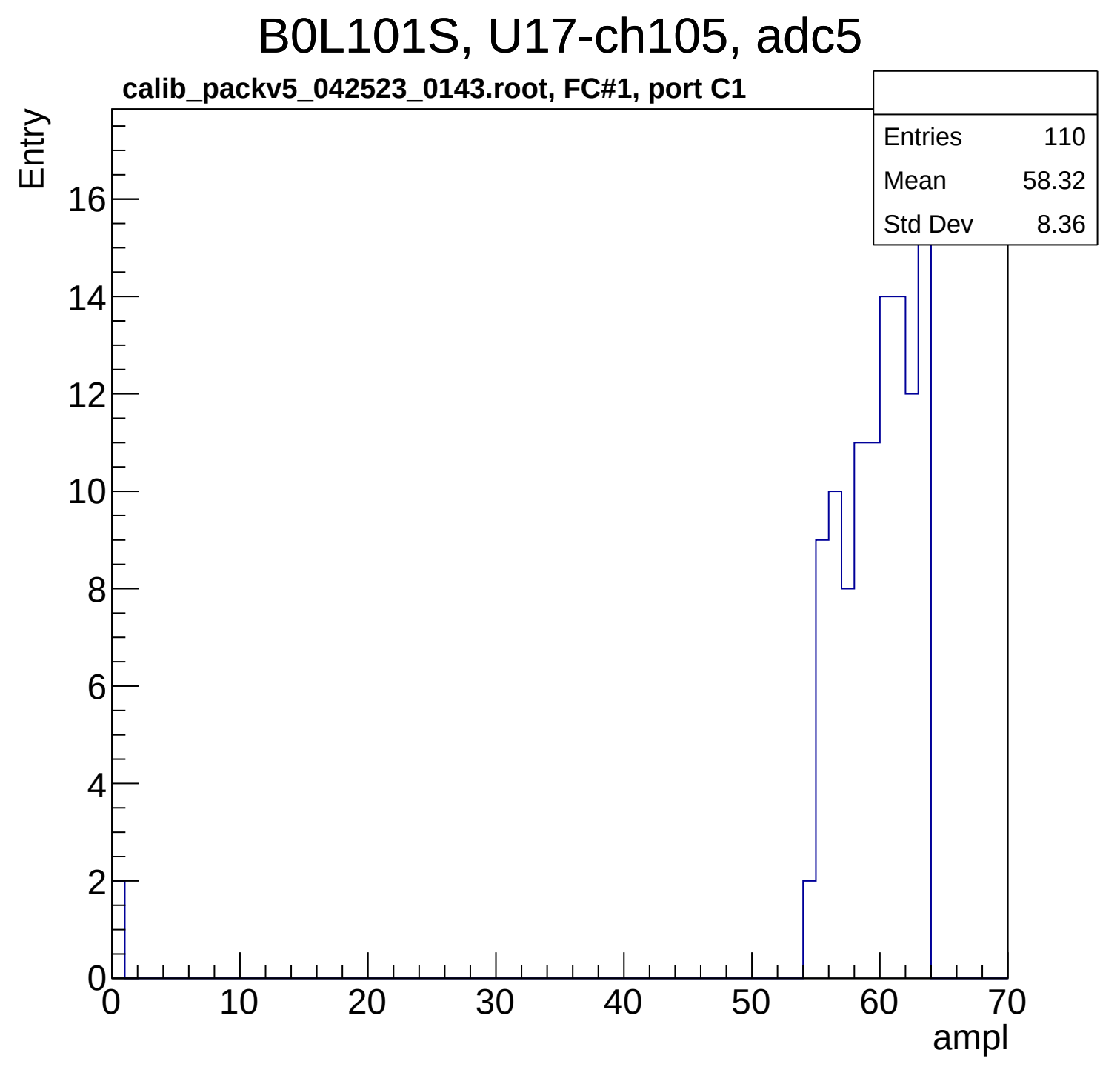

B0L101S, U17-ch105, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 27.68
Std Dev 3.805
Gaus mean : 28.1369
Gaus Width: 3.9218
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

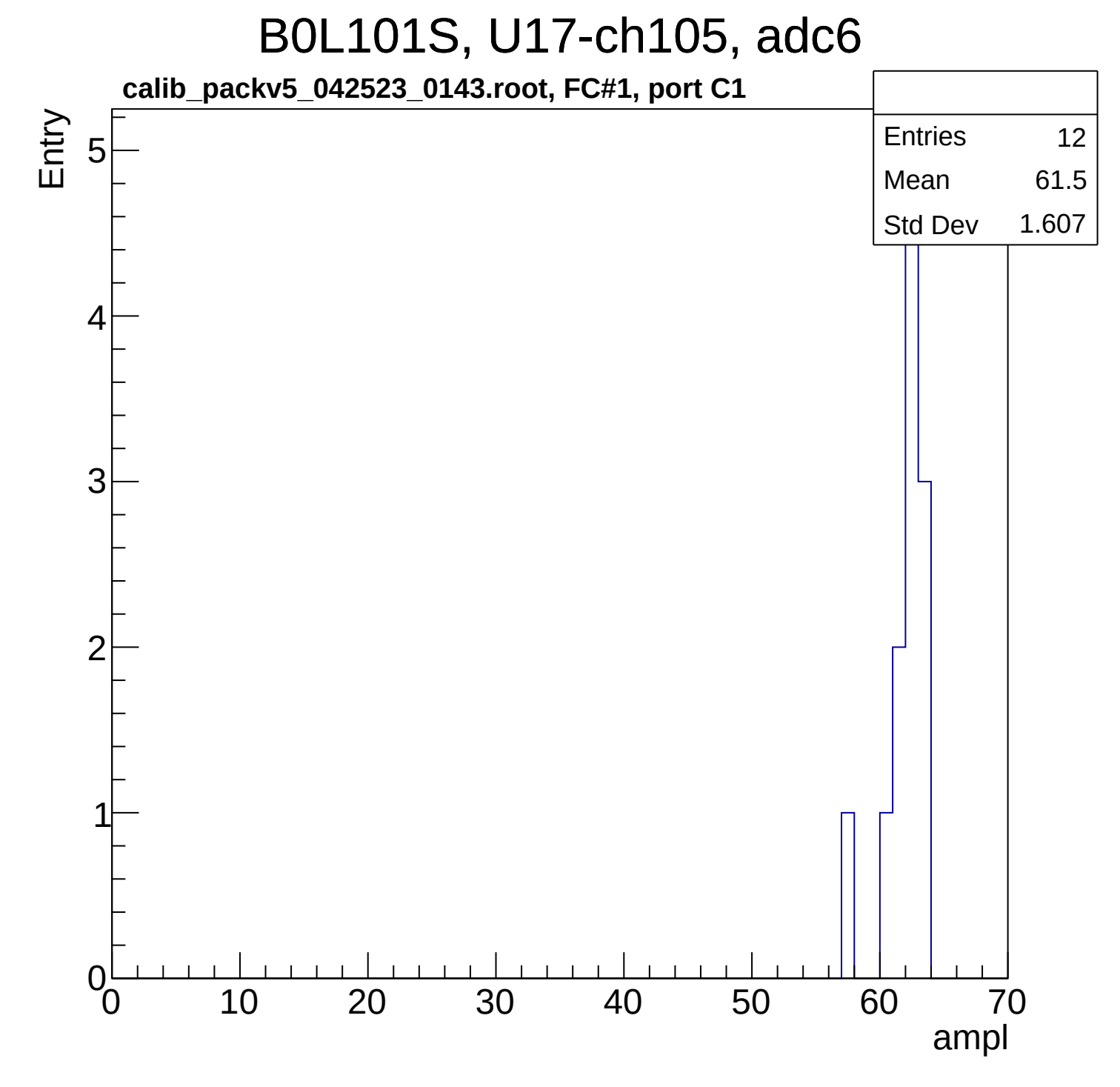

B0L101S, U17-ch105, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 164
Mean 35.1
Std Dev 4.342
Gaus mean : 36.0934
Gaus Width: 4.1037
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

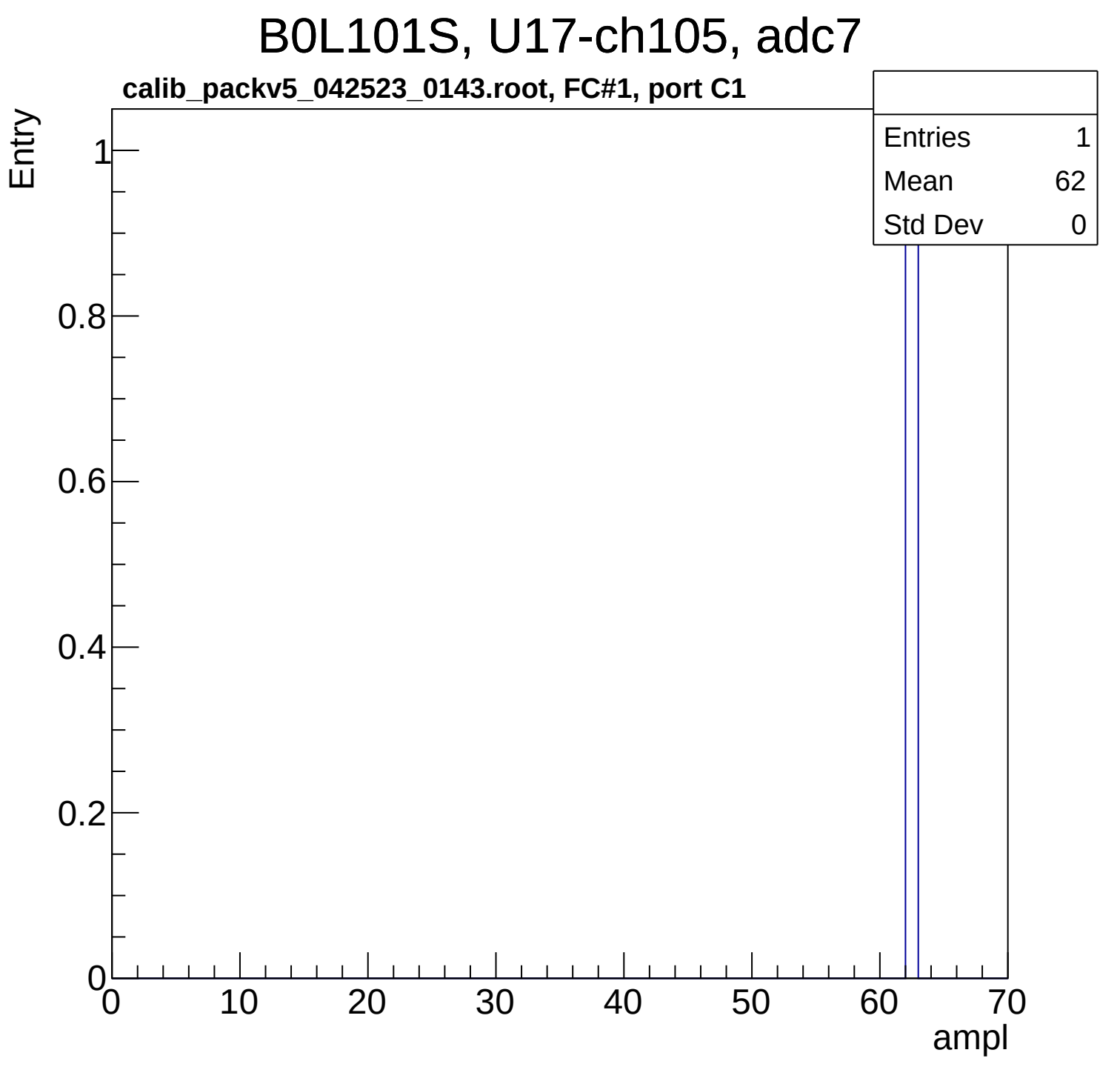

B0L101S, U17-ch105, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 42.78
Std Dev 3.482
Gaus mean : 43.3297
Gaus Width: 3.4789
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

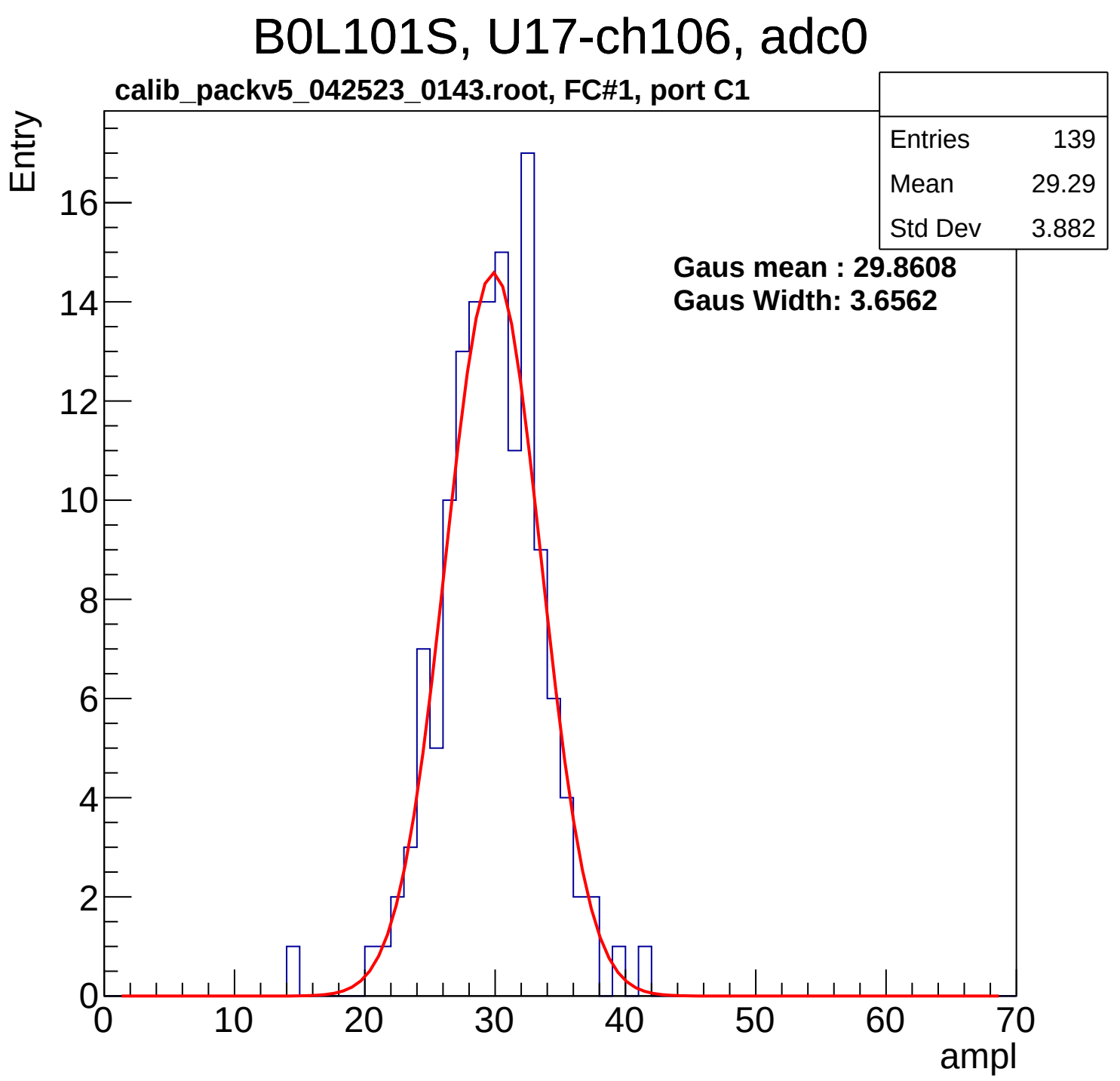

B0L101S, U17-ch105, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 49.34
Std Dev 4.243
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

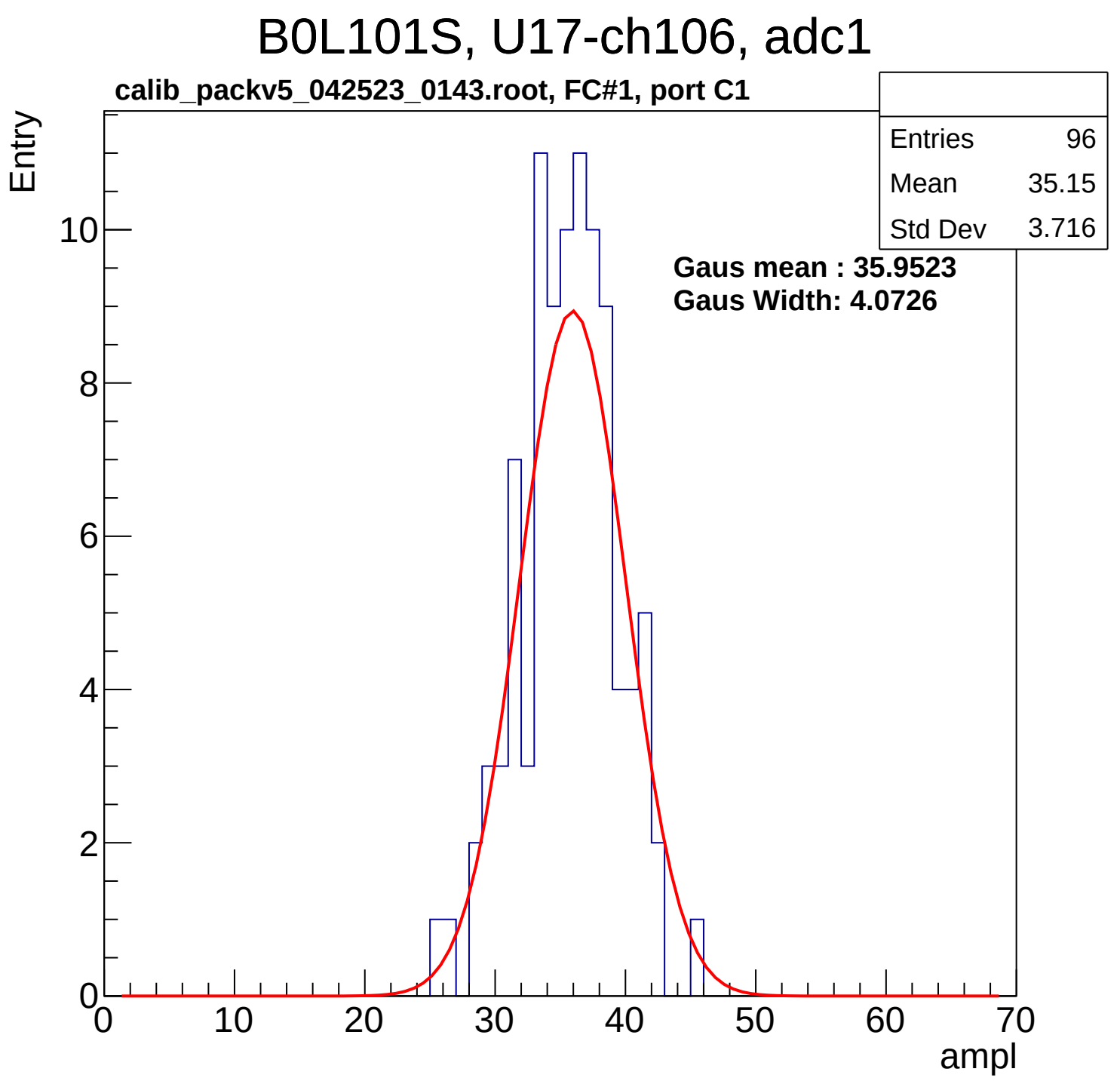

B0L101S, U17-ch105, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 91
Mean 54.84
Std Dev 3.701
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

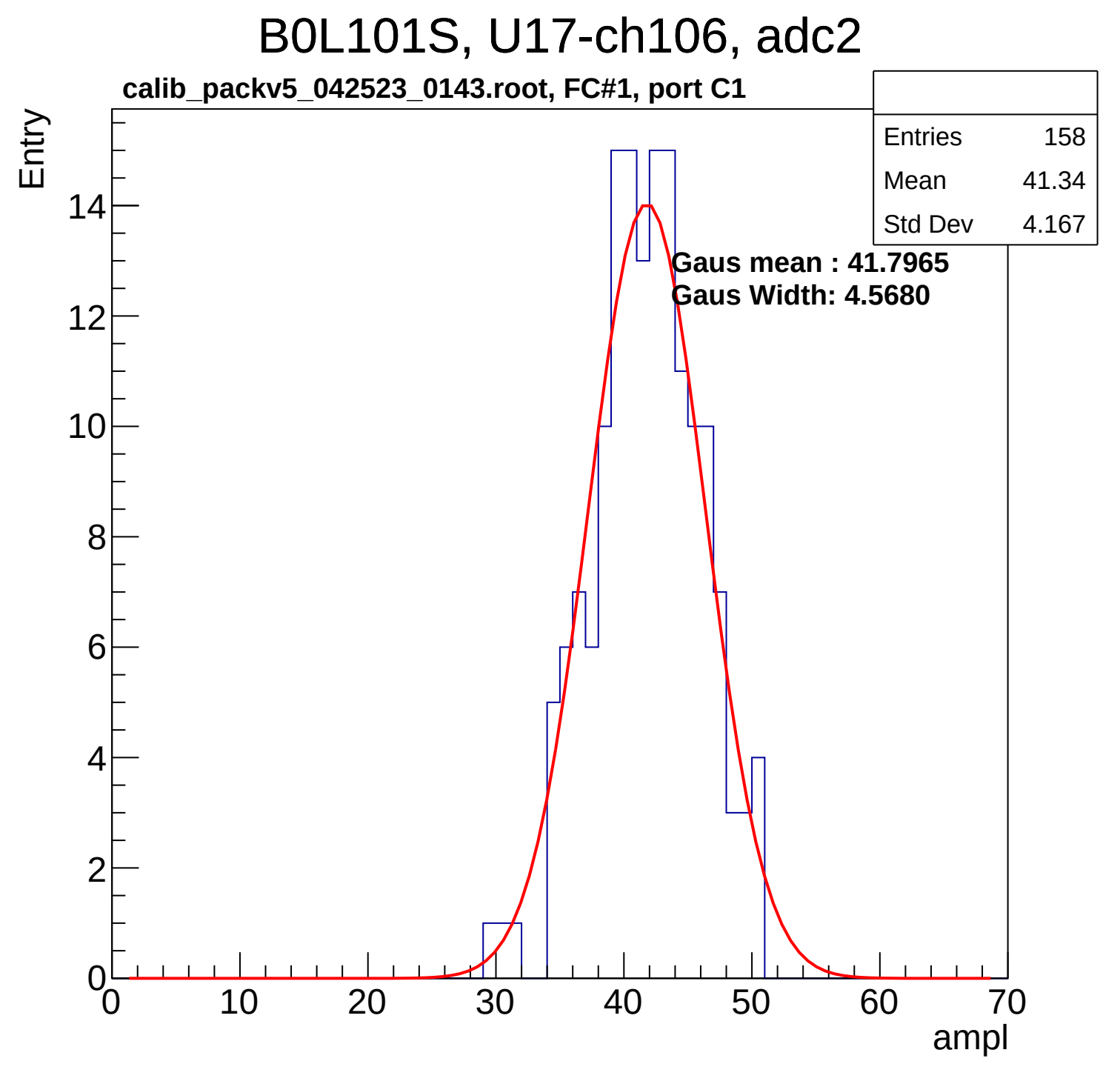

B0L101S, U17-ch105, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 58.32
Std Dev 8.36
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

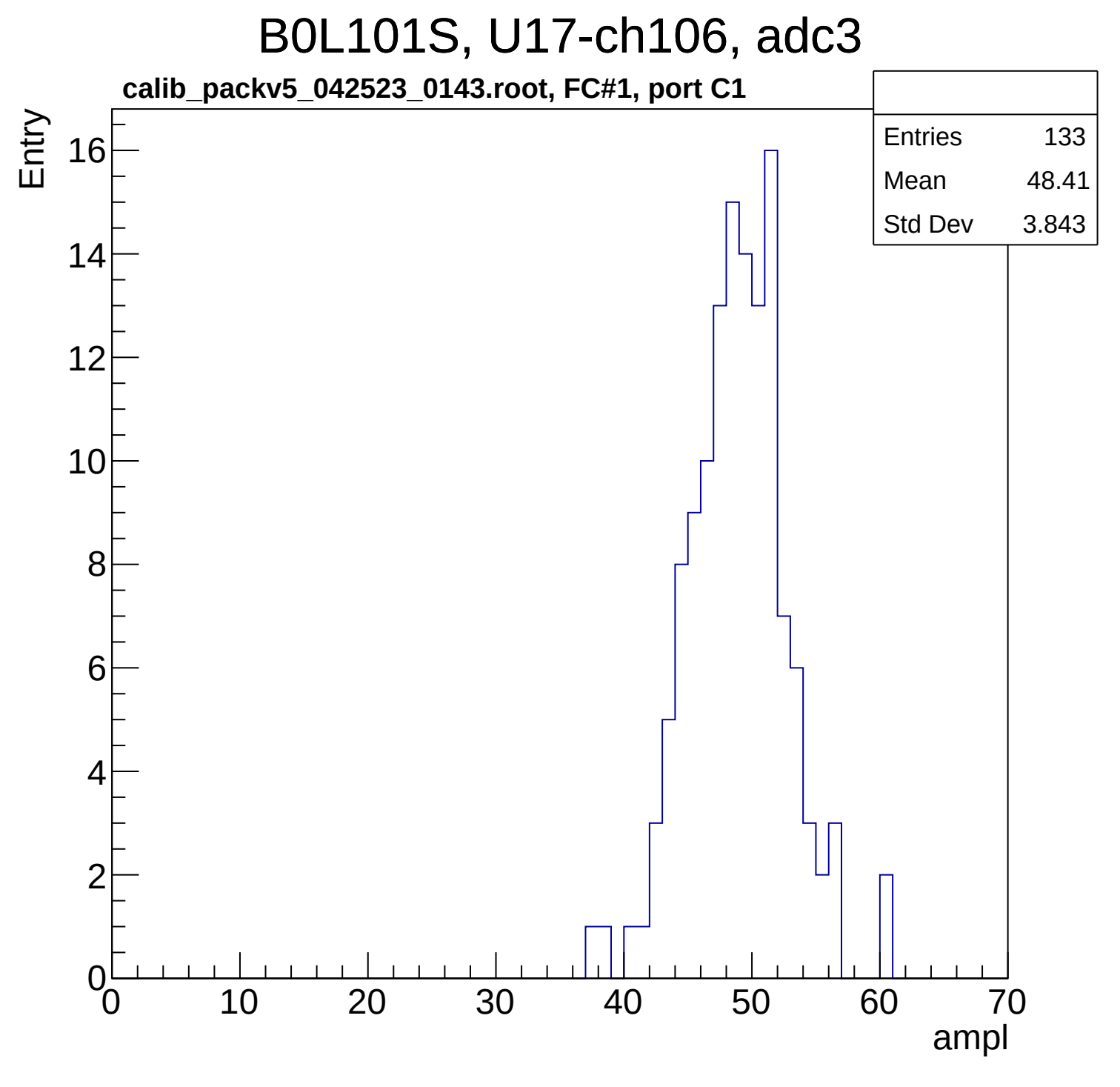

B0L101S, U17-ch105, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 61.5
Std Dev 1.607
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

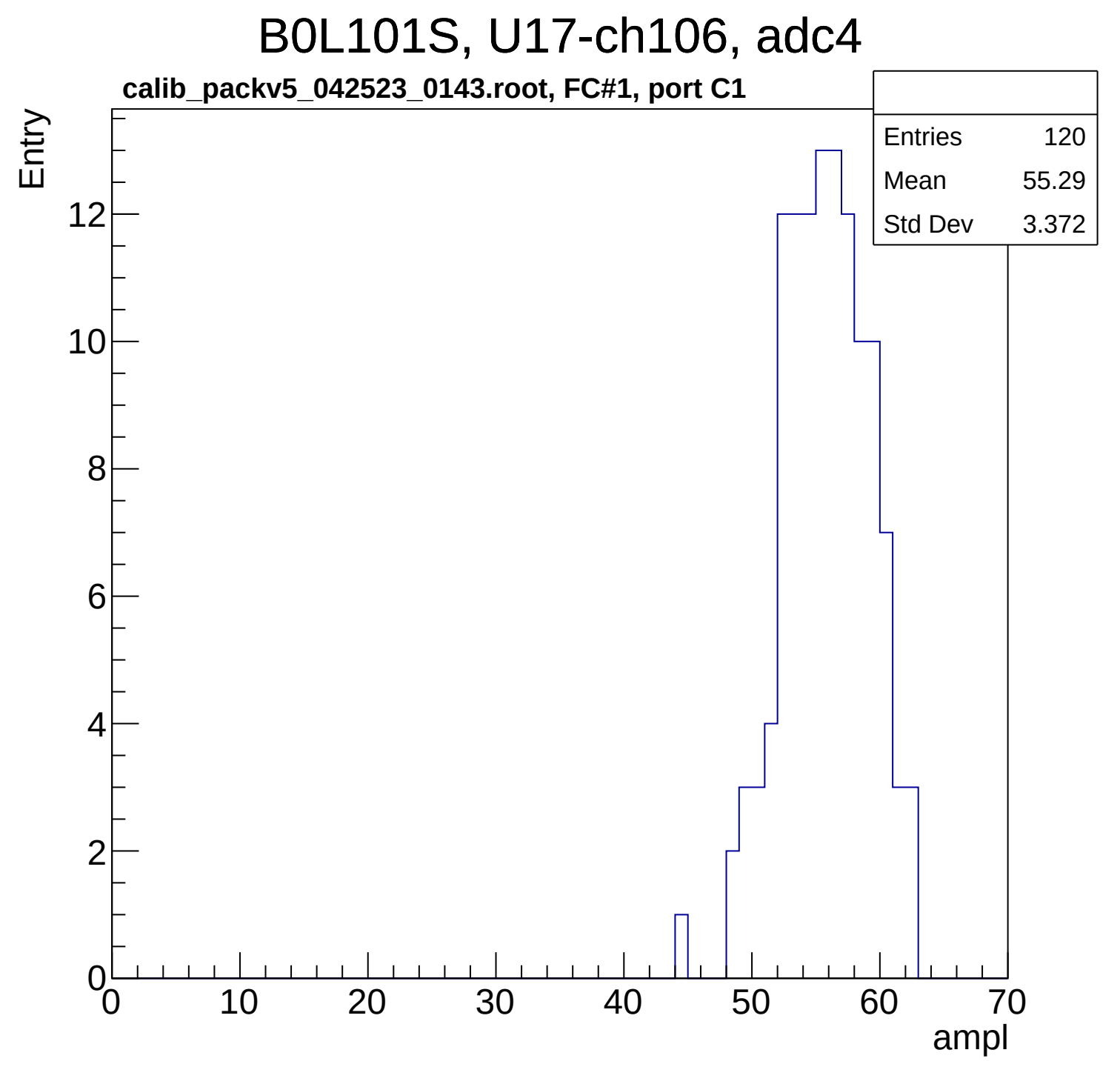

B0L101S, U17-ch105, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

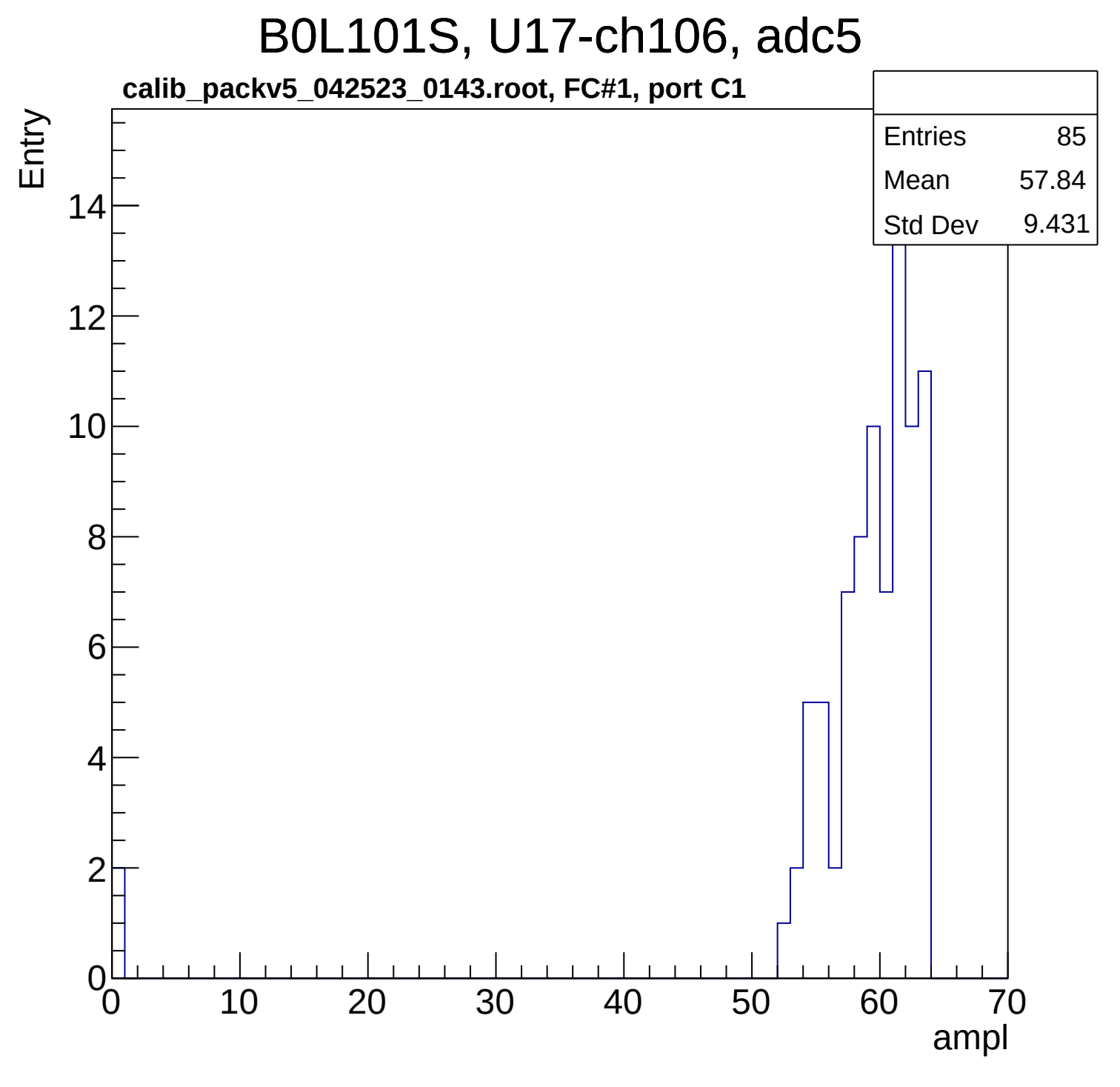

B0L101S, U17-ch106, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 29.29
Std Dev 3.882
Gaus mean : 29.8608
Gaus Width: 3.6562
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

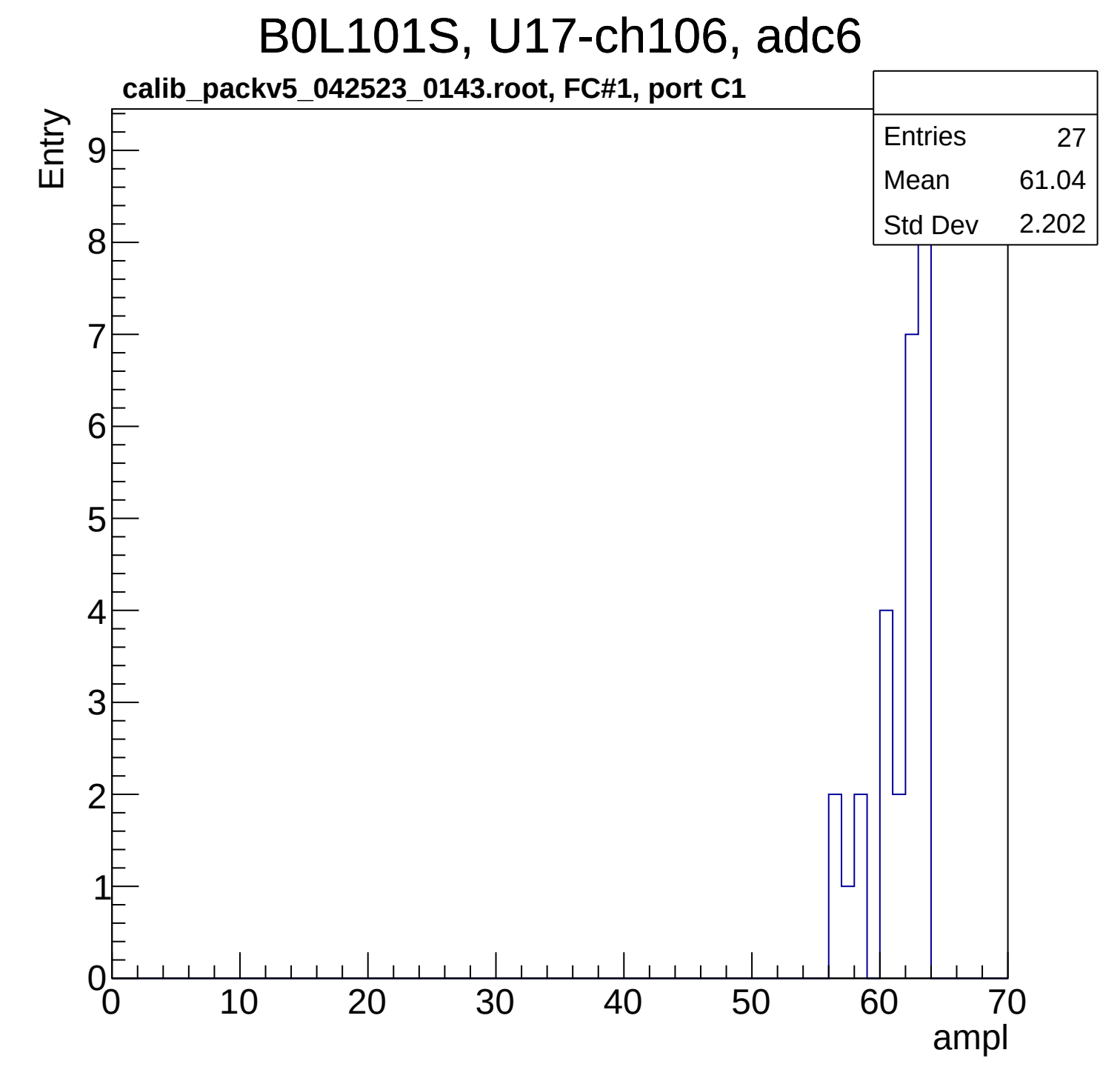

B0L101S, U17-ch106, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 35.15
Std Dev 3.716
Gaus mean : 35.9523
Gaus Width: 4.0726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

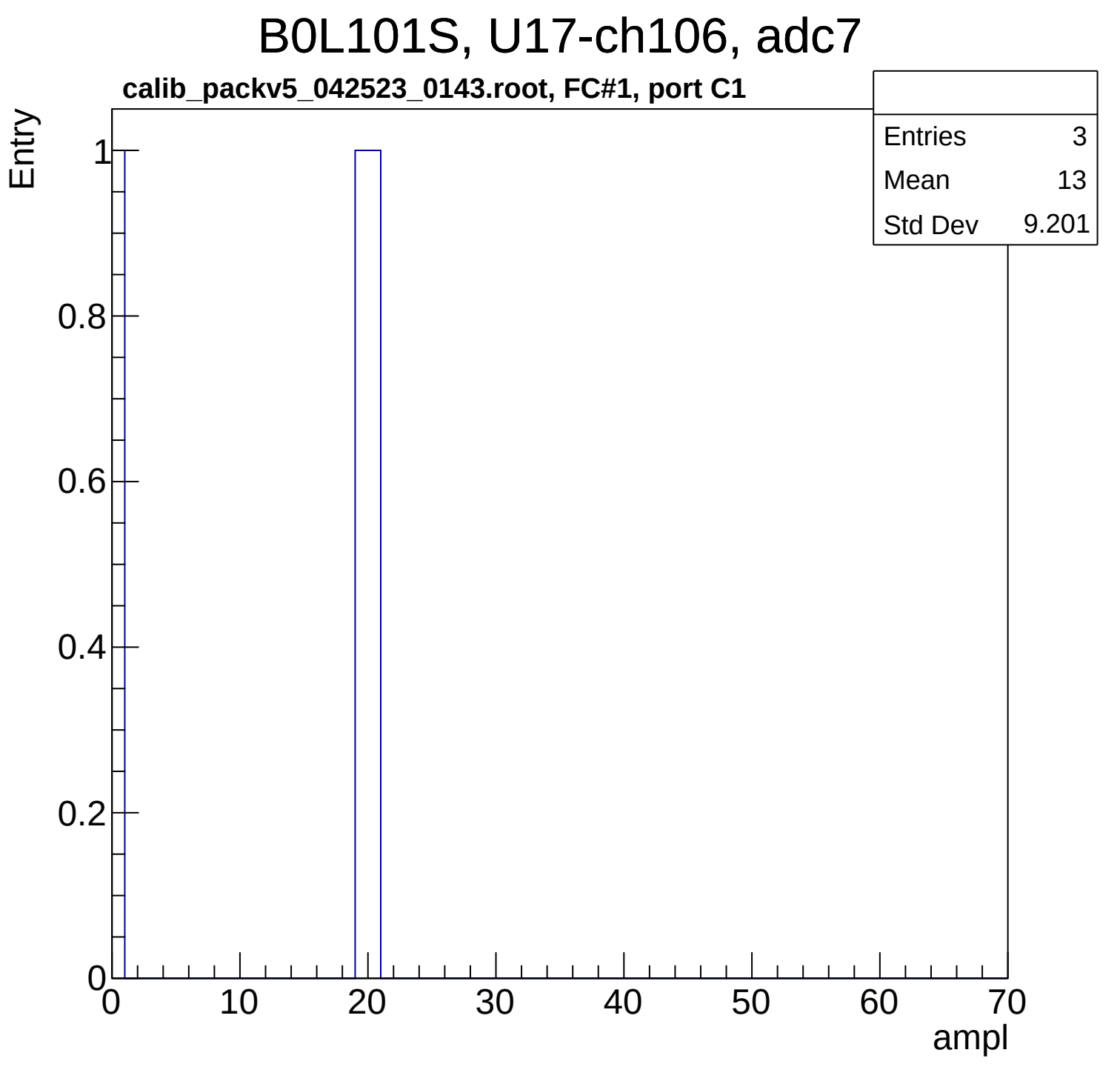

B0L101S, U17-ch106, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 158
Mean 41.34
Std Dev 4.167
Gaus mean : 41.7965
Gaus Width: 4.5680
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

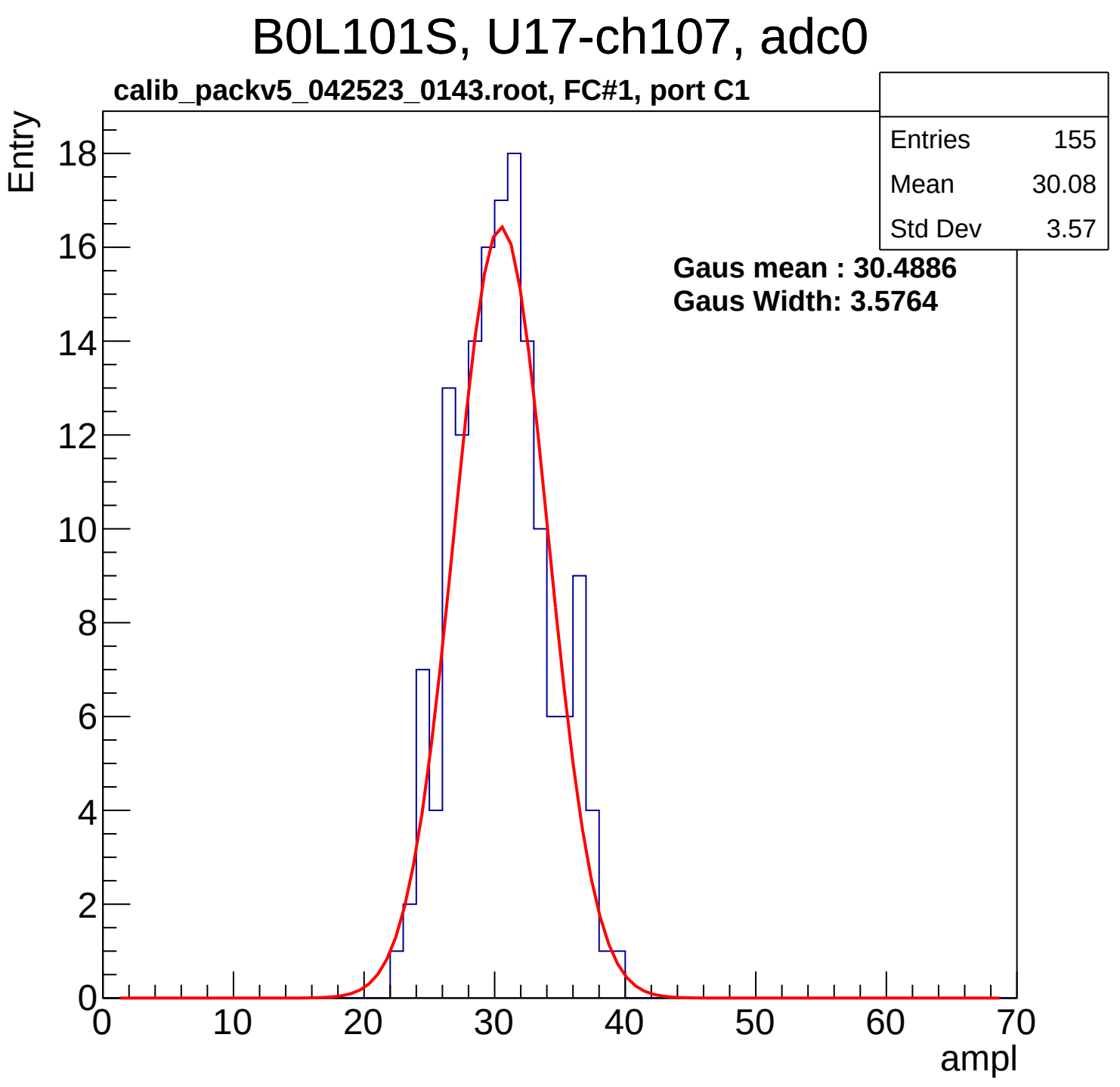

B0L101S, U17-ch106, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 48.41
Std Dev 3.843
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

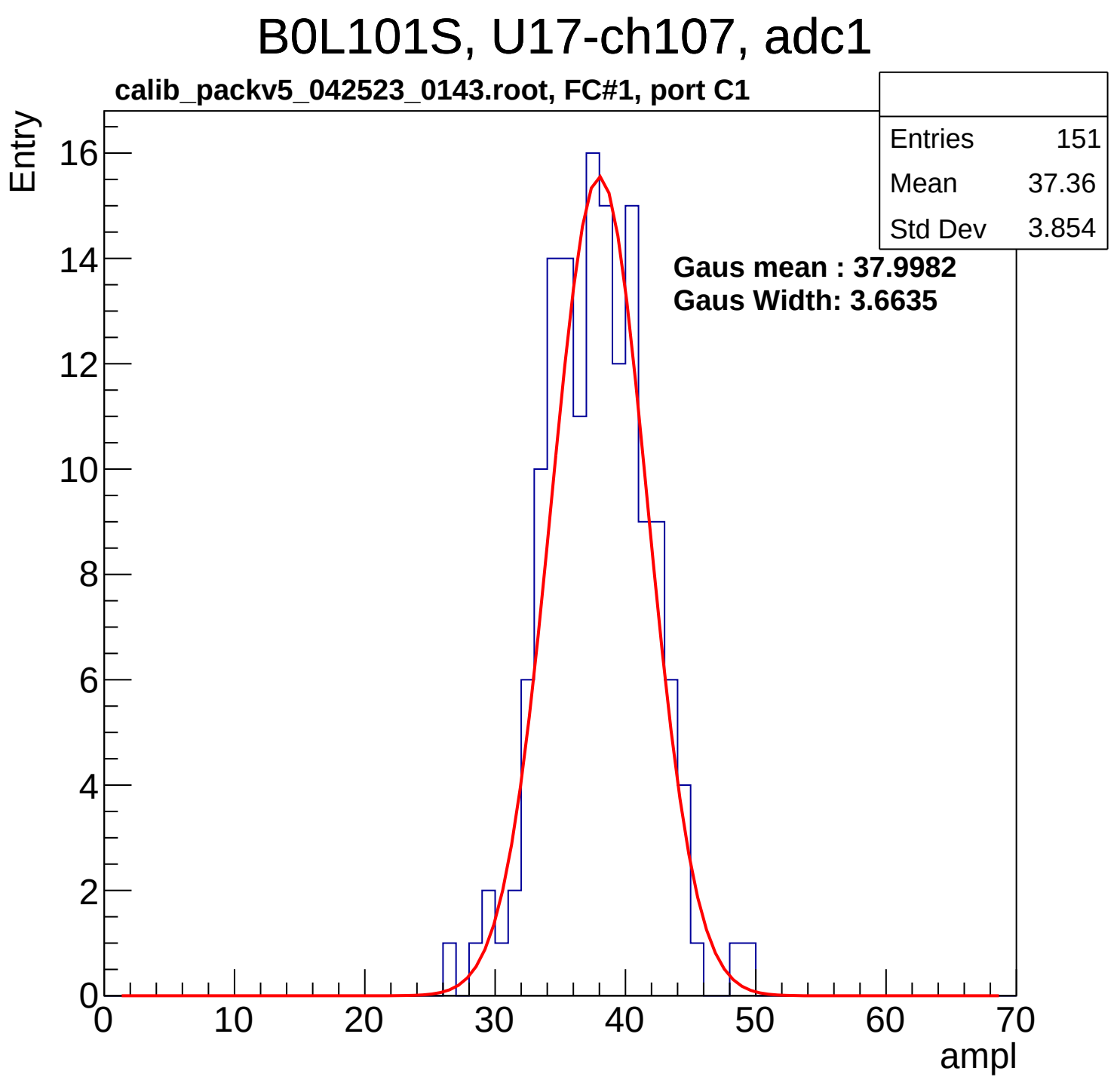

B0L101S, U17-ch106, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 55.29
Std Dev 3.372
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

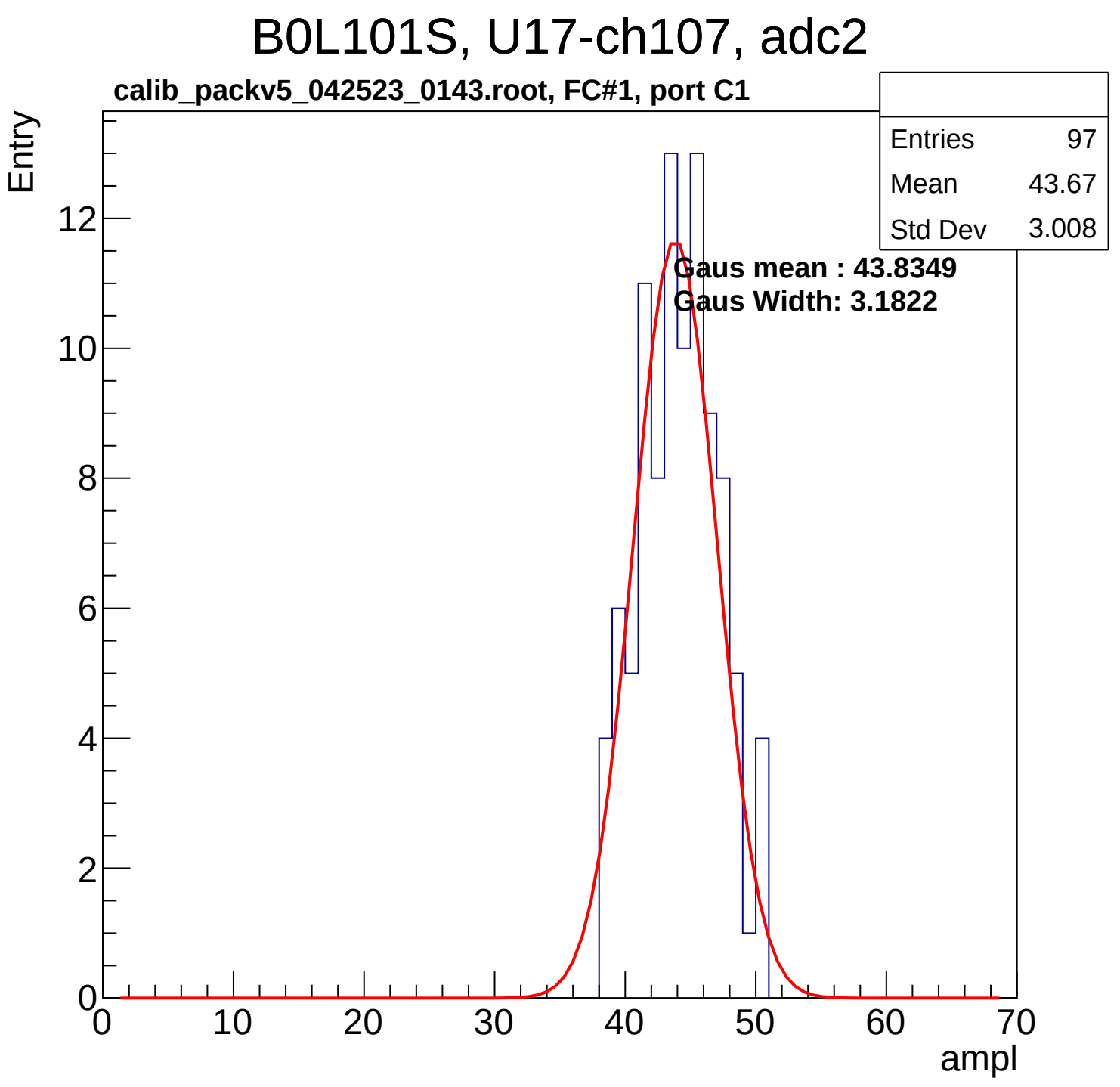

B0L101S, U17-ch106, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 85
Mean 57.84
Std Dev 9.431
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

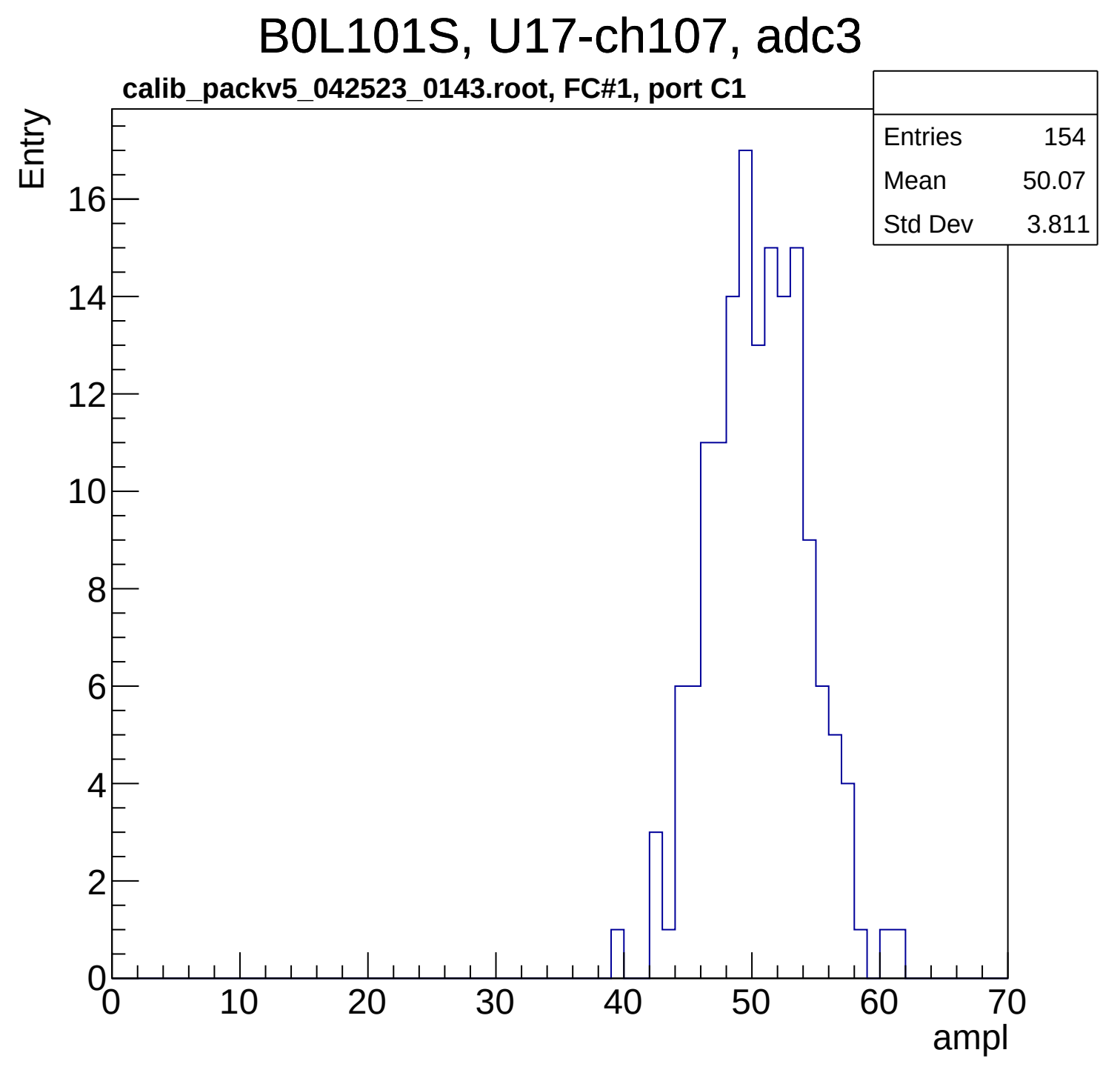

B0L101S, U17-ch106, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 27
Mean 61.04
Std Dev 2.202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

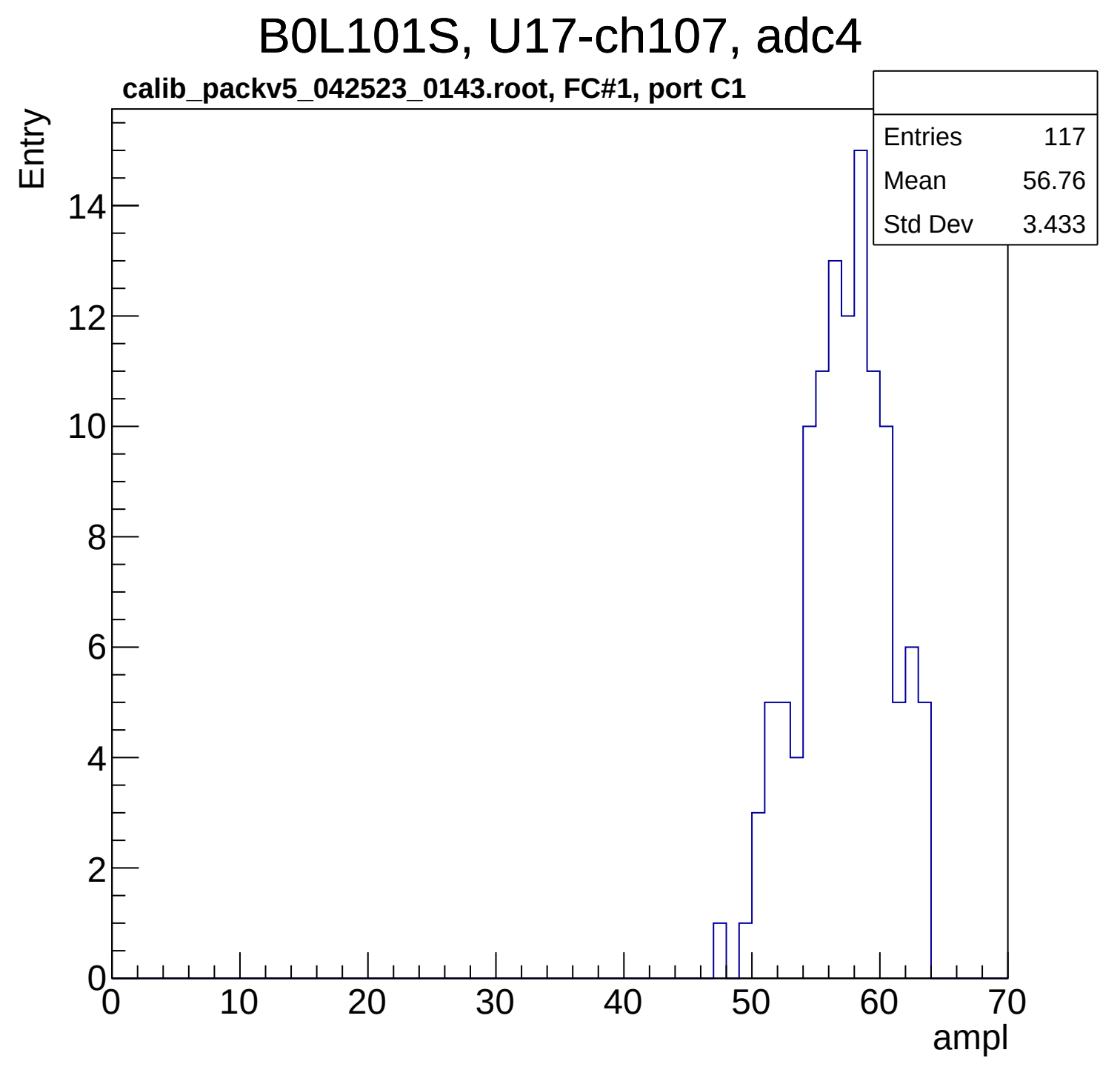

B0L101S, U17-ch106, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 13
Std Dev 9.201
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

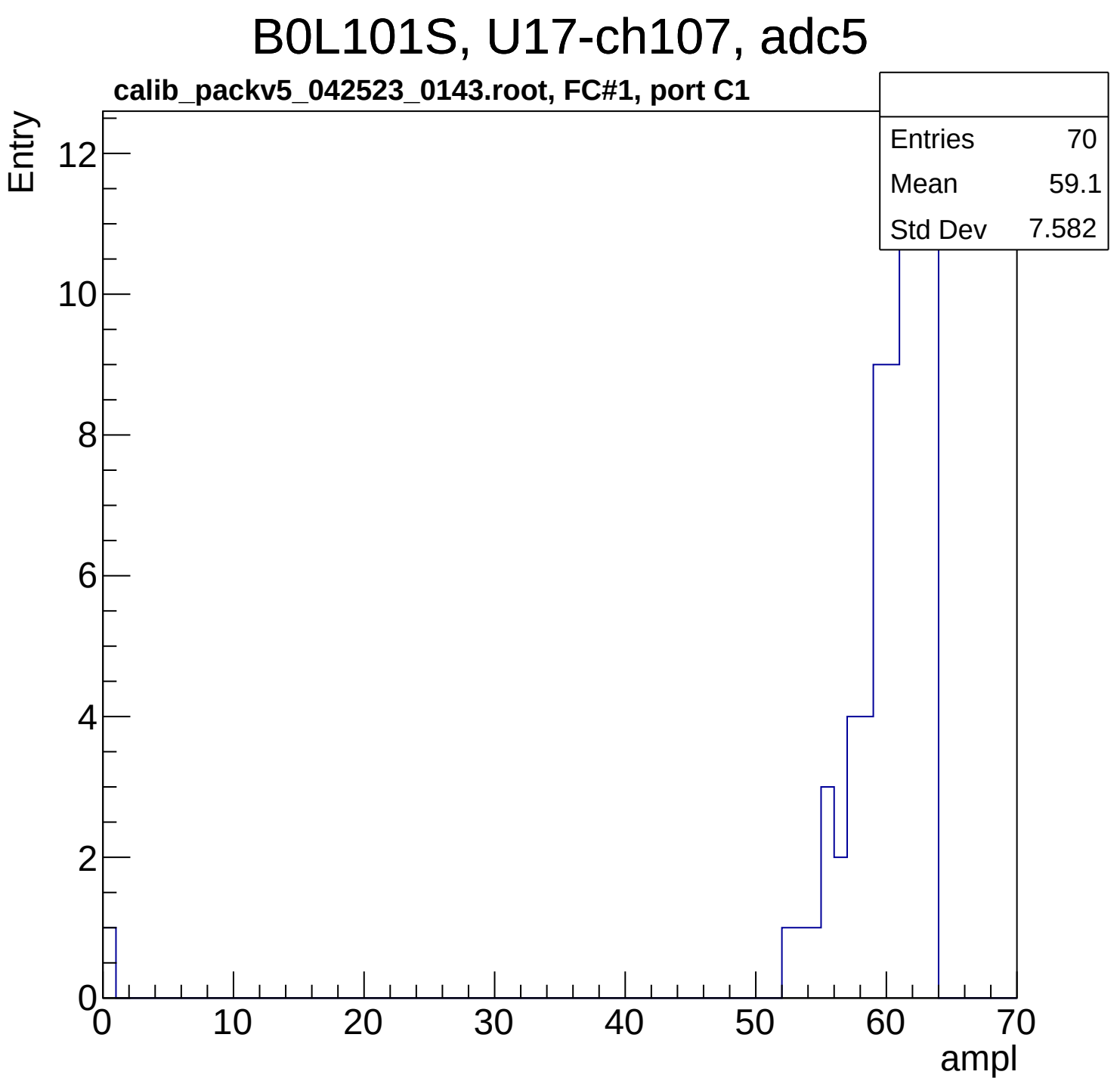

B0L101S, U17-ch107, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 30.08
Std Dev 3.57
Gaus mean : 30.4886
Gaus Width: 3.5764
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

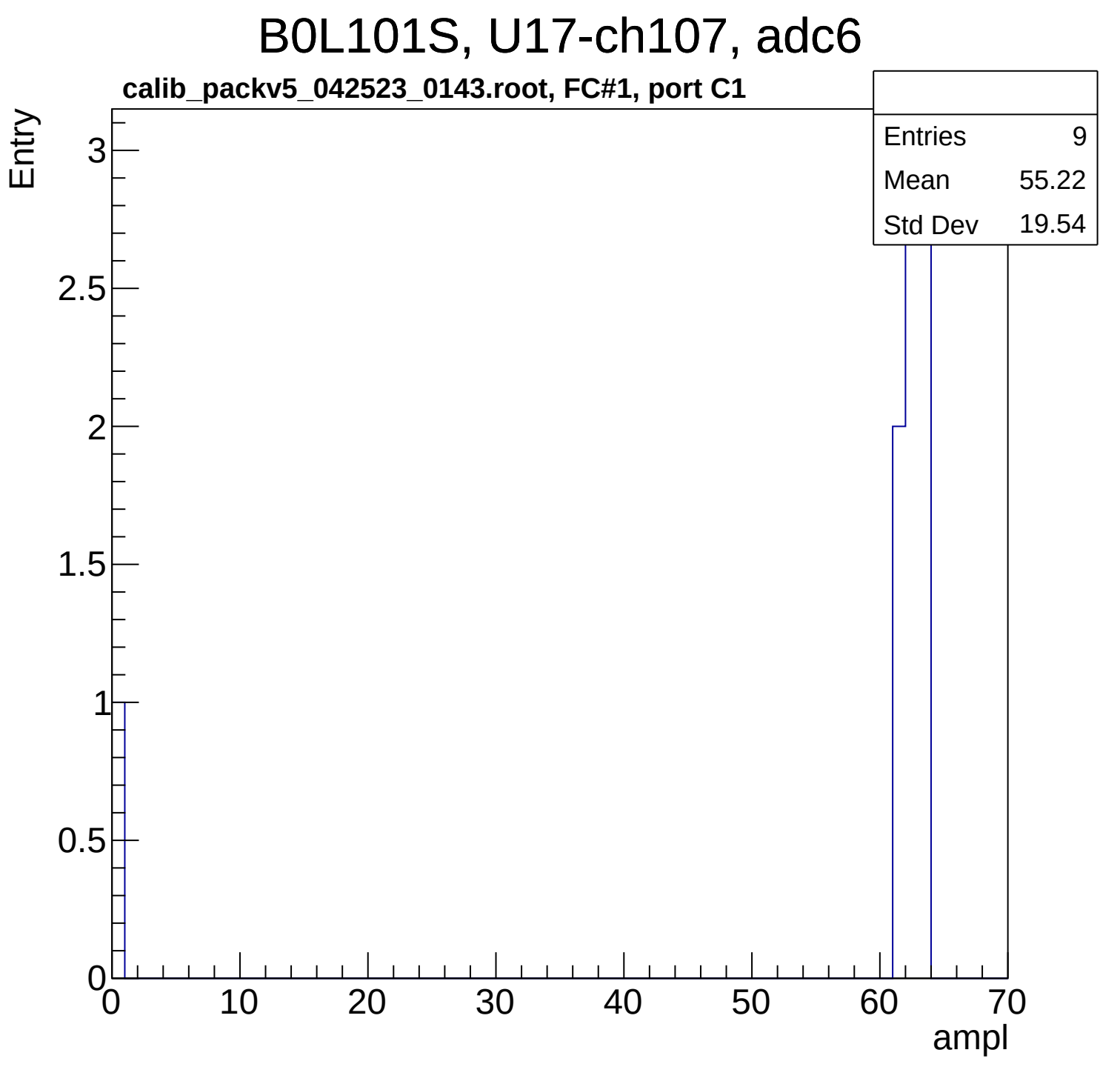

B0L101S, U17-ch107, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 37.36
Std Dev 3.854
Gaus mean : 37.9982
Gaus Width: 3.6635
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

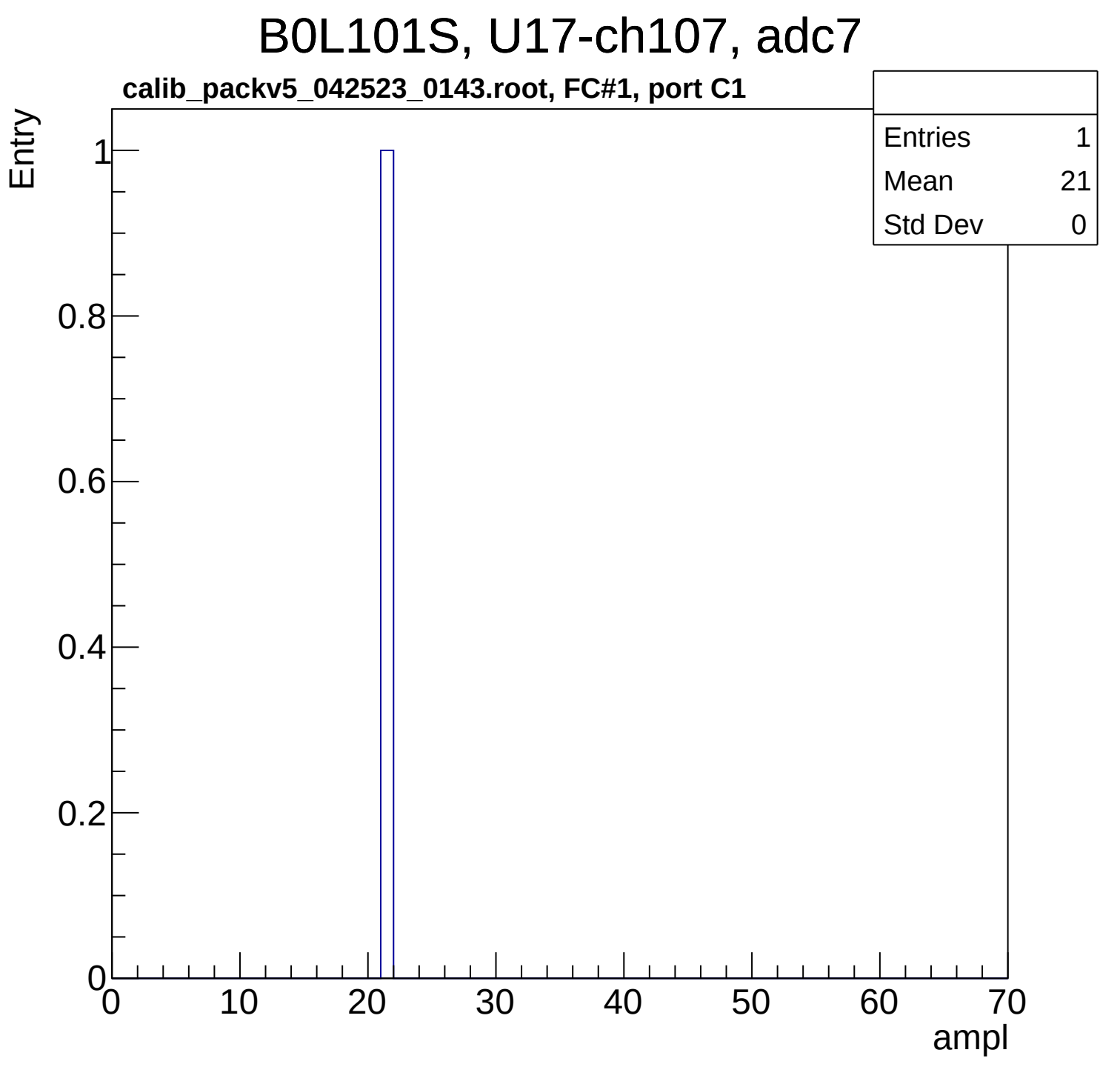

B0L101S, U17-ch107, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 97
Mean 43.67
Std Dev 3.008
Gaus mean : 43.8349
Gaus Width: 3.1822
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

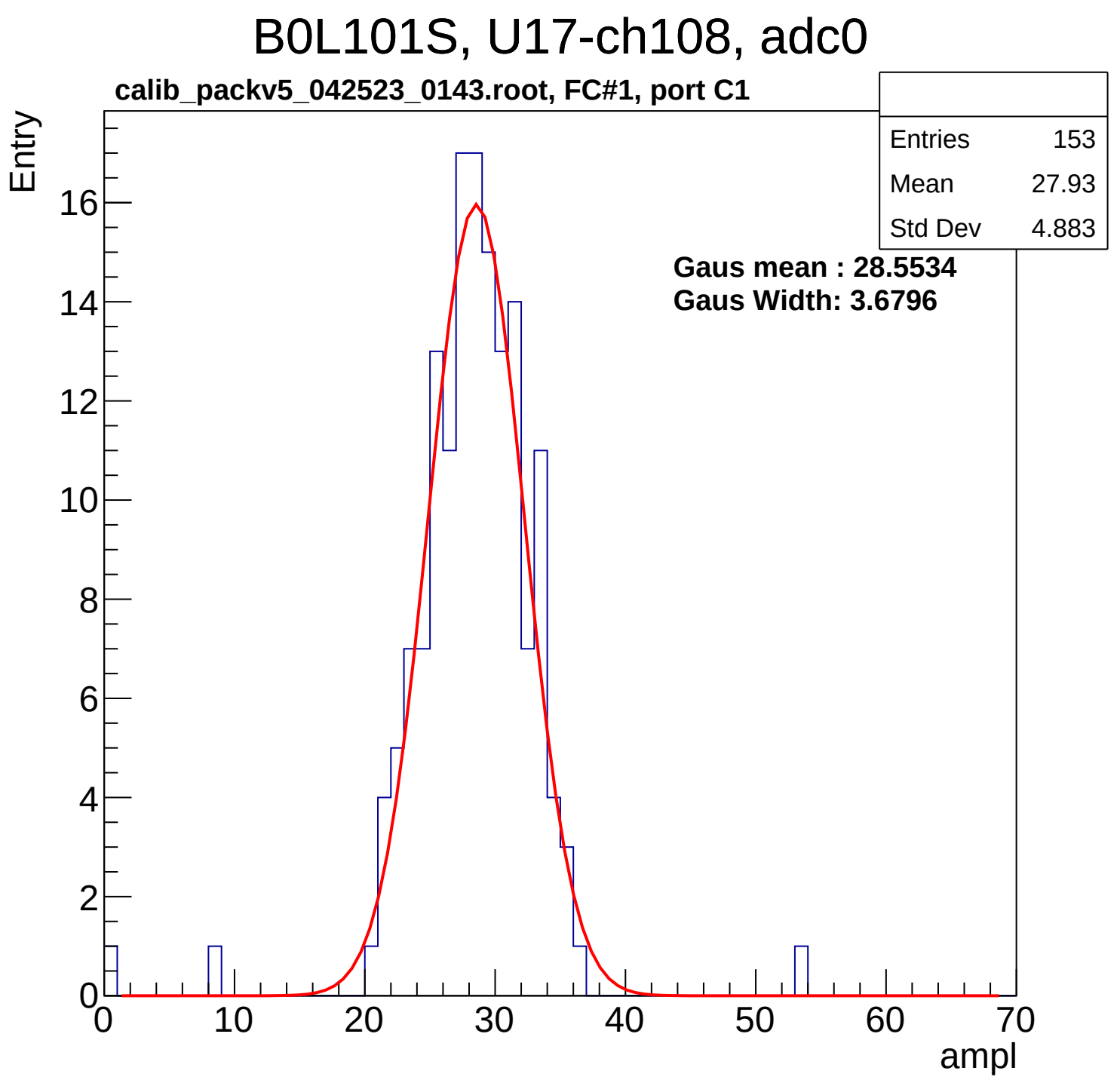

B0L101S, U17-ch107, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 50.07
Std Dev 3.811
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

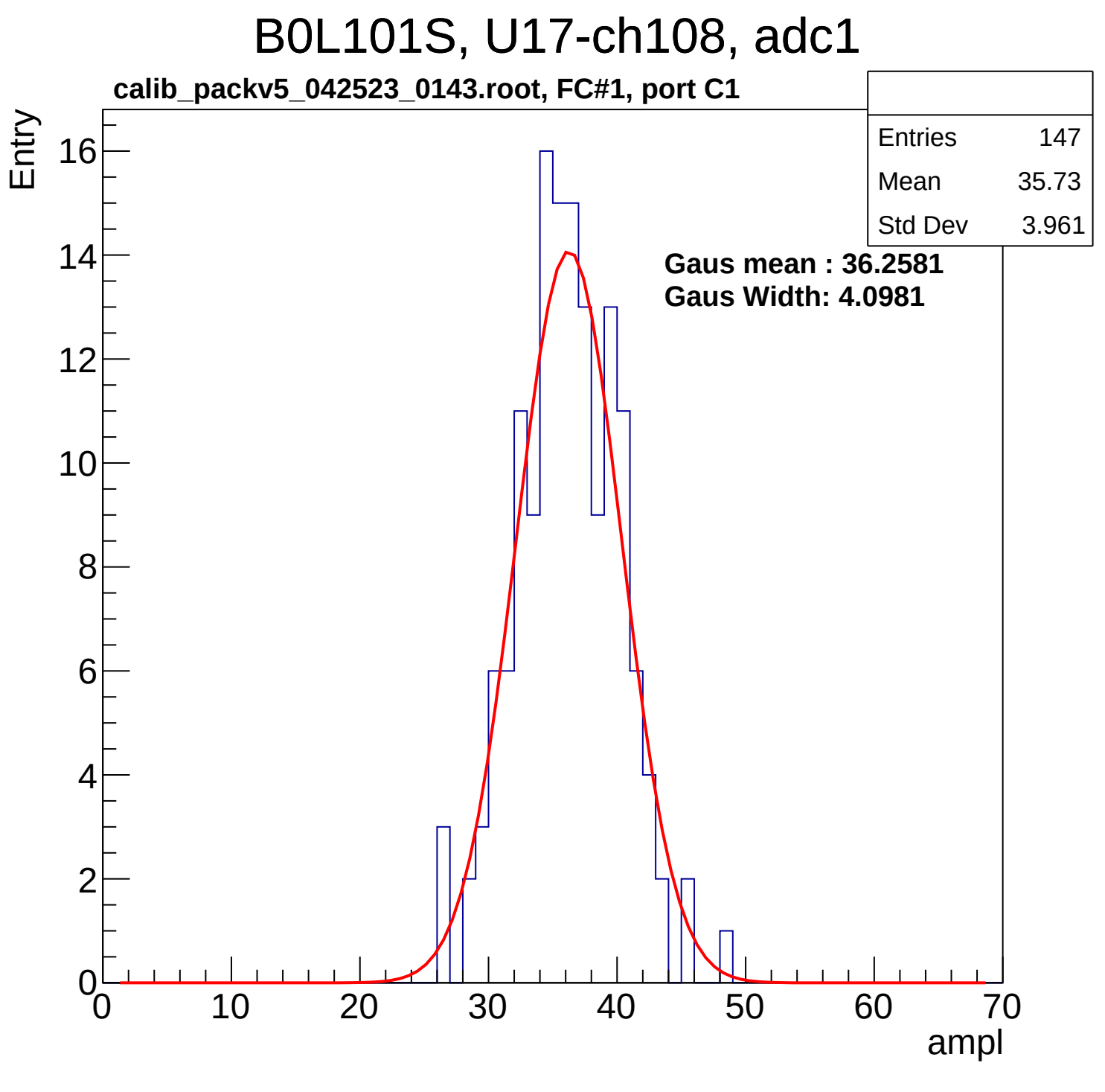

B0L101S, U17-ch107, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 56.76
Std Dev 3.433
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

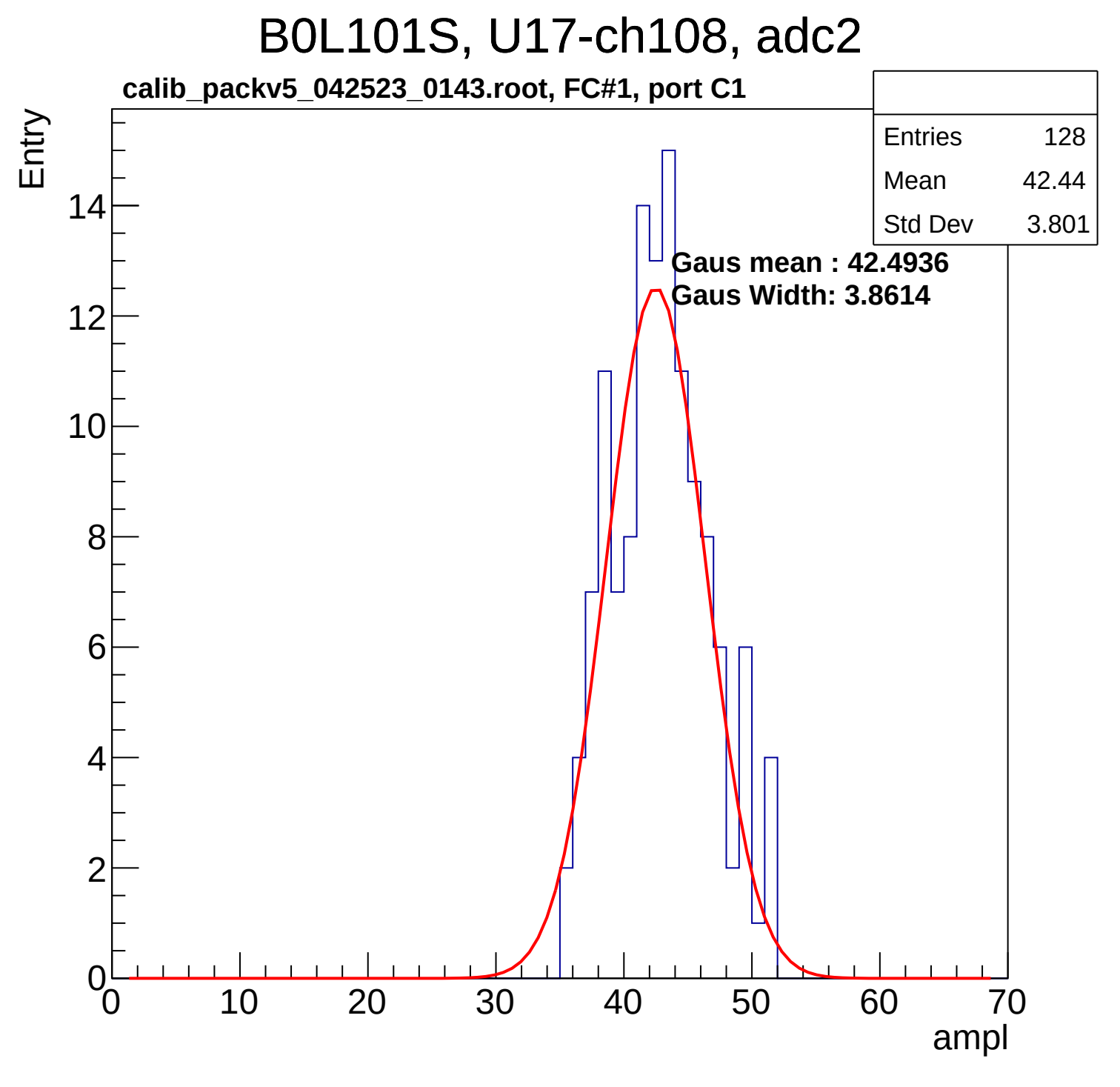

B0L101S, U17-ch107, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 70
Mean 59.1
Std Dev 7.582
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

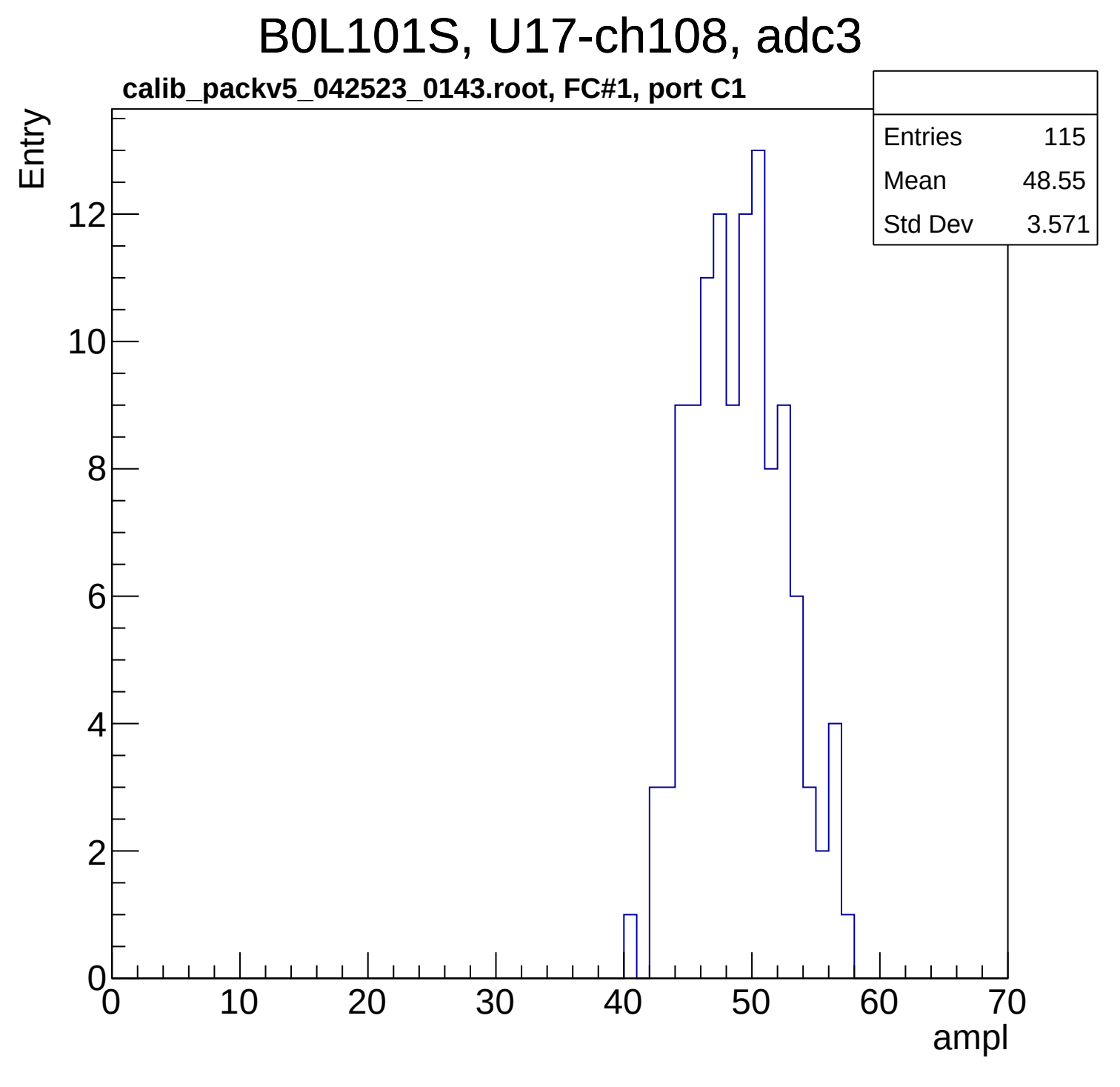

B0L101S, U17-ch107, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 9
Mean 55.22
Std Dev 19.54
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

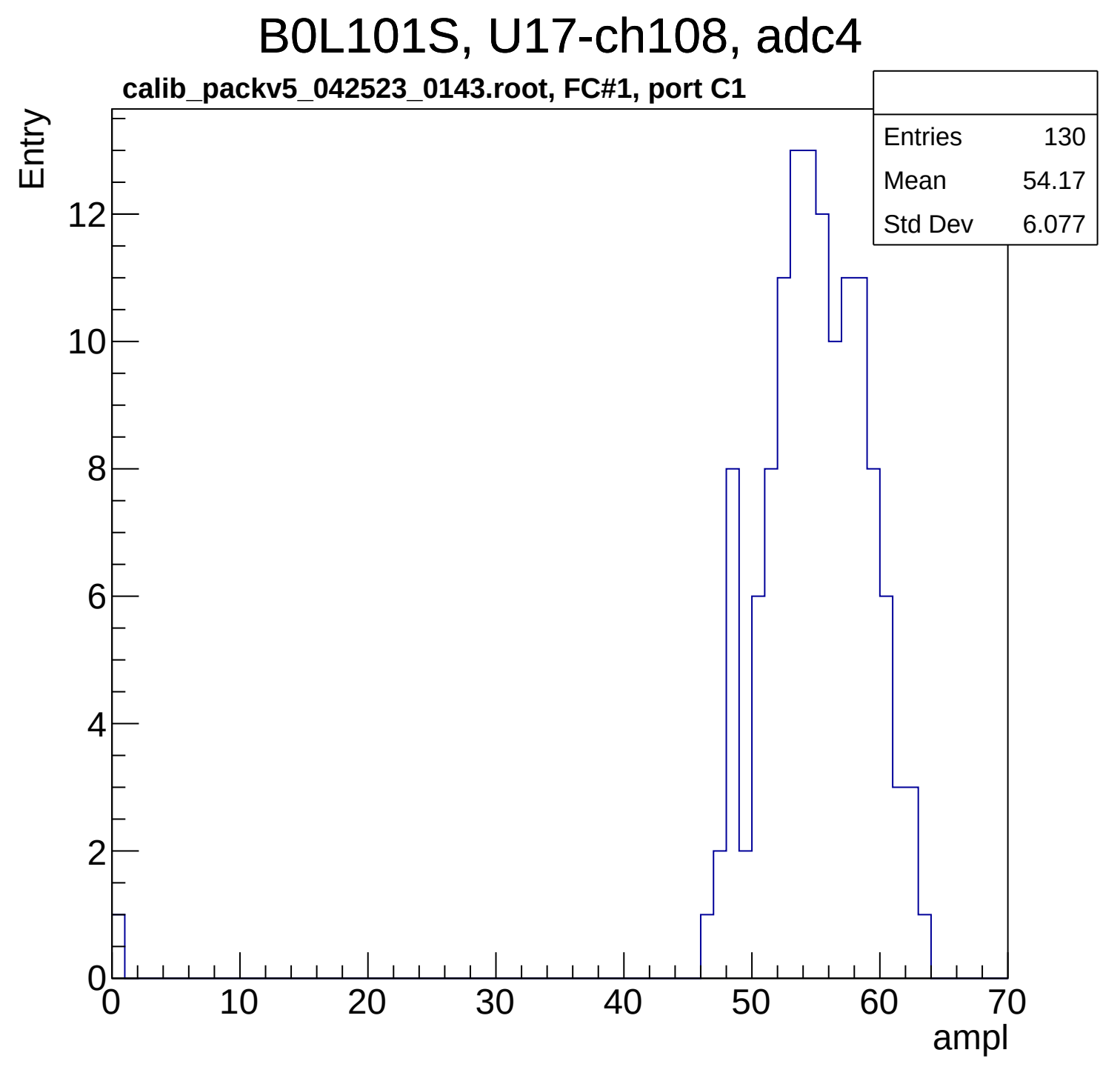

B0L101S, U17-ch107, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

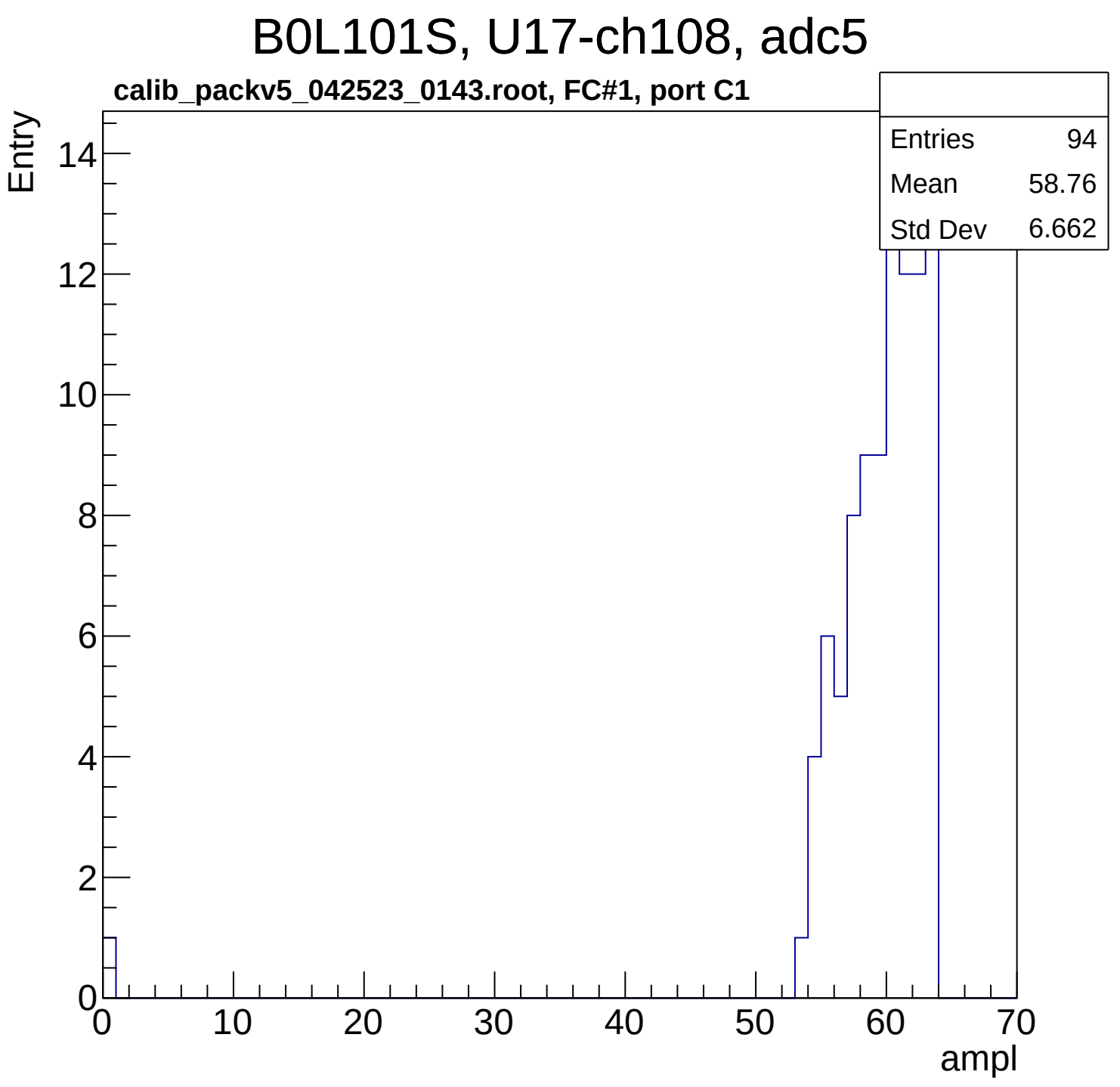

B0L101S, U17-ch108, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 153
Mean 27.93
Std Dev 4.883
Gaus mean : 28.5534
Gaus Width: 3.6796
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

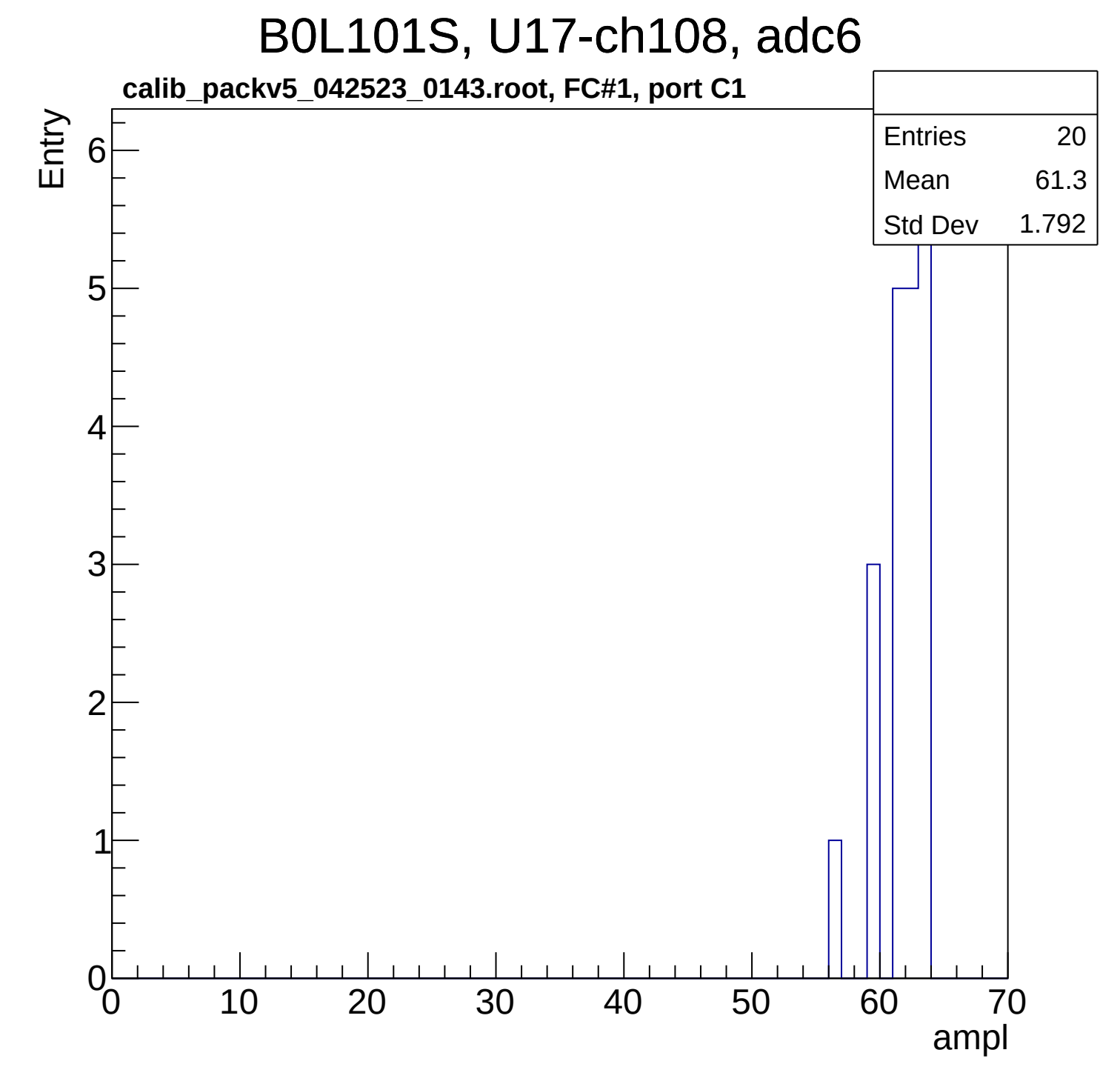

B0L101S, U17-ch108, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 35.73
Std Dev 3.961
Gaus mean : 36.2581
Gaus Width: 4.0981
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

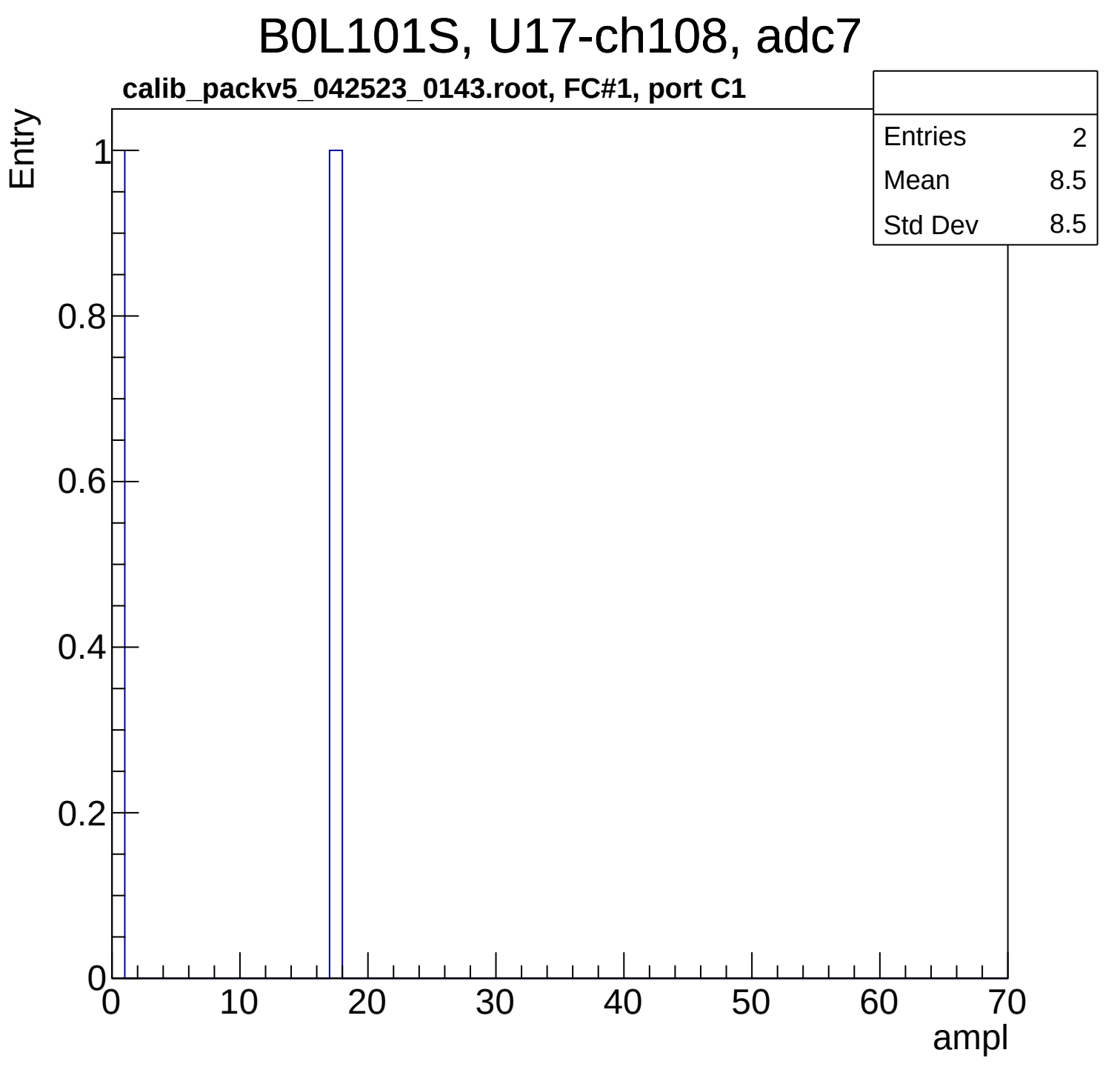

B0L101S, U17-ch108, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 42.44
Std Dev 3.801
Gaus mean : 42.4936
Gaus Width: 3.8614
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

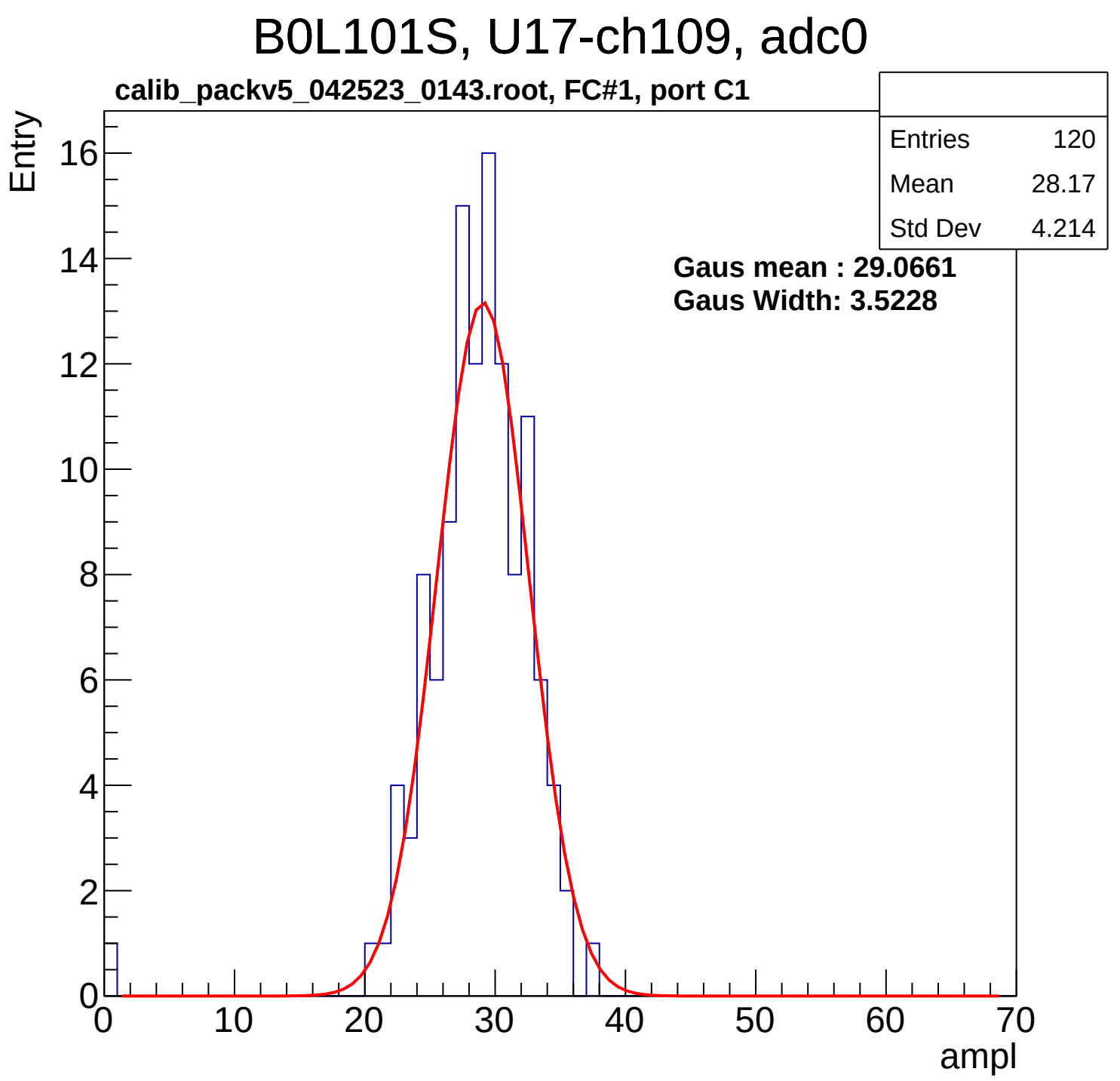

B0L101S, U17-ch108, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 48.55
Std Dev 3.571
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

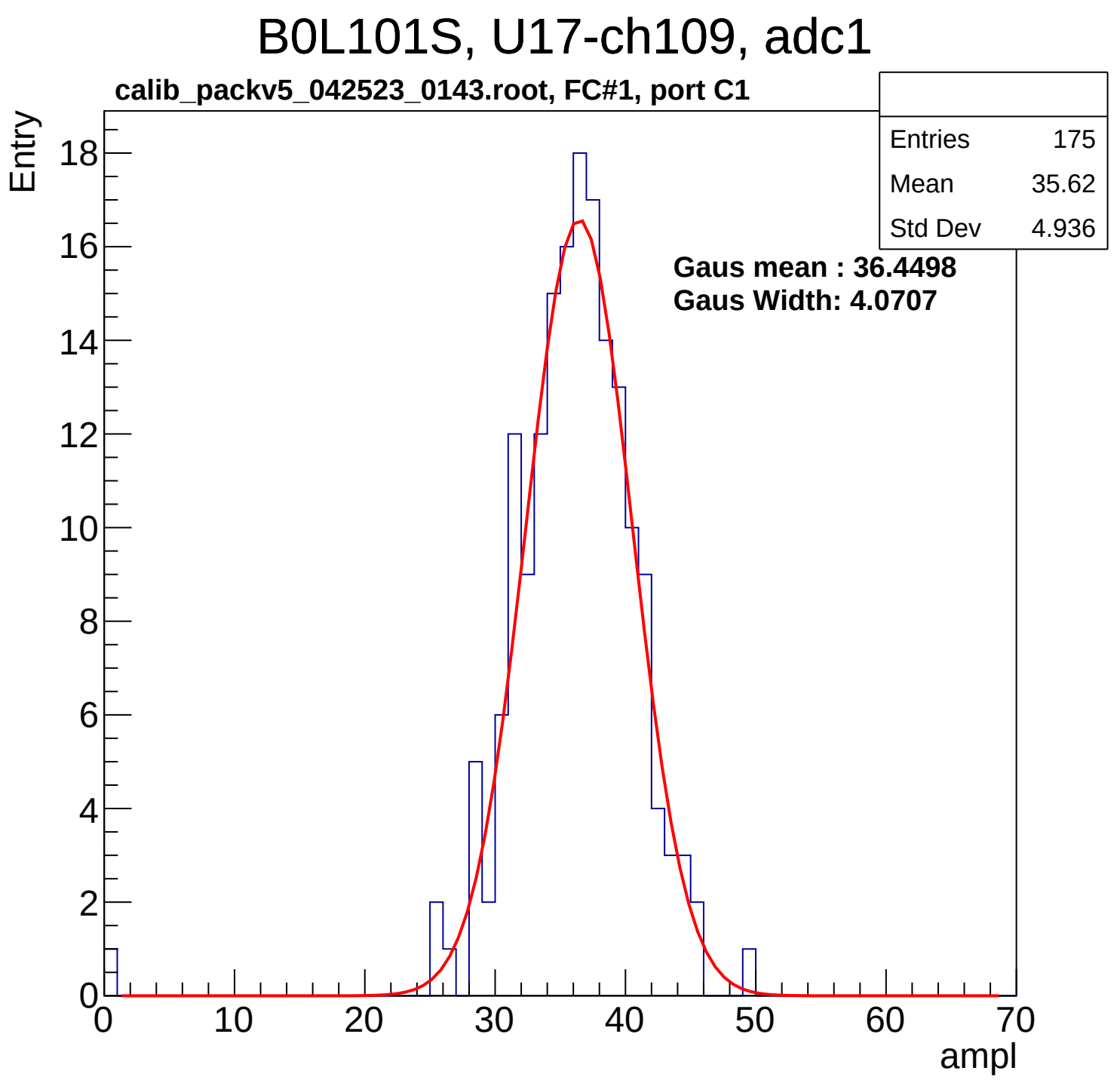

B0L101S, U17-ch108, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 54.17
Std Dev 6.077
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

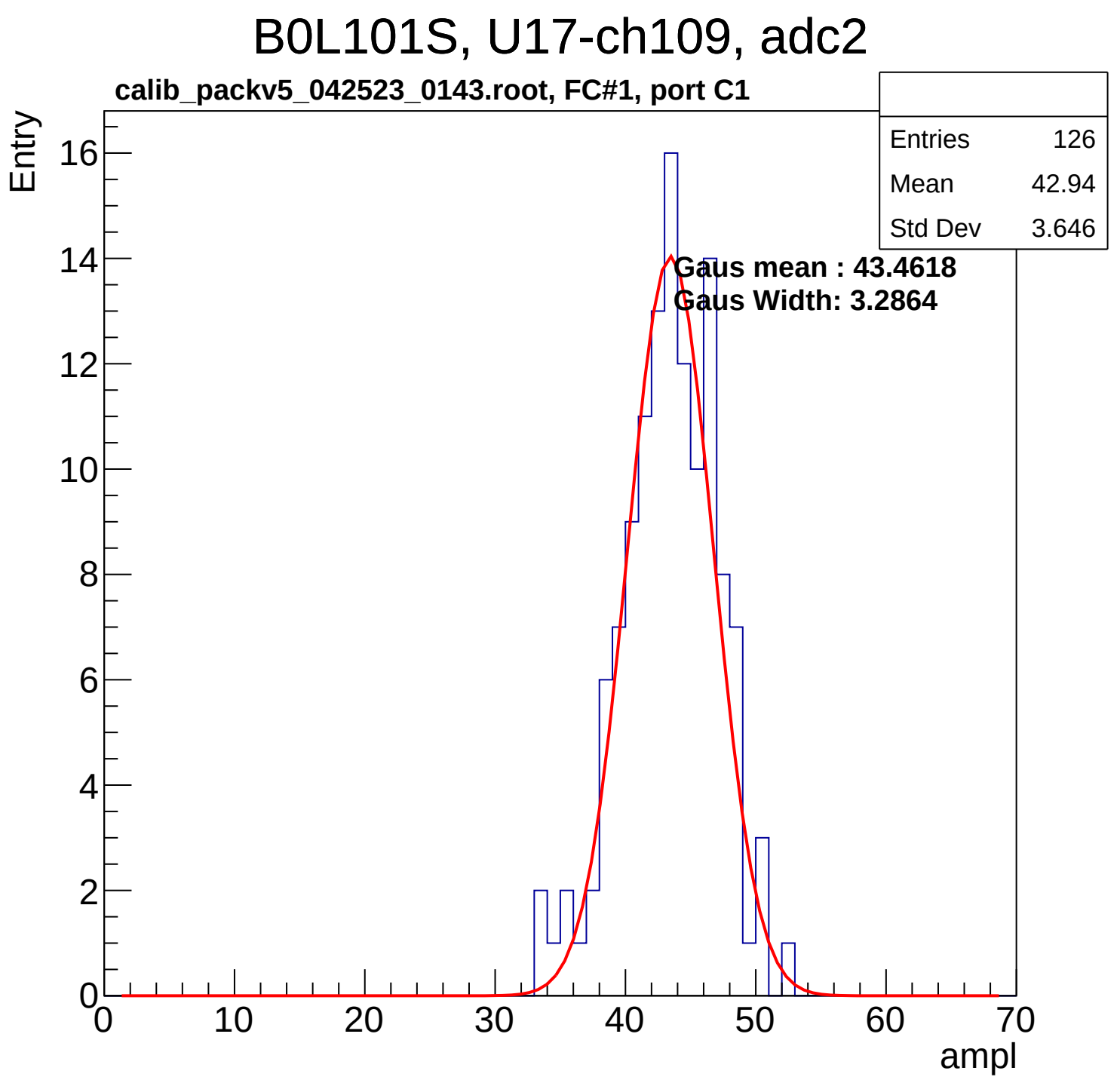

B0L101S, U17-ch108, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 94
Mean 58.76
Std Dev 6.662
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

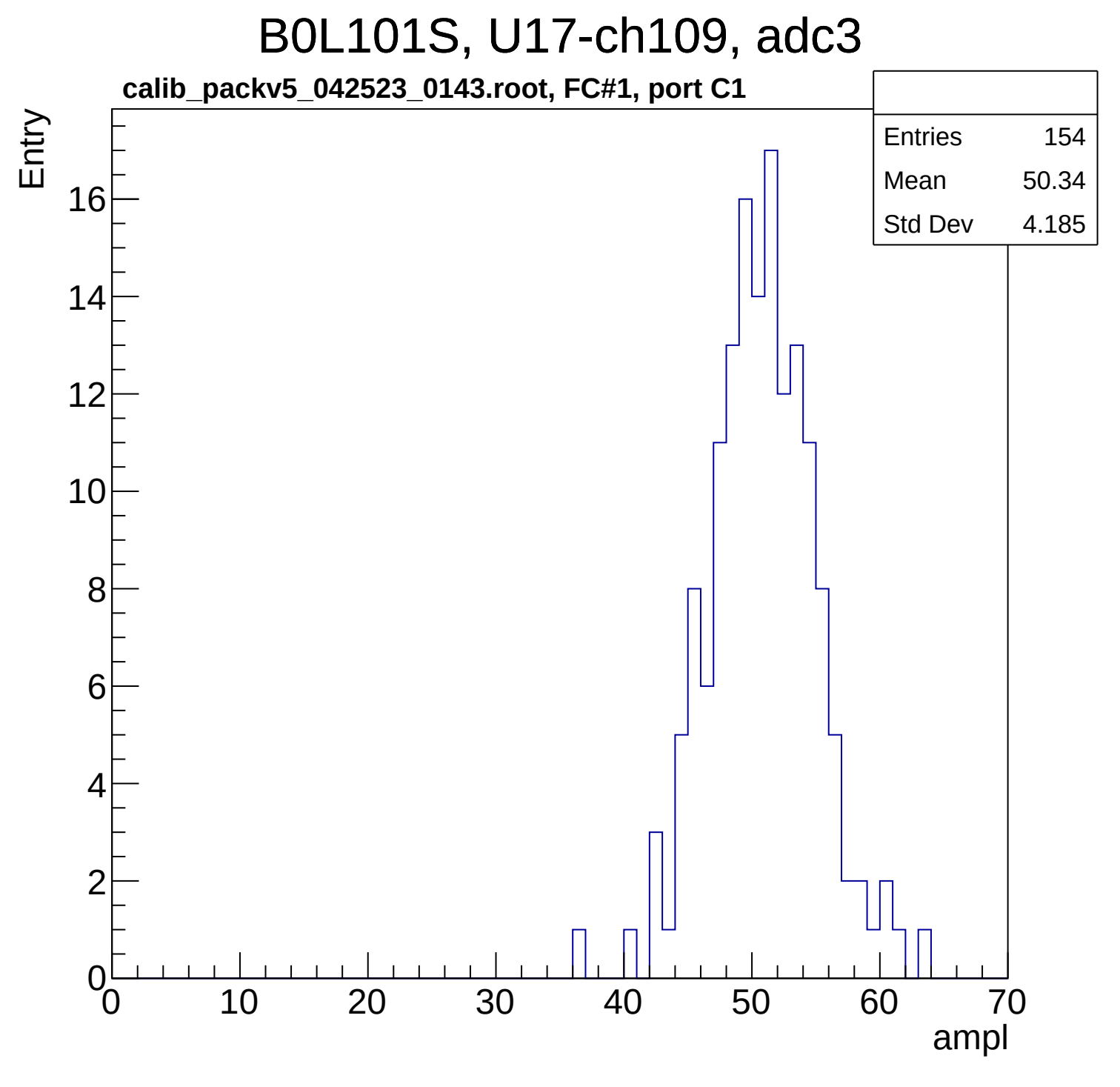

B0L101S, U17-ch108, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 20
Mean 61.3
Std Dev 1.792
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

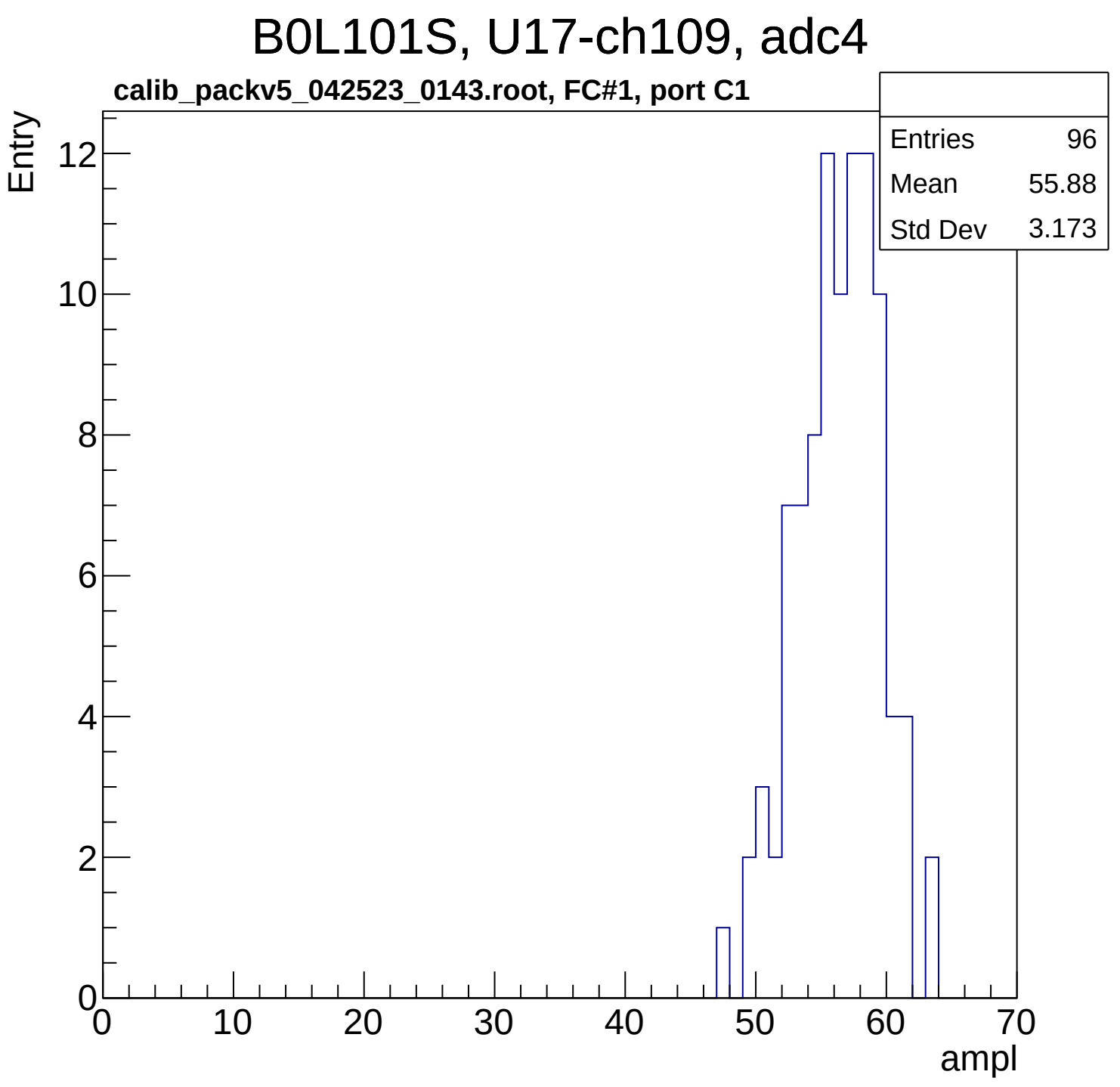

B0L101S, U17-ch108, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 8.5
Std Dev 8.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

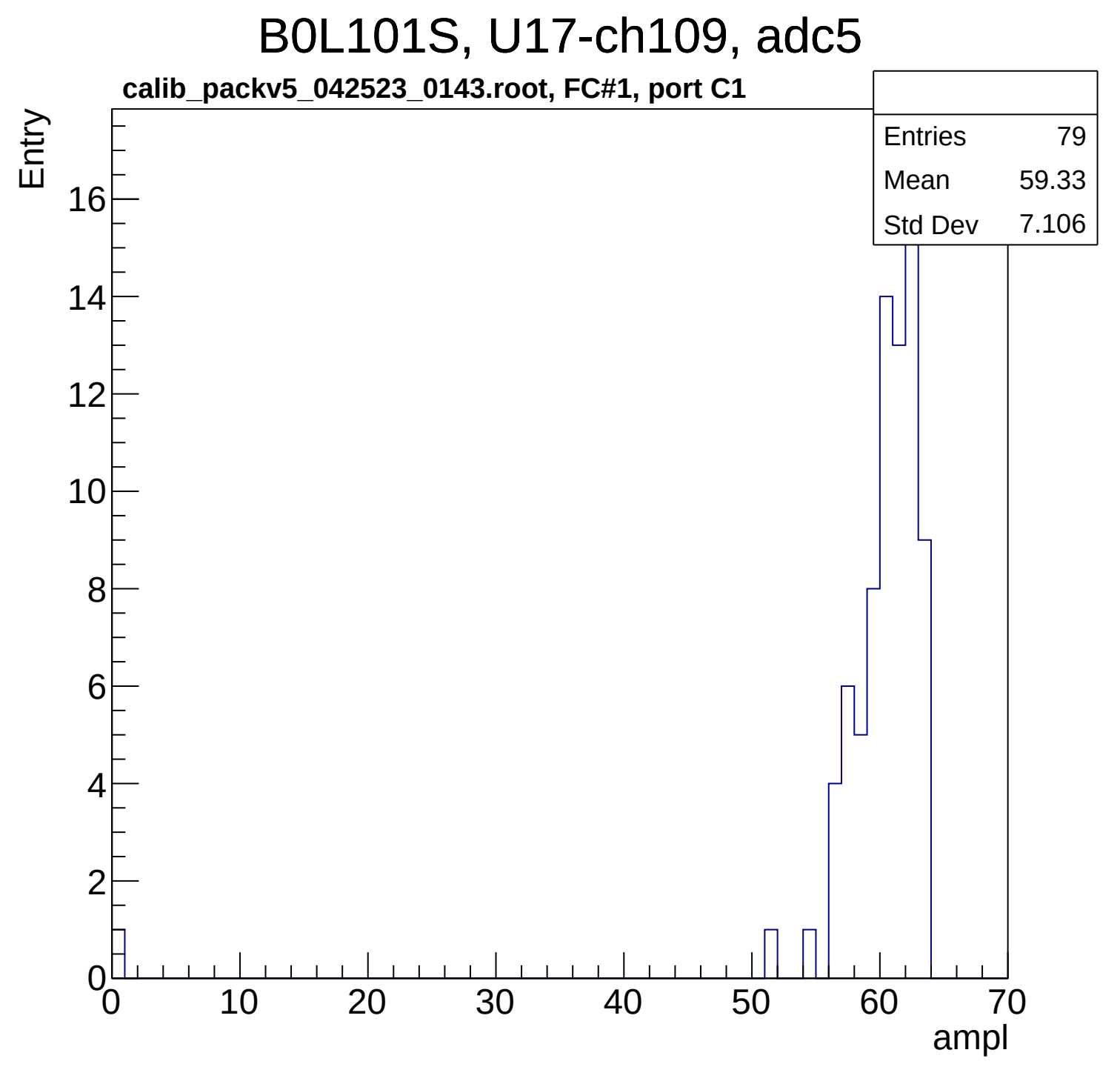

B0L101S, U17-ch109, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 28.17
Std Dev 4.214
Gaus mean : 29.0661
Gaus Width: 3.5228
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

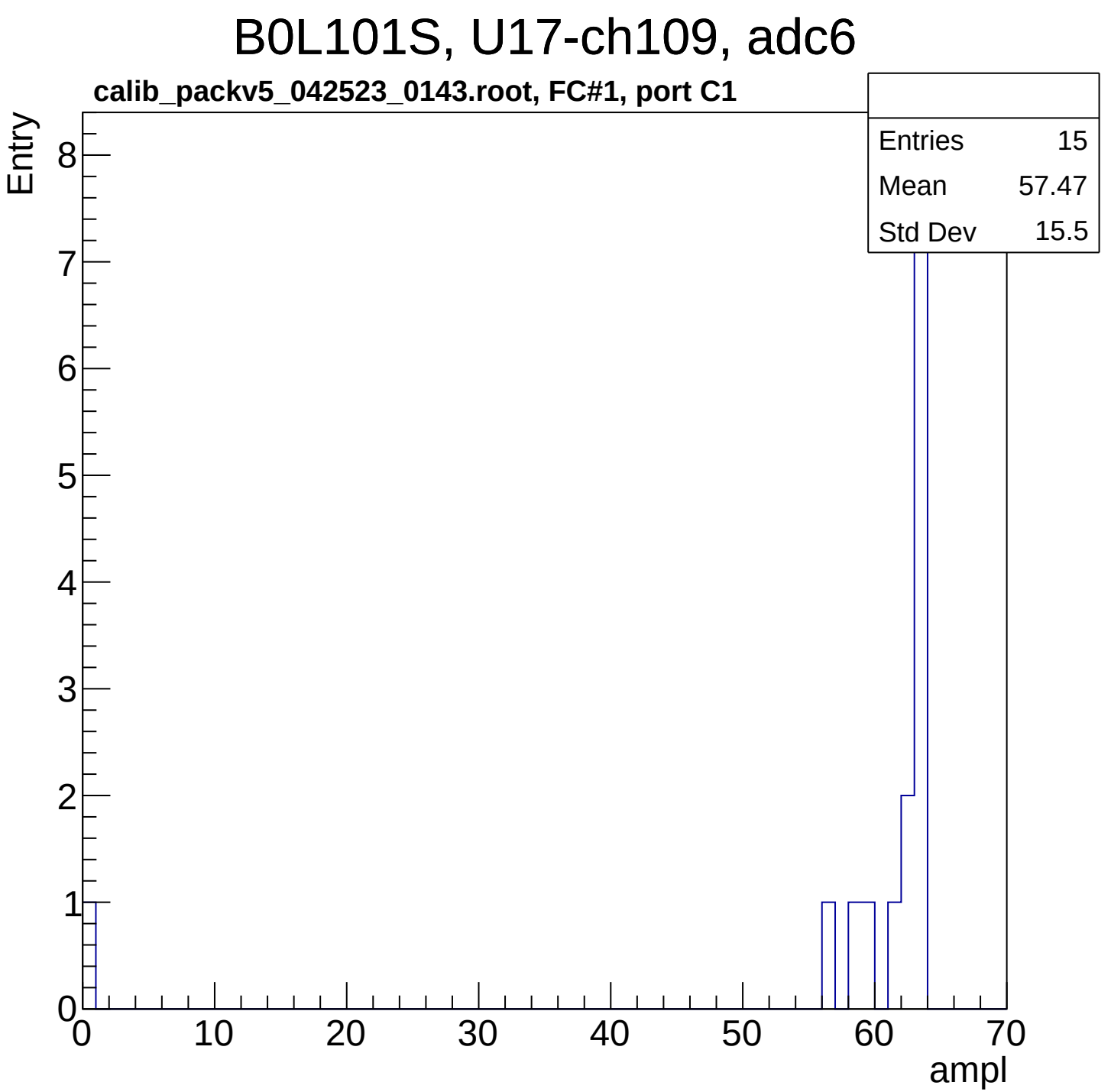

B0L101S, U17-ch109, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 175
Mean 35.62
Std Dev 4.936
Gaus mean : 36.4498
Gaus Width: 4.0707
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

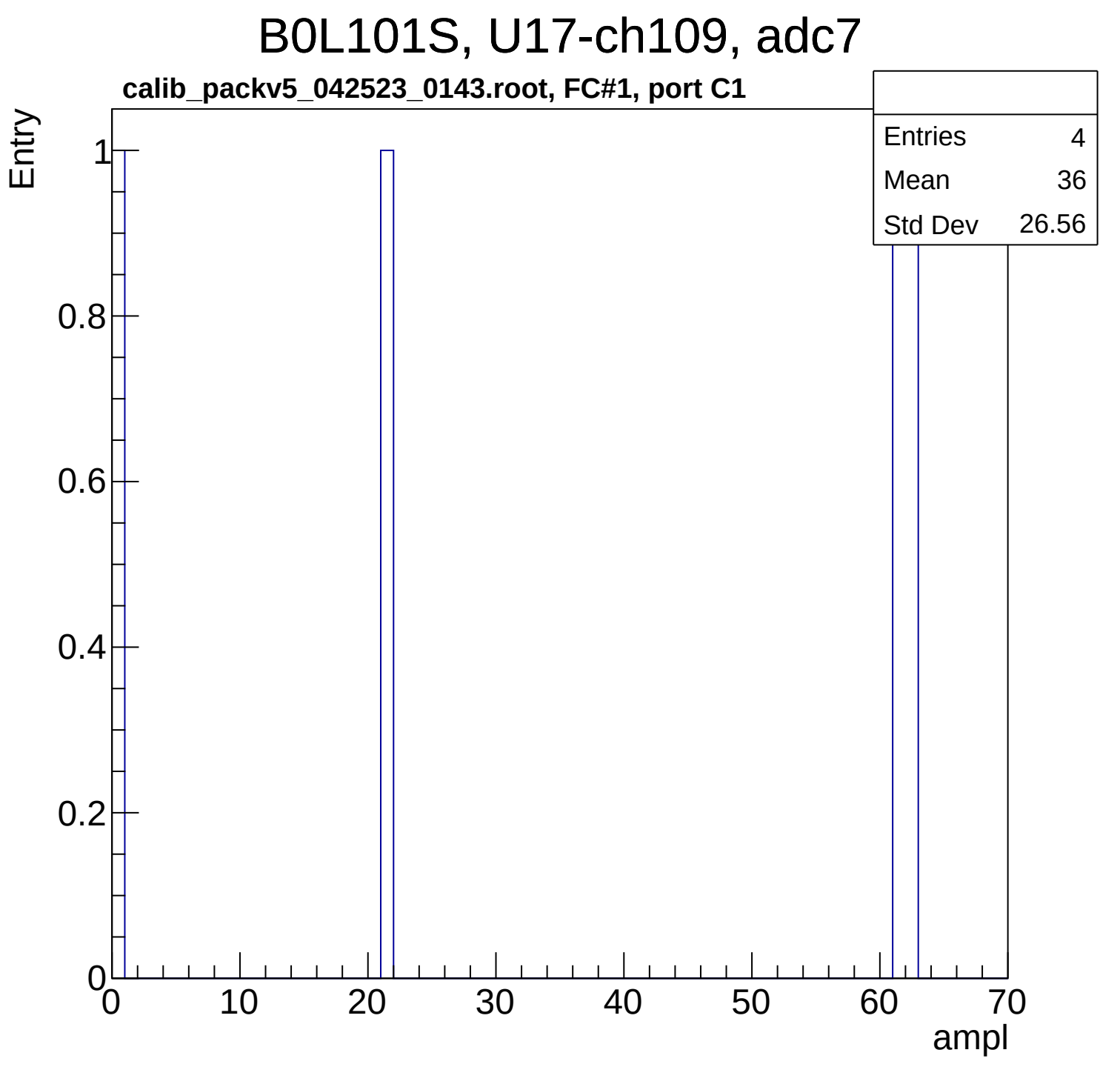

B0L101S, U17-ch109, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 42.94
Std Dev 3.646
Gaus mean : 43.4618
Gaus Width: 3.2864
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

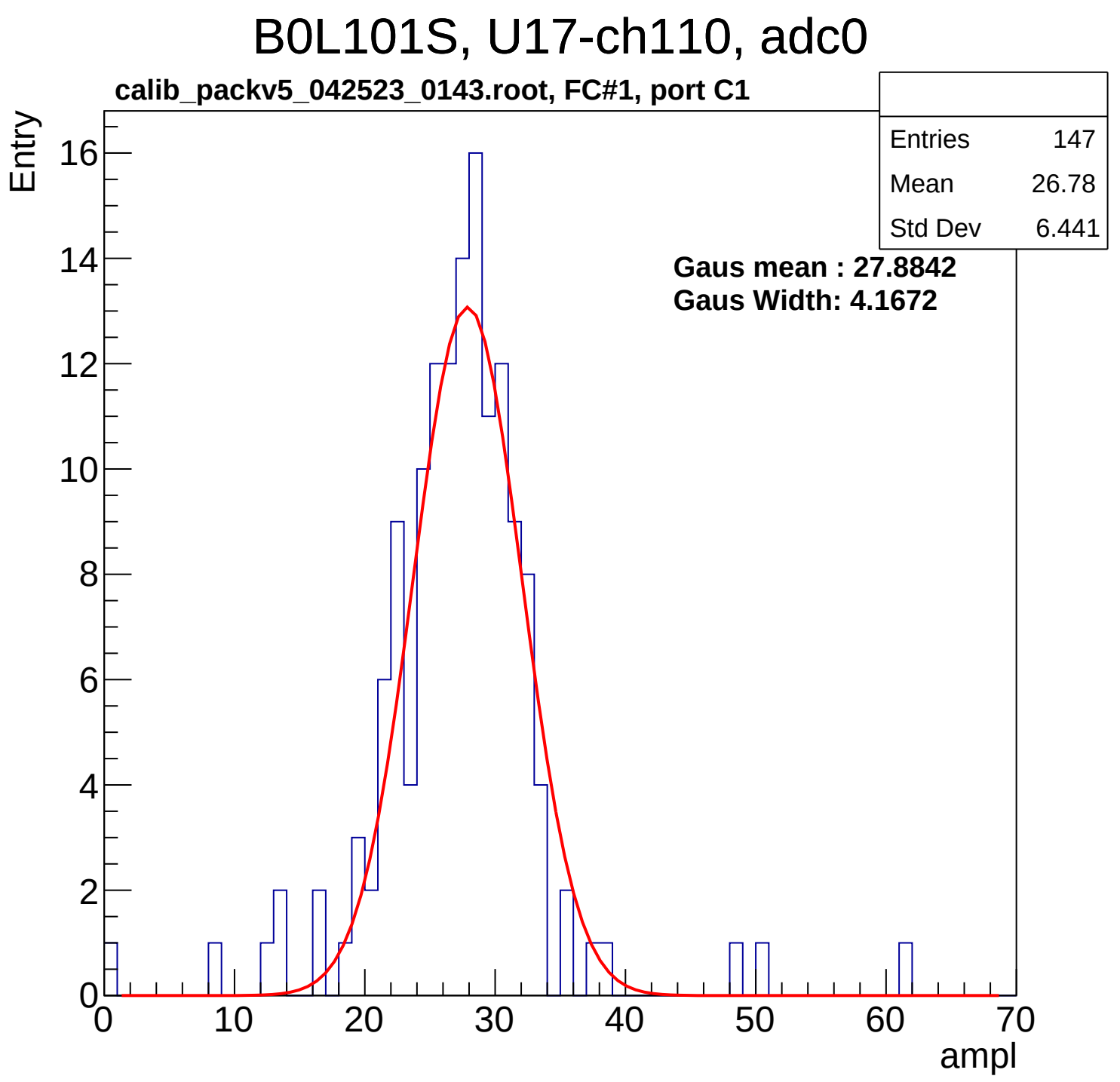

B0L101S, U17-ch109, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 50.34
Std Dev 4.185
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

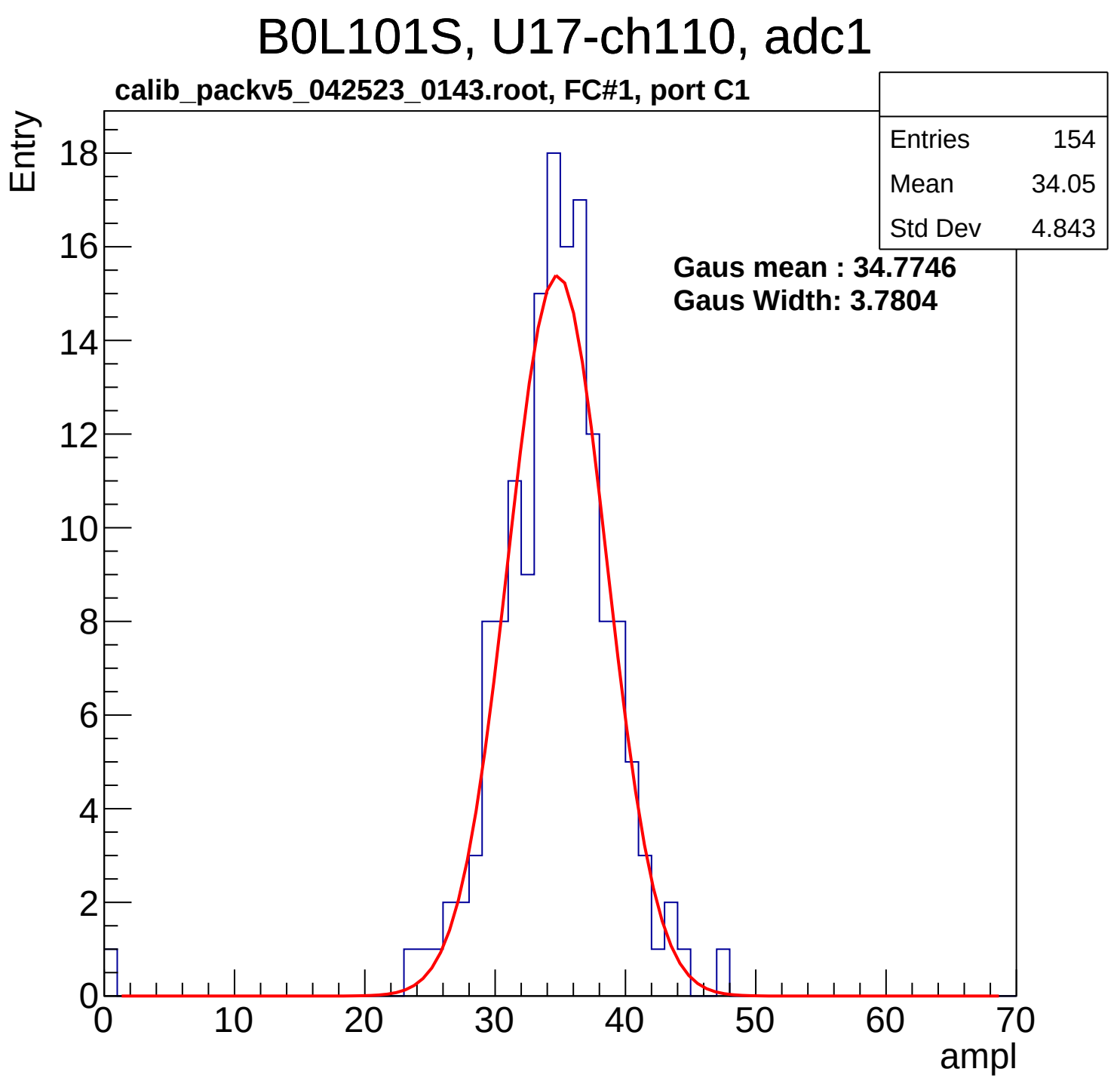

B0L101S, U17-ch109, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 55.88
Std Dev 3.173
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

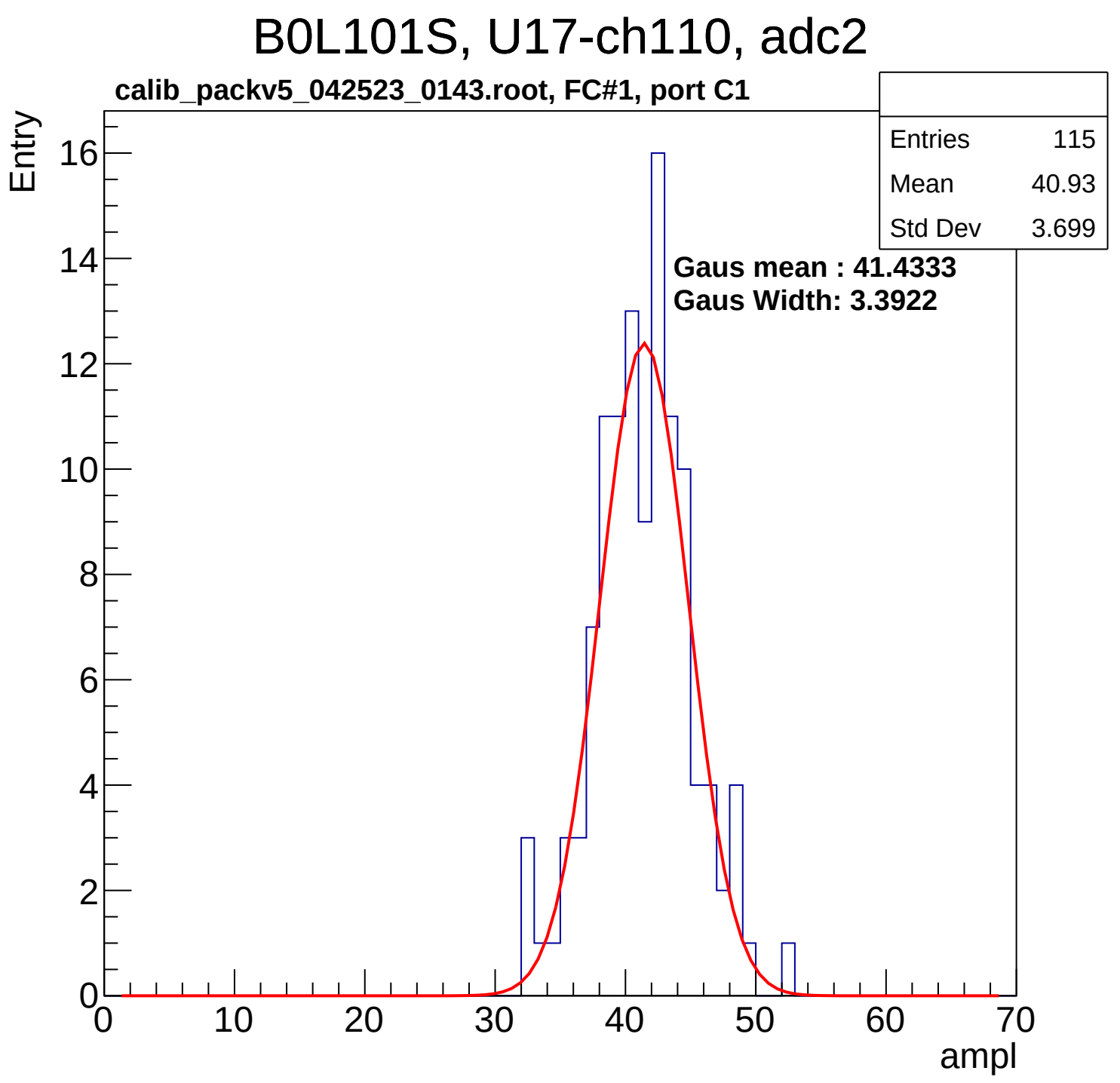

B0L101S, U17-ch109, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 79
Mean 59.33
Std Dev 7.106
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

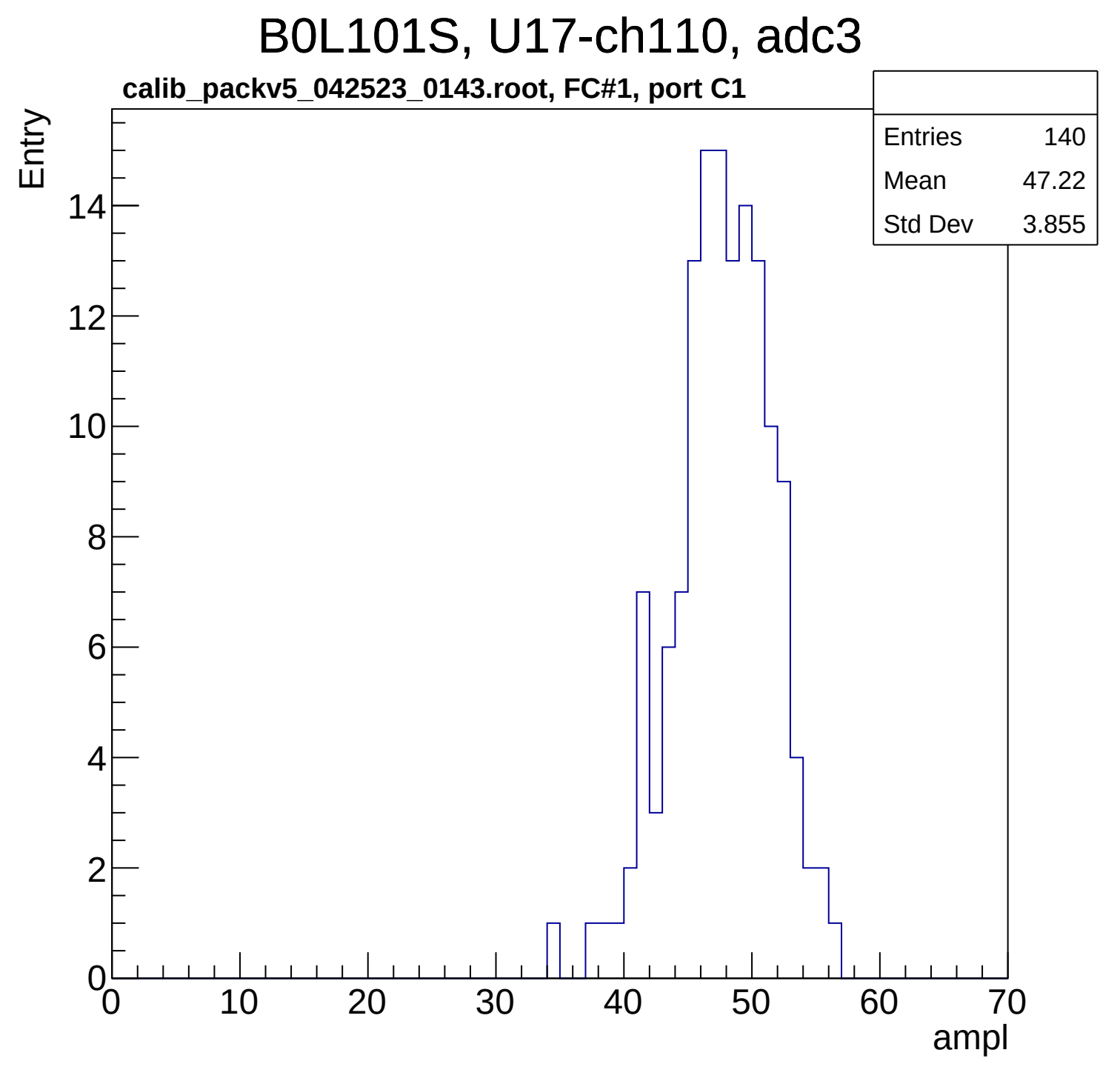

B0L101S, U17-ch109, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 15
Mean 57.47
Std Dev 15.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

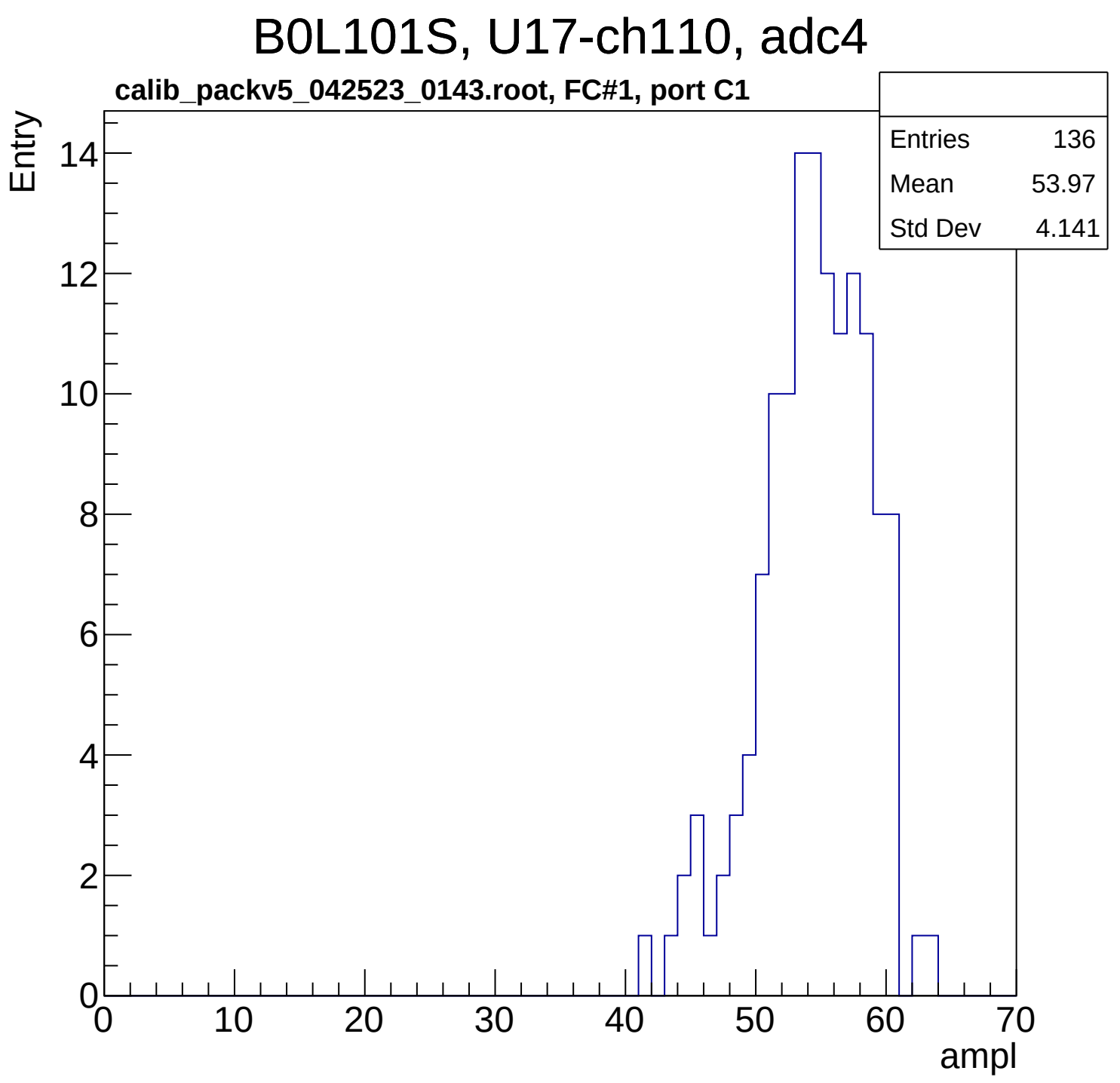

B0L101S, U17-ch109, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 36
Std Dev 26.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

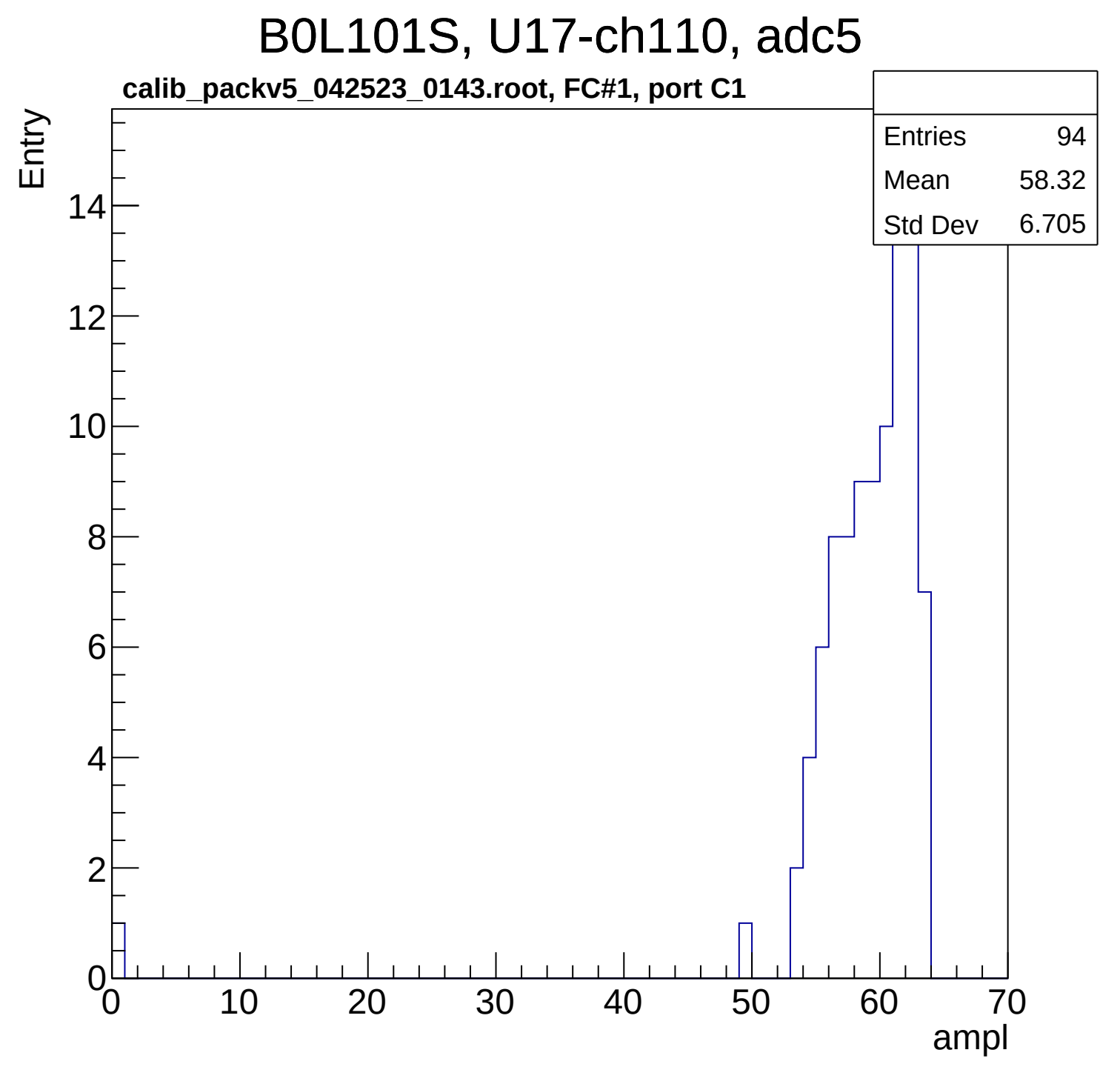

B0L101S, U17-ch110, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 26.78
Std Dev 6.441
Gaus mean : 27.8842
Gaus Width: 4.1672
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

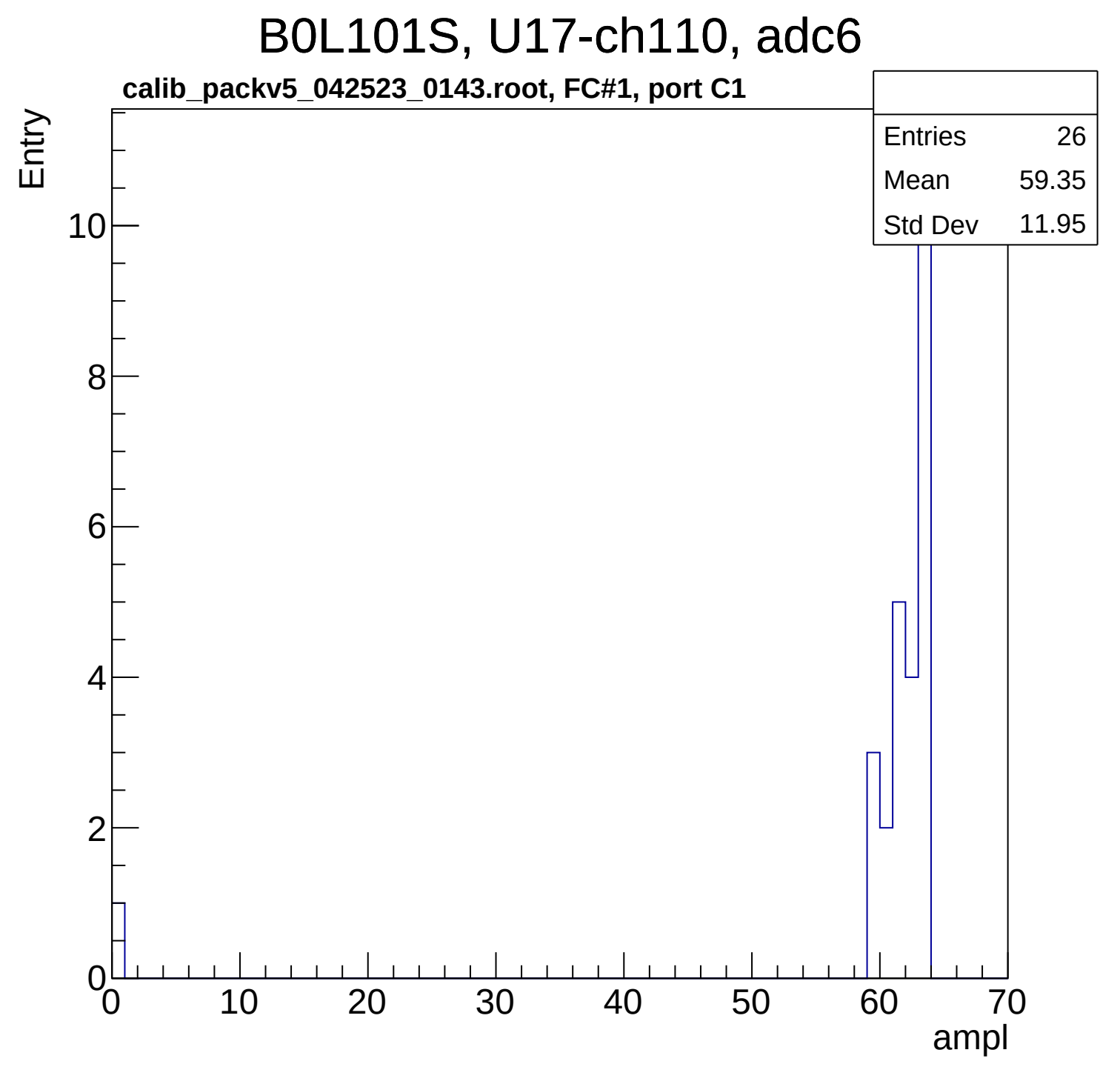

B0L101S, U17-ch110, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 154
Mean 34.05
Std Dev 4.843
Gaus mean : 34.7746
Gaus Width: 3.7804
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

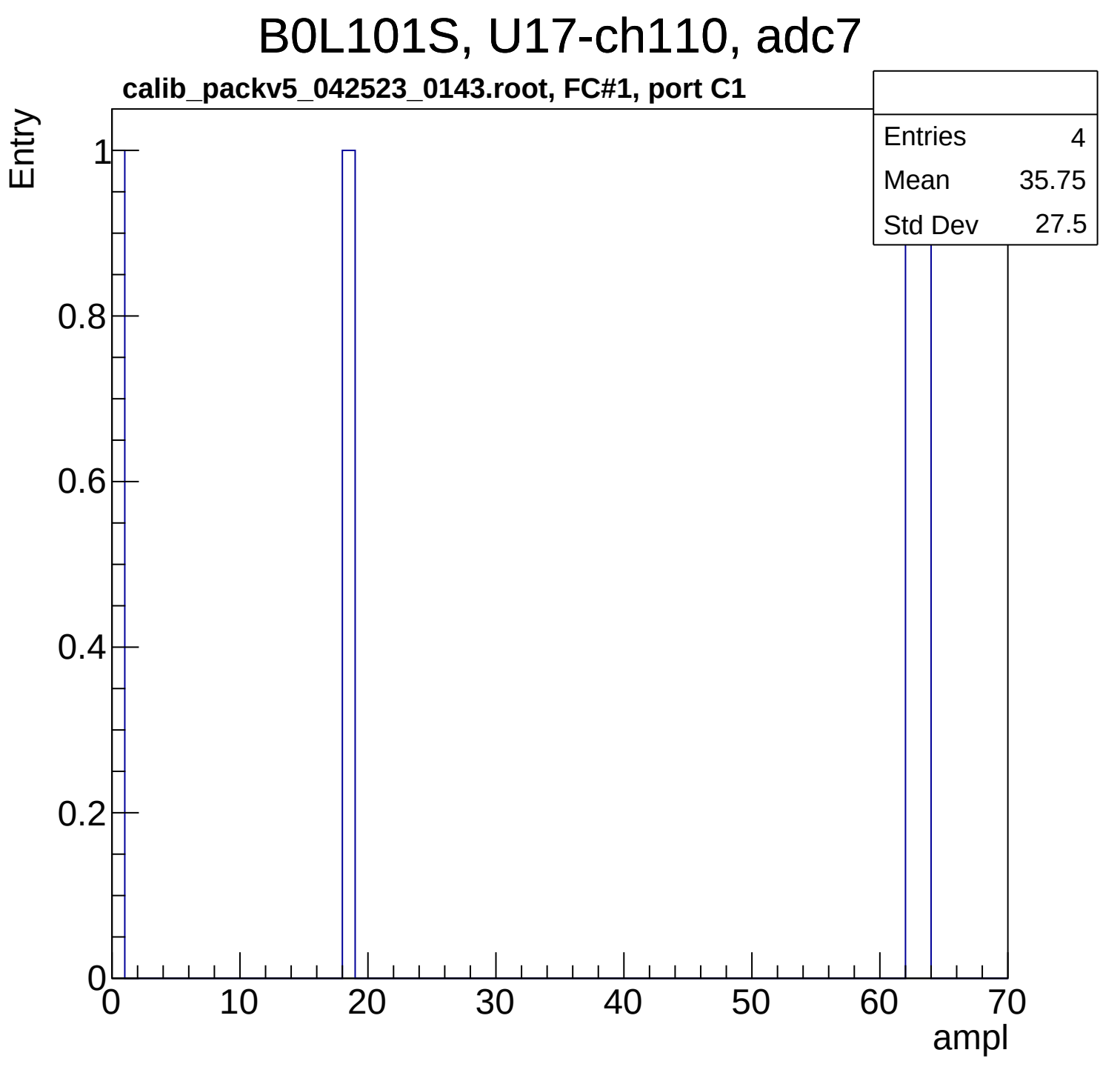

B0L101S, U17-ch110, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 40.93
Std Dev 3.699
Gaus mean : 41.4333
Gaus Width: 3.3922
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

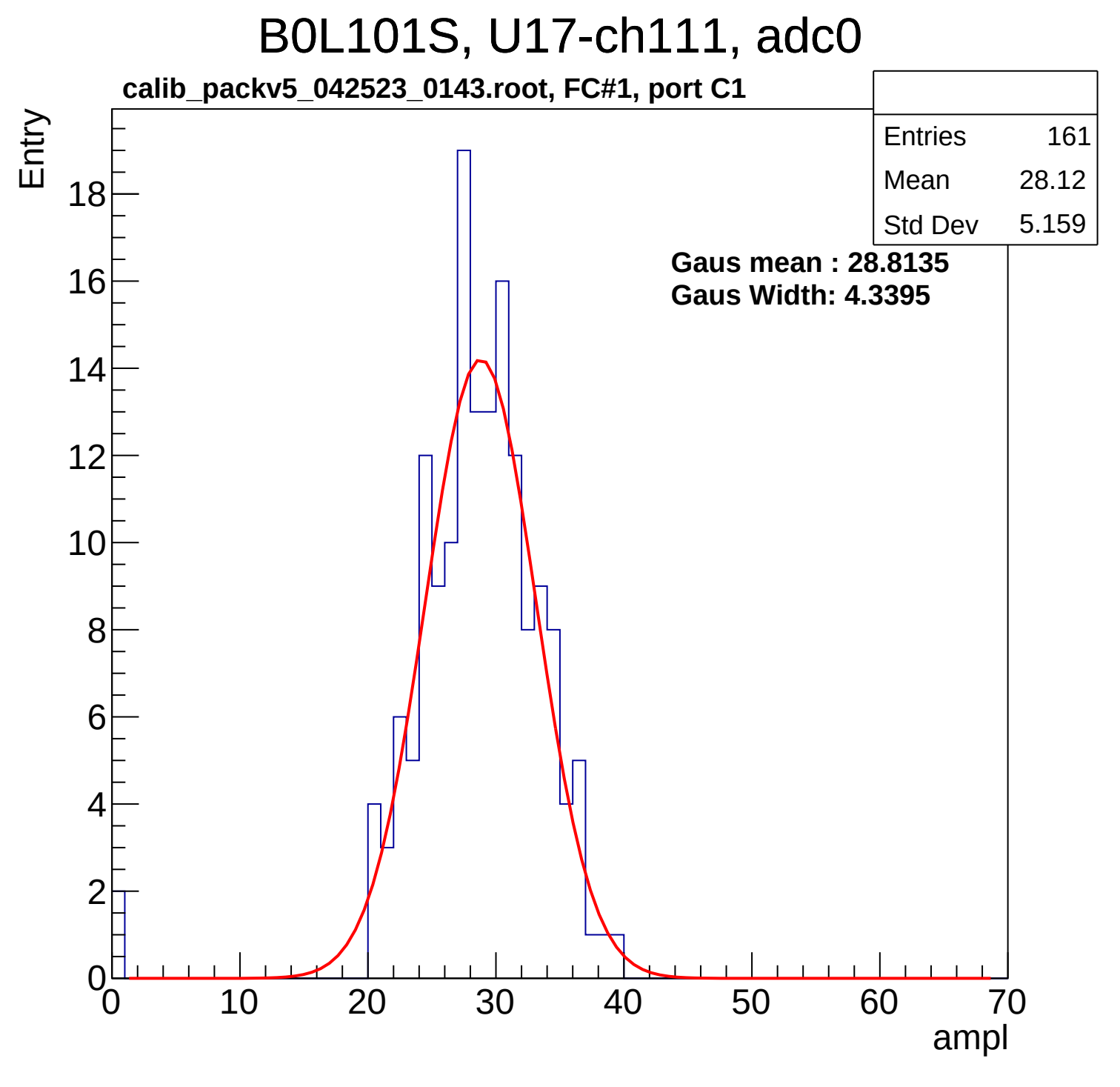

B0L101S, U17-ch110, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 47.22
Std Dev 3.855
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

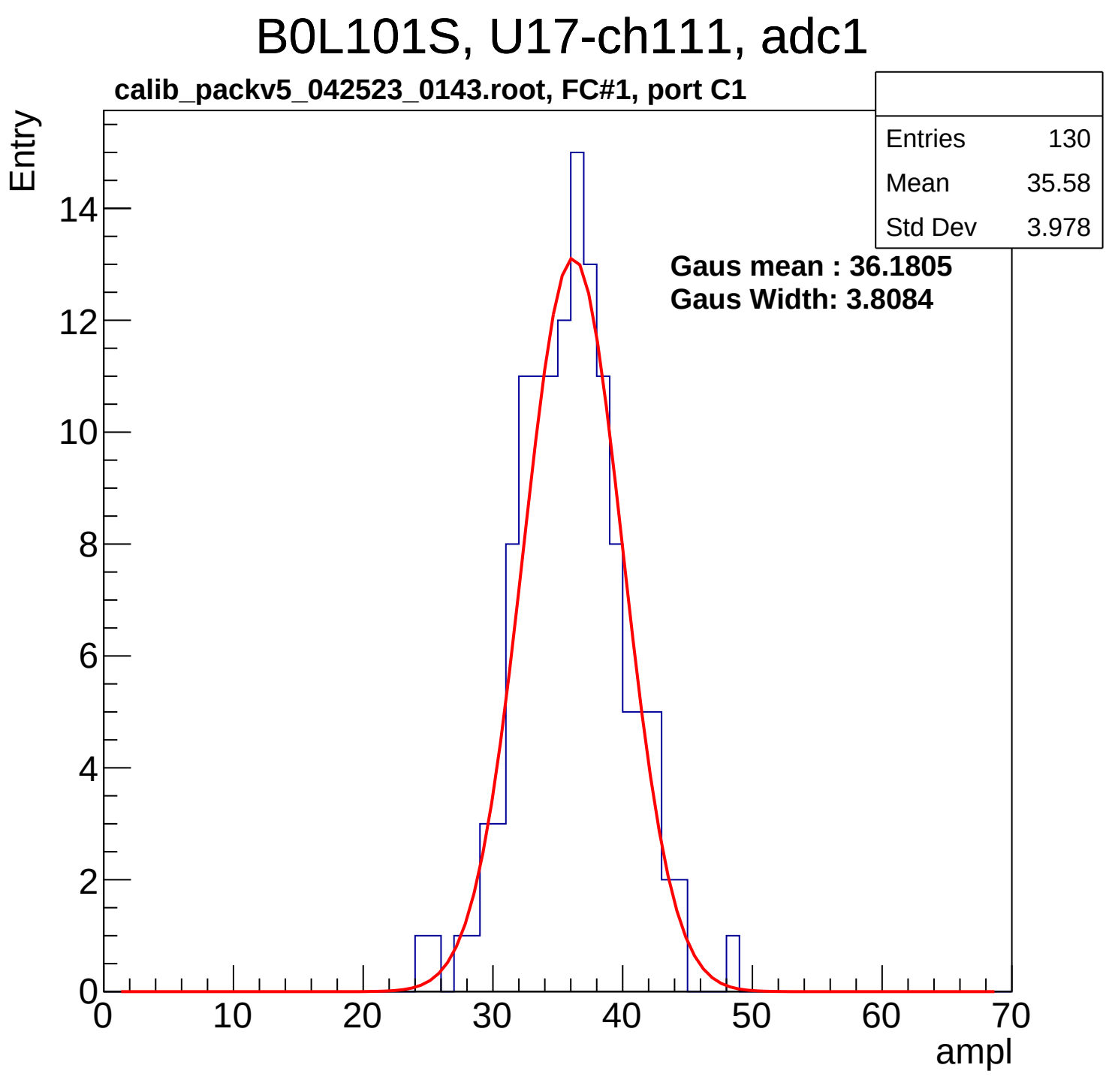

B0L101S, U17-ch110, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 53.97
Std Dev 4.141
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

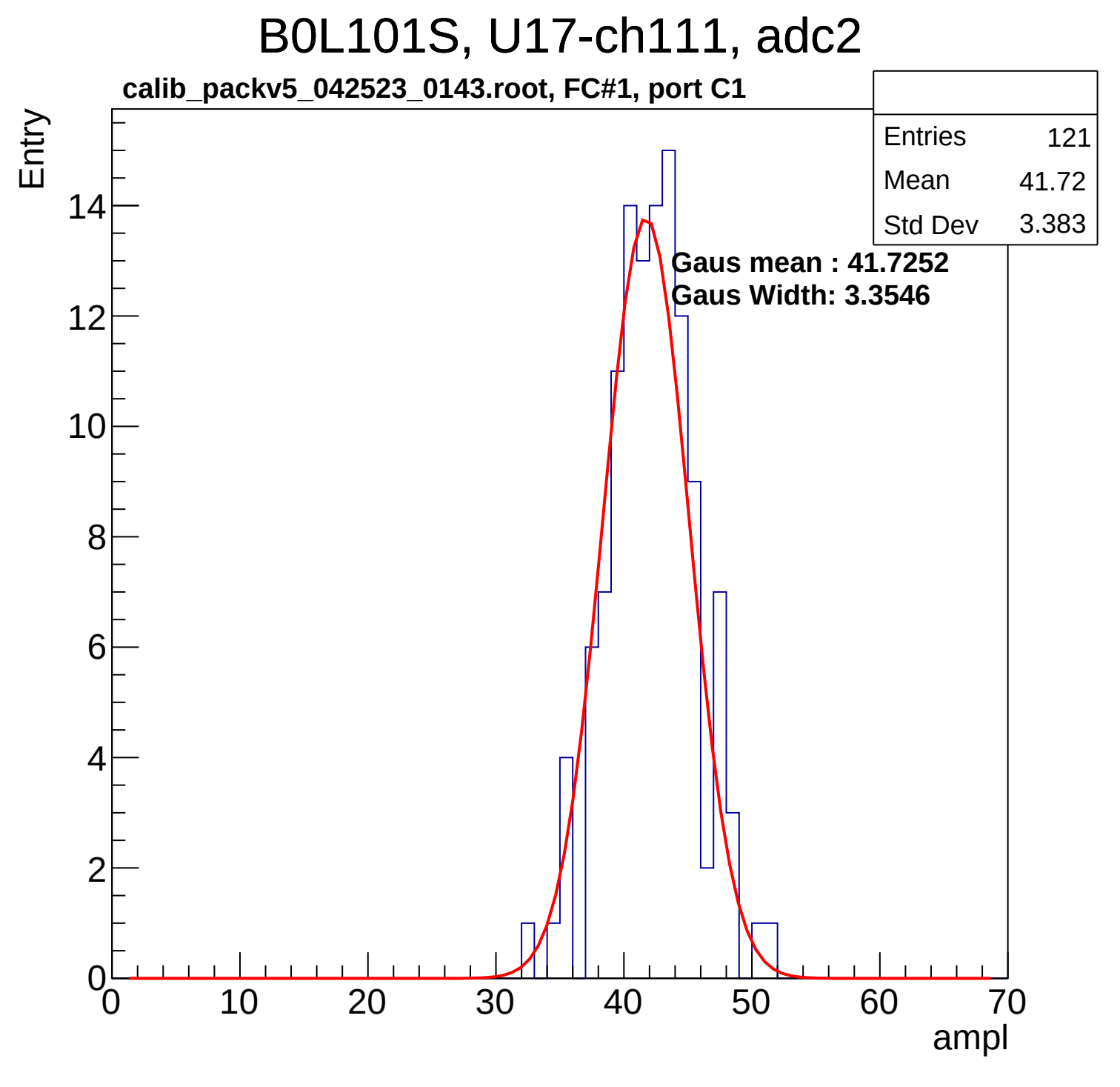

B0L101S, U17-ch110, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 94
Mean 58.32
Std Dev 6.705
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

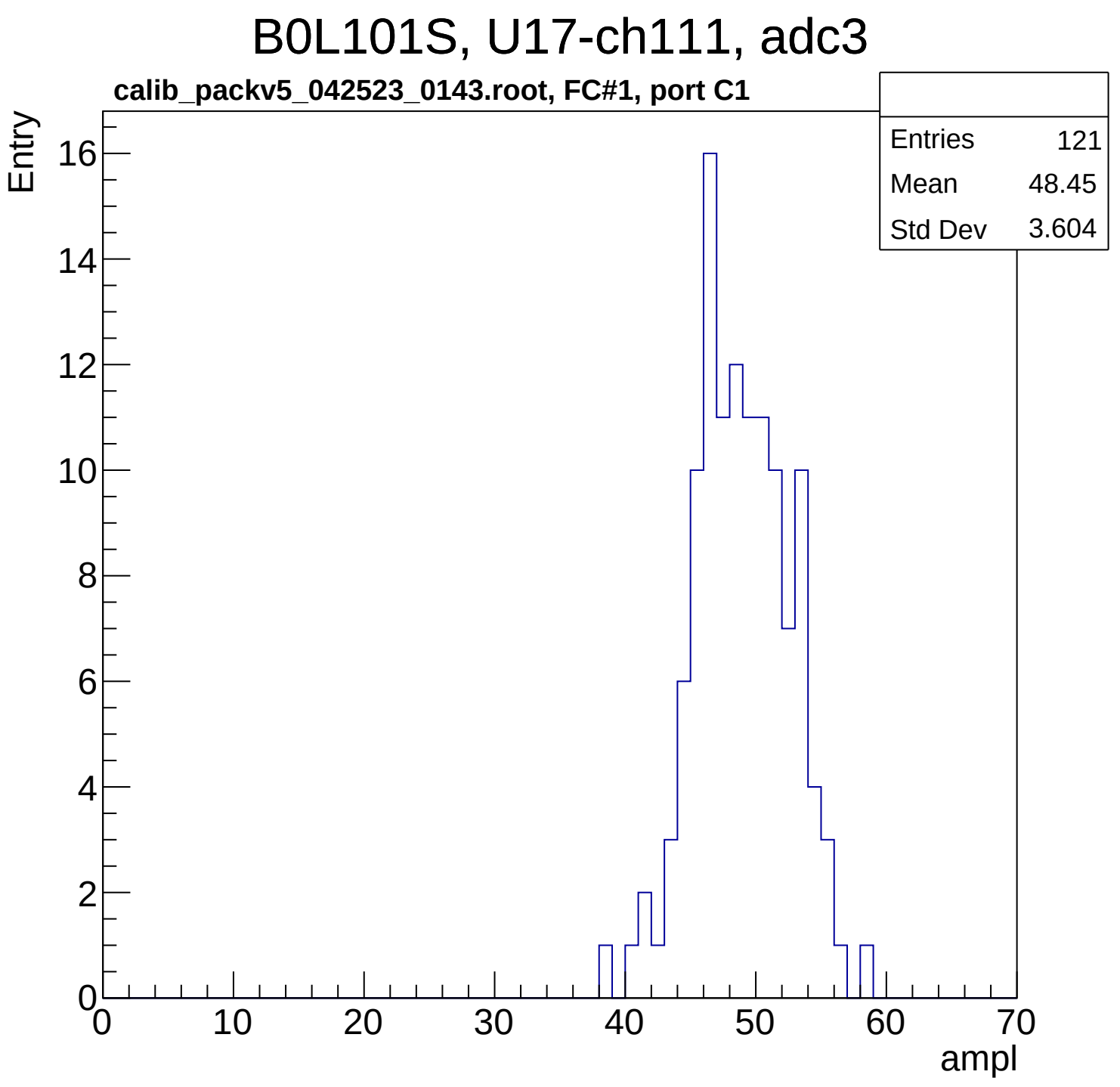

B0L101S, U17-ch110, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 26
Mean 59.35
Std Dev 11.95
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

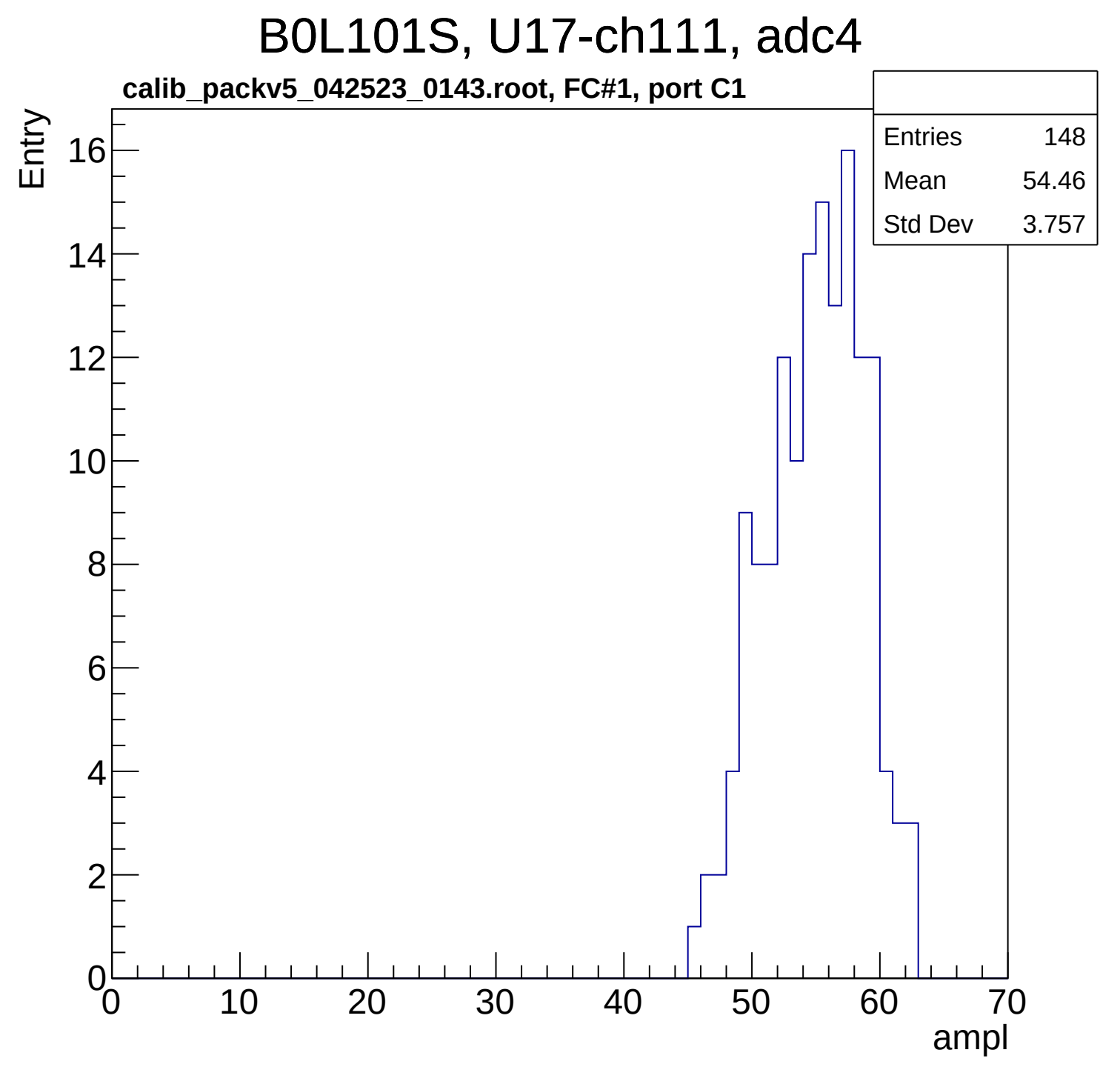

B0L101S, U17-ch110, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 35.75
Std Dev 27.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

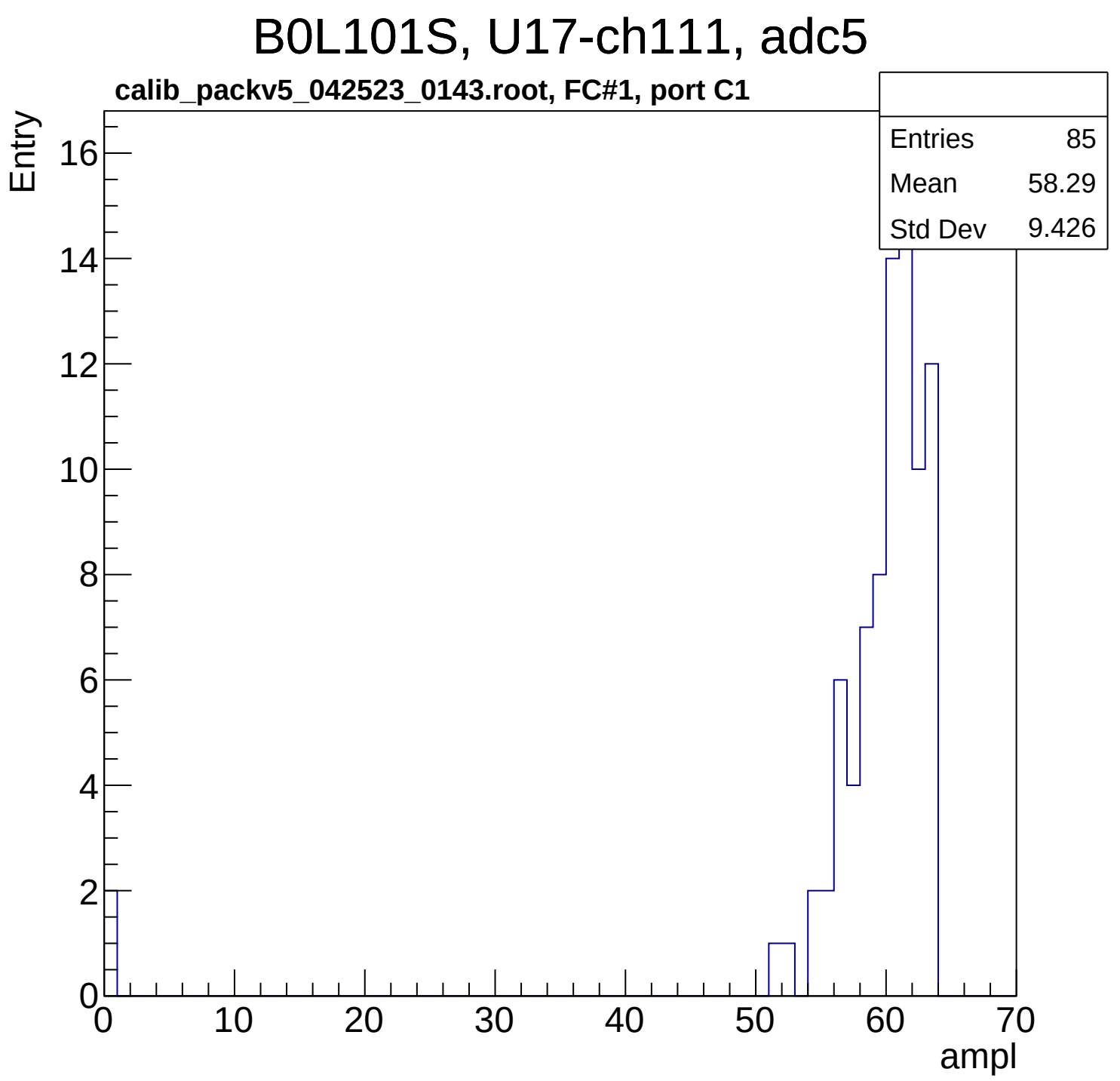

B0L101S, U17-ch111, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 161
Mean 28.12
Std Dev 5.159
Gaus mean : 28.8135
Gaus Width: 4.3395
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

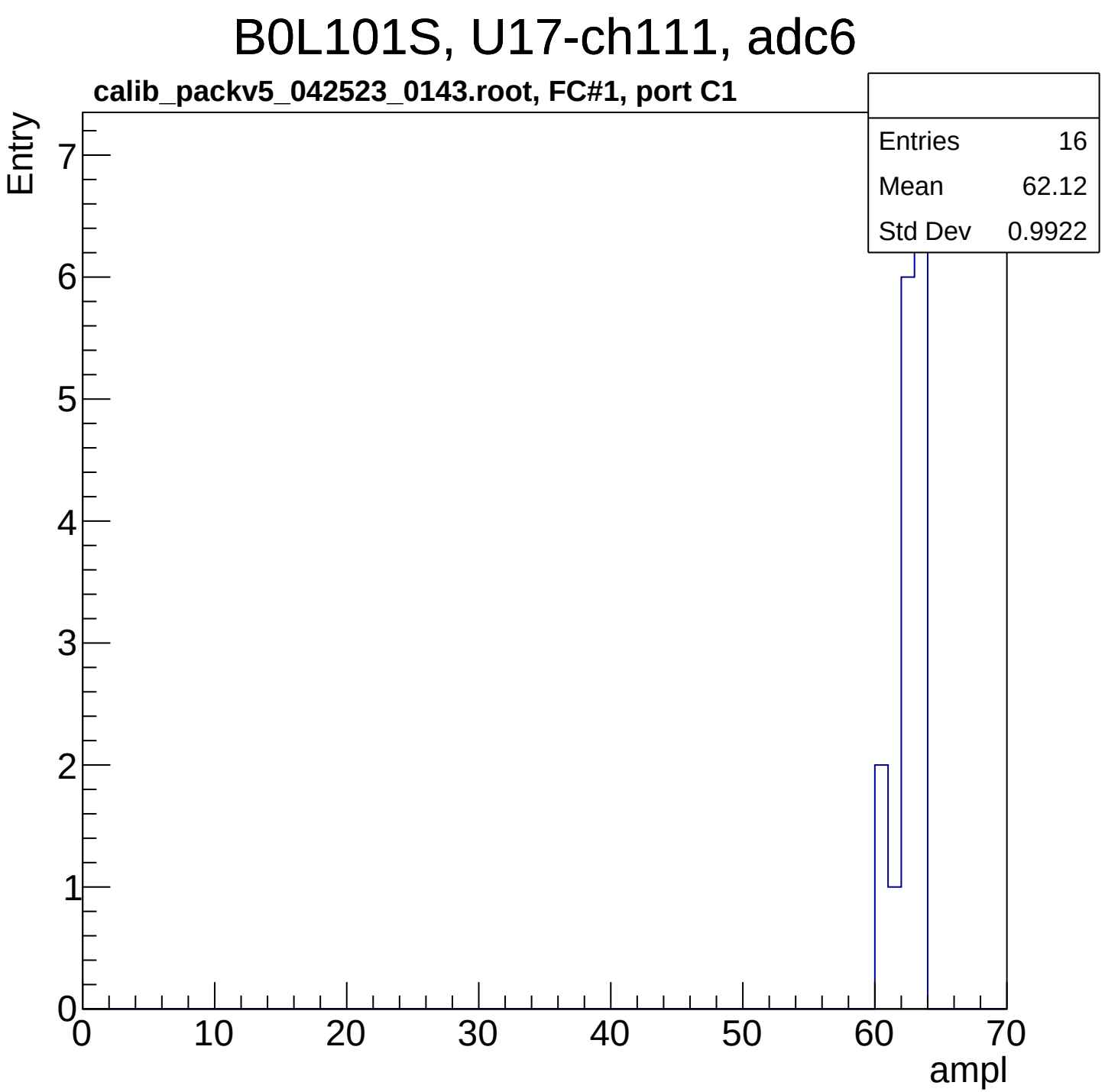

B0L101S, U17-ch111, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 130
Mean 35.58
Std Dev 3.978
Gaus mean : 36.1805
Gaus Width: 3.8084
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

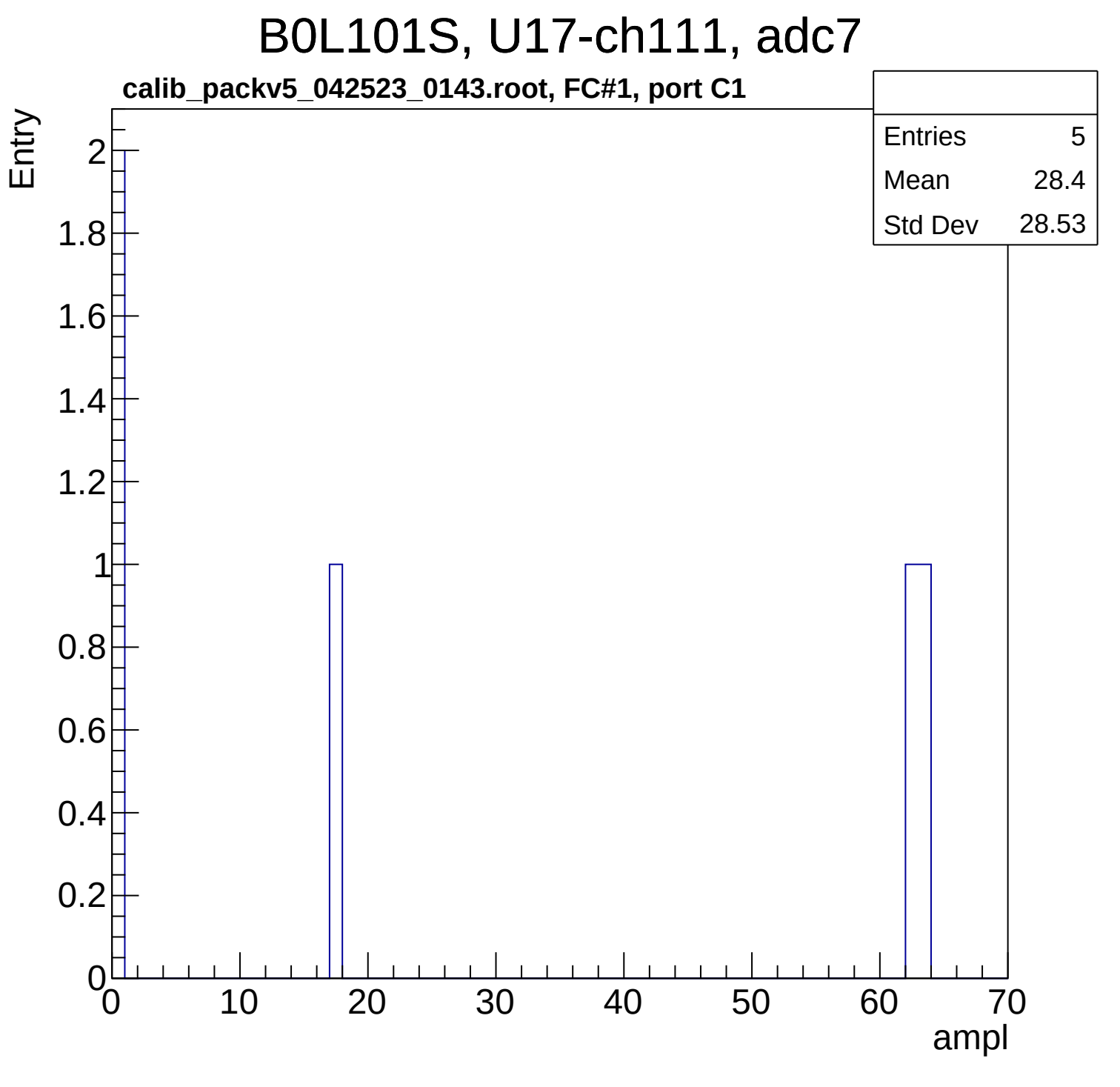

B0L101S, U17-ch111, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 41.72
Std Dev 3.383
Gaus mean : 41.7252
Gaus Width: 3.3546
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

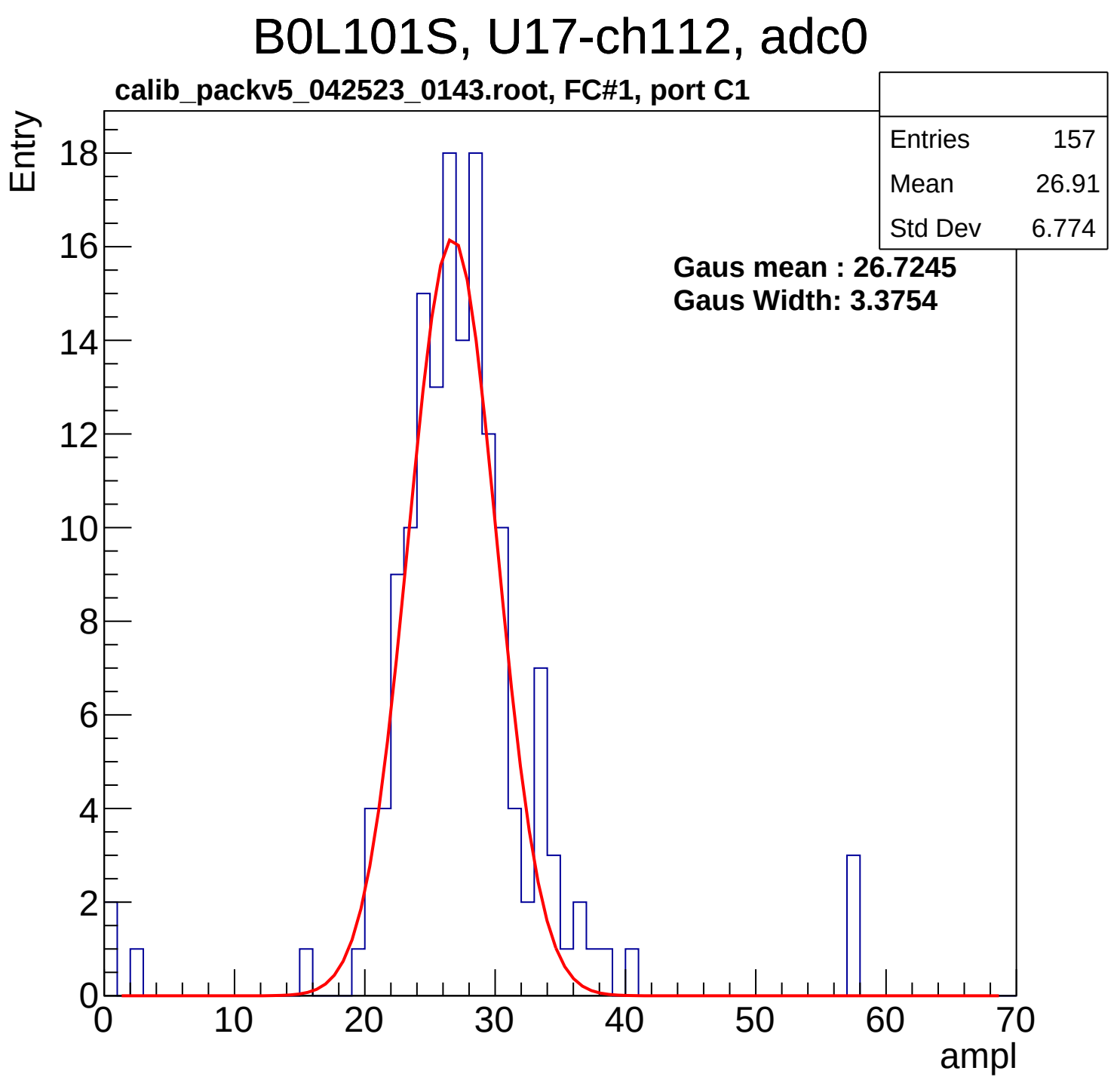

B0L101S, U17-ch111, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 121
Mean 48.45
Std Dev 3.604
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

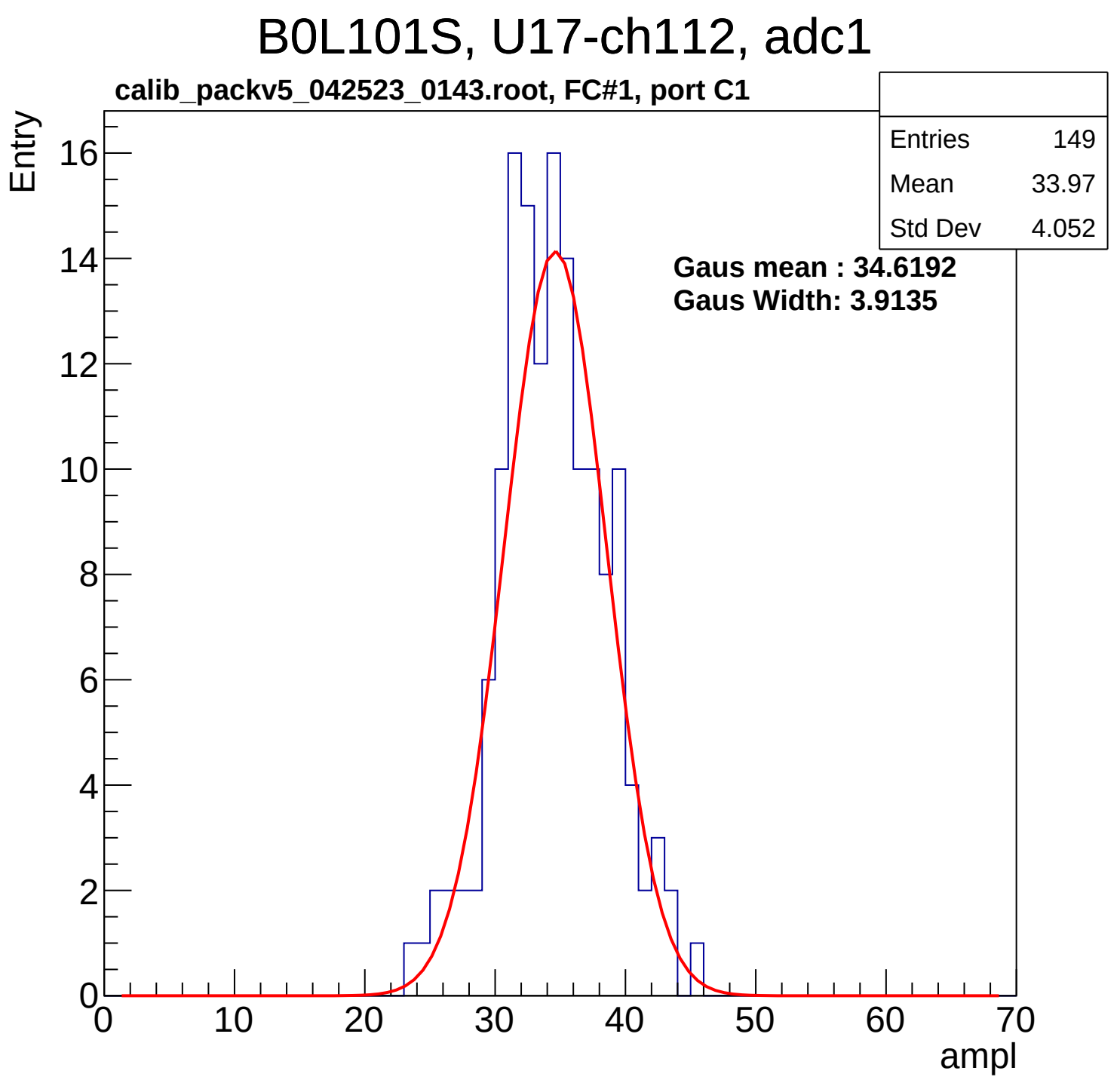

B0L101S, U17-ch111, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 148
Mean 54.46
Std Dev 3.757
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

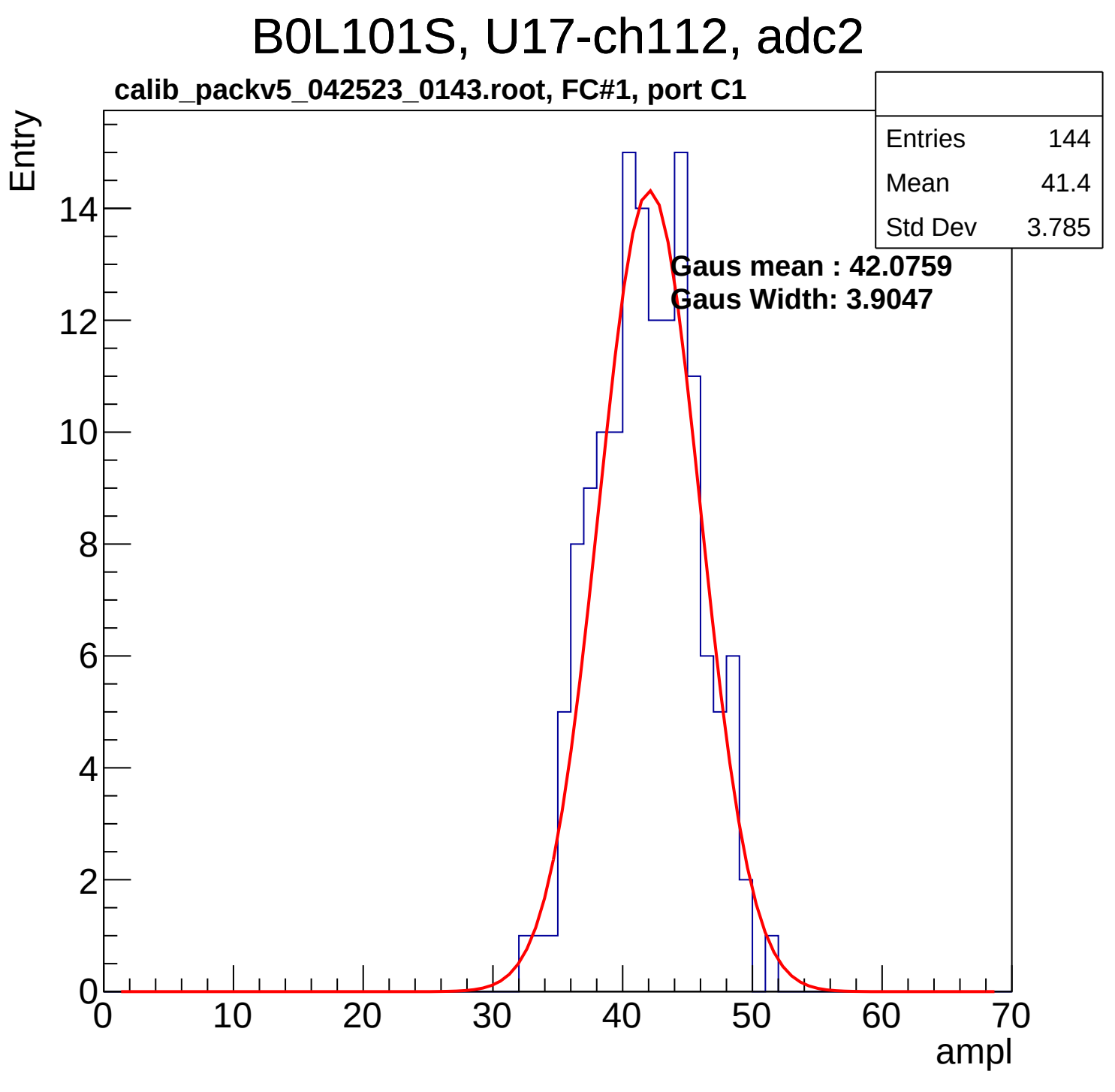

B0L101S, U17-ch111, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 85
Mean 58.29
Std Dev 9.426
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

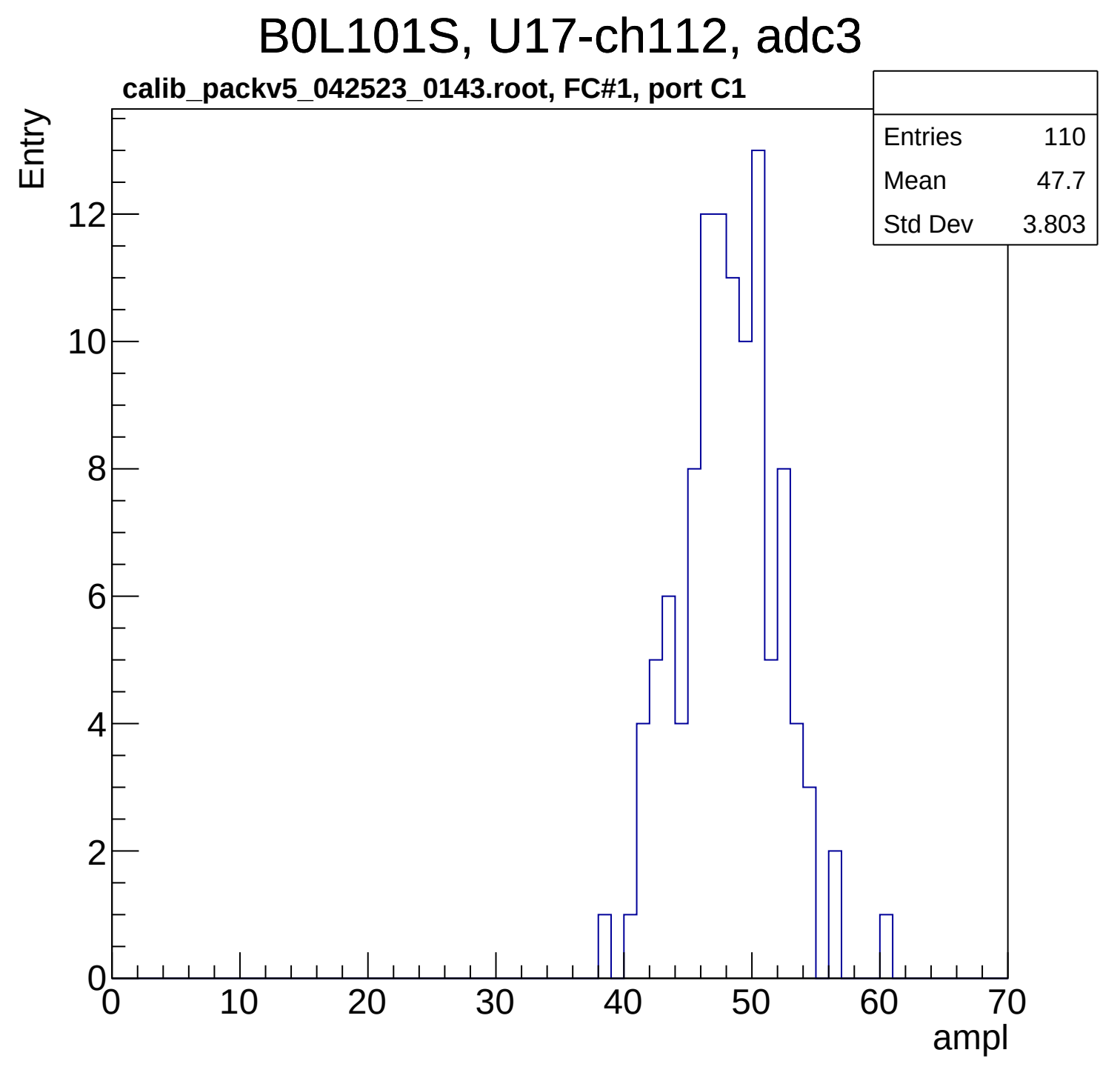

B0L101S, U17-ch111, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 62.12
Std Dev 0.9922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

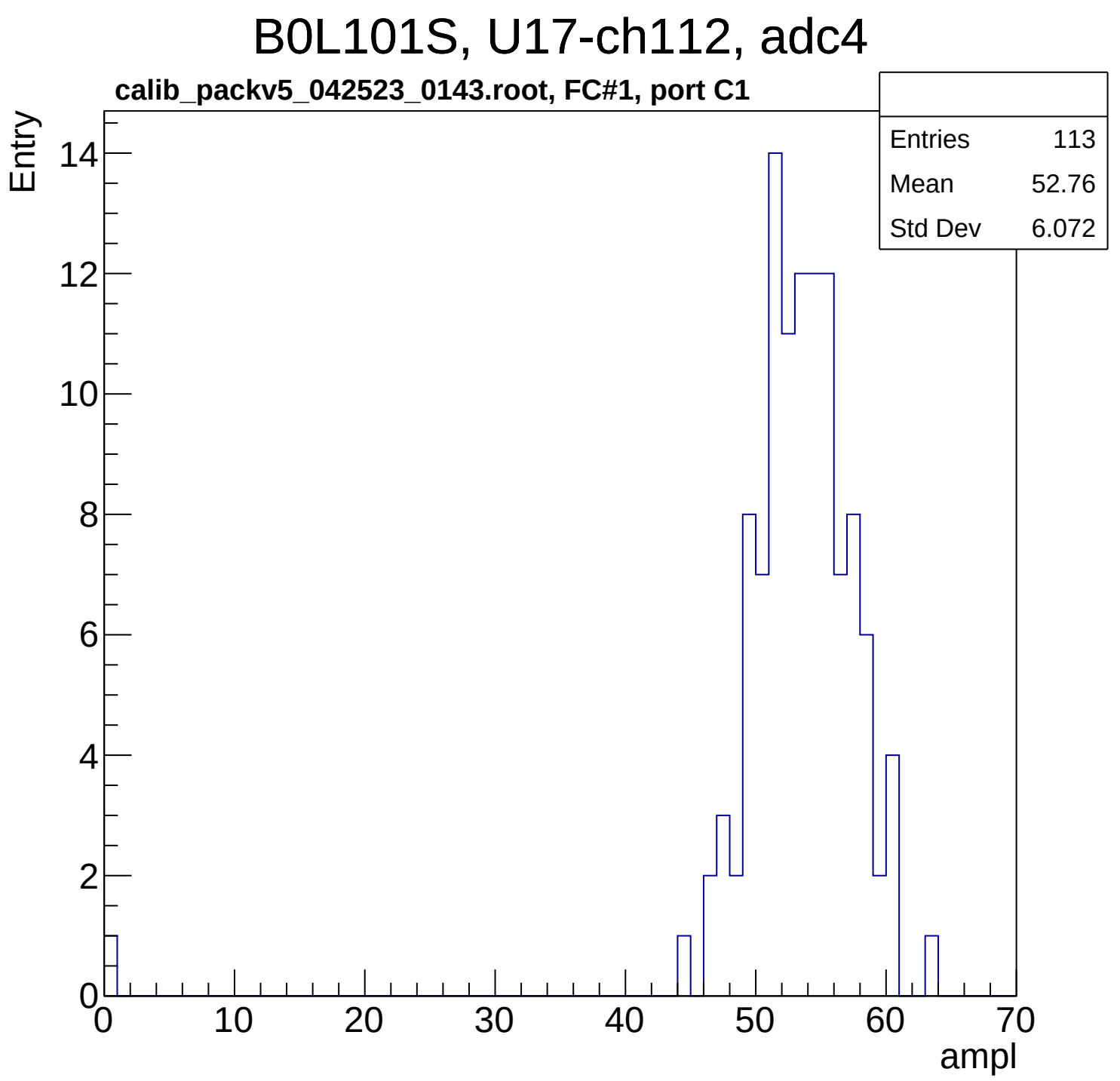

B0L101S, U17-ch111, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 28.4
Std Dev 28.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

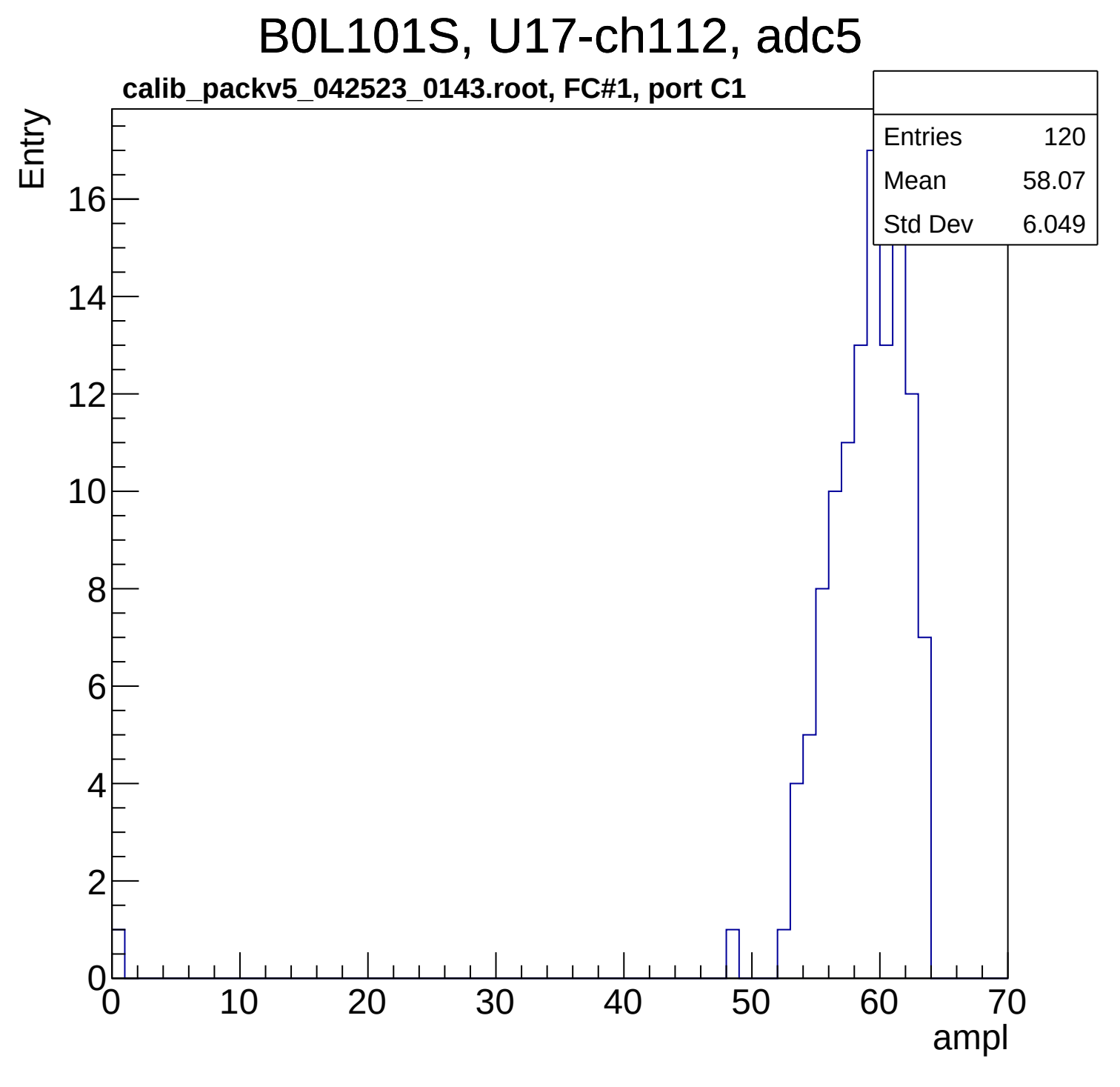

B0L101S, U17-ch112, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 26.91
Std Dev 6.774
Gaus mean : 26.7245
Gaus Width: 3.3754
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

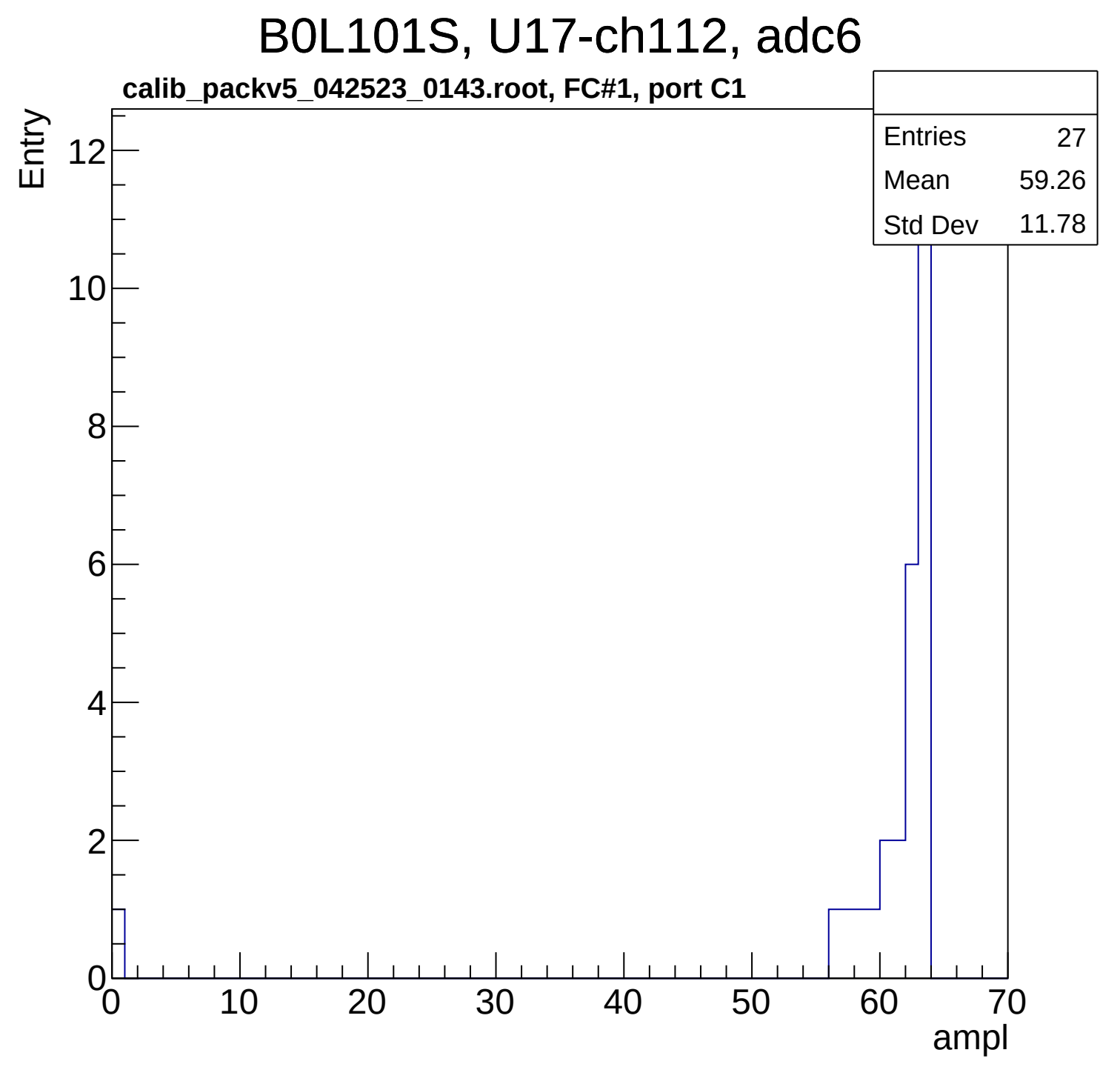

B0L101S, U17-ch112, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 33.97
Std Dev 4.052
Gaus mean : 34.6192
Gaus Width: 3.9135
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

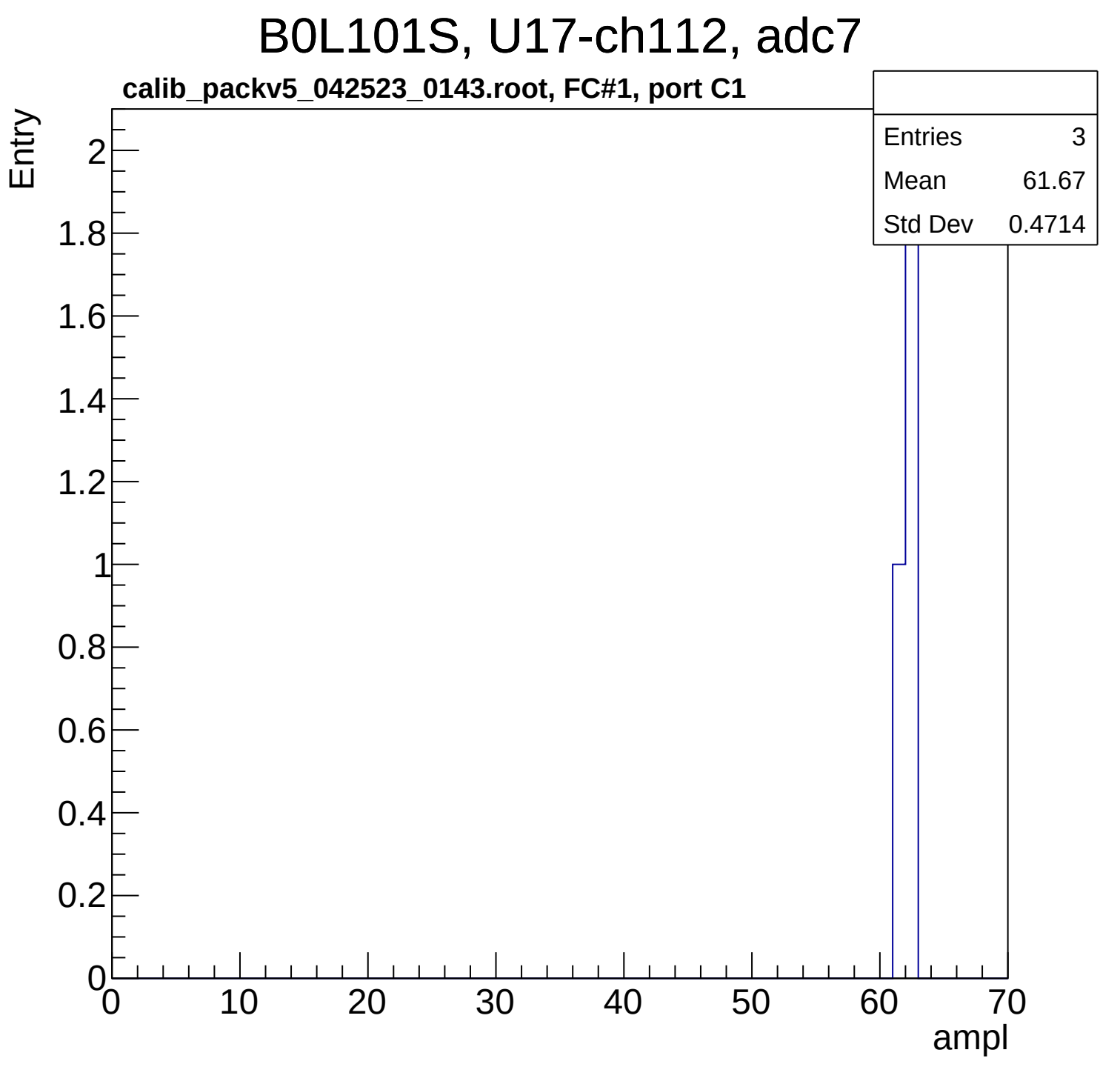

B0L101S, U17-ch112, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 41.4
Std Dev 3.785
Gaus mean : 42.0759
Gaus Width: 3.9047
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

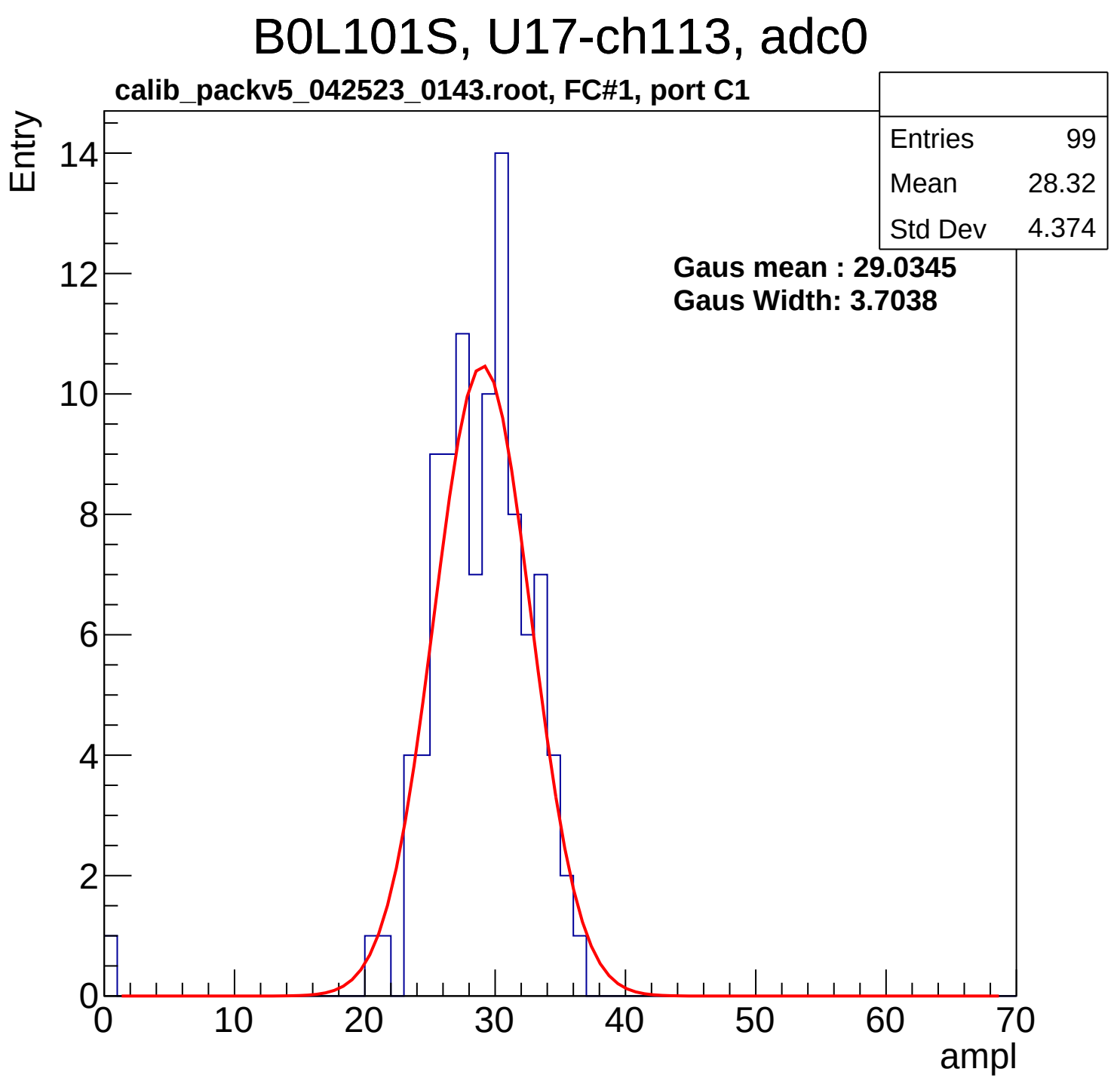

B0L101S, U17-ch112, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 110
Mean 47.7
Std Dev 3.803
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

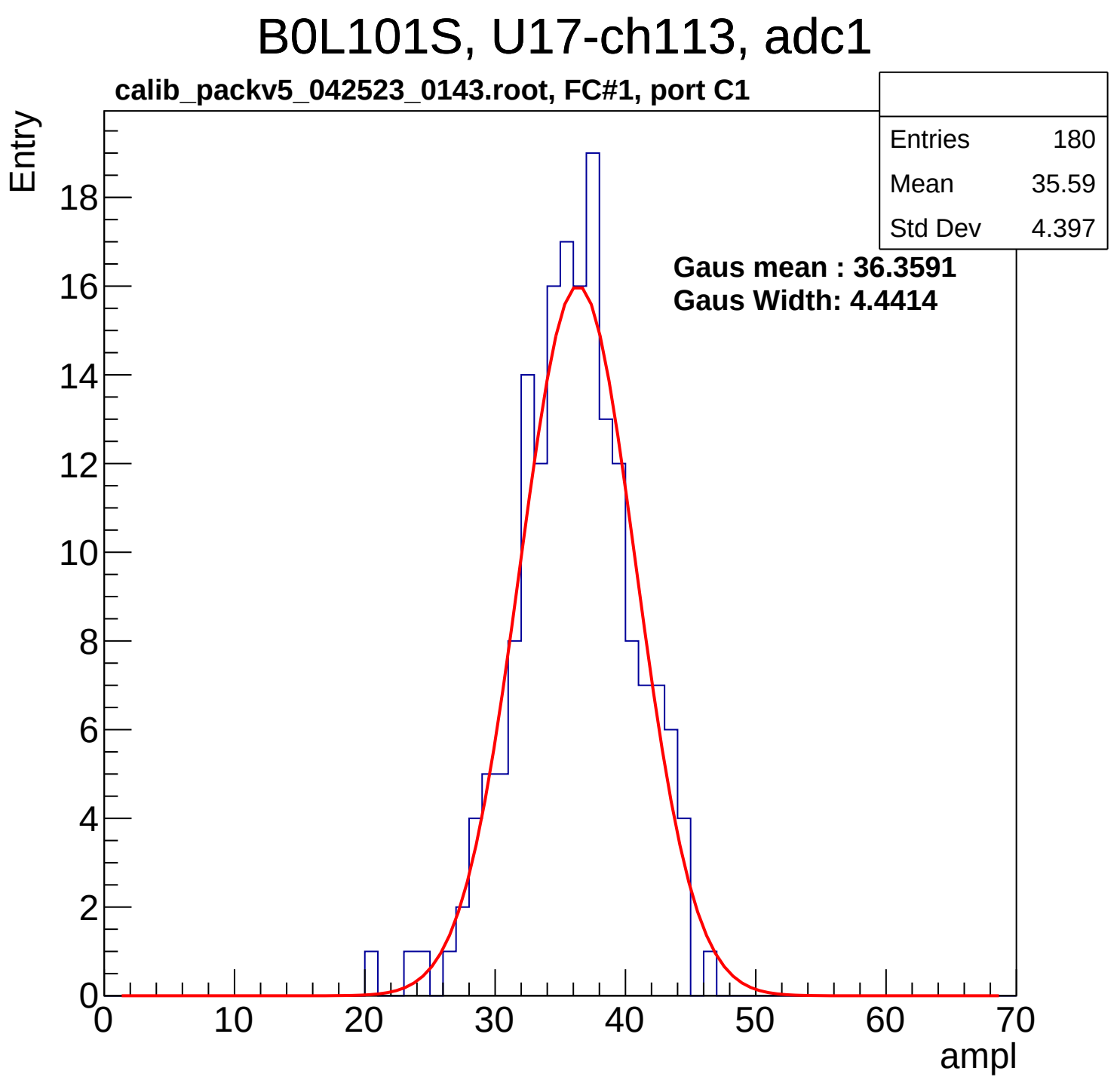

B0L101S, U17-ch112, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 52.76
Std Dev 6.072
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

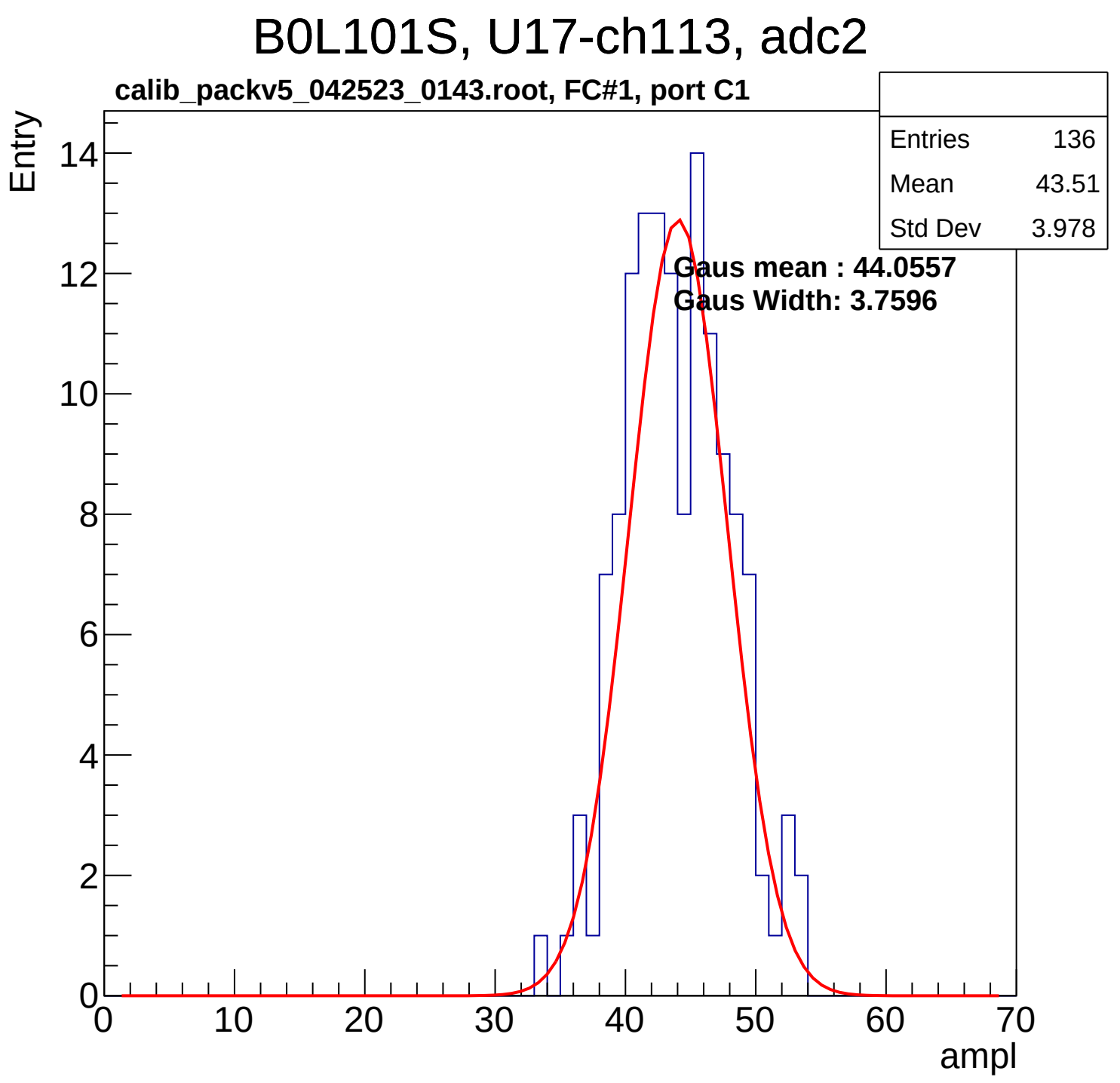

B0L101S, U17-ch112, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 58.07
Std Dev 6.049
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

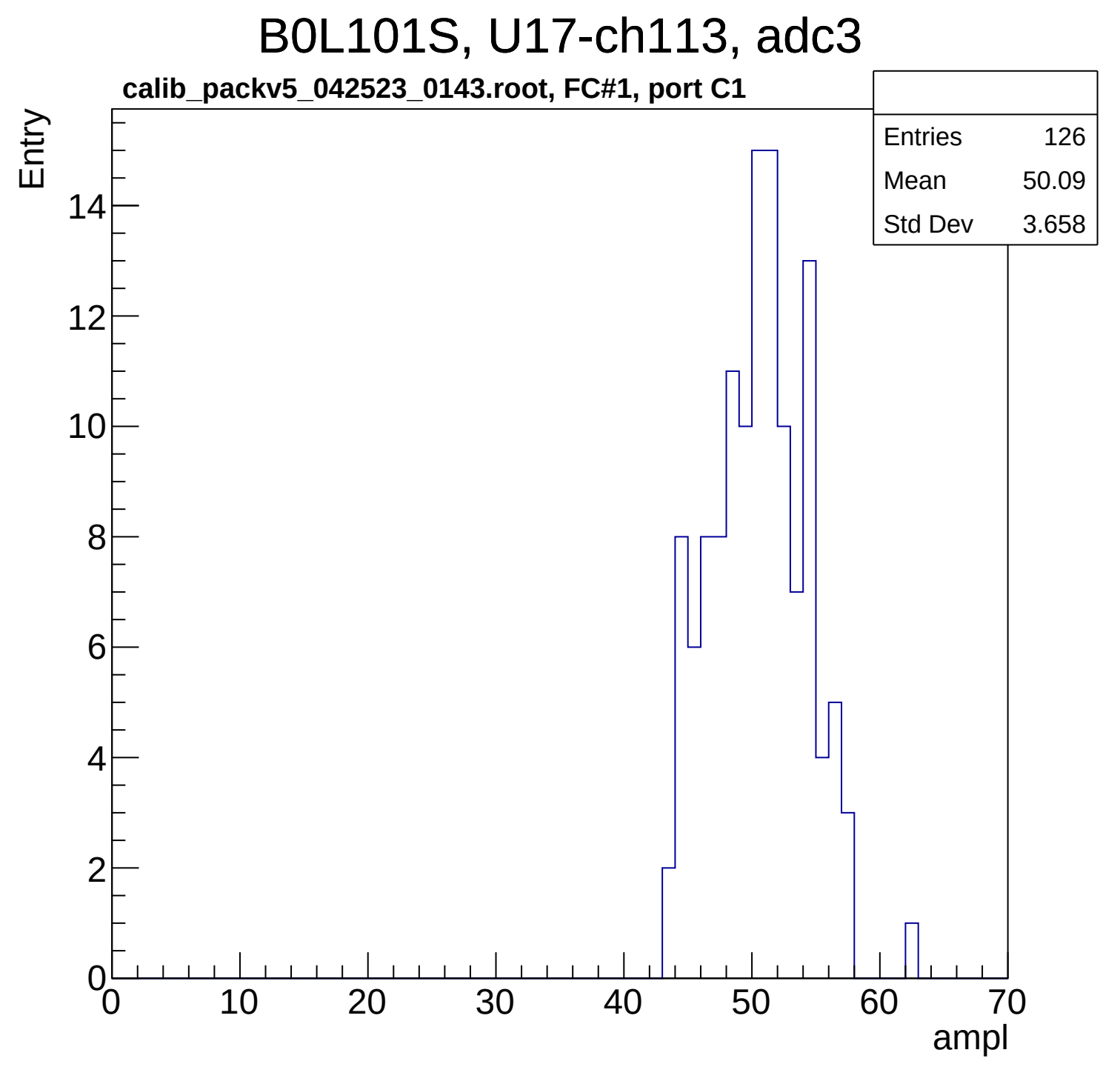

B0L101S, U17-ch112, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 27
Mean 59.26
Std Dev 11.78
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

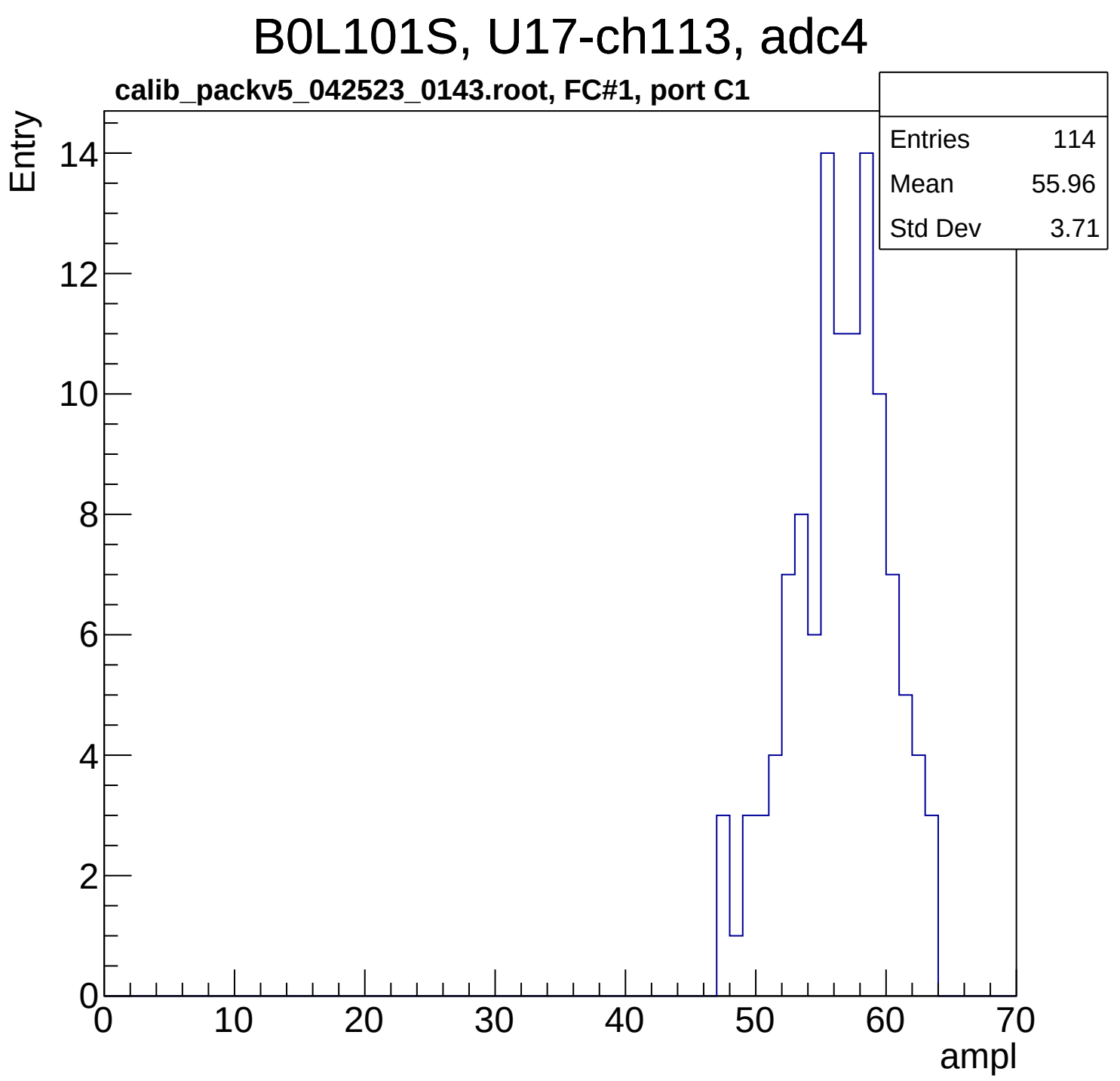

B0L101S, U17-ch112, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

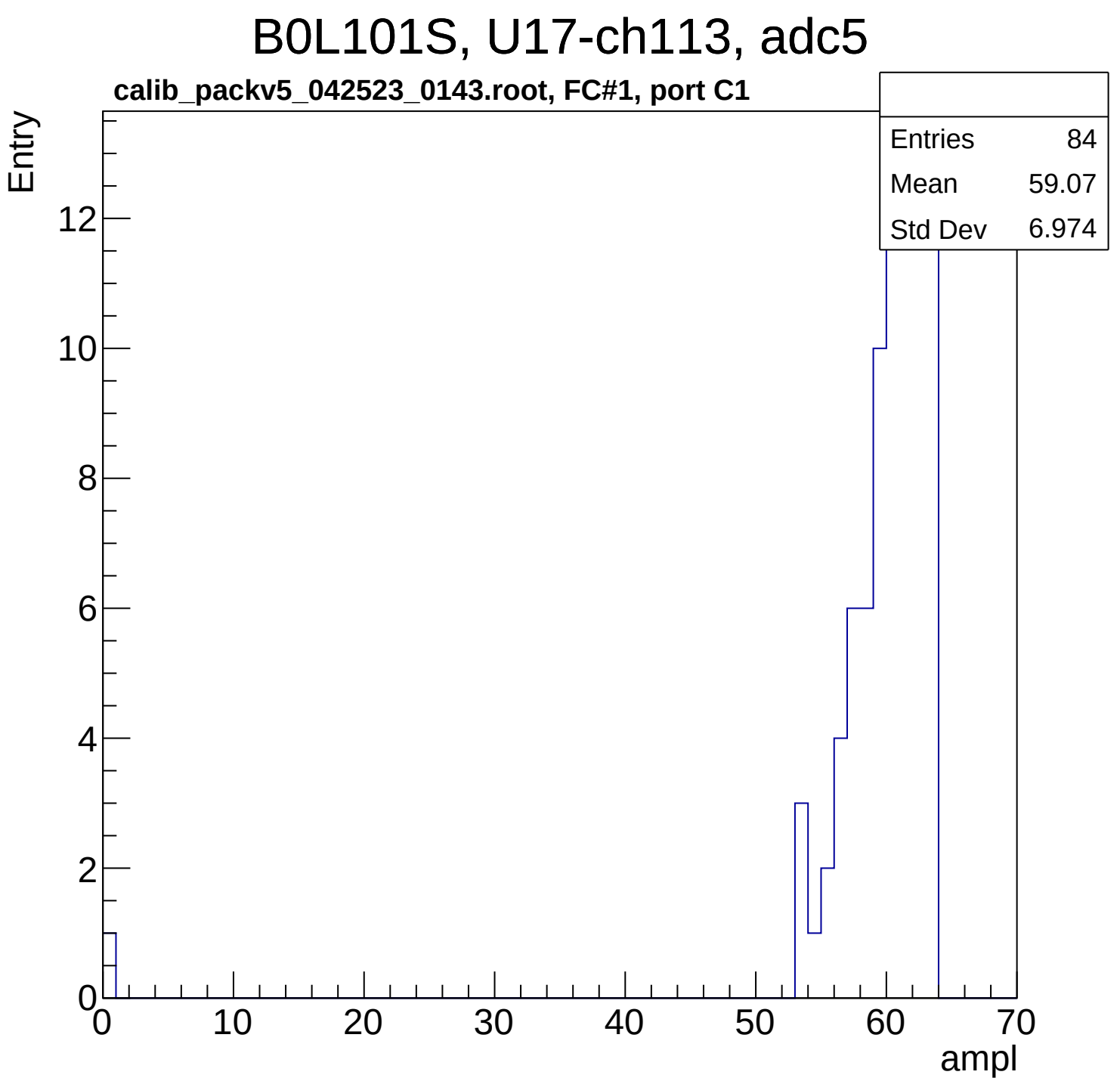

B0L101S, U17-ch113, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 99
Mean 28.32
Std Dev 4.374
Gaus mean : 29.0345
Gaus Width: 3.7038
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

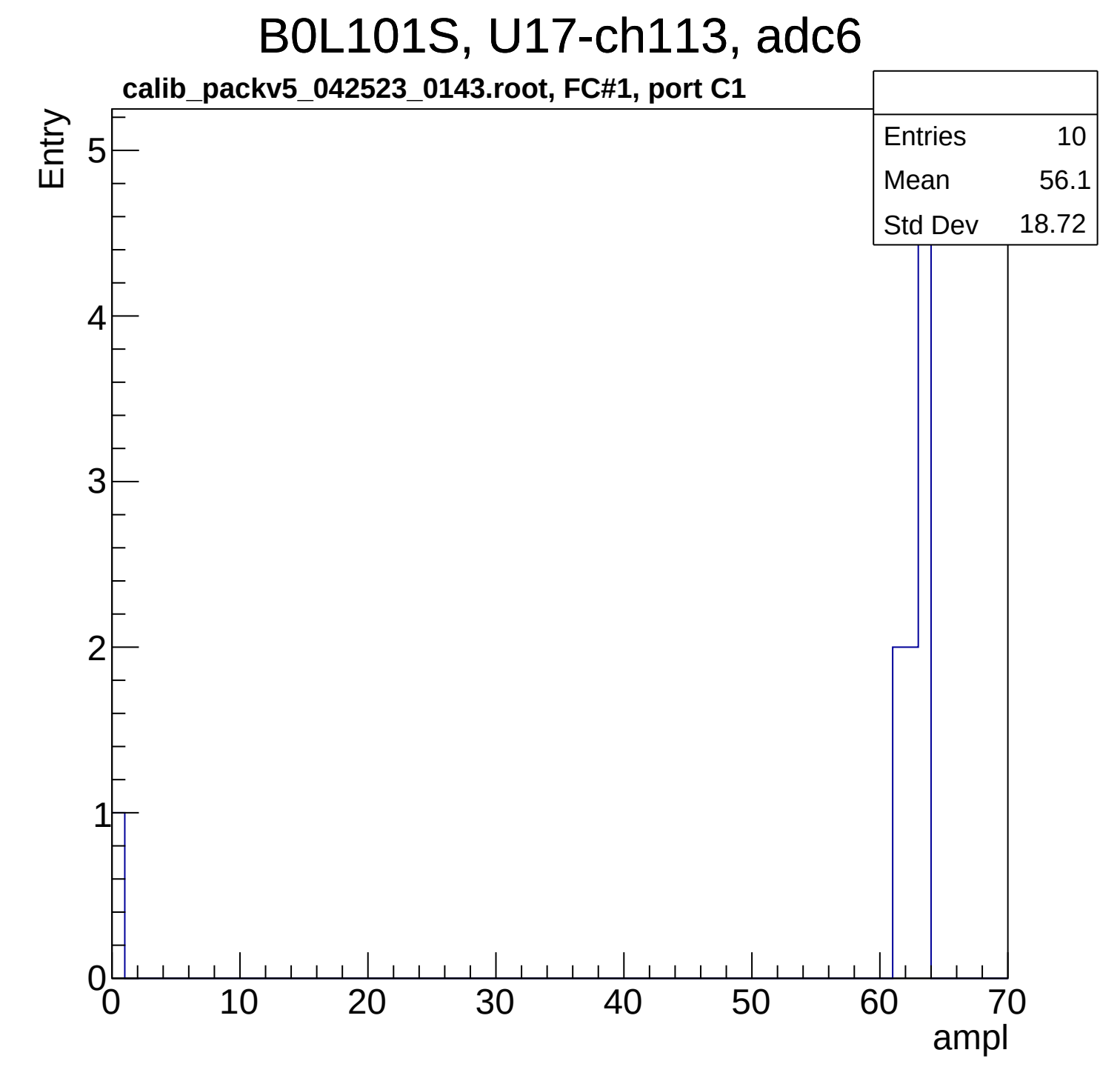

B0L101S, U17-ch113, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 180
Mean 35.59
Std Dev 4.397
Gaus mean : 36.3591
Gaus Width: 4.4414
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

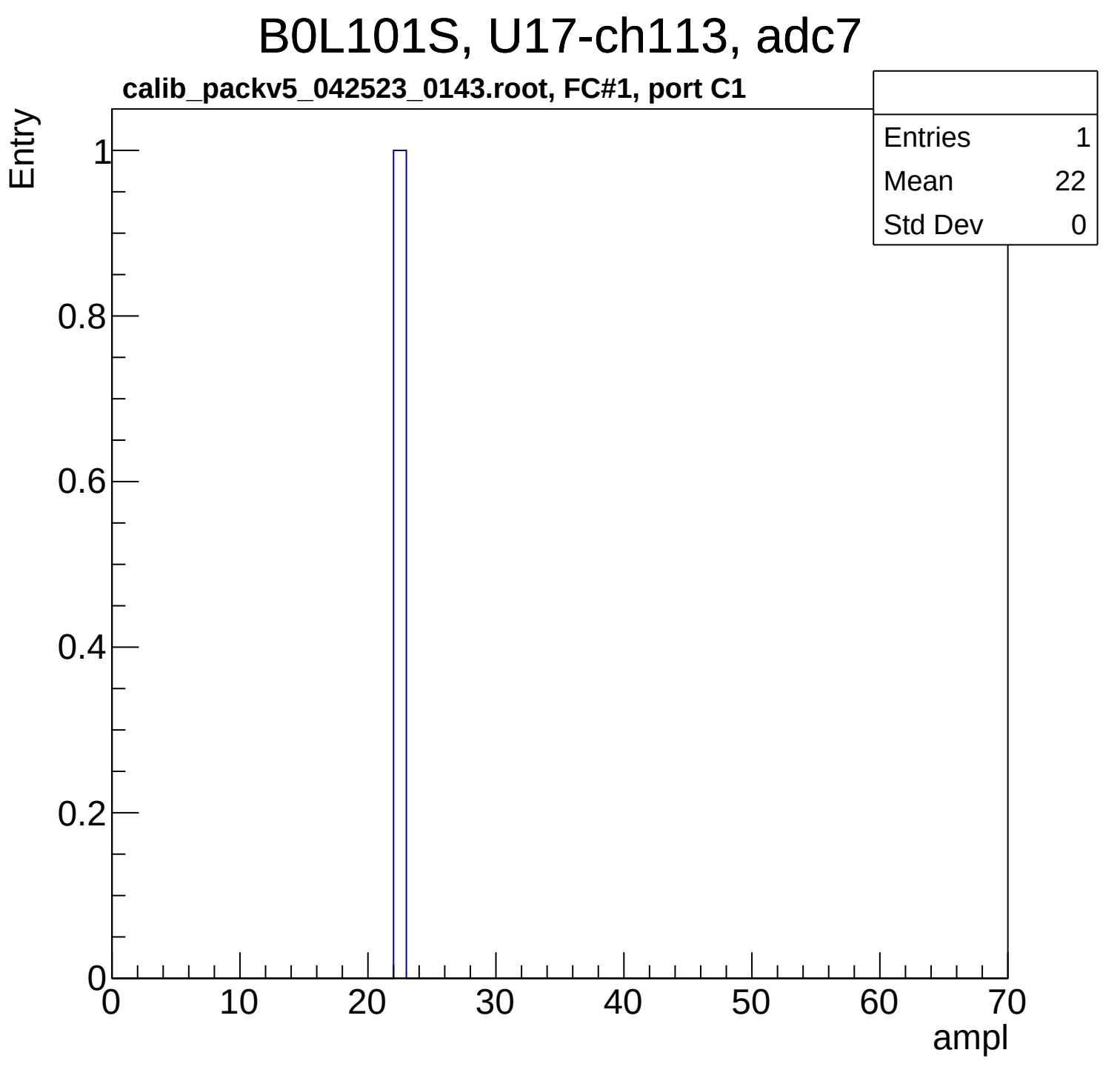

B0L101S, U17-ch113, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 43.51
Std Dev 3.978
Gaus mean : 44.0557
Gaus Width: 3.7596
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

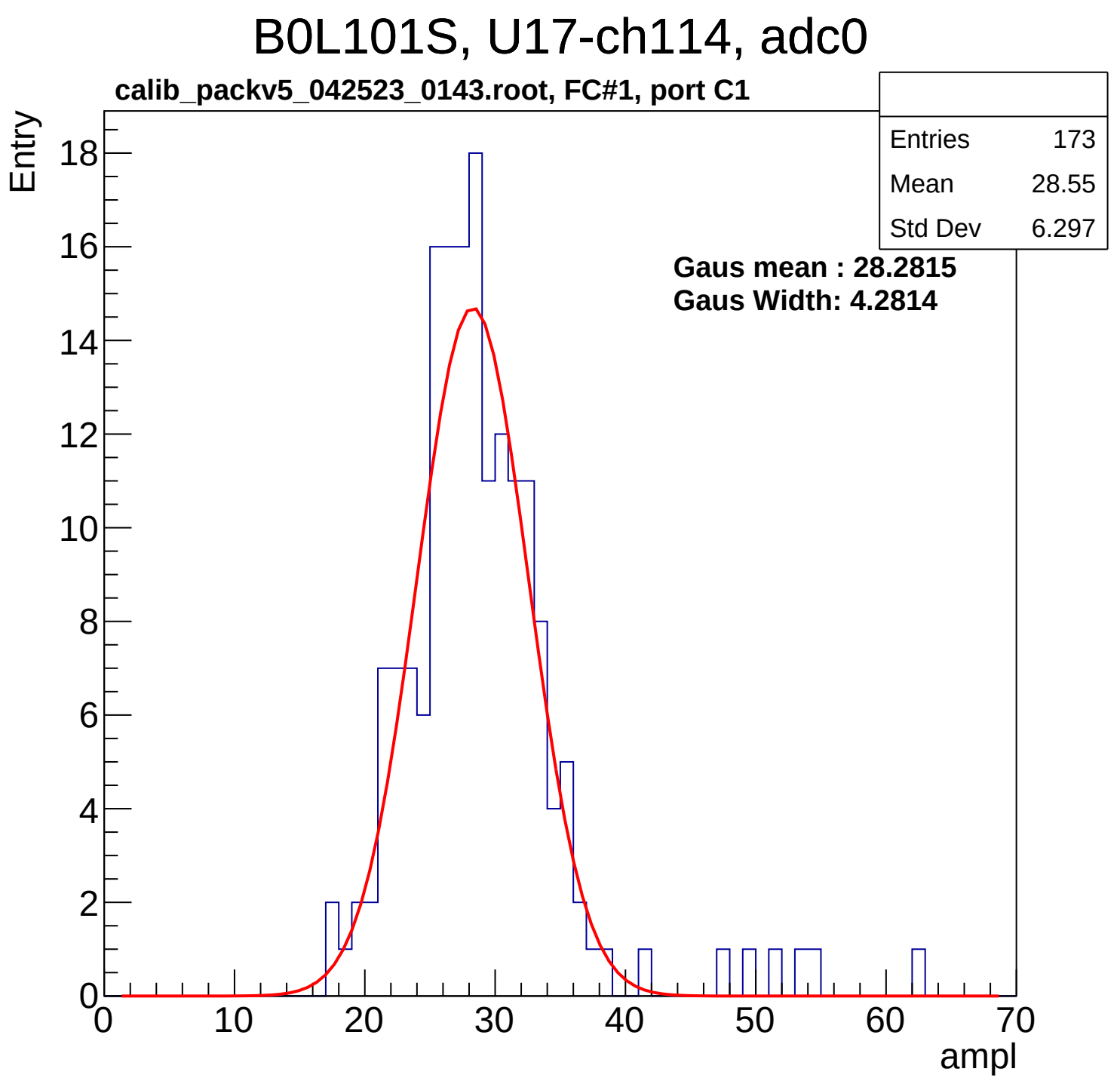

B0L101S, U17-ch113, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 50.09
Std Dev 3.658
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

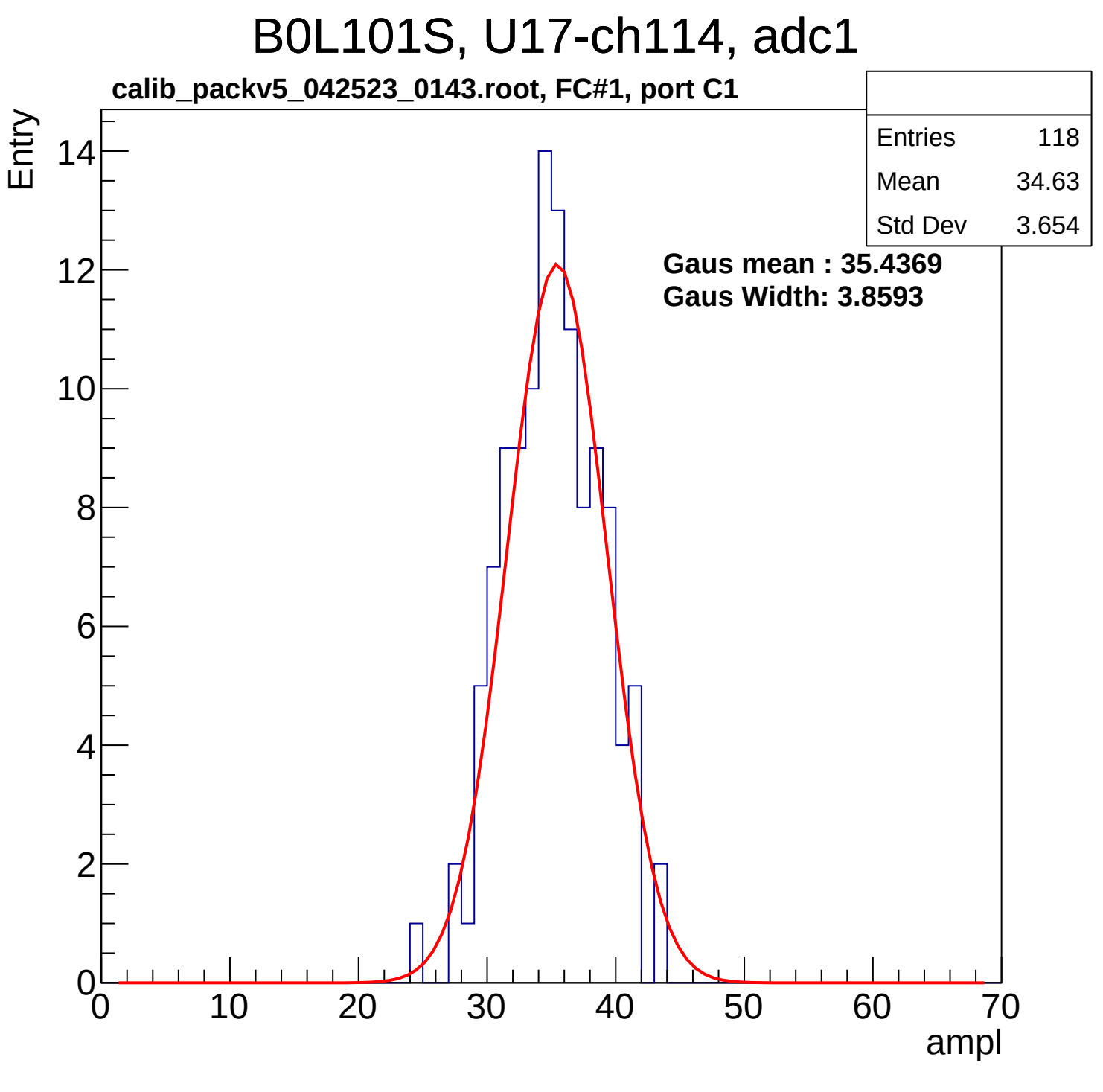

B0L101S, U17-ch113, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 55.96
Std Dev 3.71
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

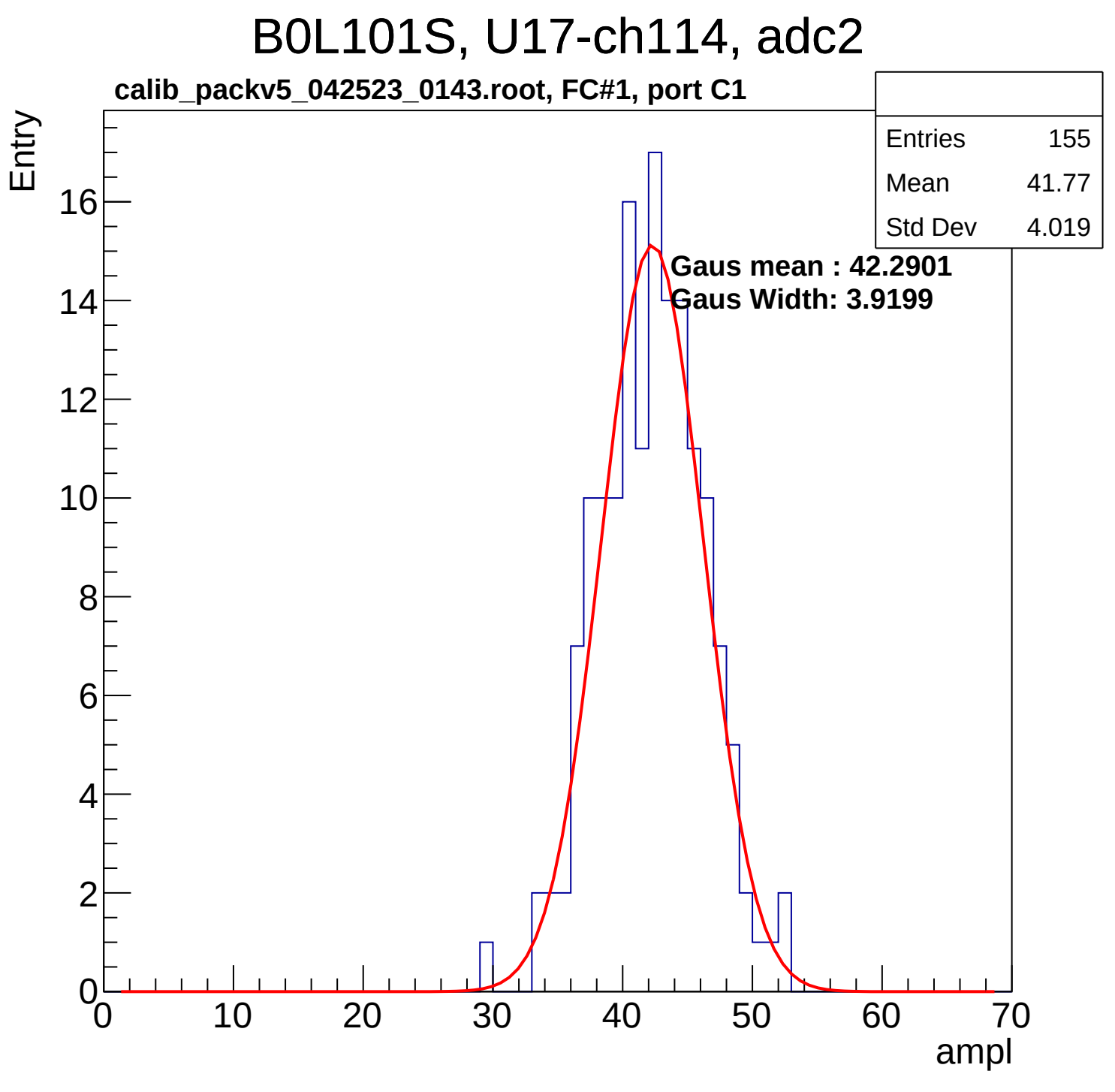

B0L101S, U17-ch113, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 84
Mean 59.07
Std Dev 6.974
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

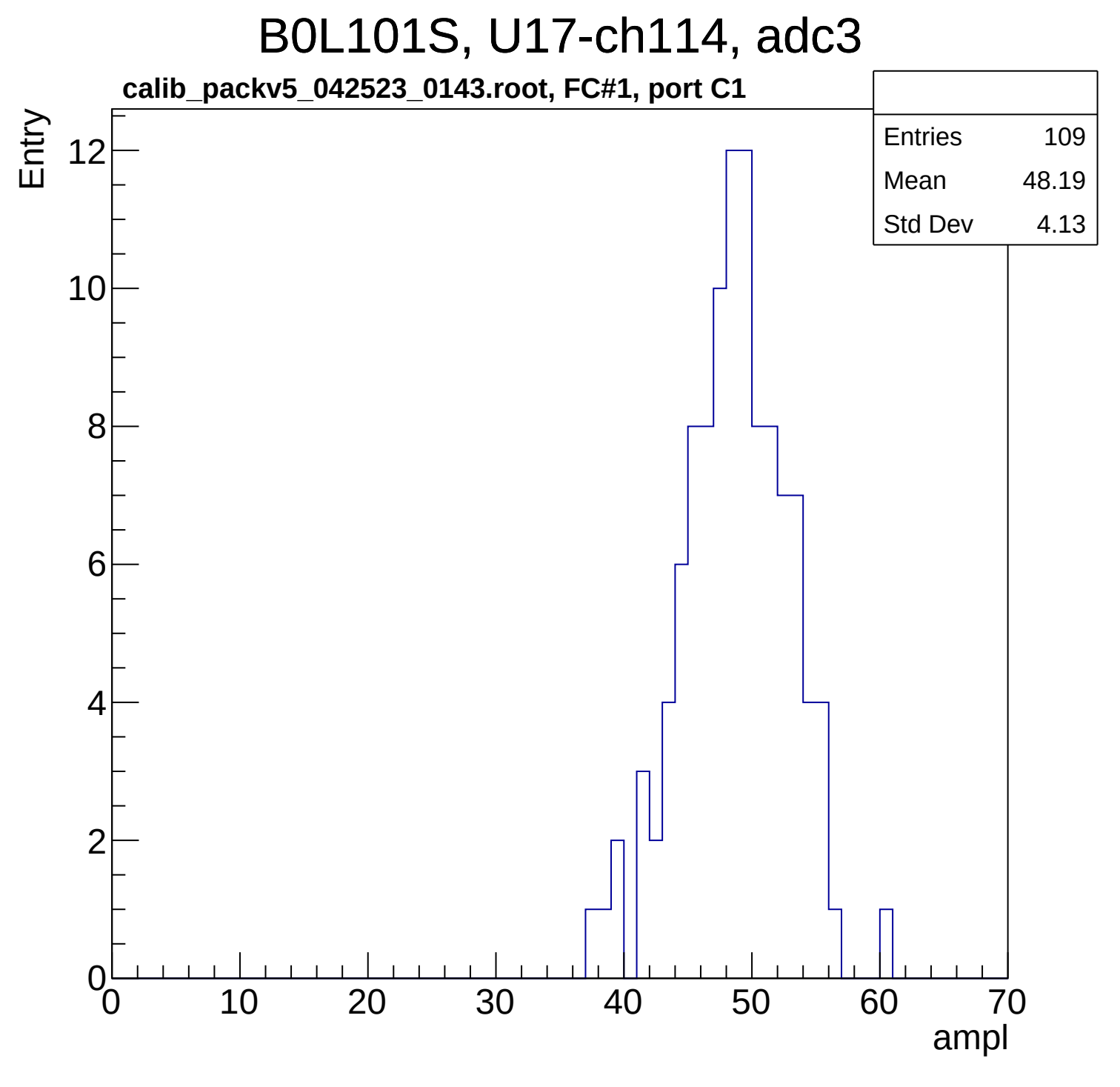

B0L101S, U17-ch113, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 10
Mean 56.1
Std Dev 18.72
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

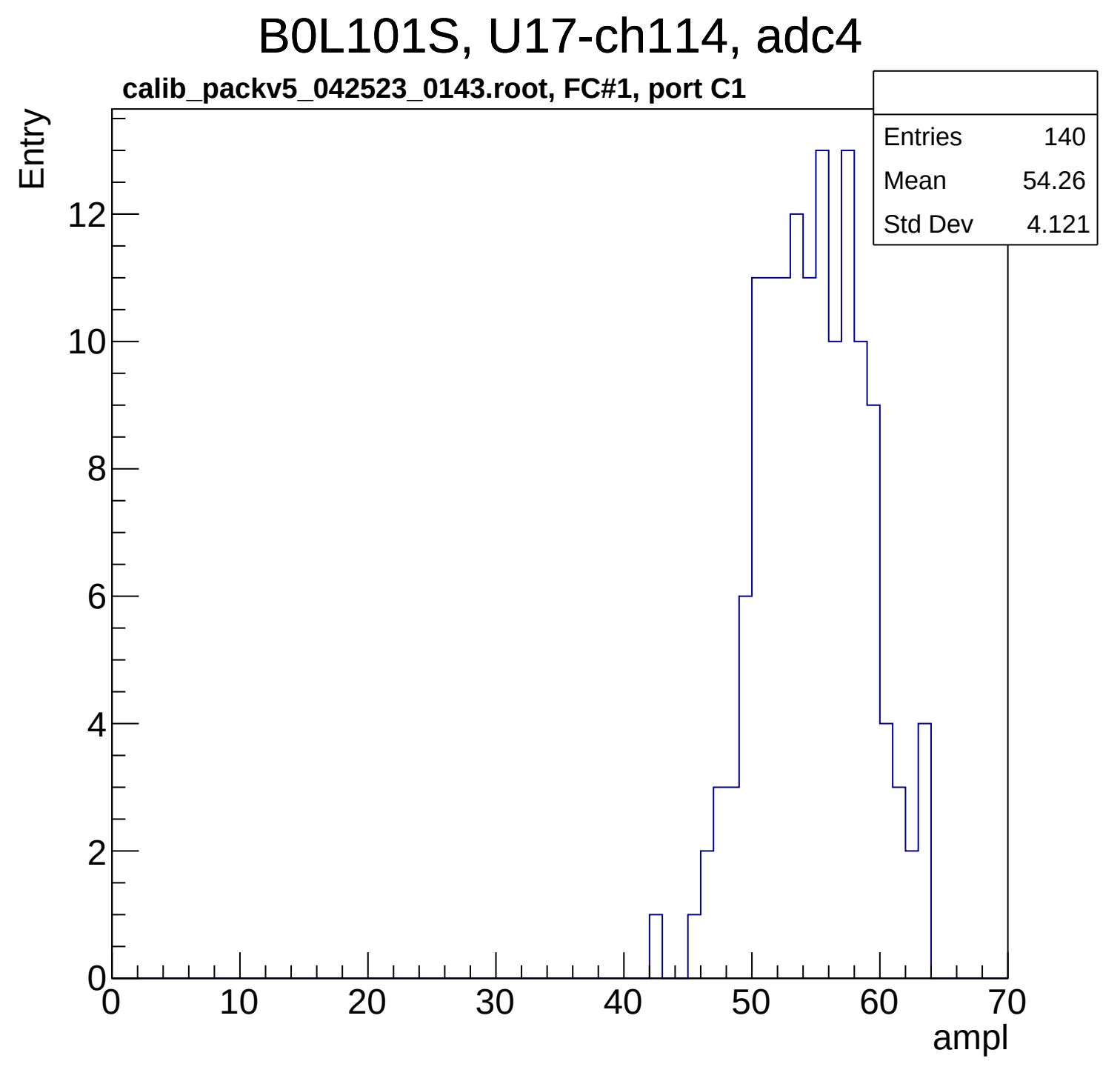

B0L101S, U17-ch113, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

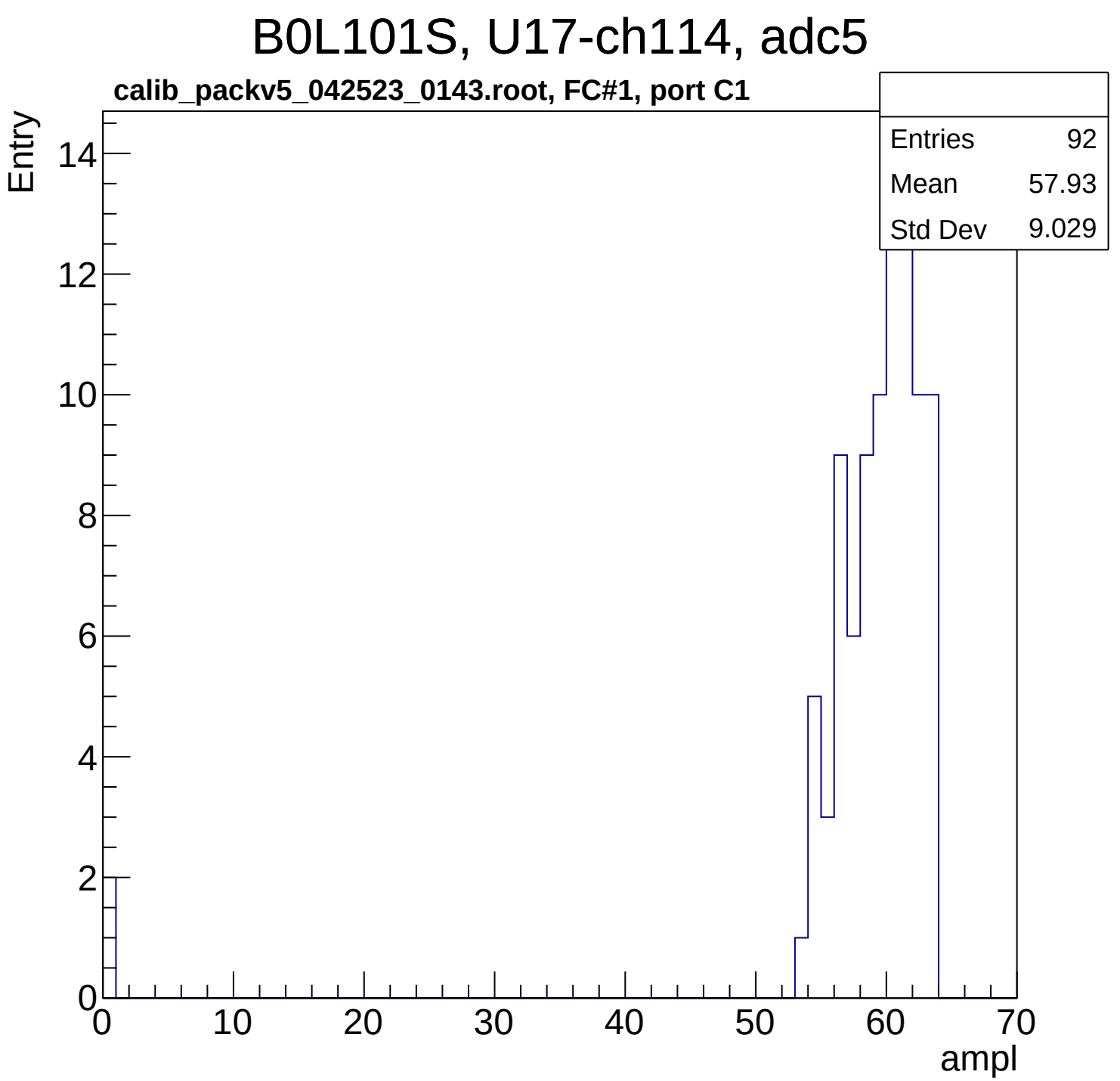

B0L101S, U17-ch114, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 173
Mean 28.55
Std Dev 6.297
Gaus mean : 28.2815
Gaus Width: 4.2814
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

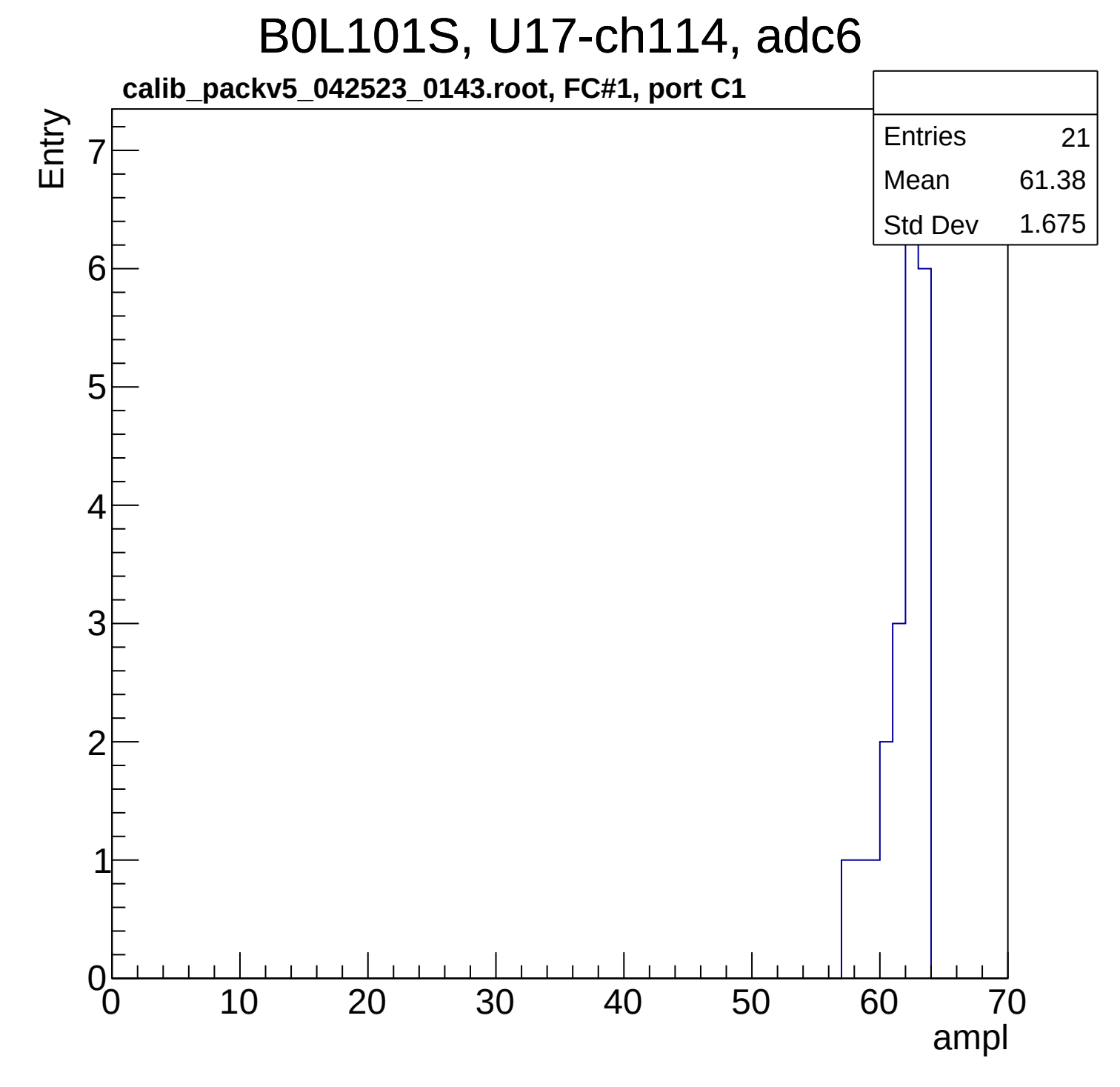

B0L101S, U17-ch114, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 34.63
Std Dev 3.654
Gaus mean : 35.4369
Gaus Width: 3.8593
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

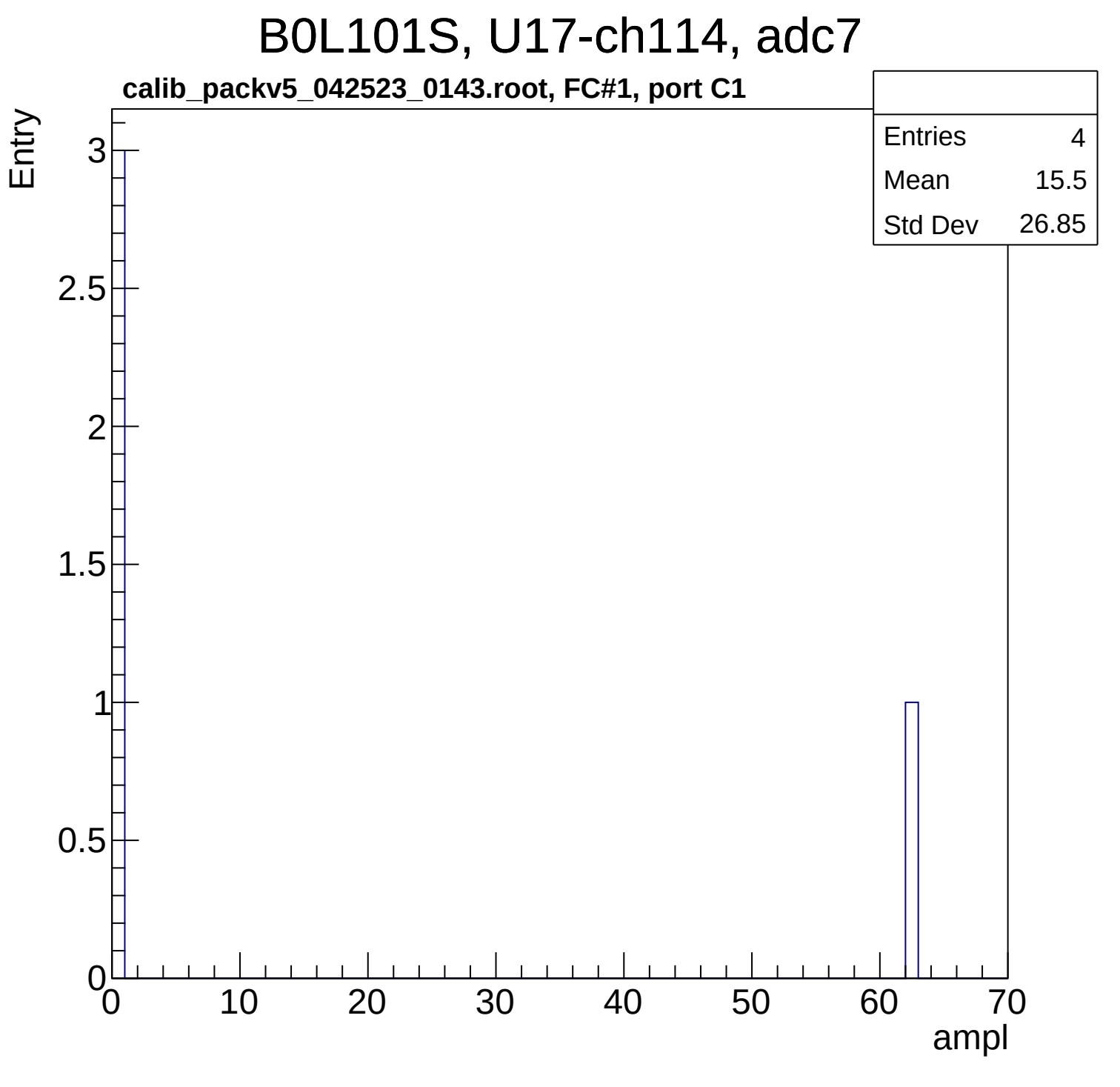

B0L101S, U17-ch114, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 41.77
Std Dev 4.019
Gaus mean : 42.2901
Gaus Width: 3.9199
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

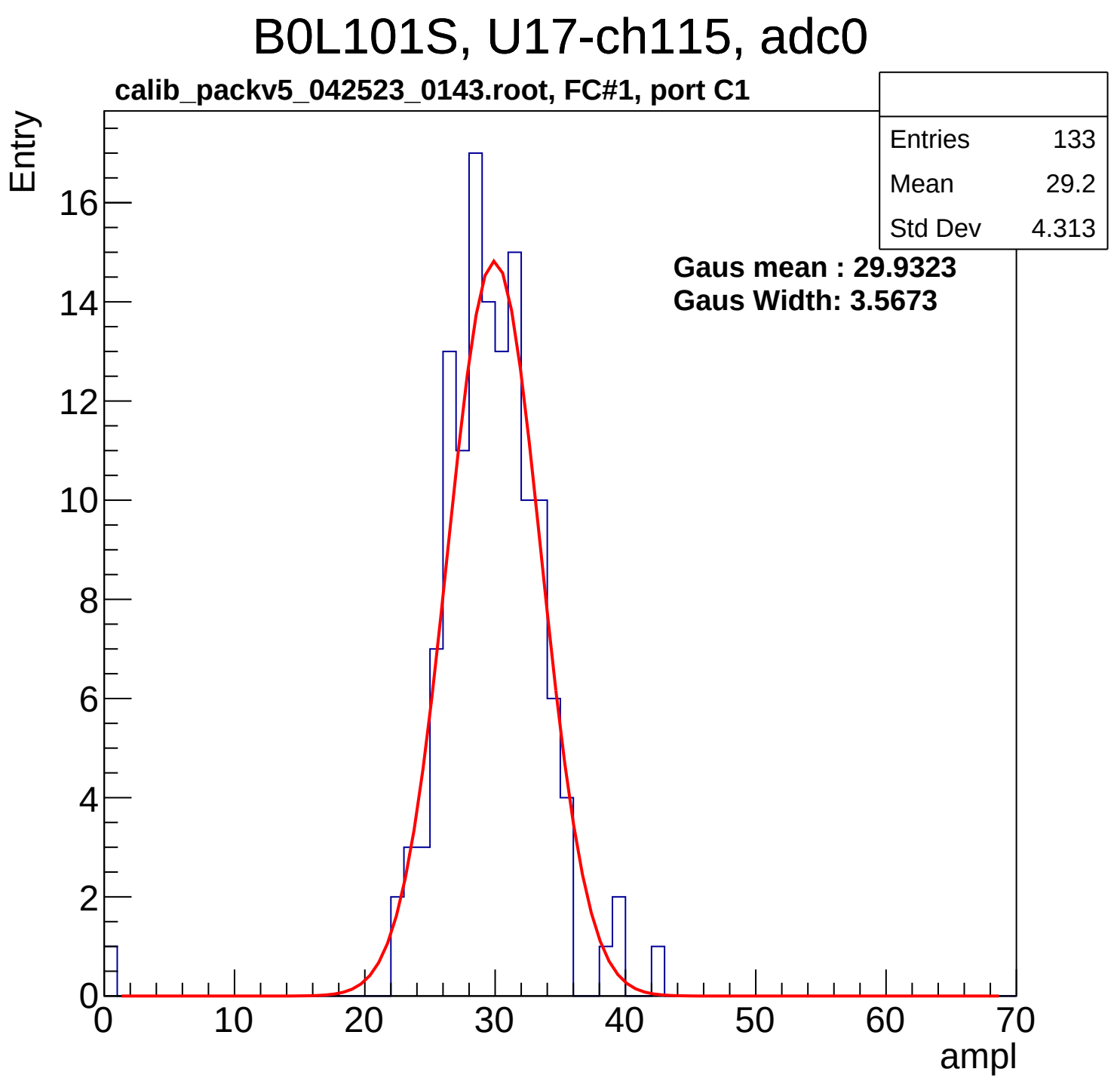

B0L101S, U17-ch114, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 48.19
Std Dev 4.13
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

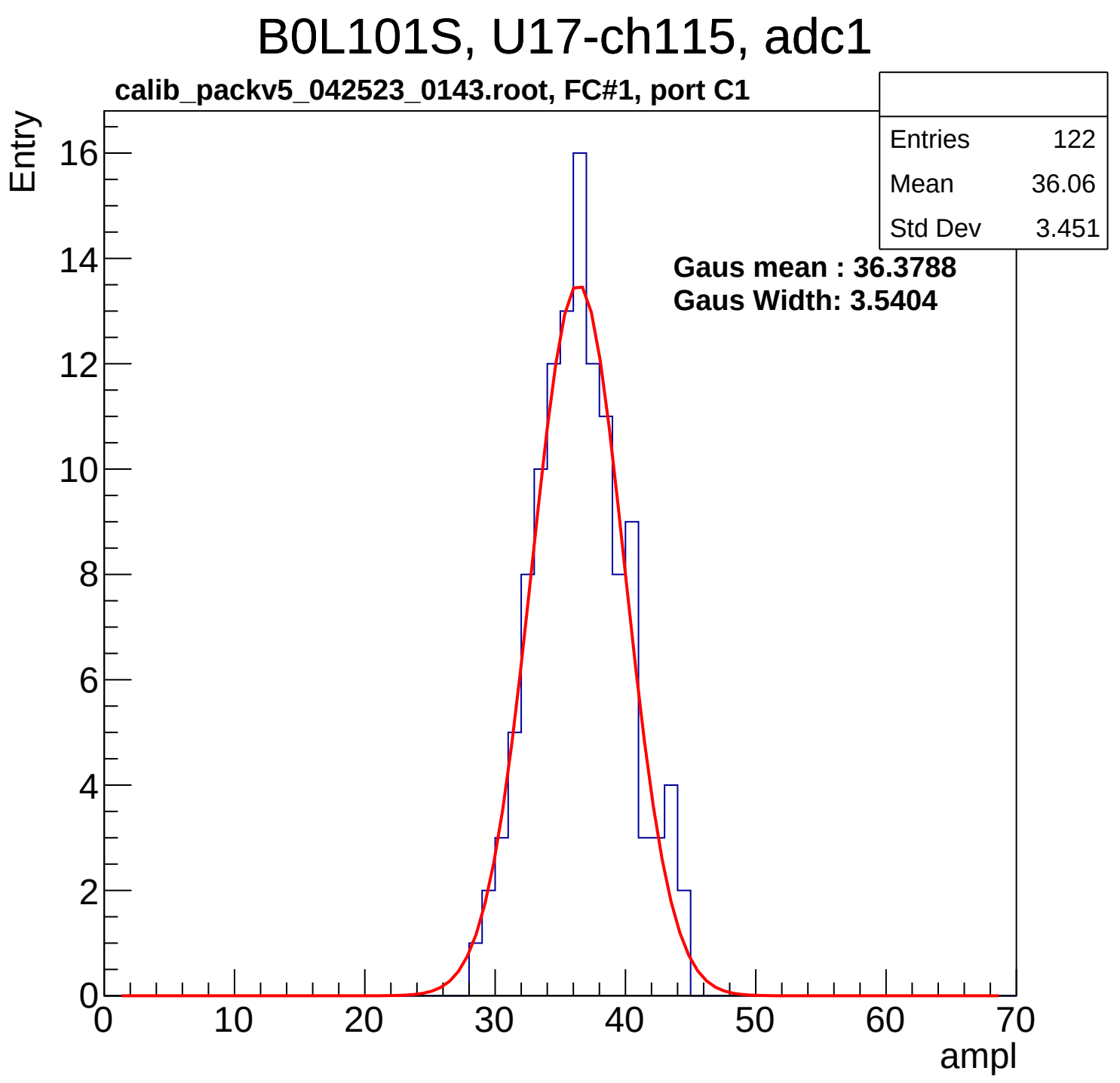

B0L101S, U17-ch114, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 54.26
Std Dev 4.121
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

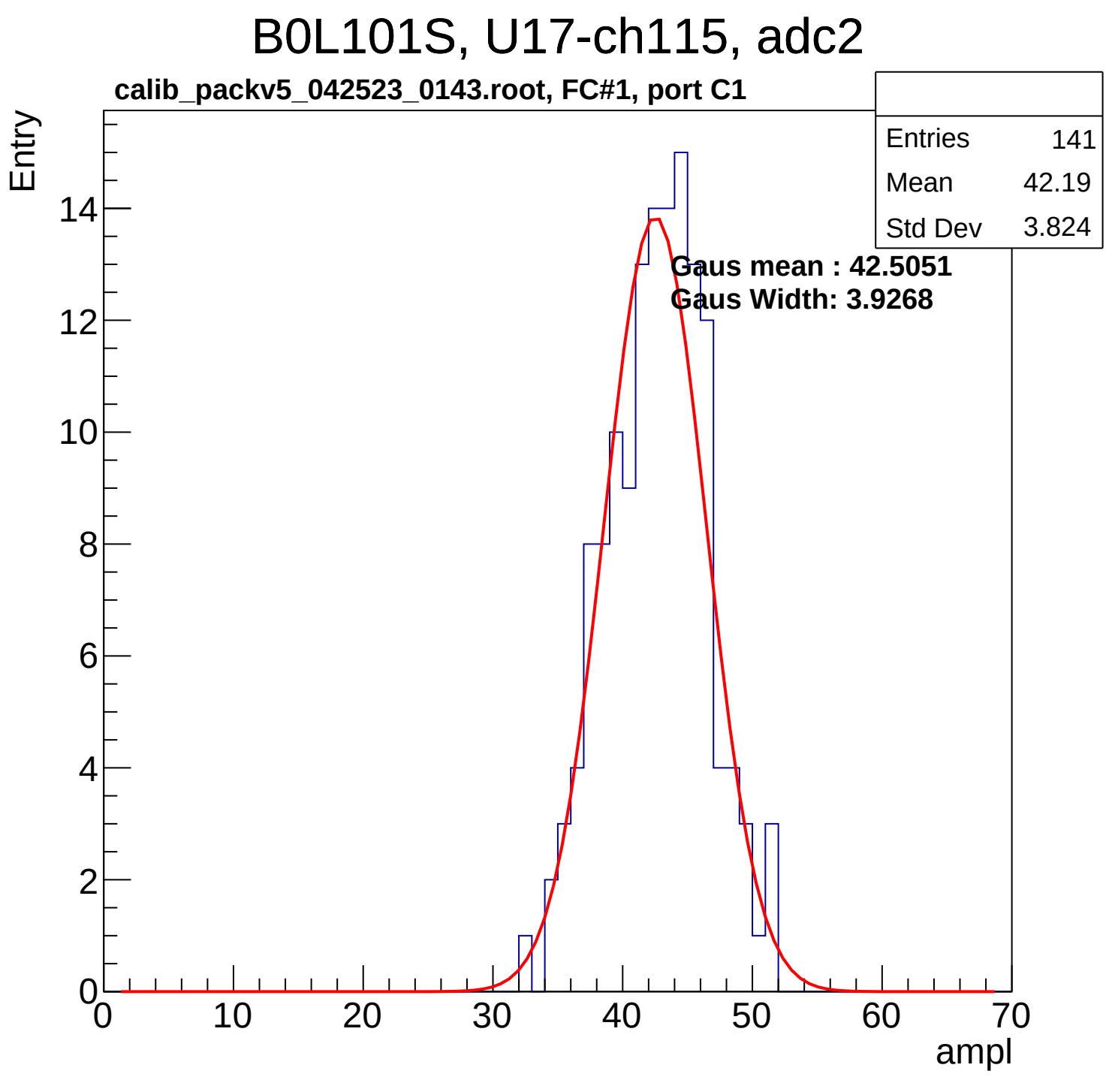

B0L101S, U17-ch114, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 92
Mean 57.93
Std Dev 9.029
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

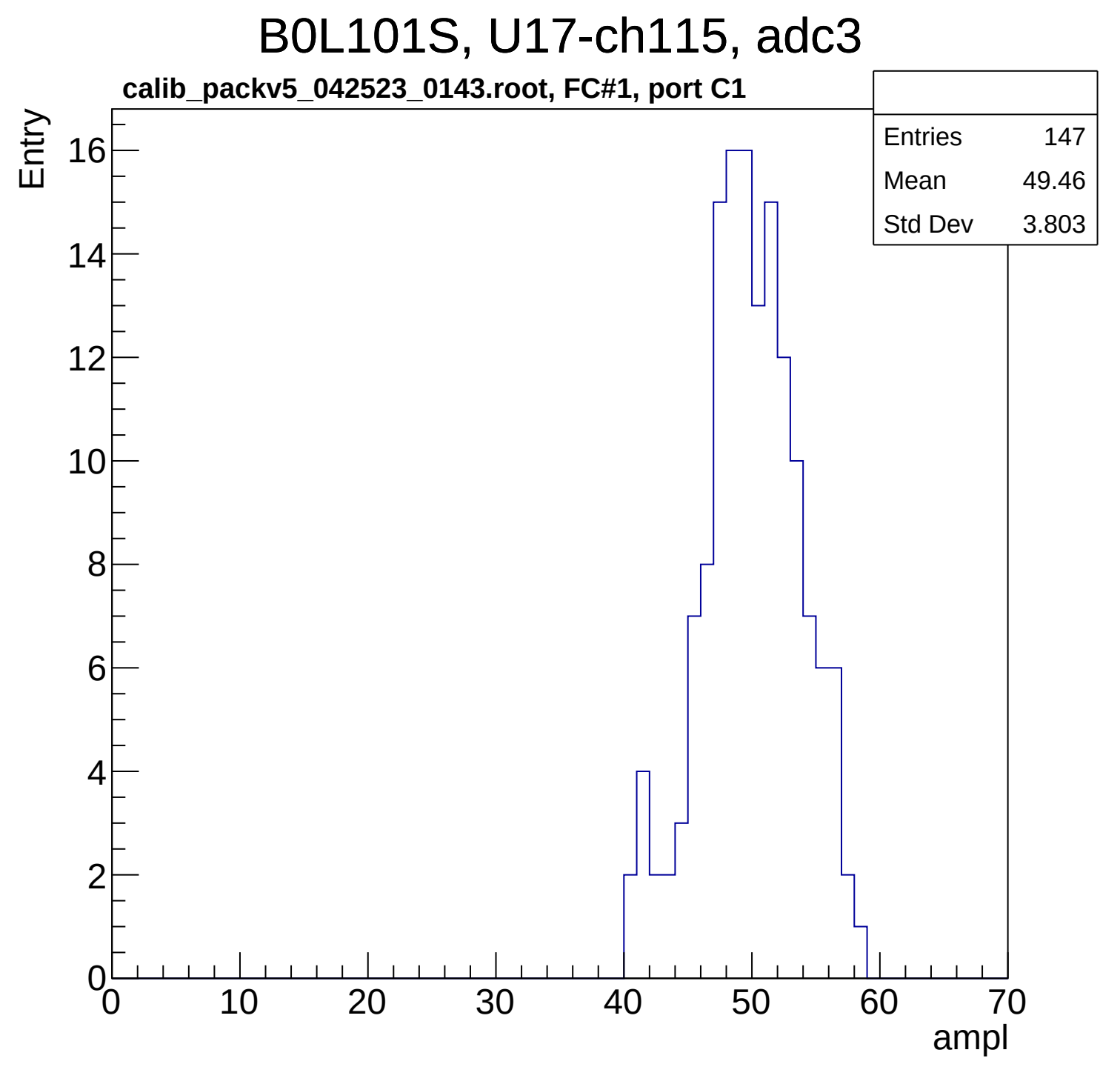

B0L101S, U17-ch114, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 21
Mean 61.38
Std Dev 1.675
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

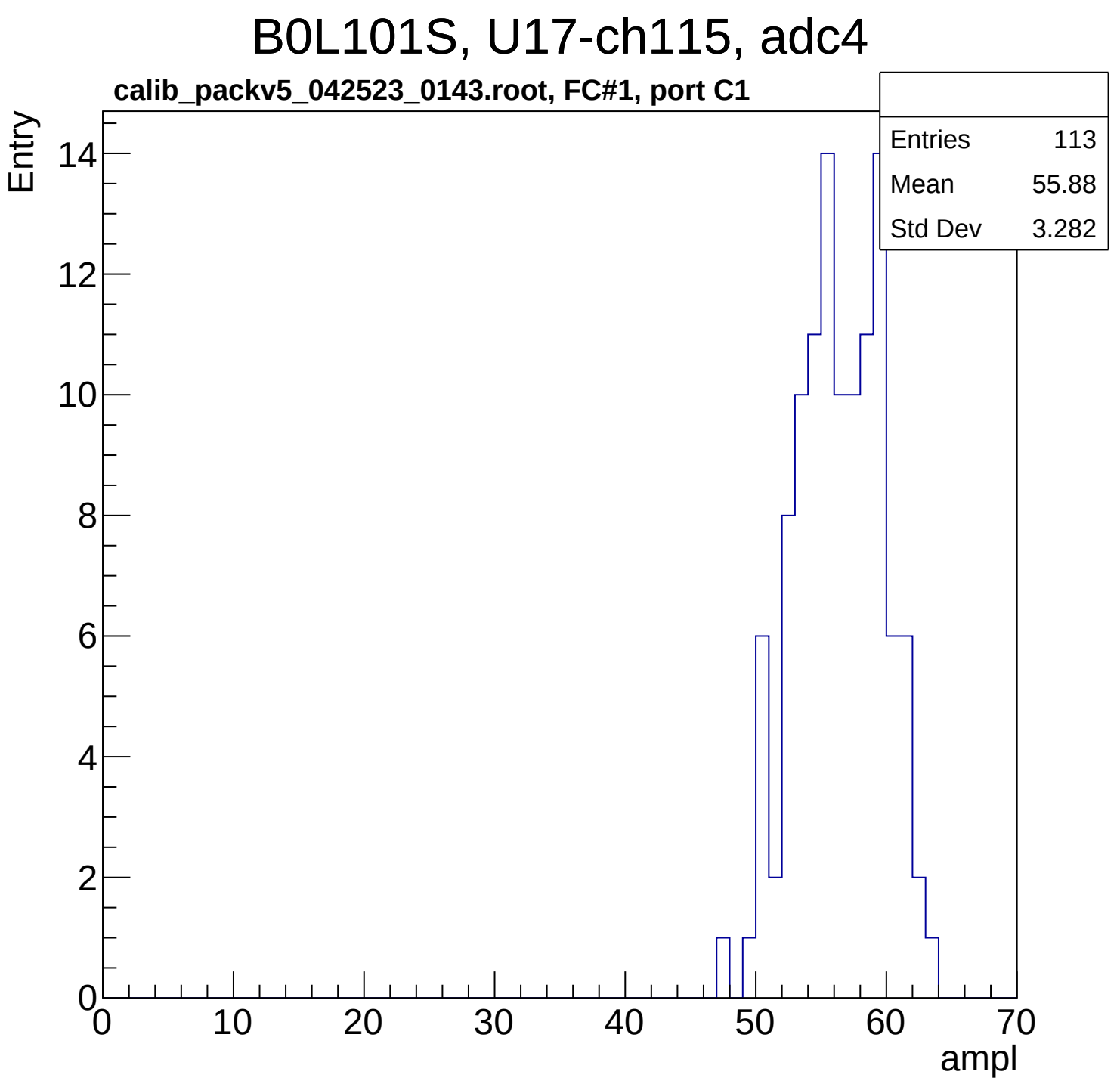

B0L101S, U17-ch114, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

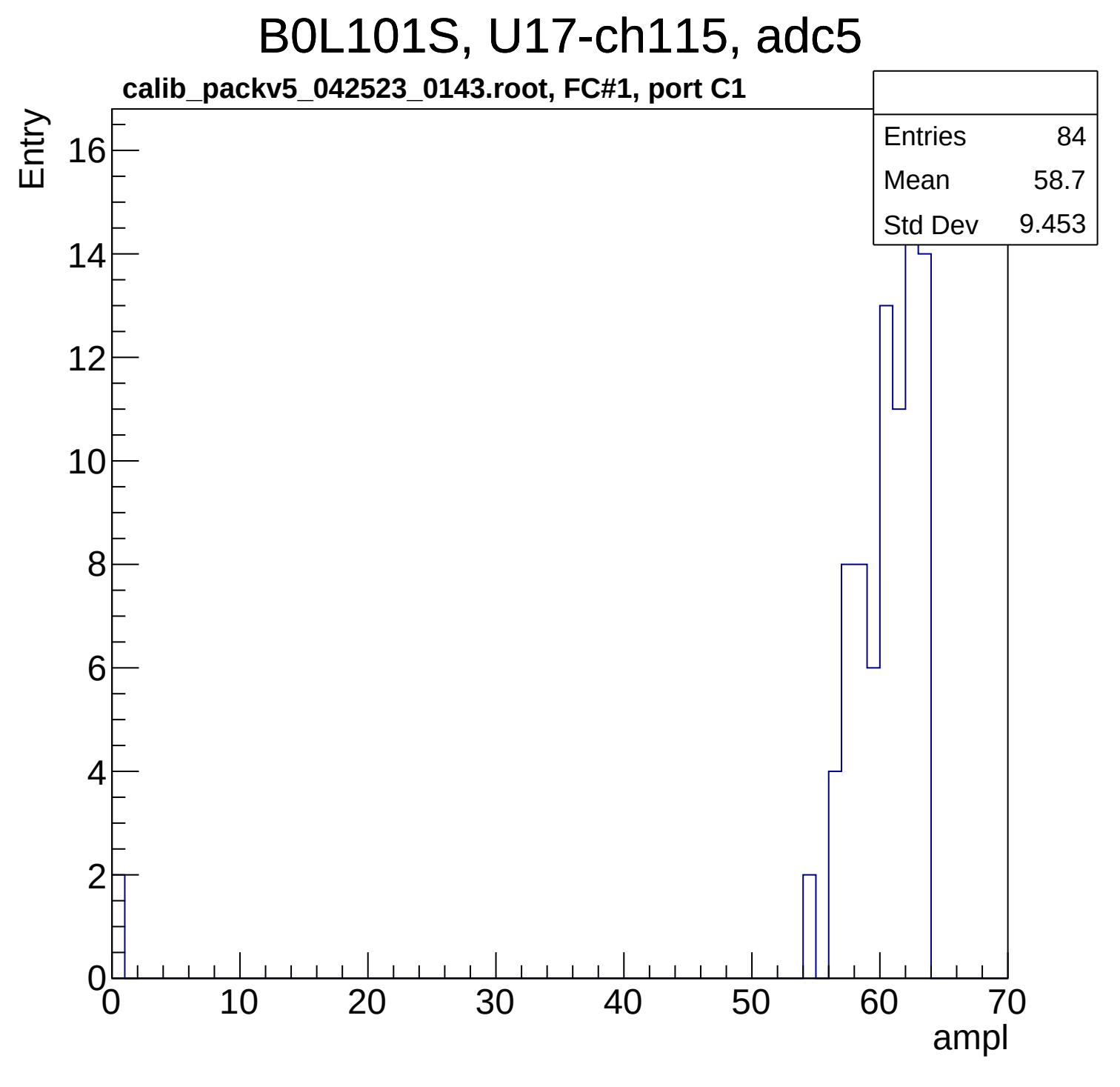

B0L101S, U17-ch115, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 133
Mean 29.2
Std Dev 4.313
Gaus mean : 29.9323
Gaus Width: 3.5673
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

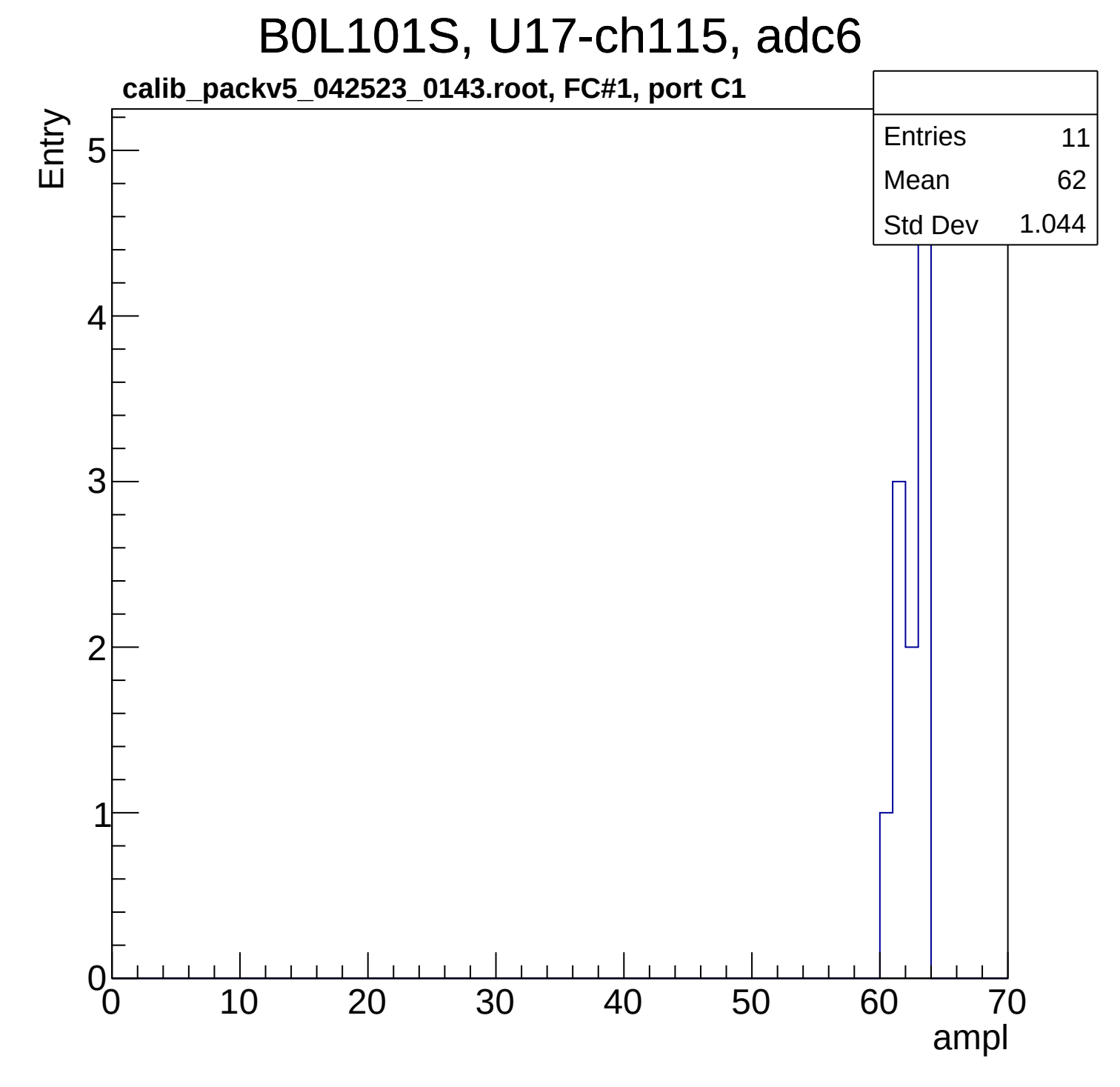

B0L101S, U17-ch115, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 36.06
Std Dev 3.451
Gaus mean : 36.3788
Gaus Width: 3.5404
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

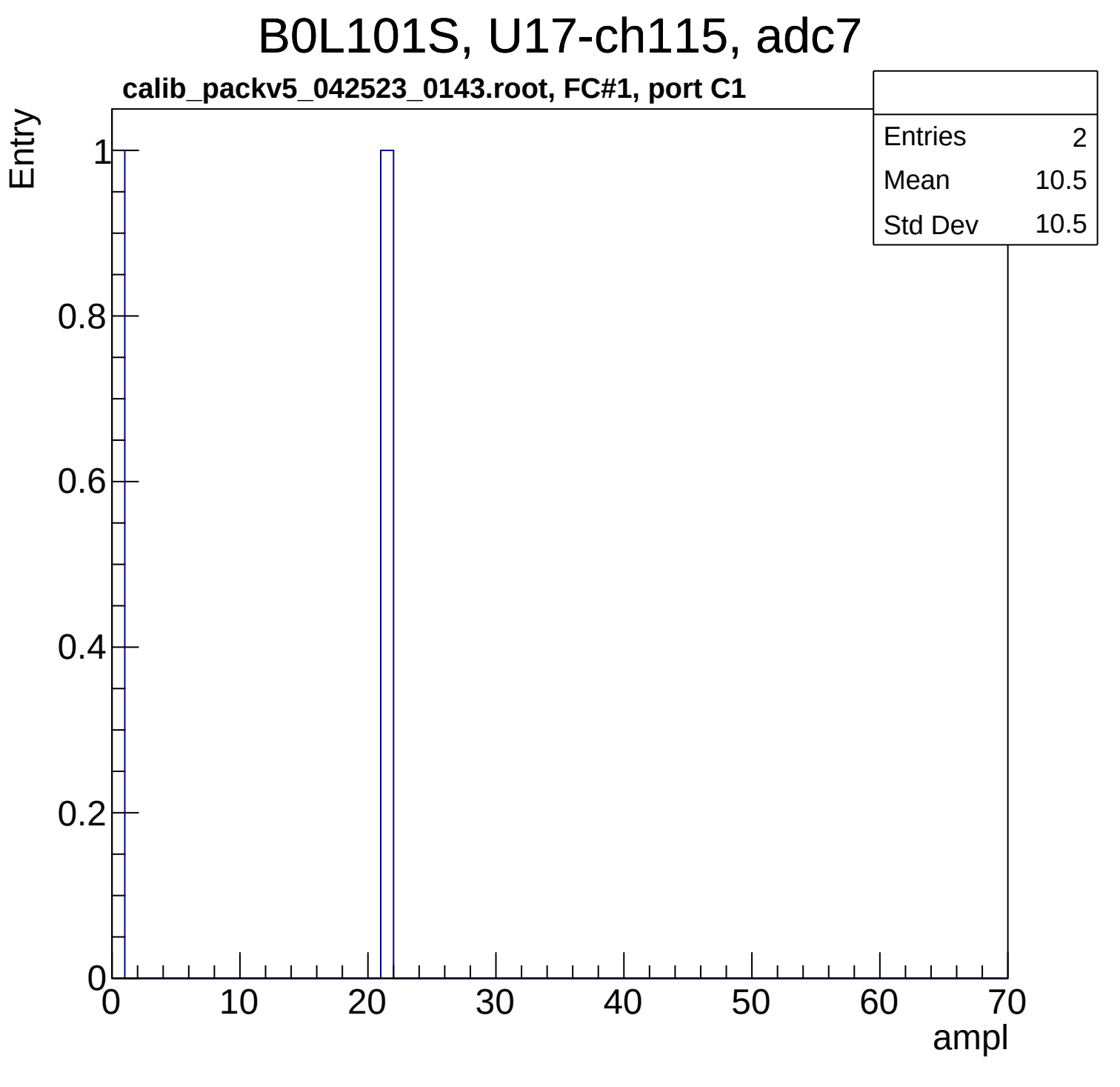

B0L101S, U17-ch115, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 42.19
Std Dev 3.824
Gaus mean : 42.5051
Gaus Width: 3.9268
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

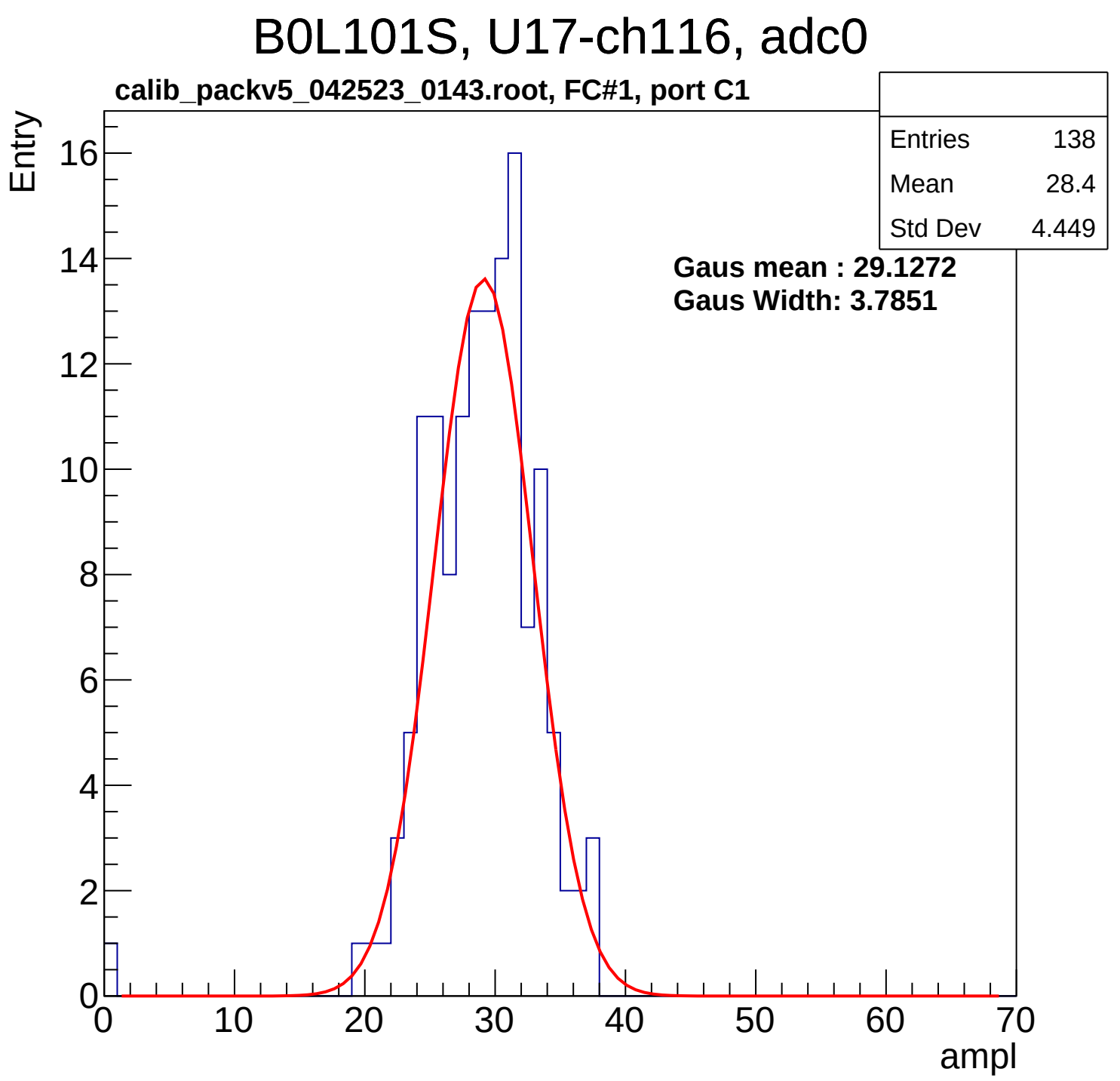

B0L101S, U17-ch115, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 147
Mean 49.46
Std Dev 3.803
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

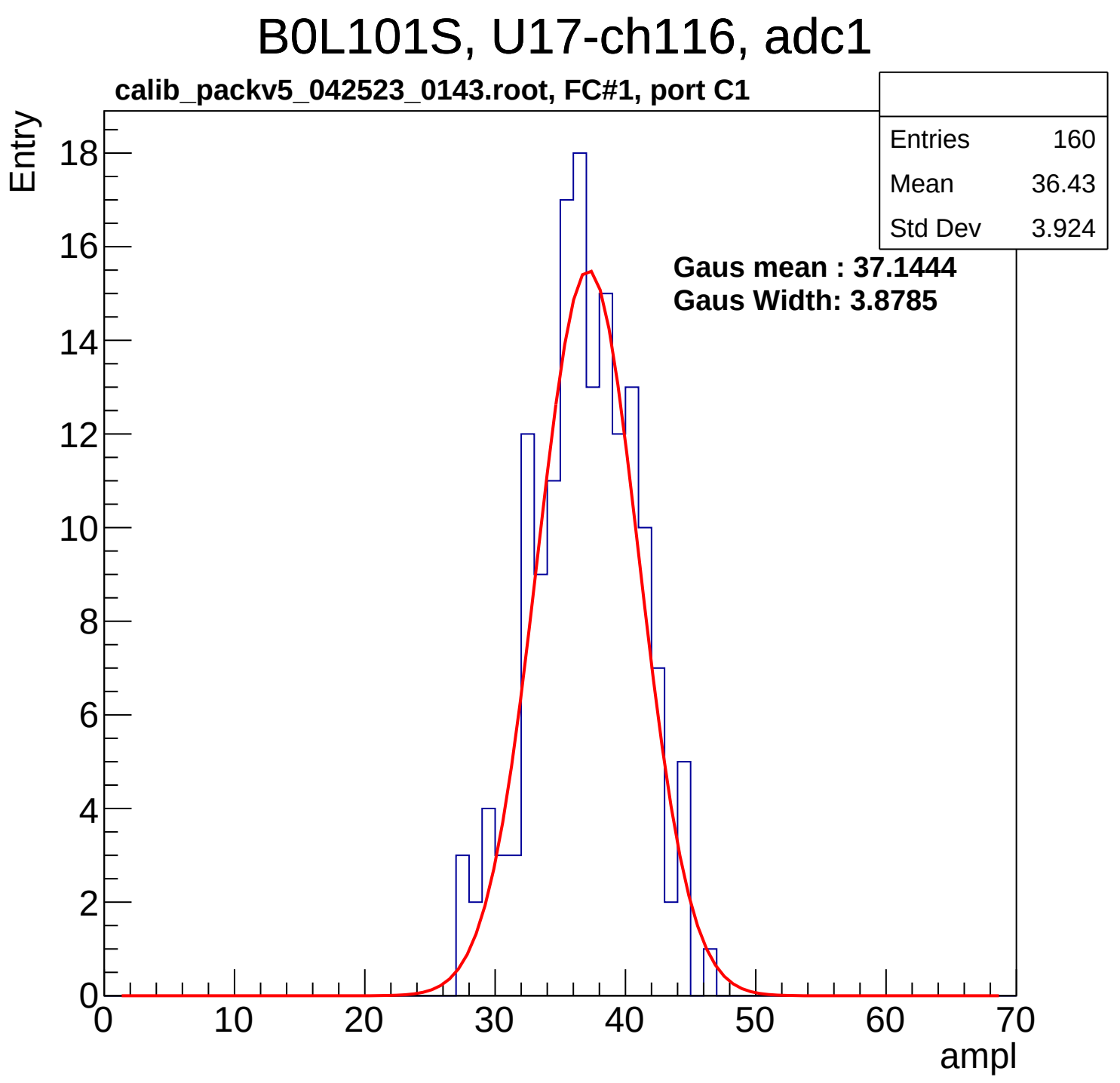

B0L101S, U17-ch115, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 55.88
Std Dev 3.282
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

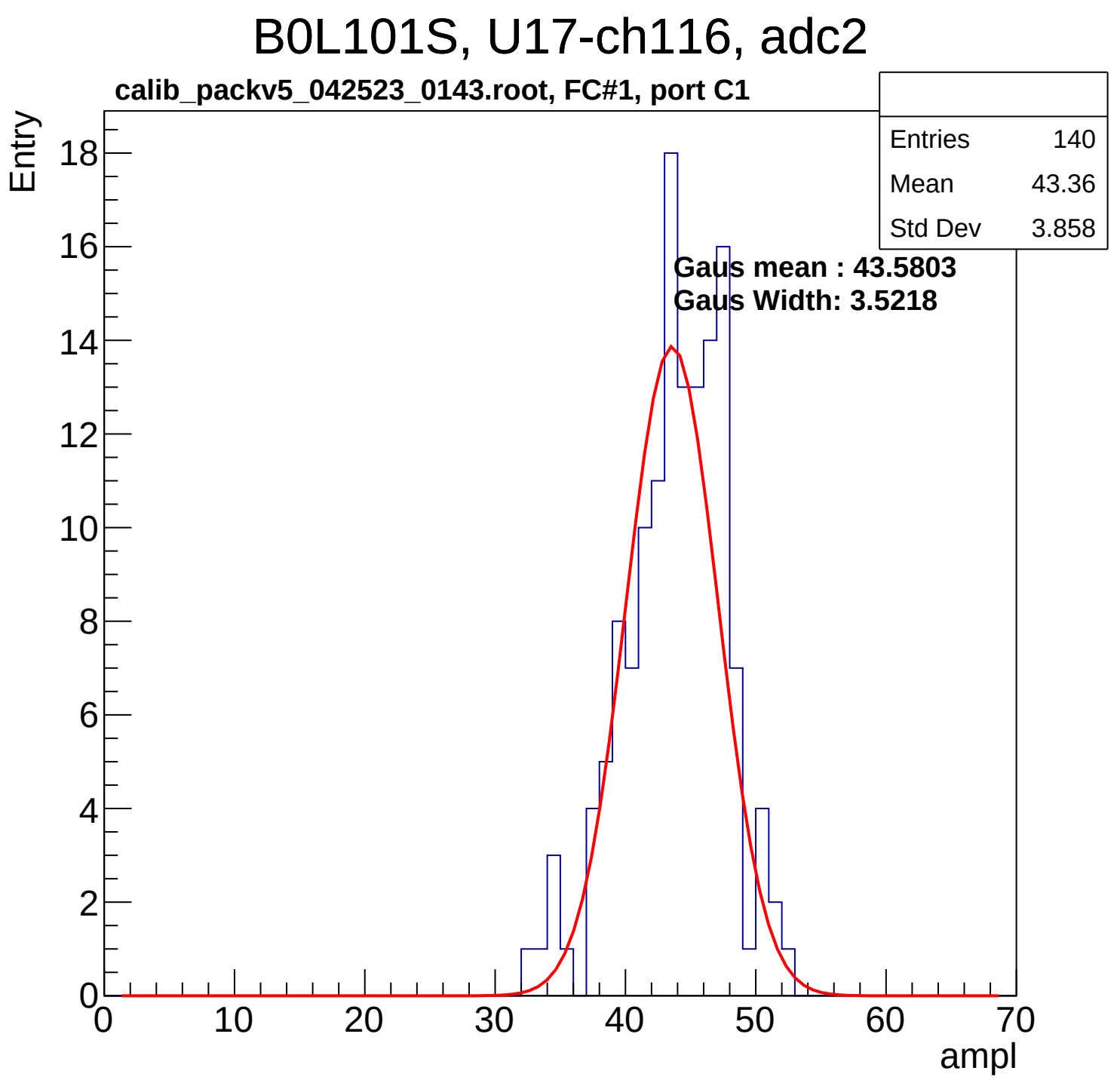

B0L101S, U17-ch115, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 84
Mean 58.7
Std Dev 9.453
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

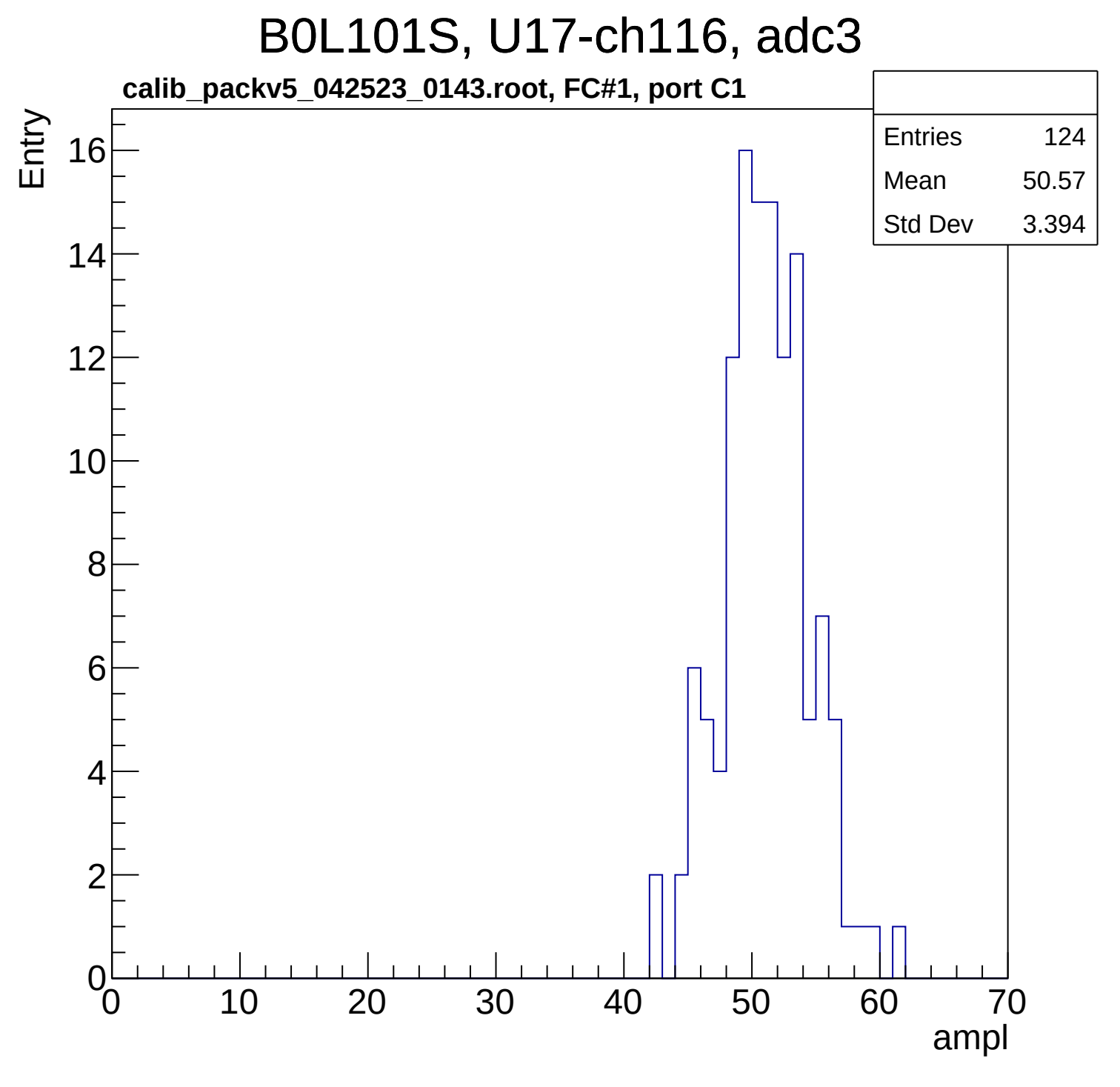

B0L101S, U17-ch115, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 62
Std Dev 1.044
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

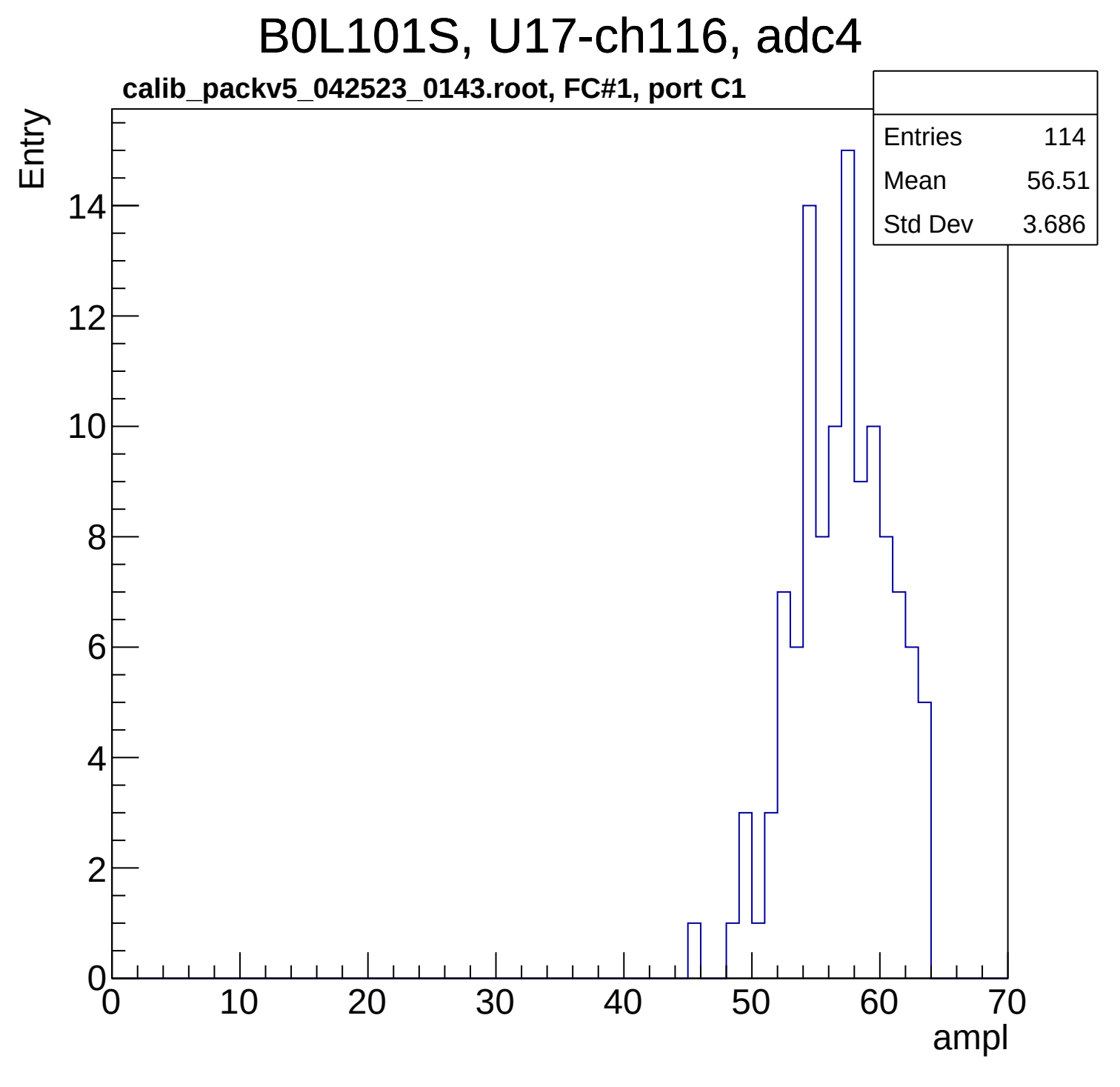

B0L101S, U17-ch115, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

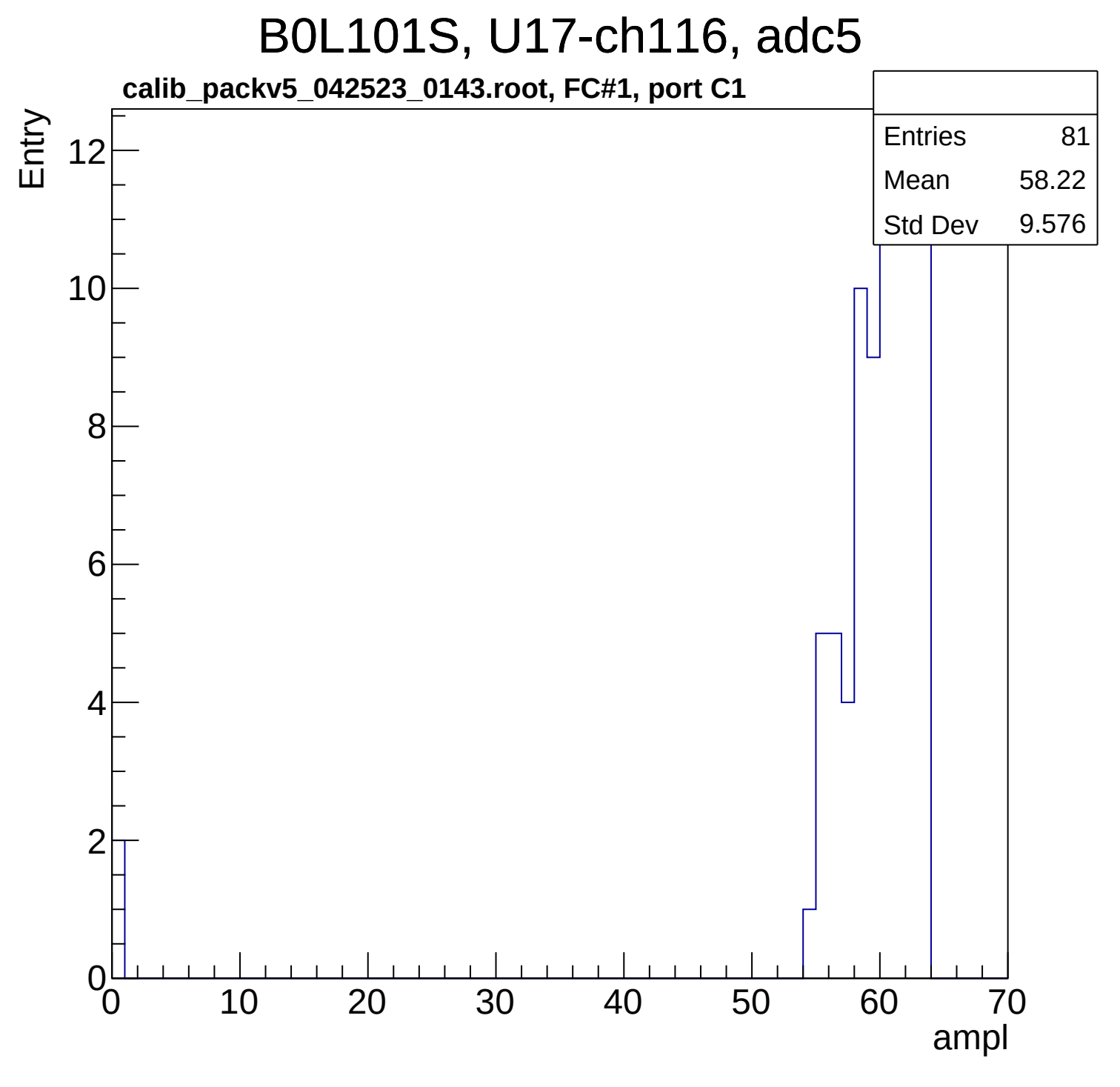

B0L101S, U17-ch116, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 28.4
Std Dev 4.449
Gaus mean : 29.1272
Gaus Width: 3.7851
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

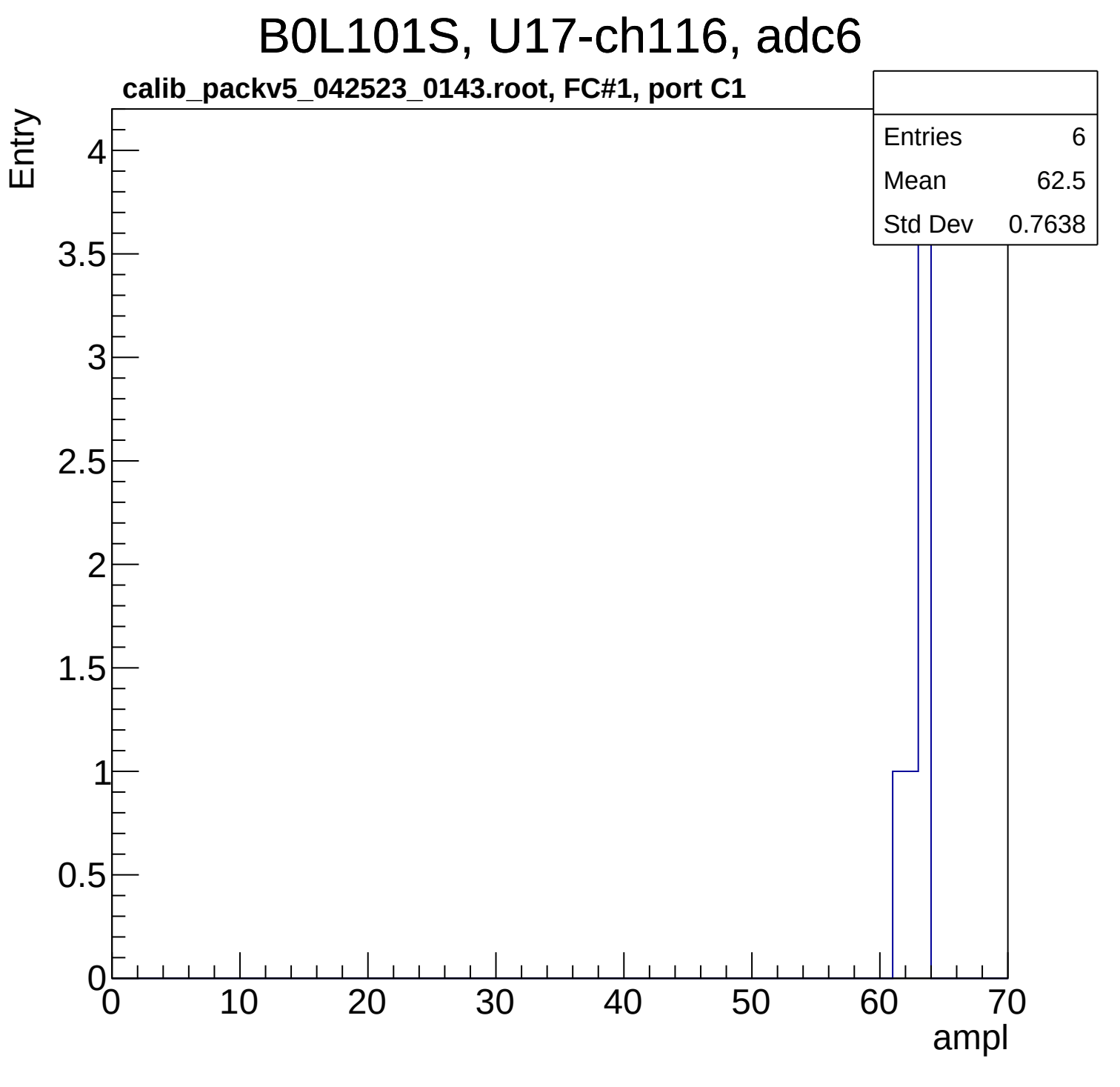

B0L101S, U17-ch116, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 160
Mean 36.43
Std Dev 3.924
Gaus mean : 37.1444
Gaus Width: 3.8785
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

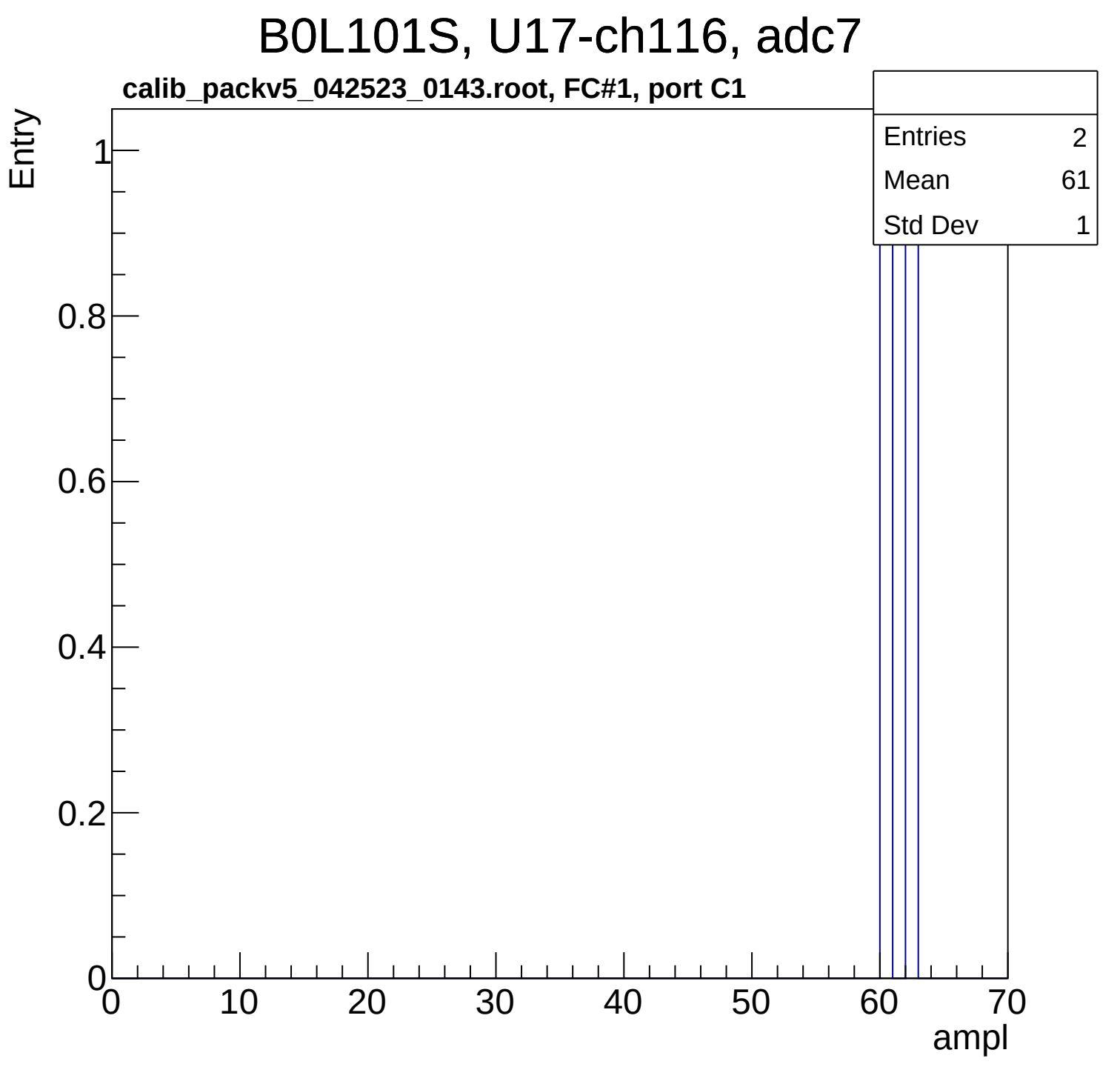

B0L101S, U17-ch116, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 140
Mean 43.36
Std Dev 3.858
Gaus mean : 43.5803
Gaus Width: 3.5218
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

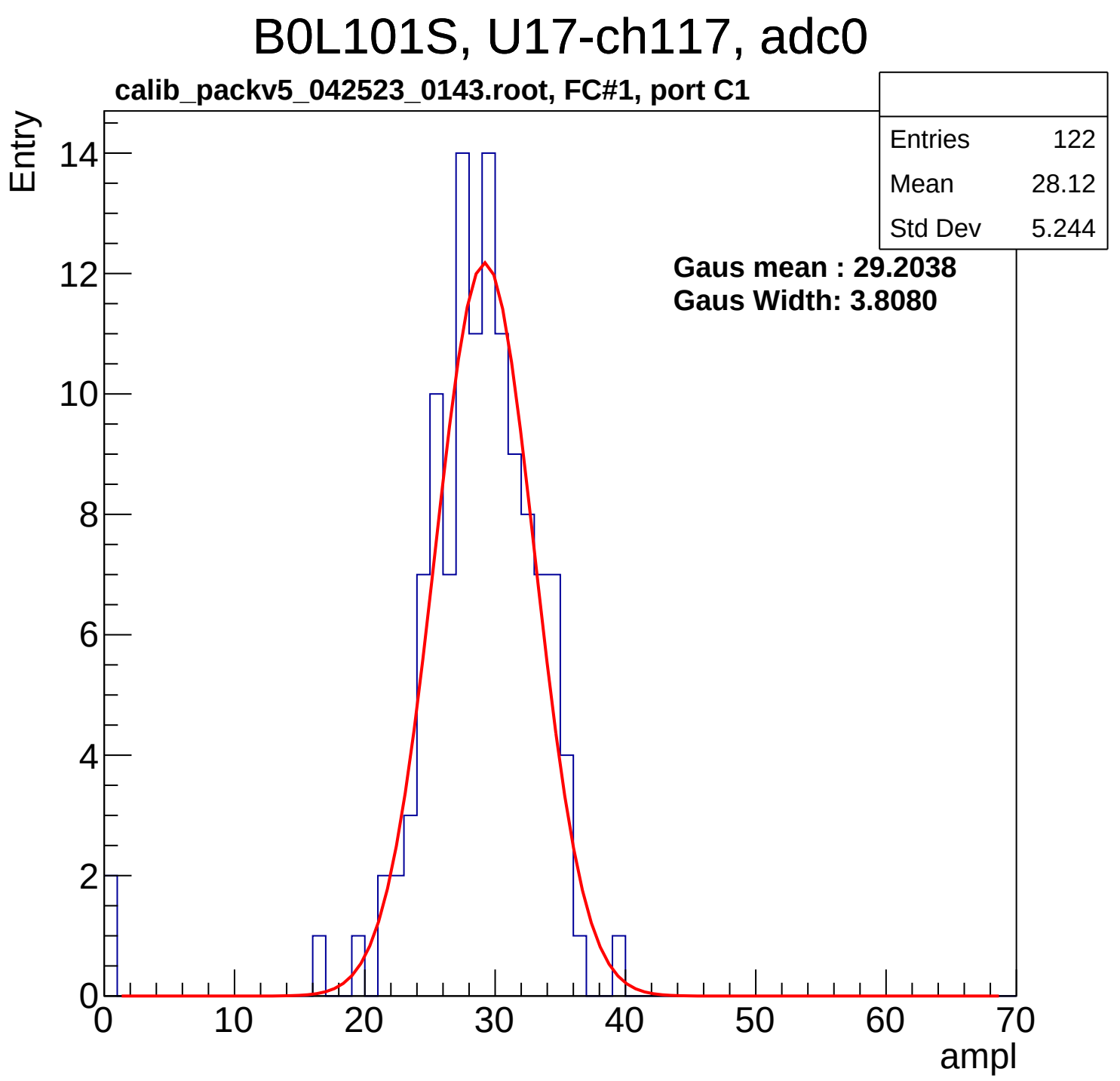

B0L101S, U17-ch116, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 50.57
Std Dev 3.394
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

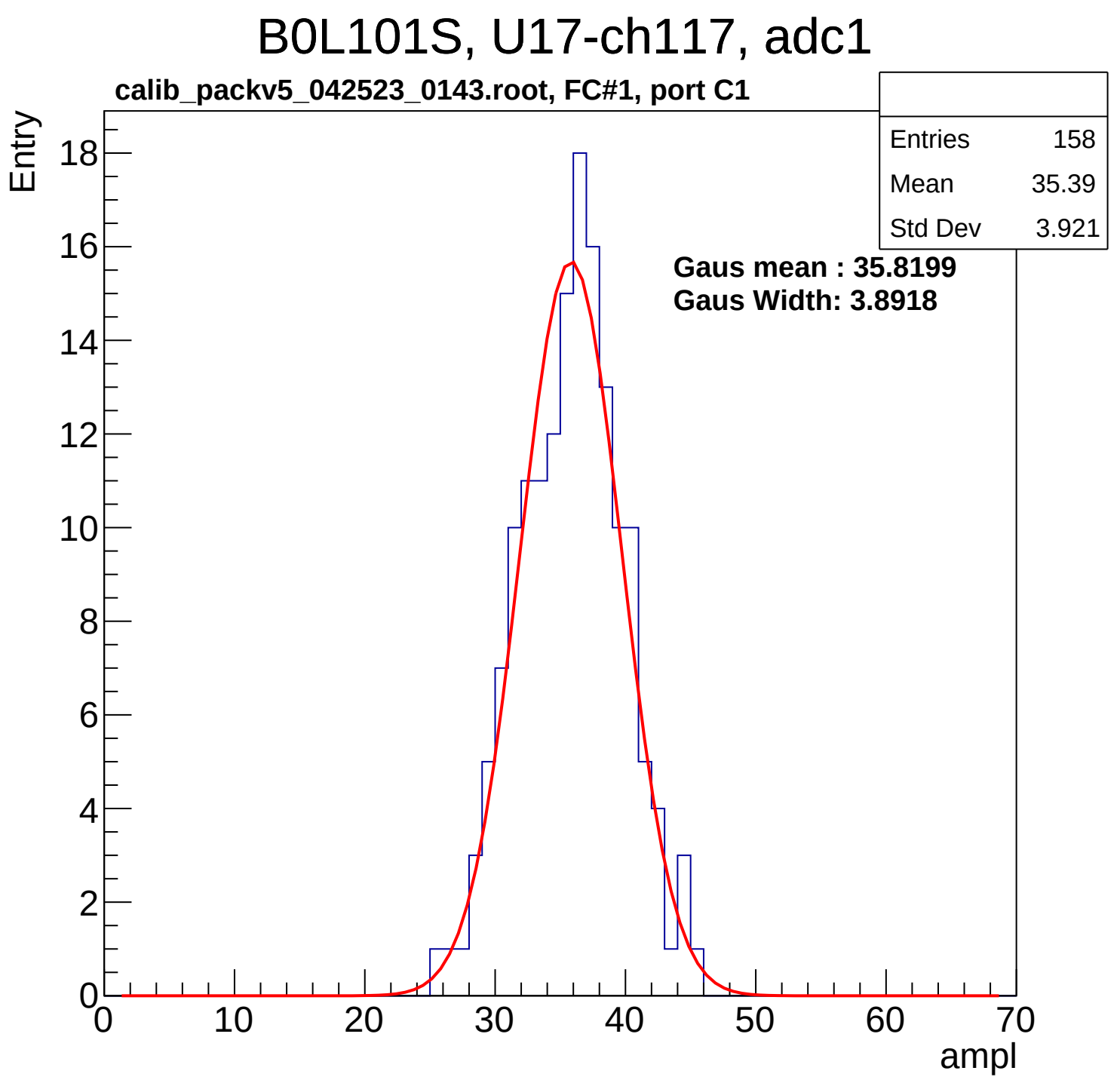

B0L101S, U17-ch116, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 114
Mean 56.51
Std Dev 3.686
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

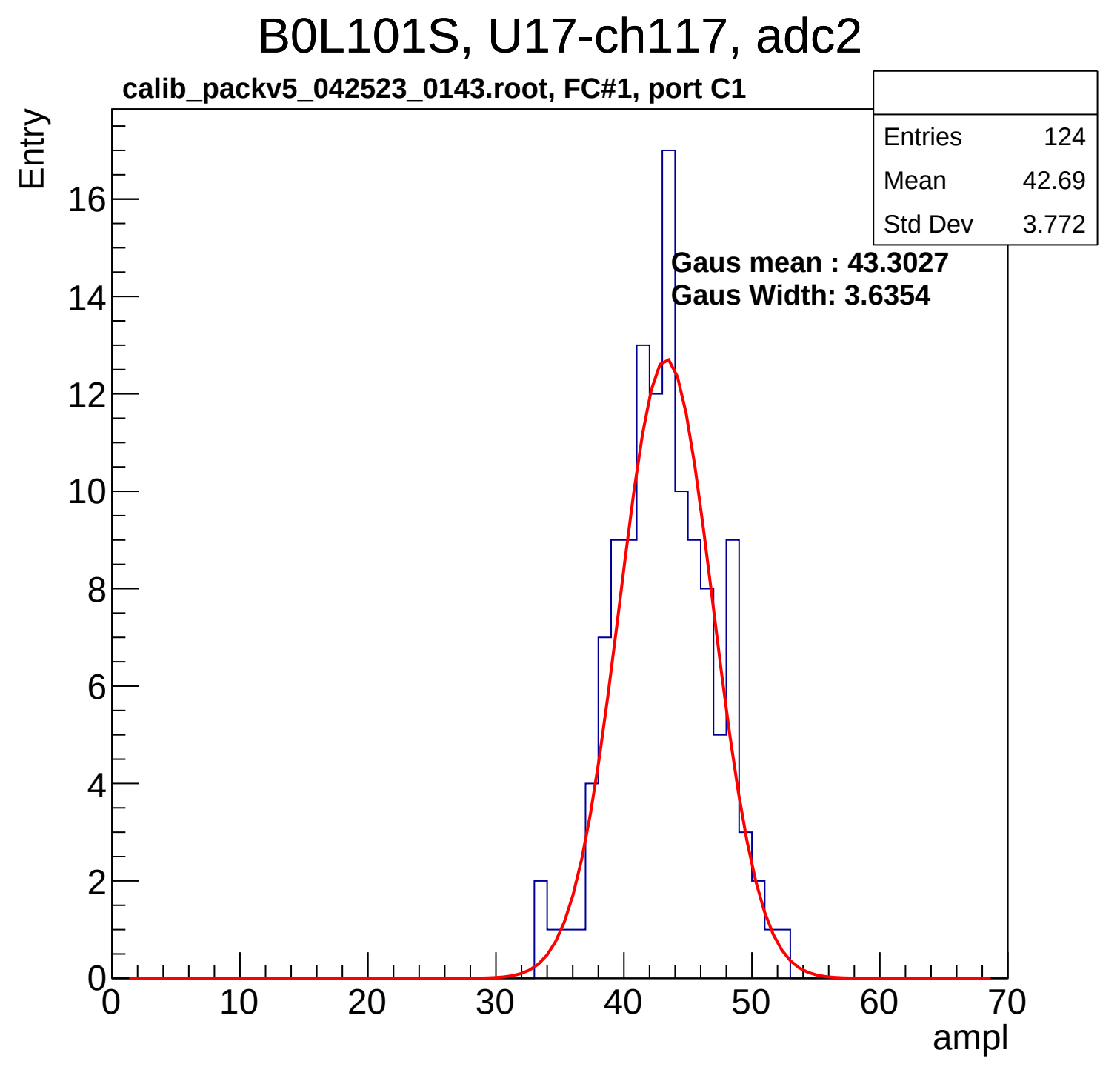

B0L101S, U17-ch116, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 58.22
Std Dev 9.576
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

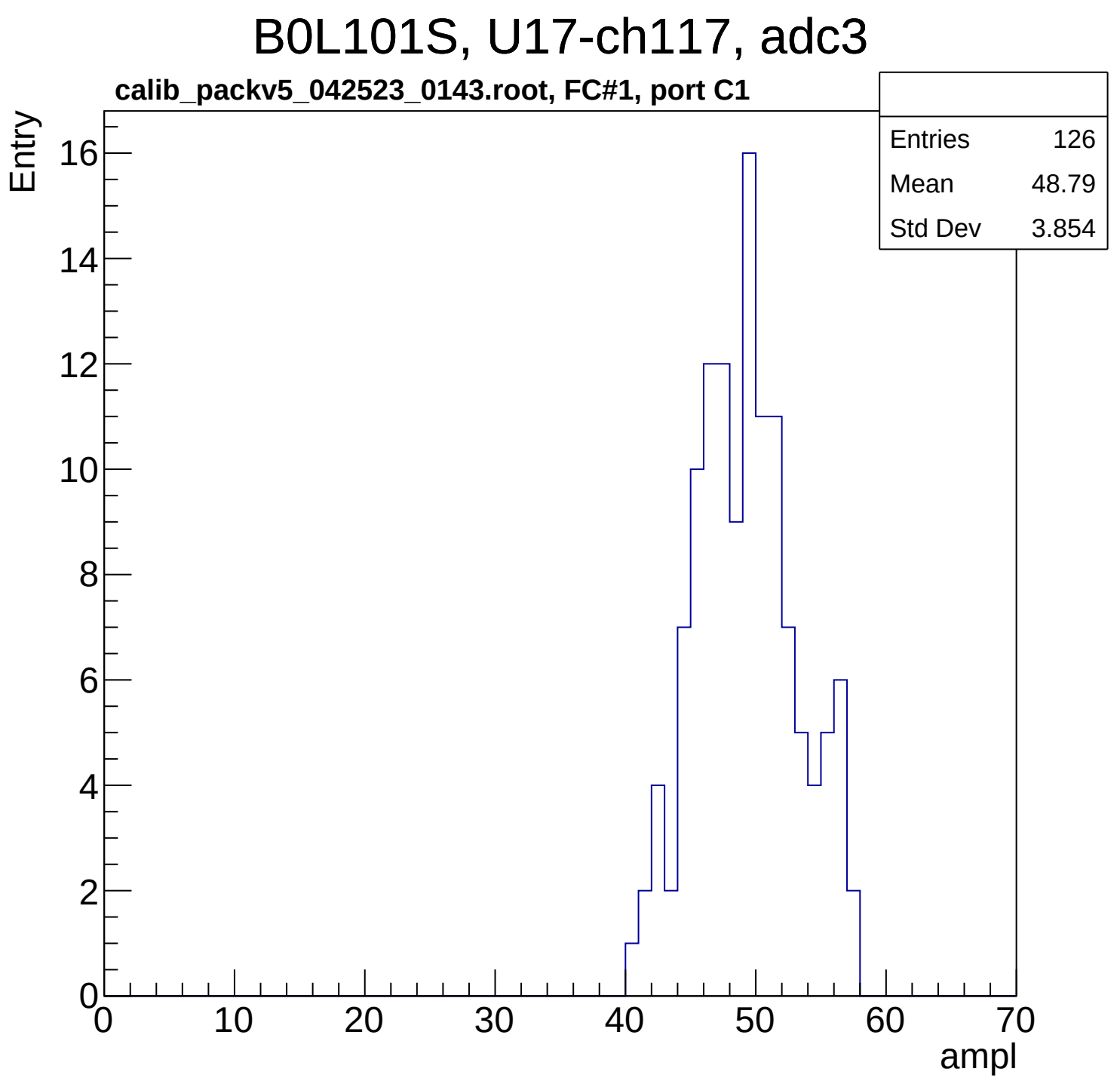

B0L101S, U17-ch116, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

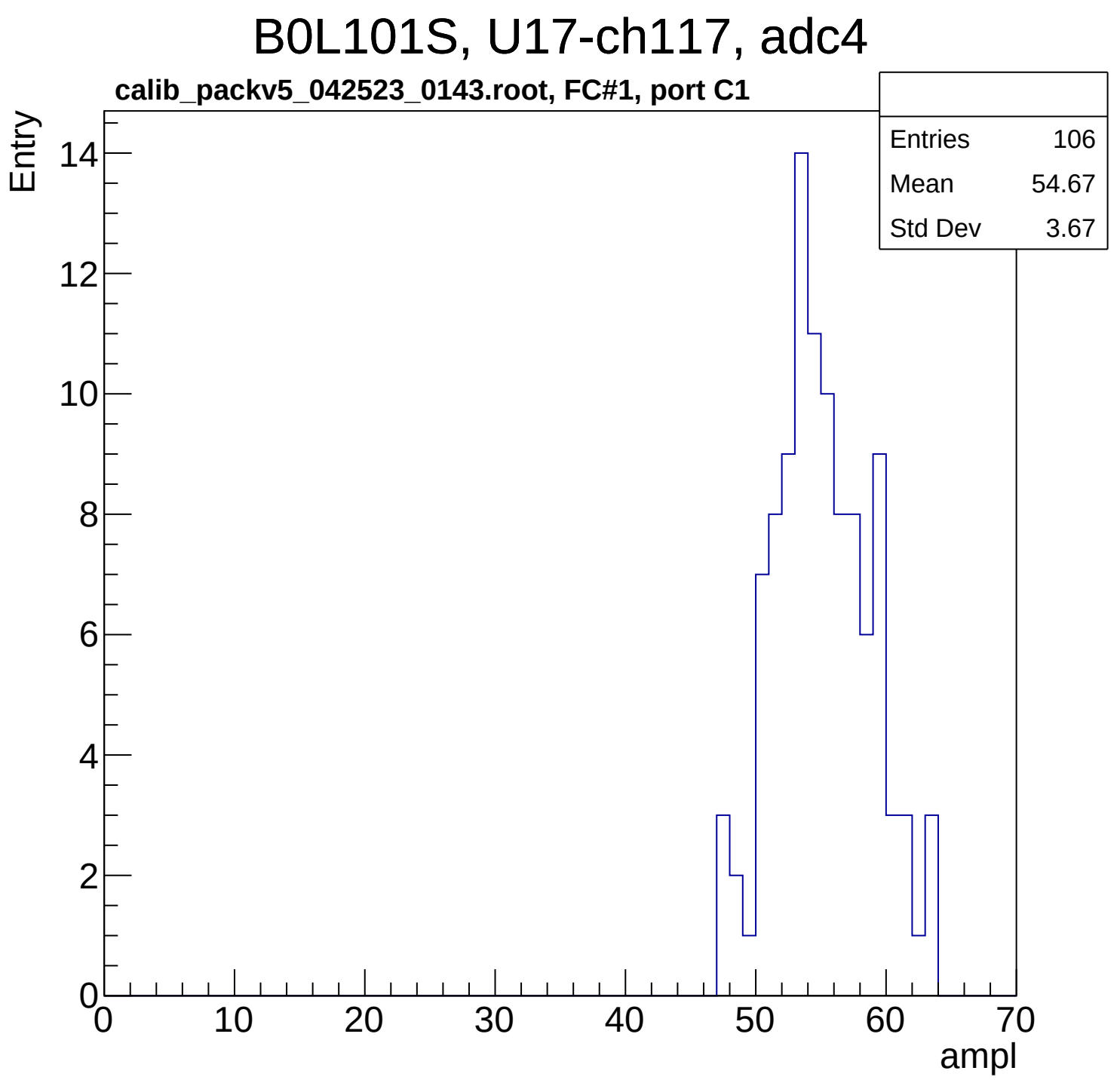

B0L101S, U17-ch116, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 61
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

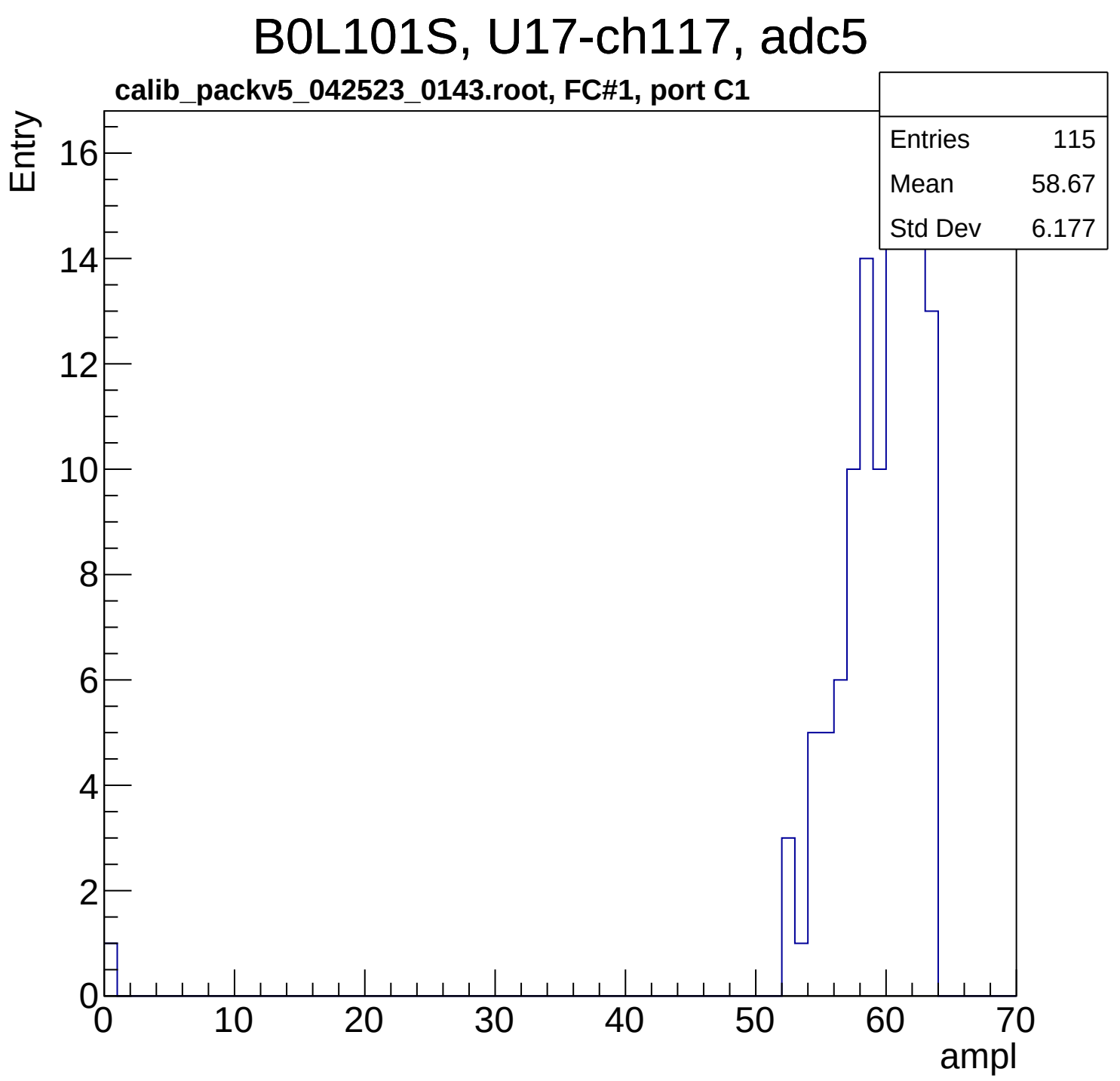

B0L101S, U17-ch117, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 28.12
Std Dev 5.244
Gaus mean : 29.2038
Gaus Width: 3.8080
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

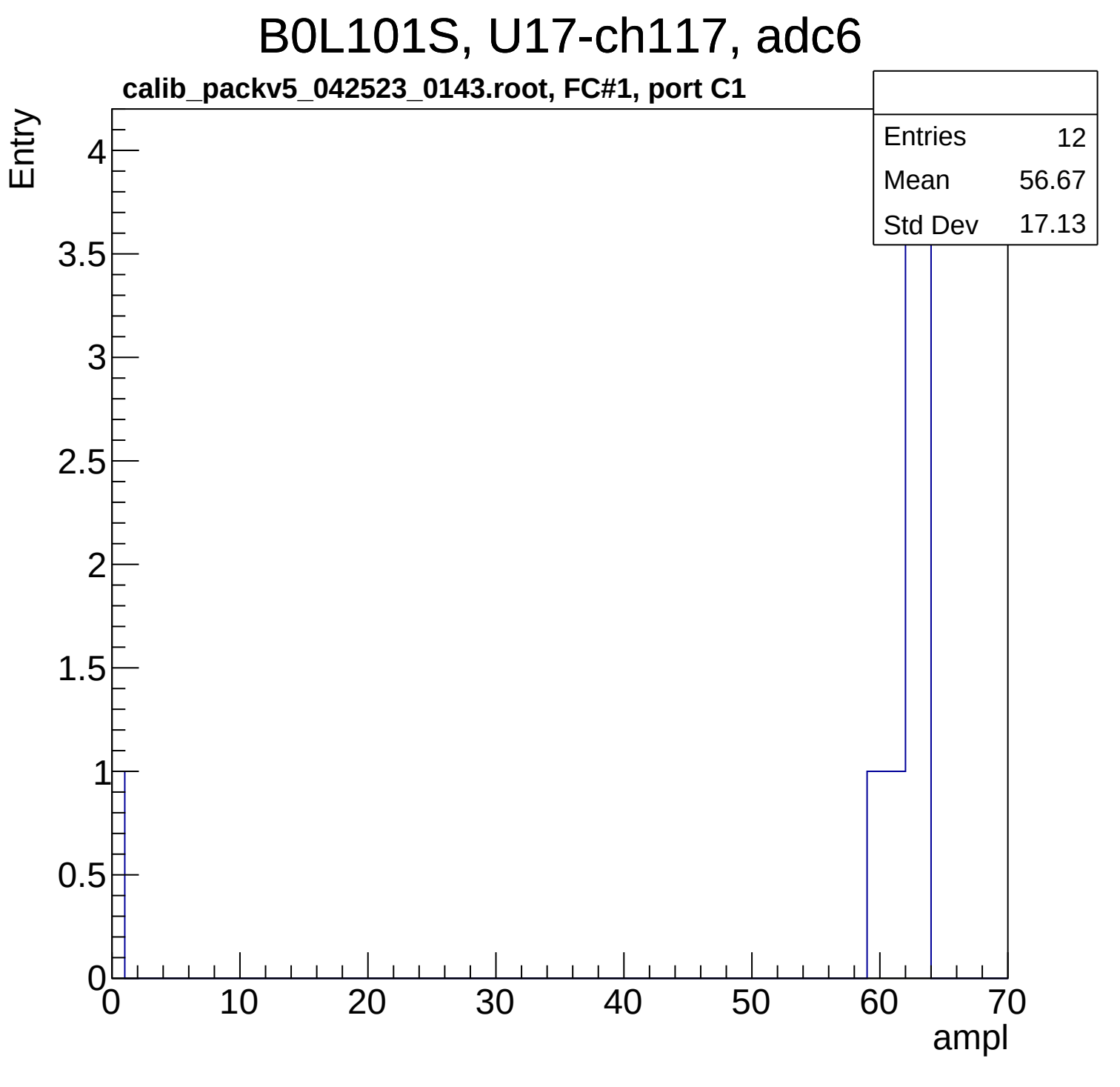

B0L101S, U17-ch117, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 158
Mean 35.39
Std Dev 3.921
Gaus mean : 35.8199
Gaus Width: 3.8918
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

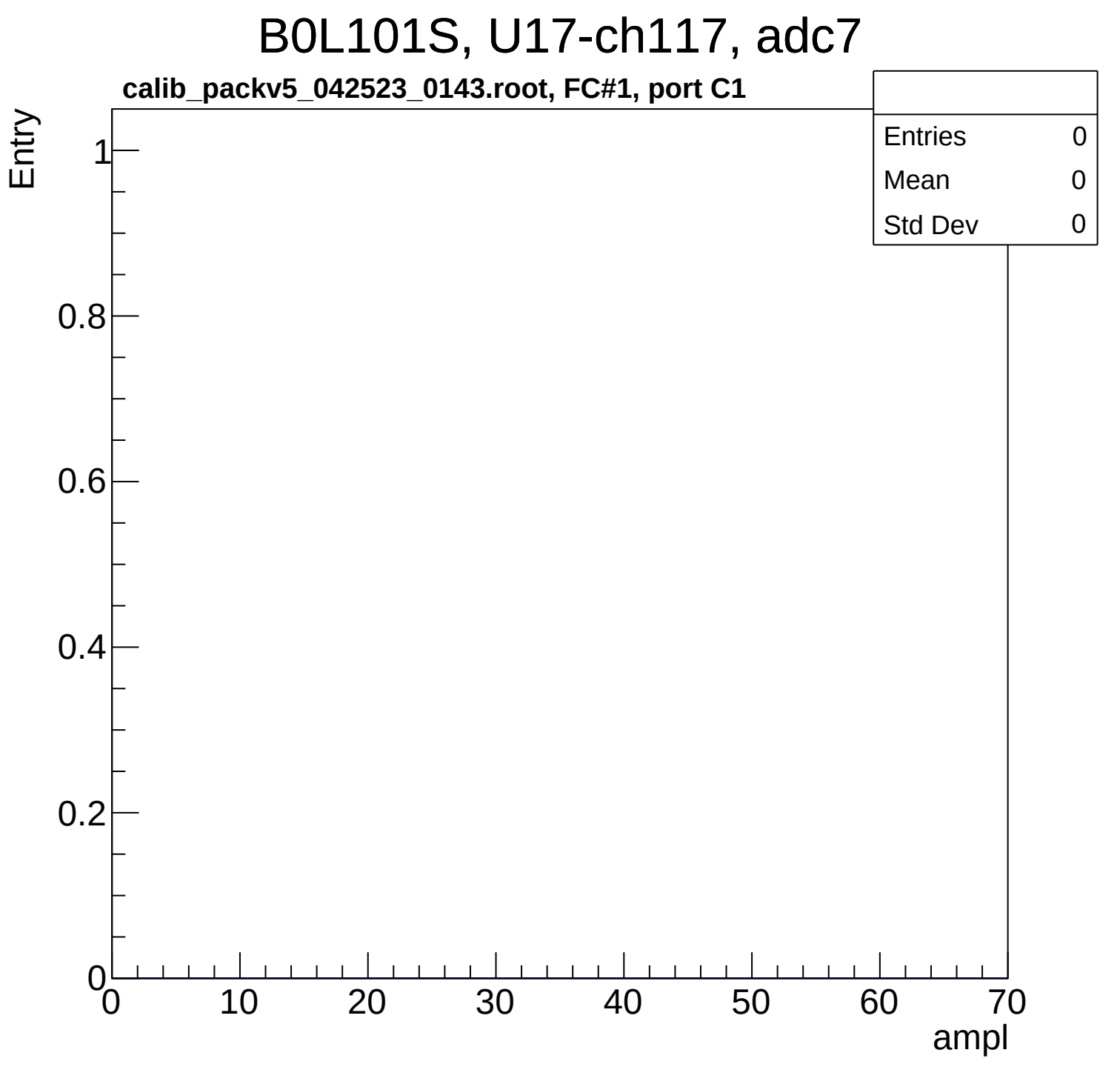

B0L101S, U17-ch117, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 42.69
Std Dev 3.772
Gaus mean : 43.3027
Gaus Width: 3.6354
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

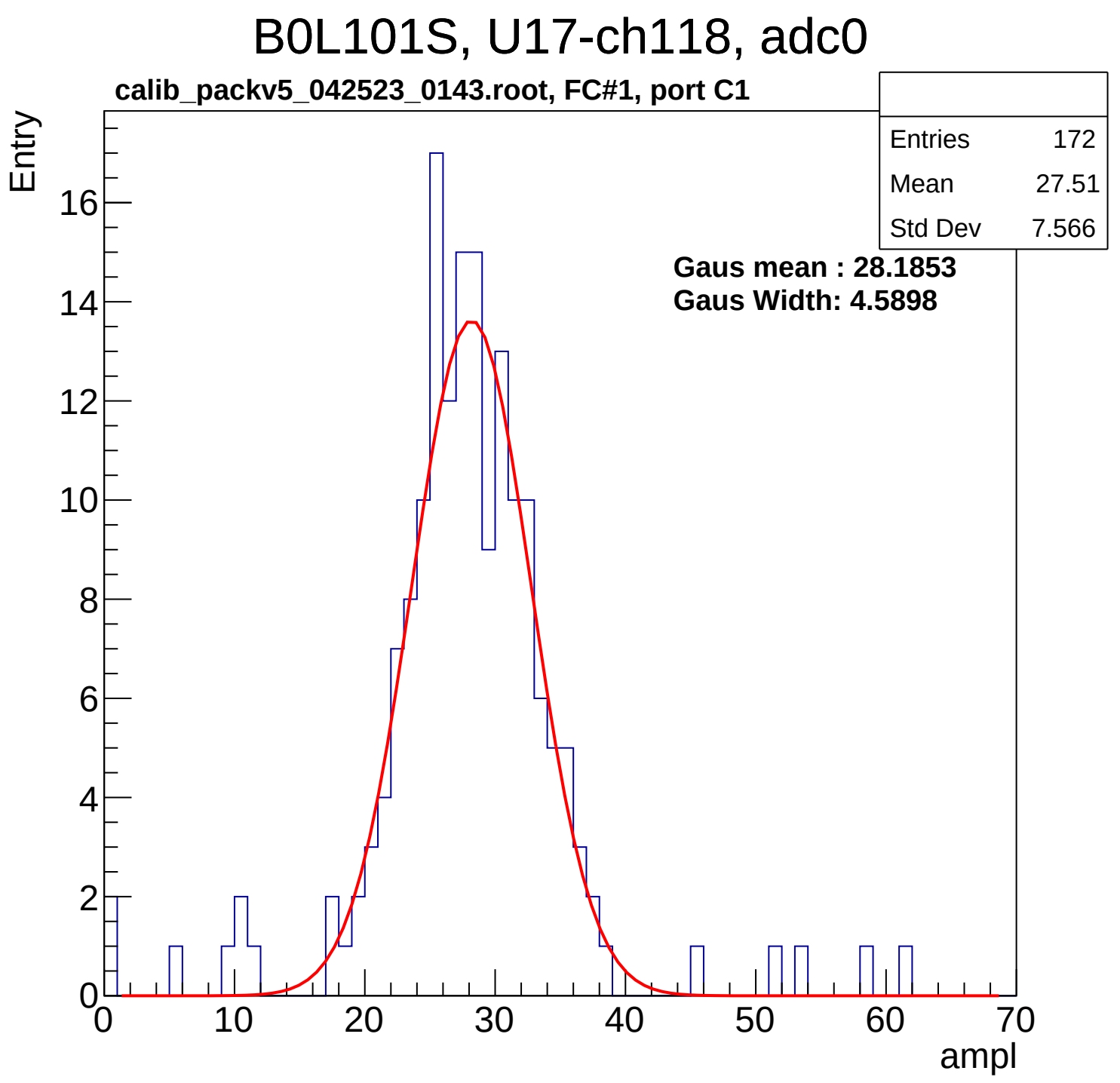

B0L101S, U17-ch117, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 48.79
Std Dev 3.854
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

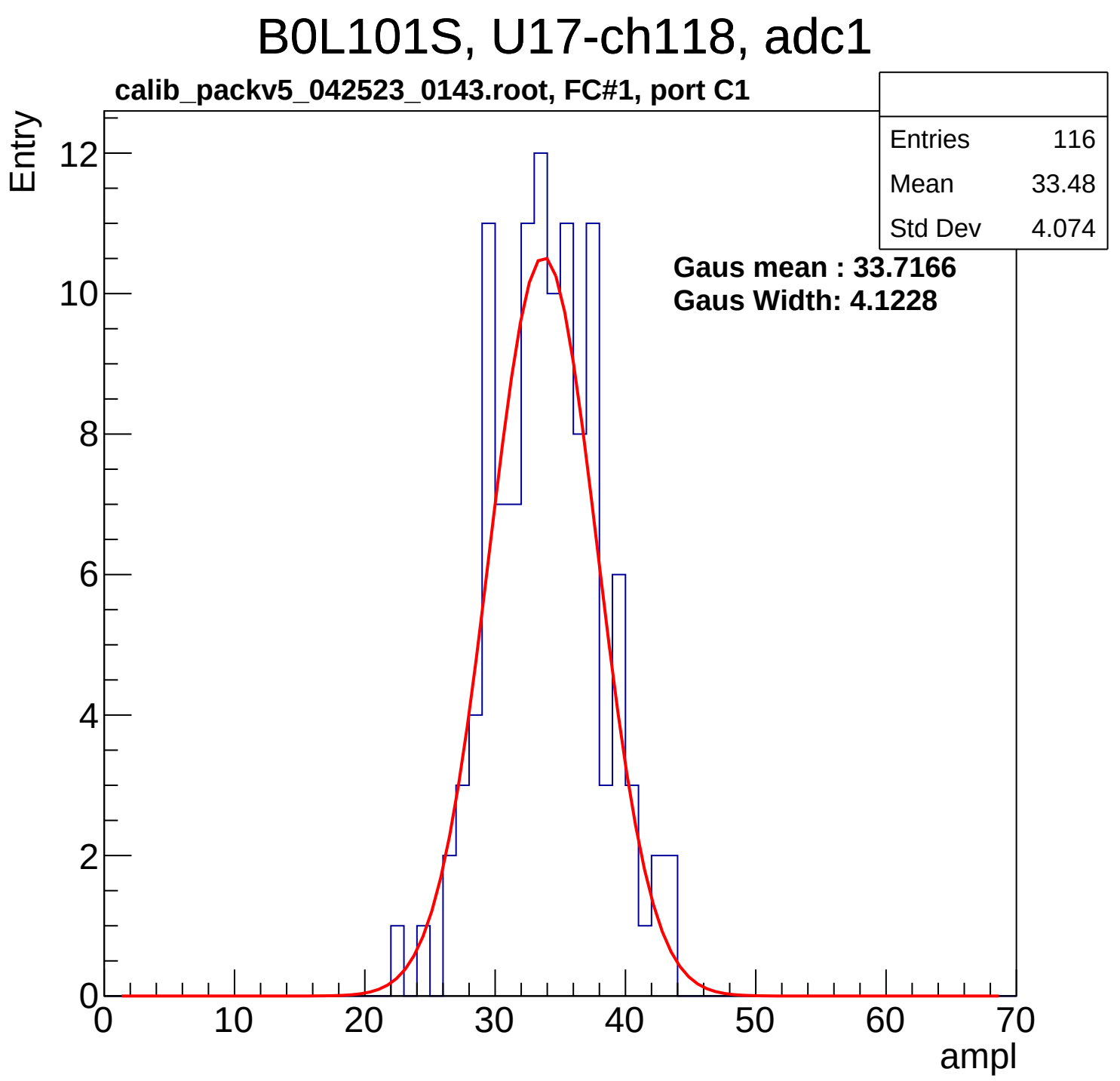

B0L101S, U17-ch117, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 106
Mean 54.67
Std Dev 3.67
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

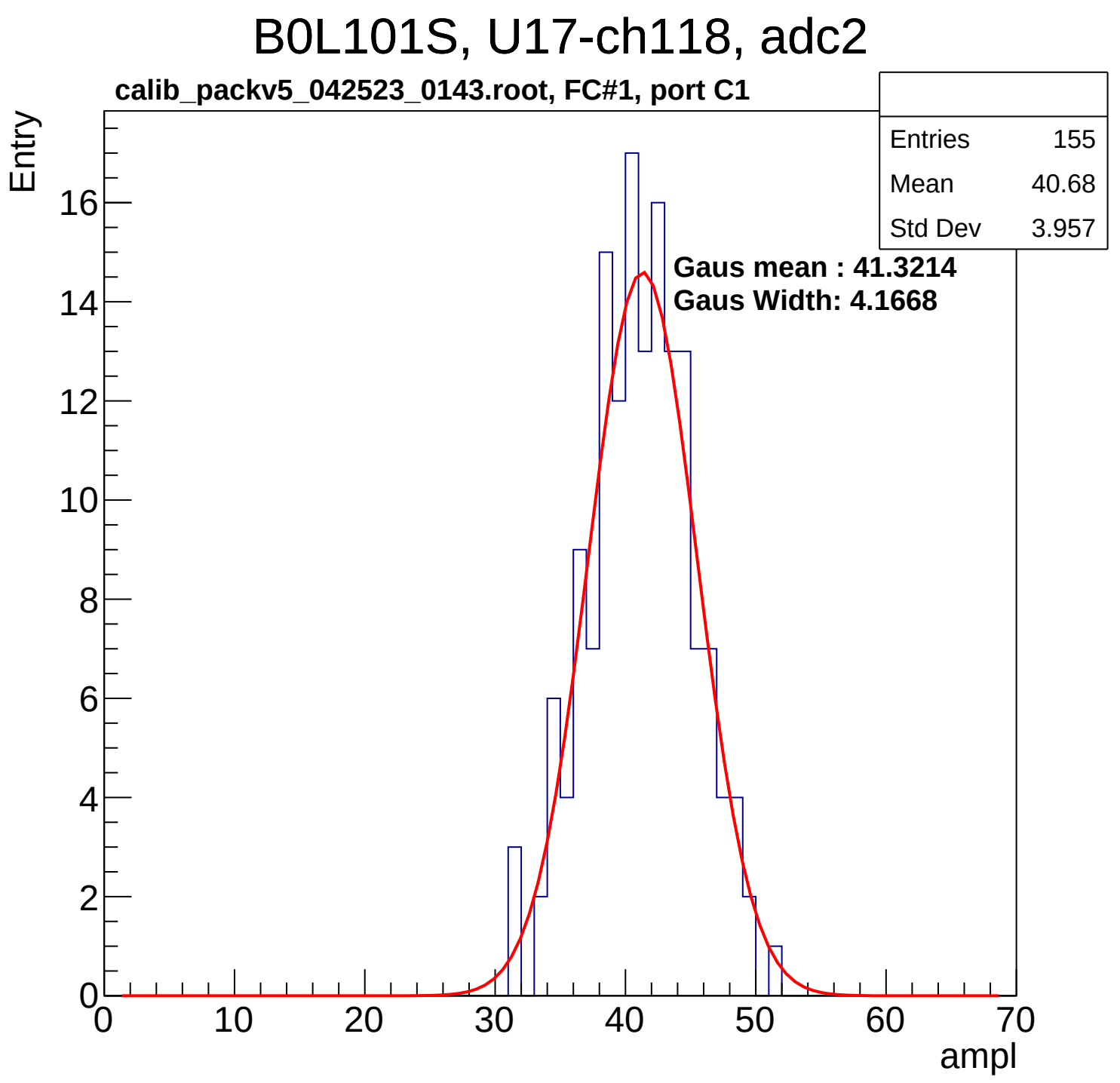

B0L101S, U17-ch117, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 115
Mean 58.67
Std Dev 6.177
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

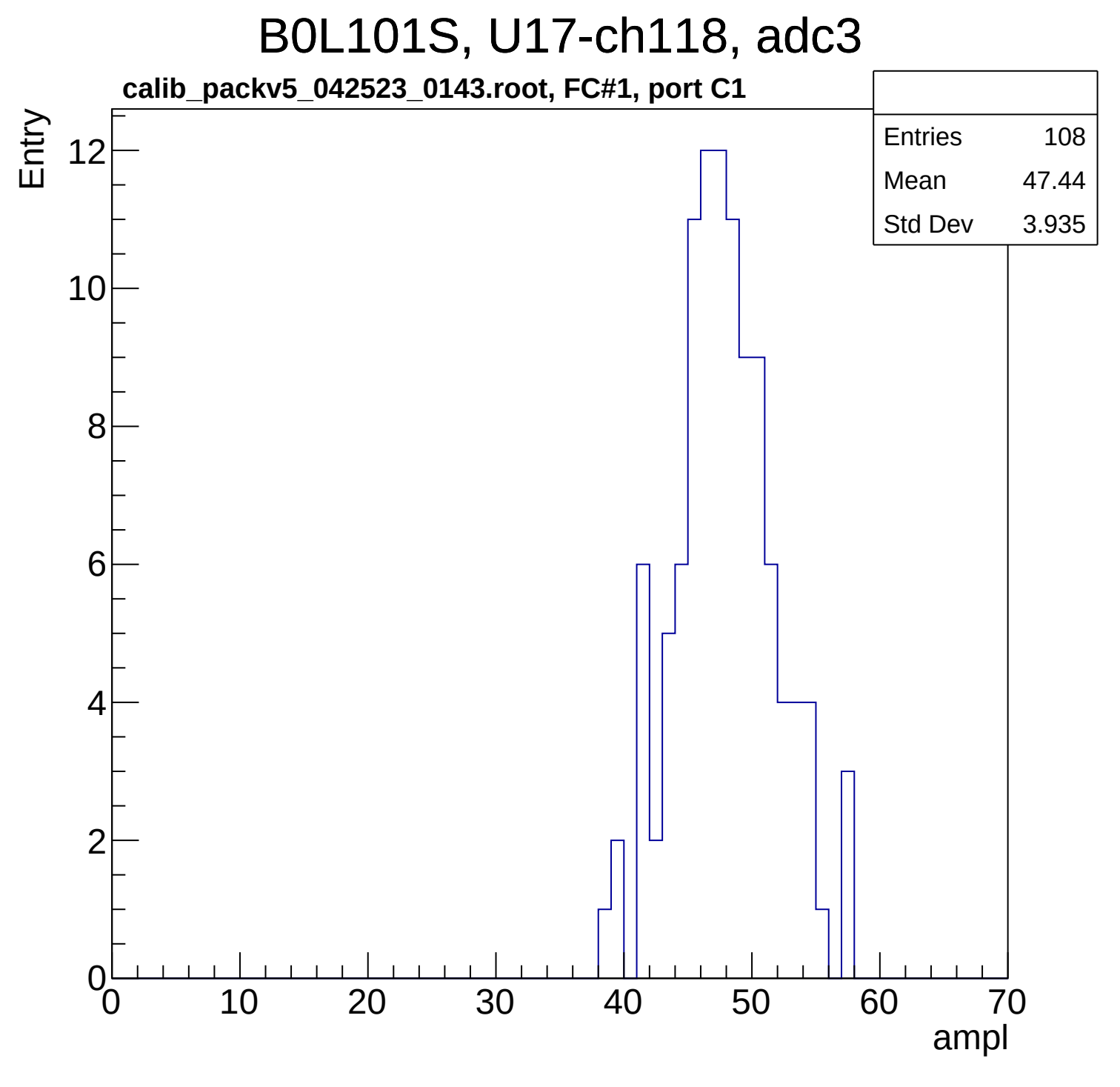

B0L101S, U17-ch117, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 12
Mean 56.67
Std Dev 17.13
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

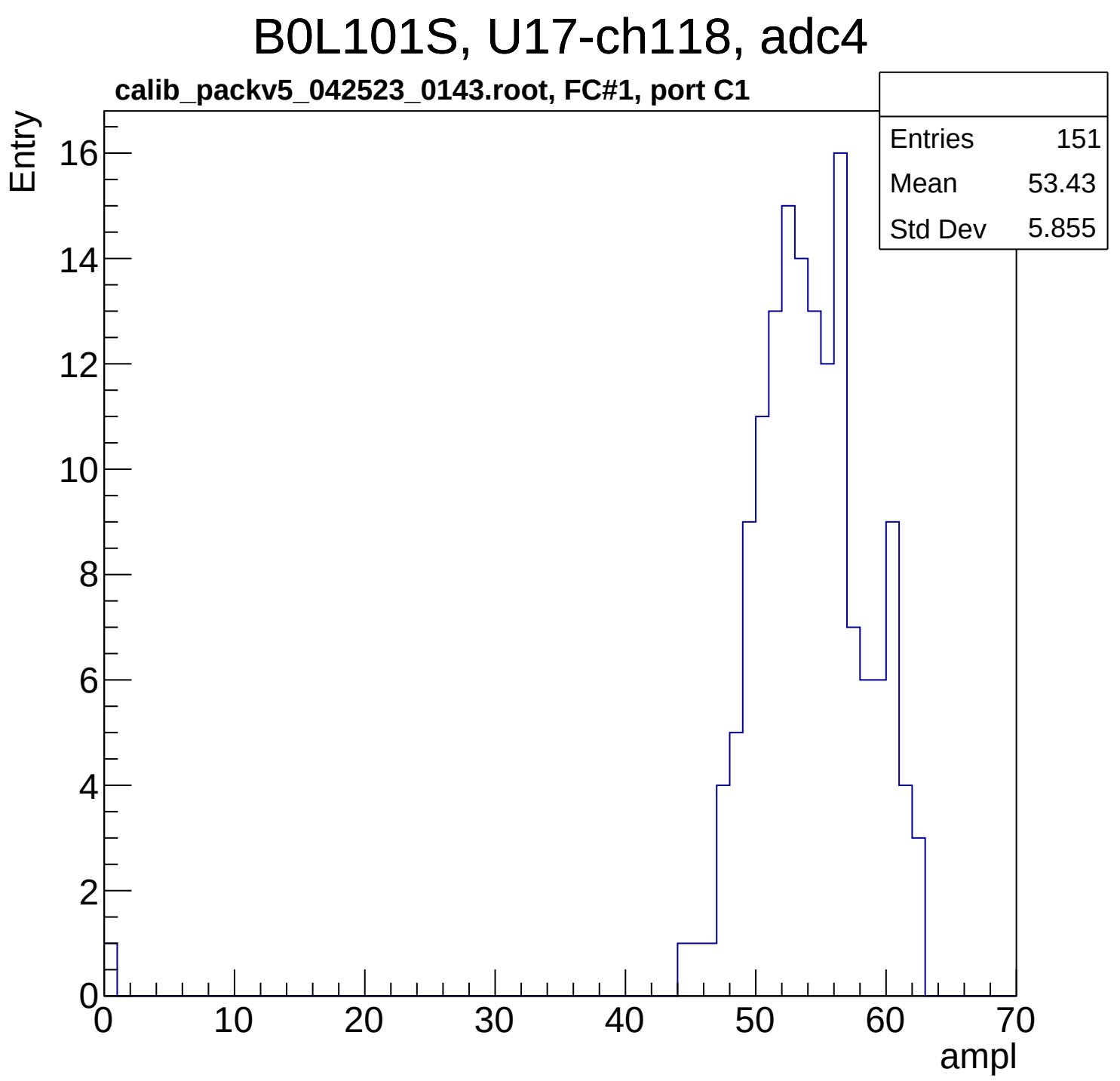

B0L101S, U17-ch117, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

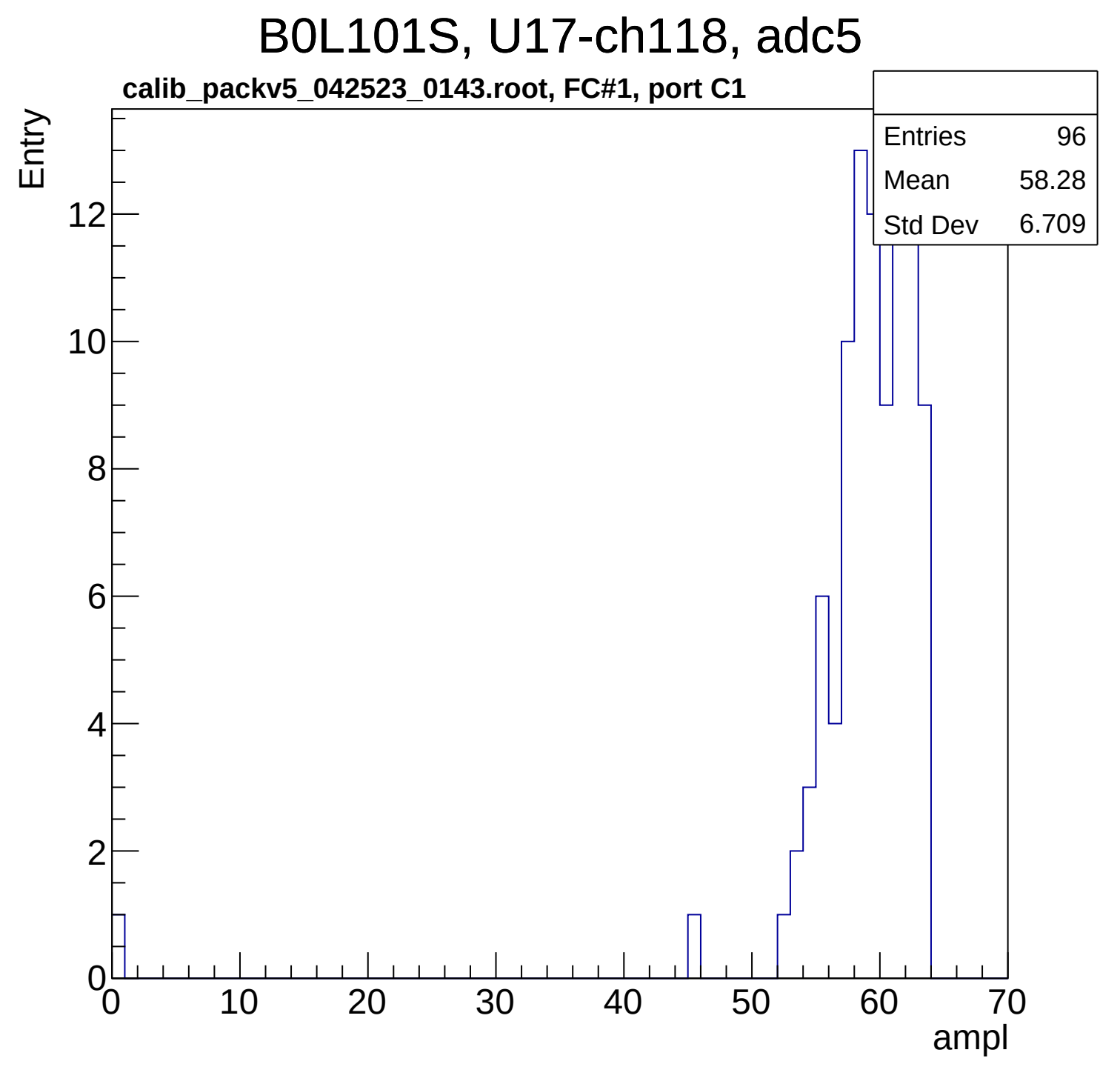

B0L101S, U17-ch118, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 172
Mean 27.51
Std Dev 7.566
Gaus mean : 28.1853
Gaus Width: 4.5898
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

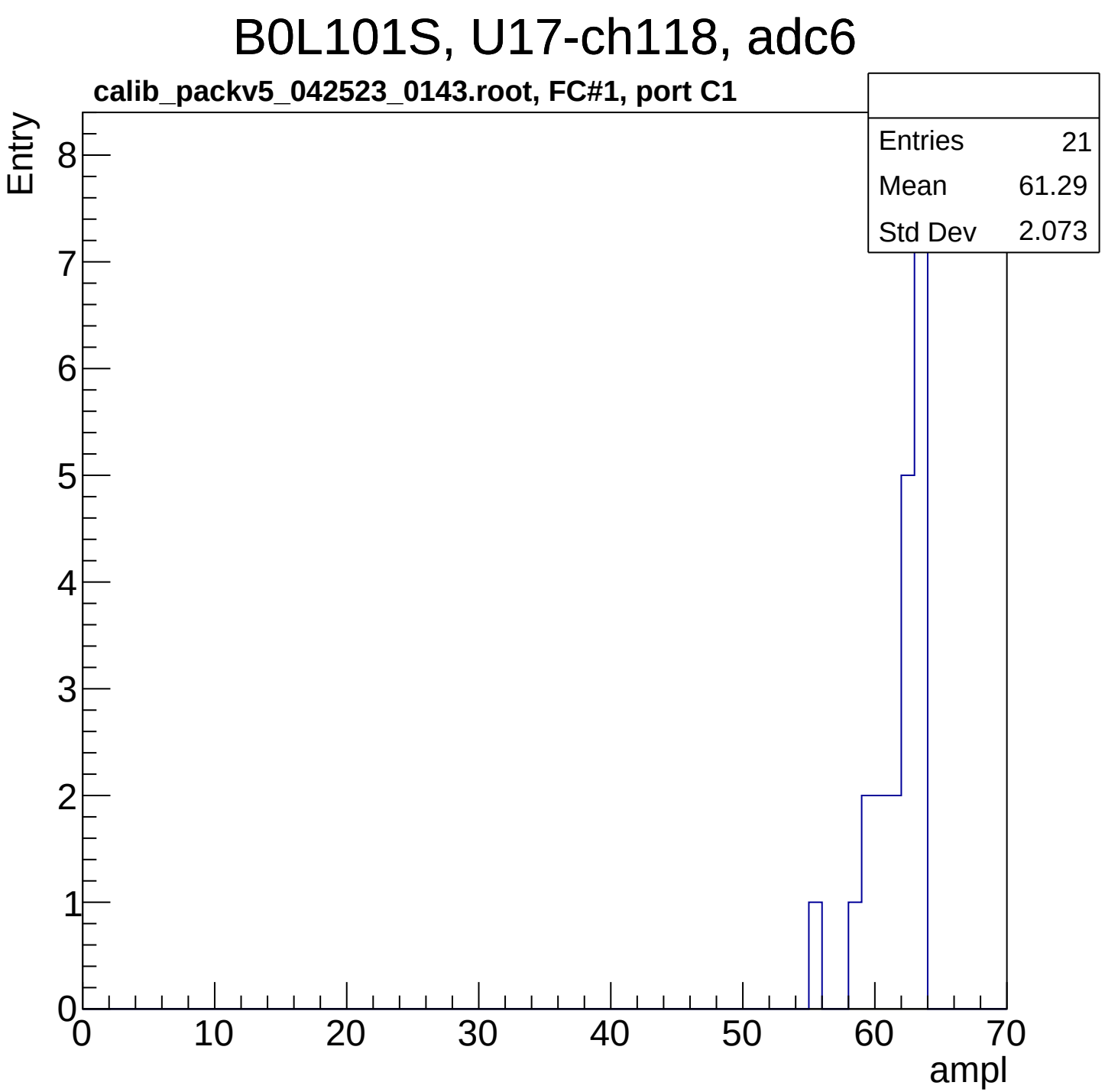

B0L101S, U17-ch118, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 33.48
Std Dev 4.074
Gaus mean : 33.7166
Gaus Width: 4.1228
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

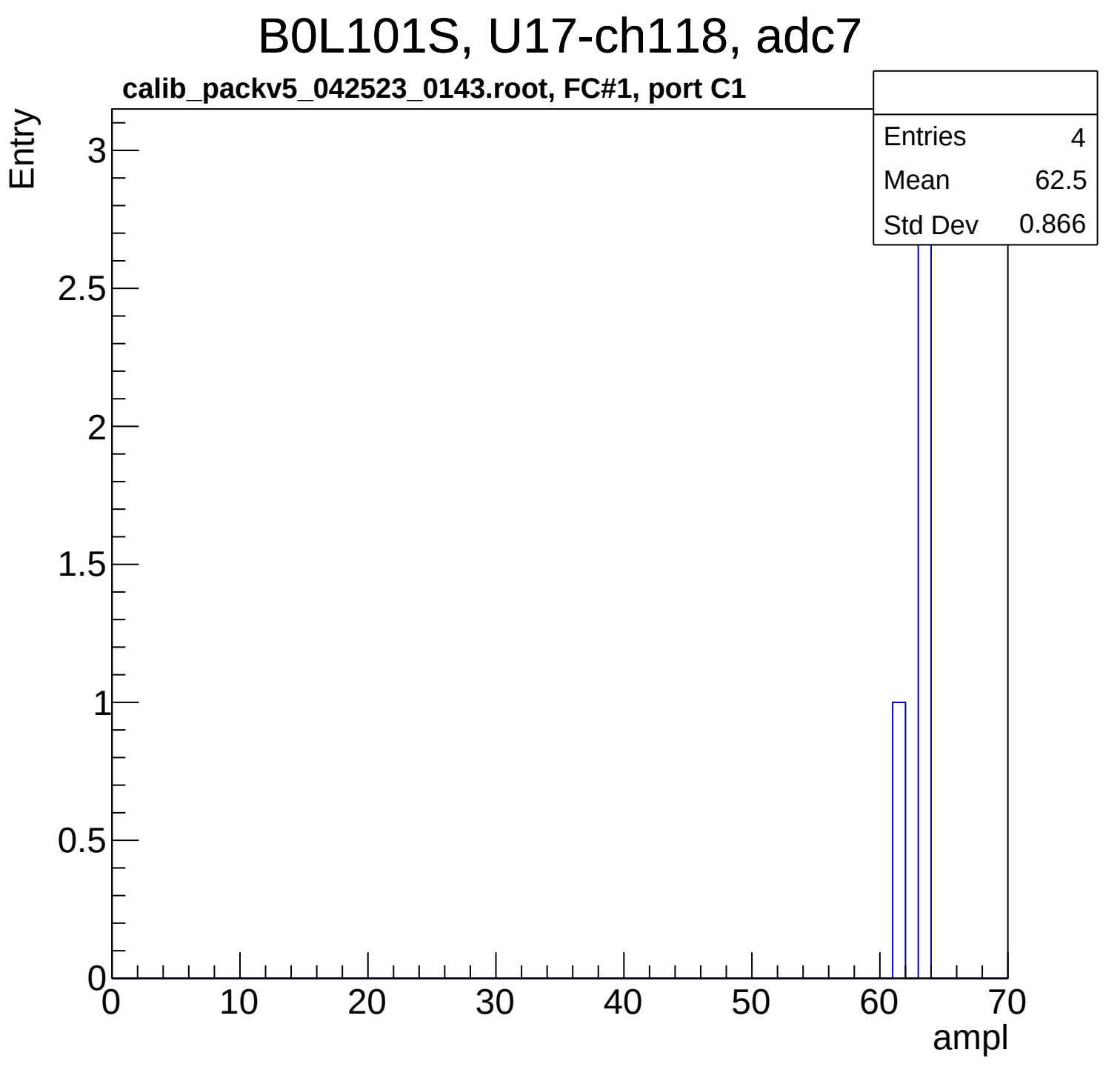

B0L101S, U17-ch118, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 40.68
Std Dev 3.957
Gaus mean : 41.3214
Gaus Width: 4.1668
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

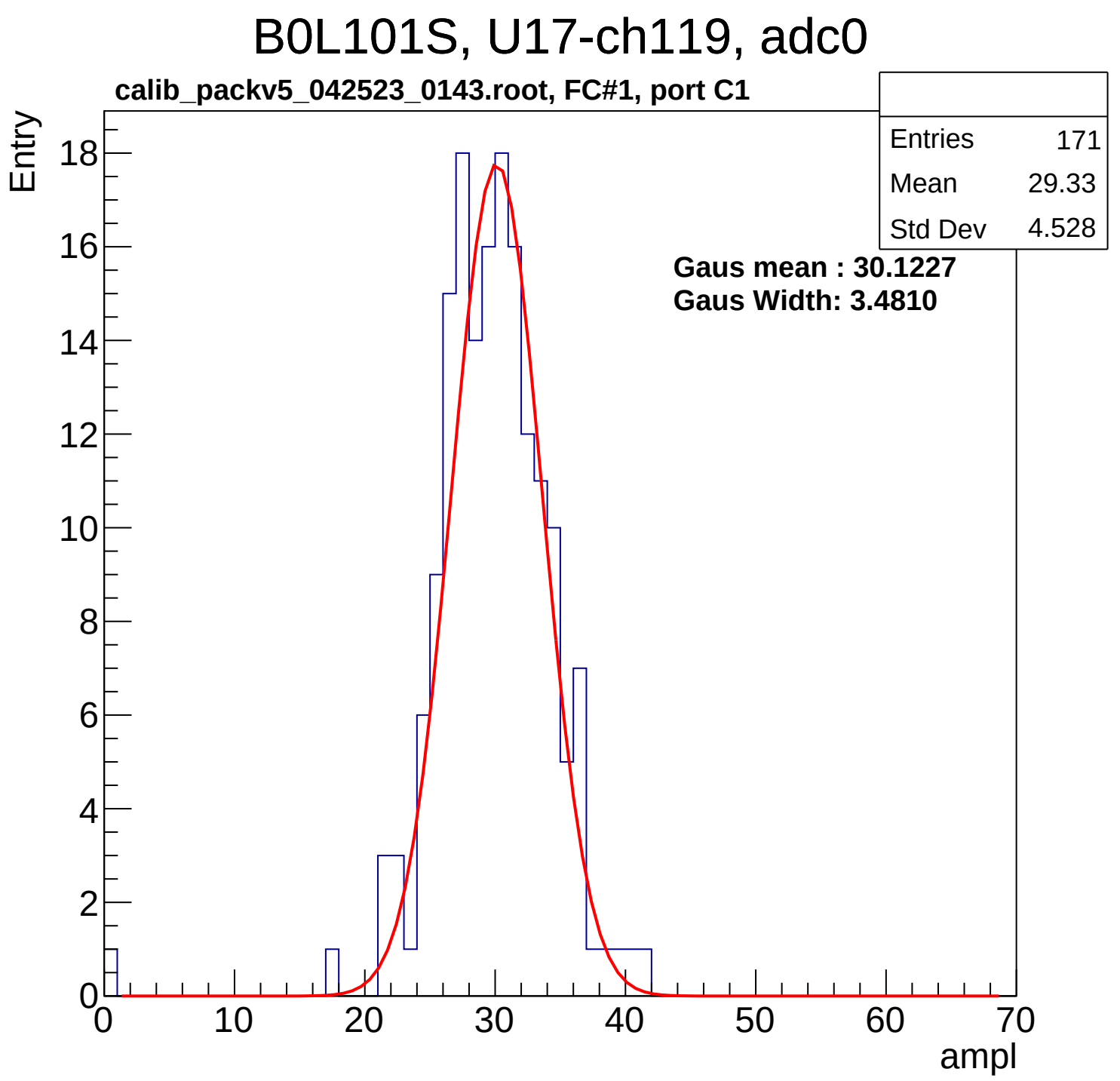

B0L101S, U17-ch118, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 108
Mean 47.44
Std Dev 3.935
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

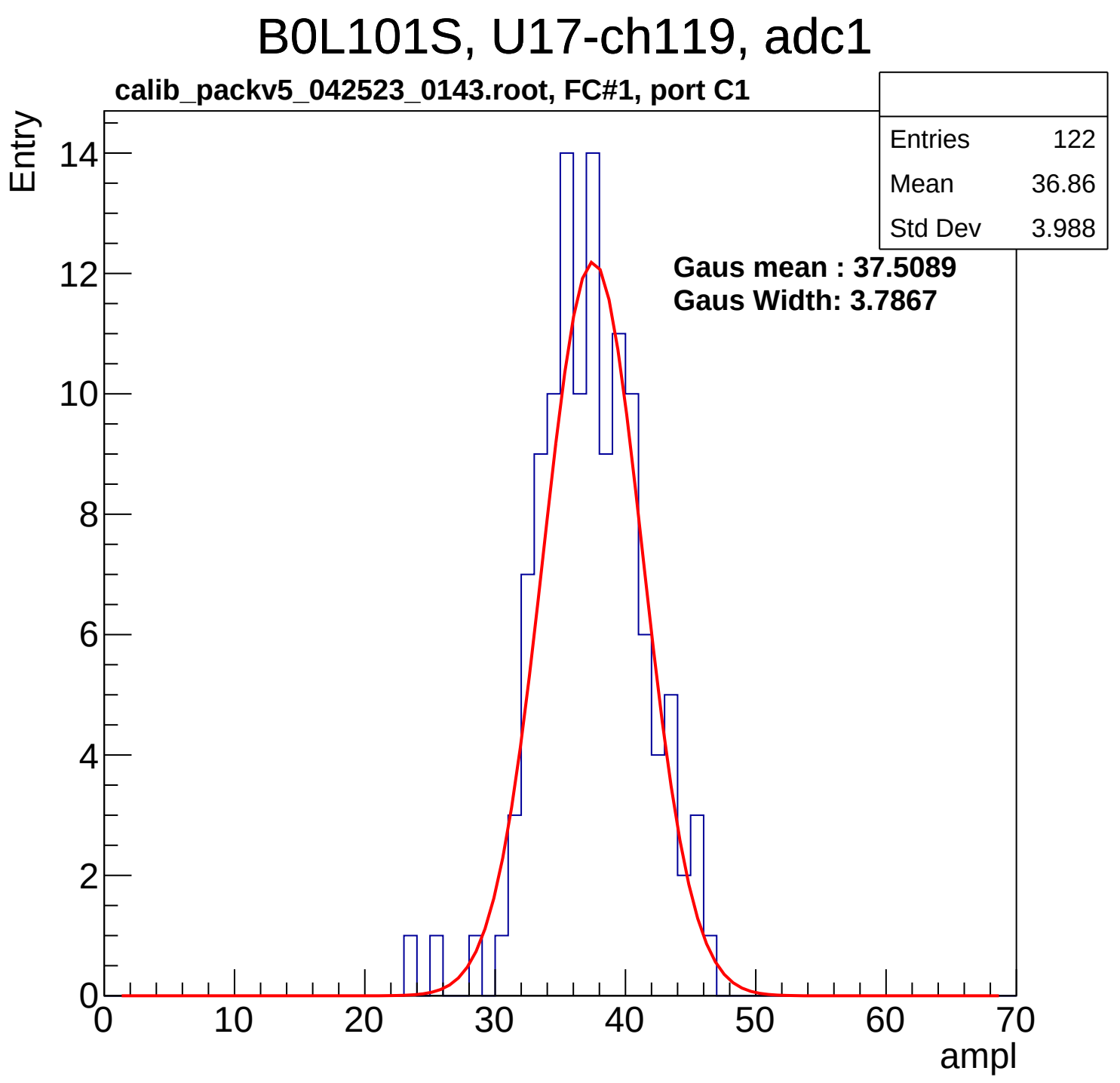

B0L101S, U17-ch118, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 53.43
Std Dev 5.855
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

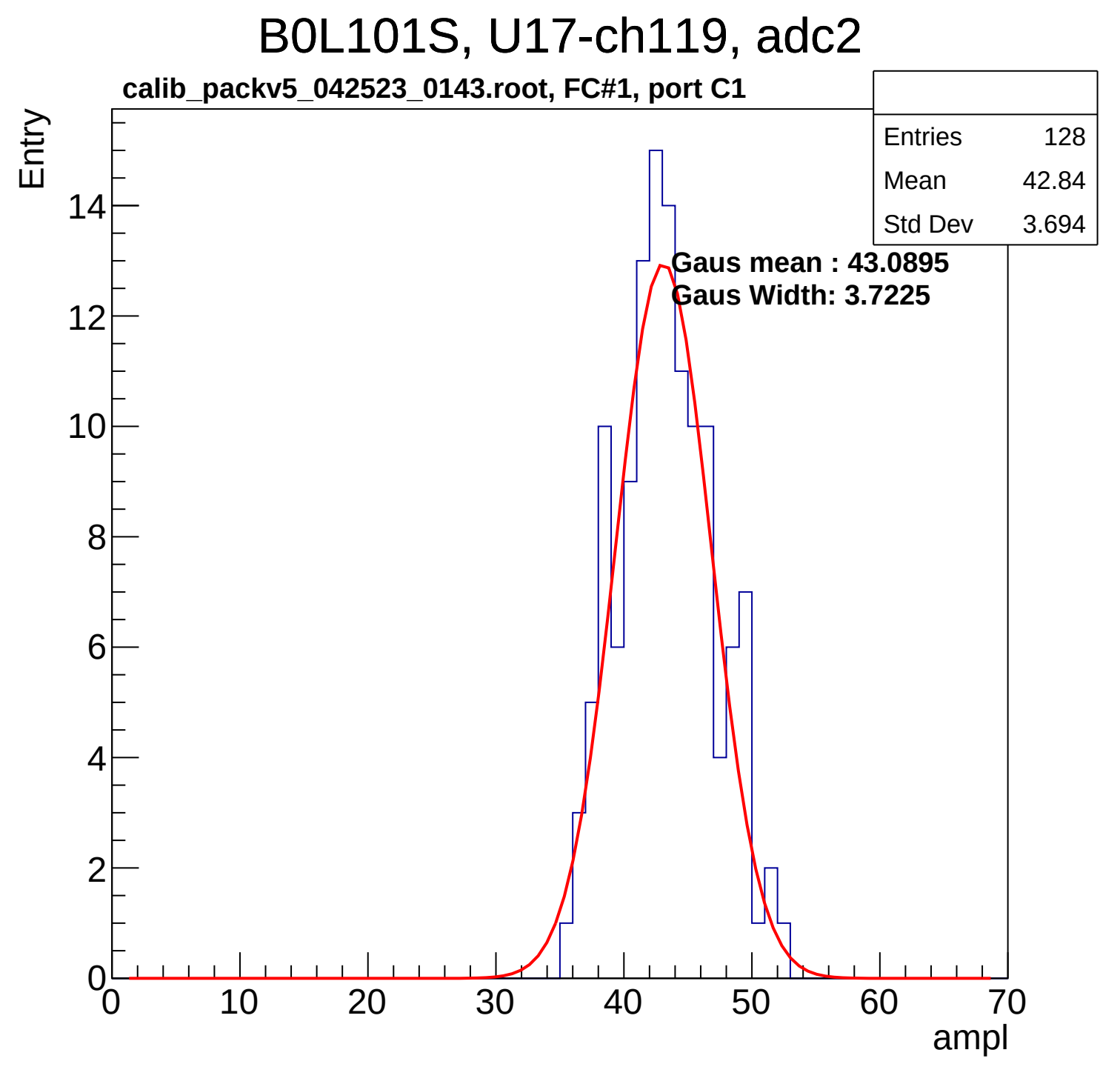

B0L101S, U17-ch118, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 96
Mean 58.28
Std Dev 6.709
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

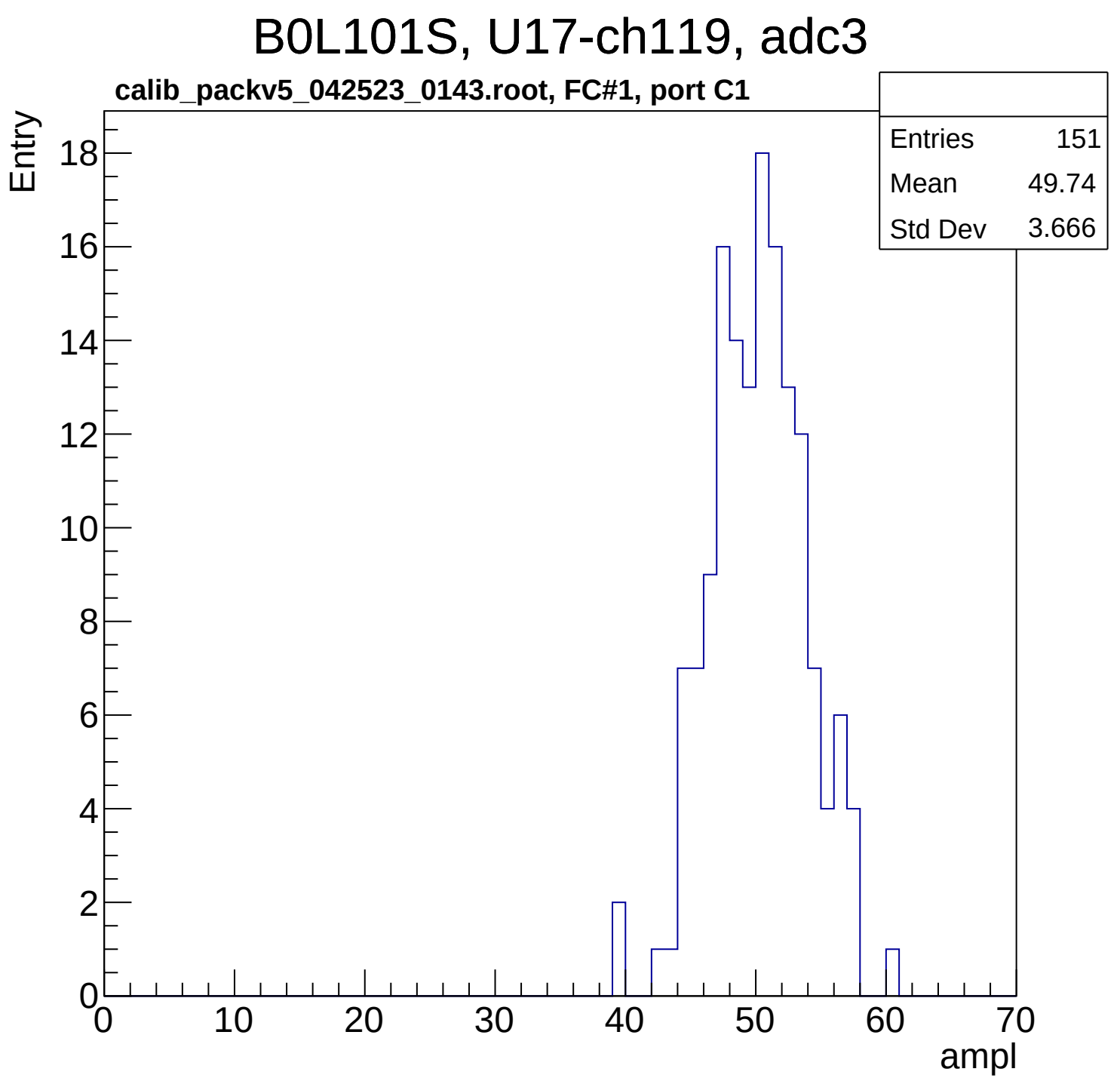

B0L101S, U17-ch118, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 21
Mean 61.29
Std Dev 2.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

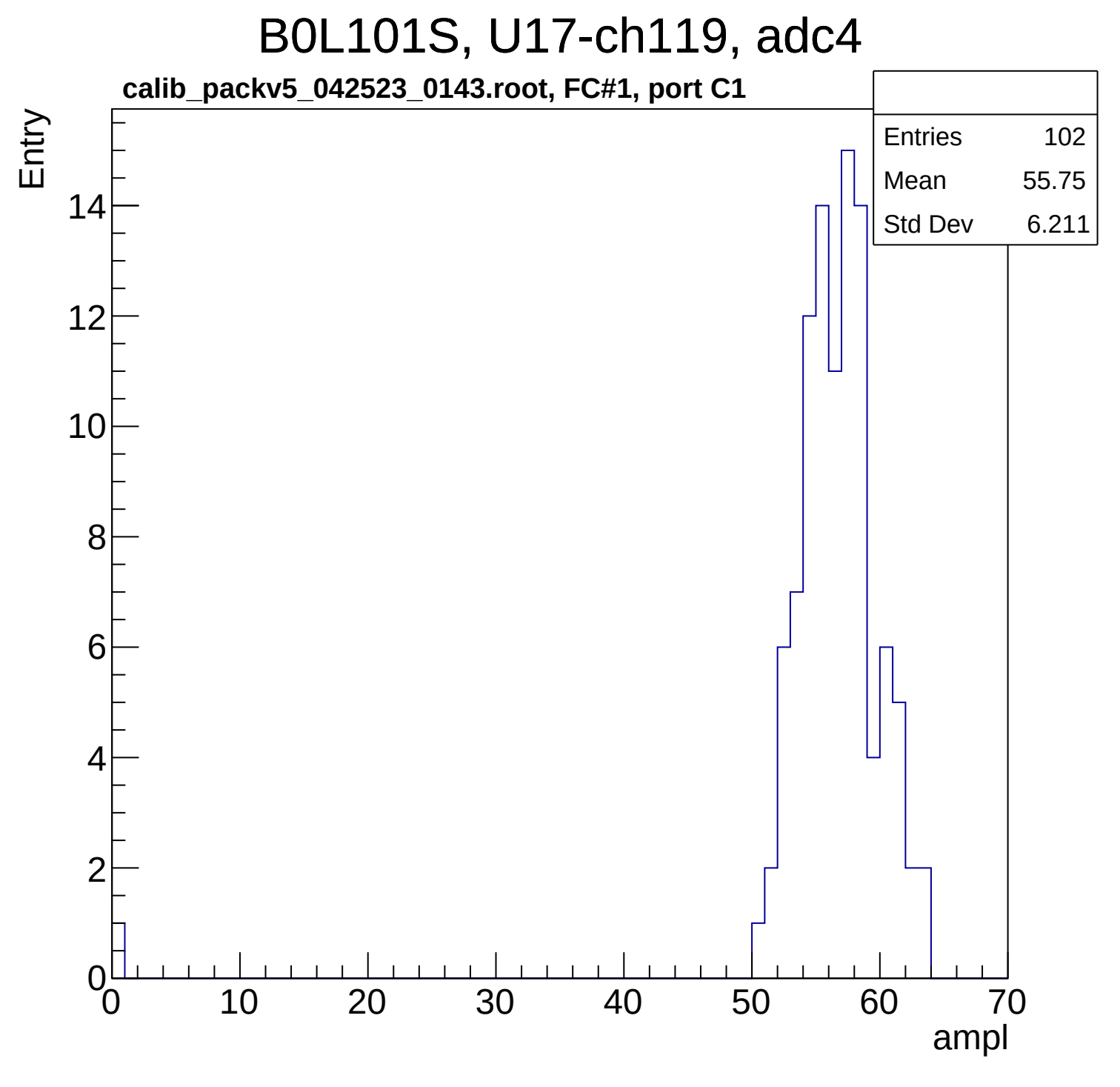

B0L101S, U17-ch118, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

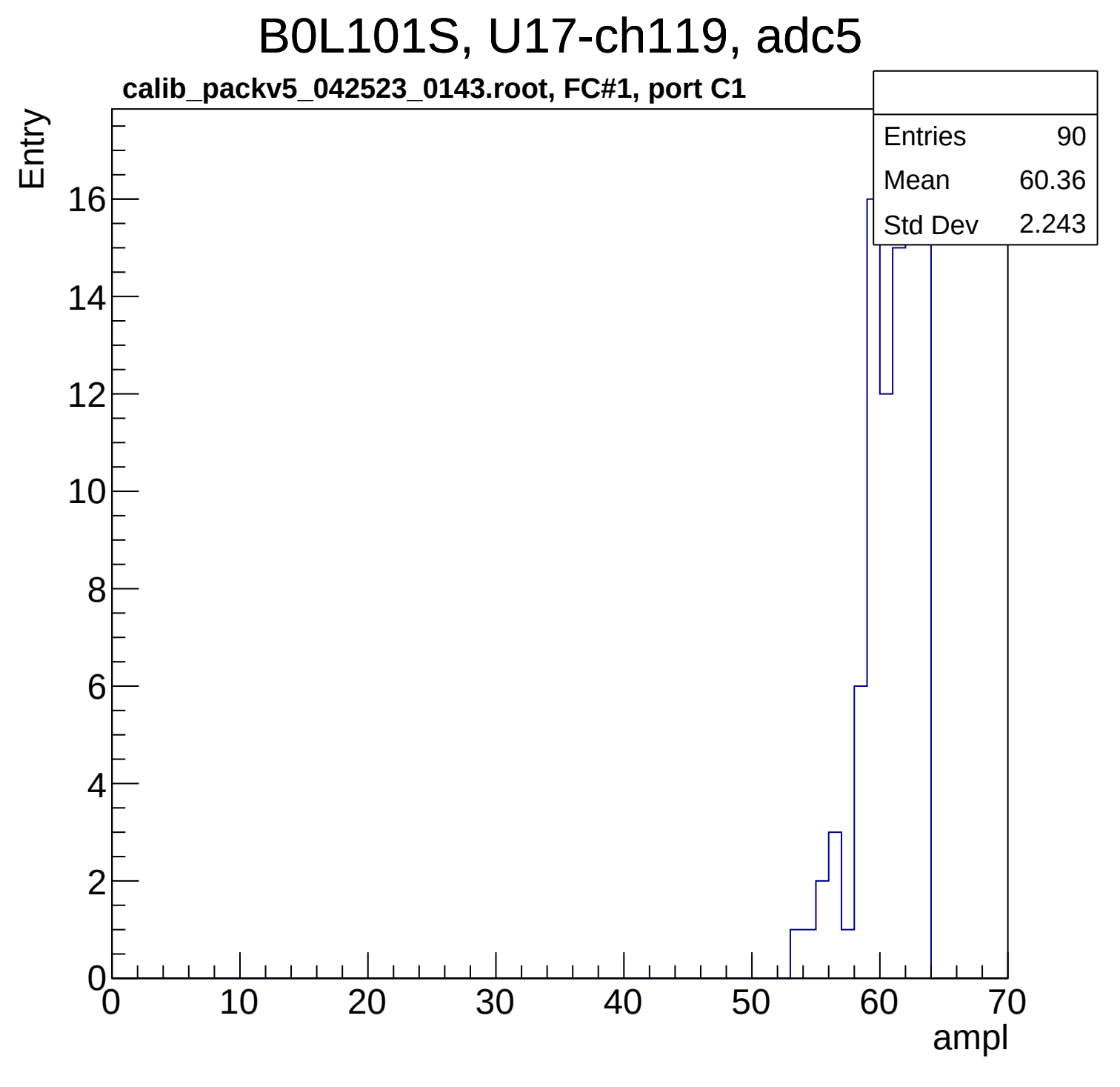

B0L101S, U17-ch119, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 171
Mean 29.33
Std Dev 4.528
Gaus mean : 30.1227
Gaus Width: 3.4810
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

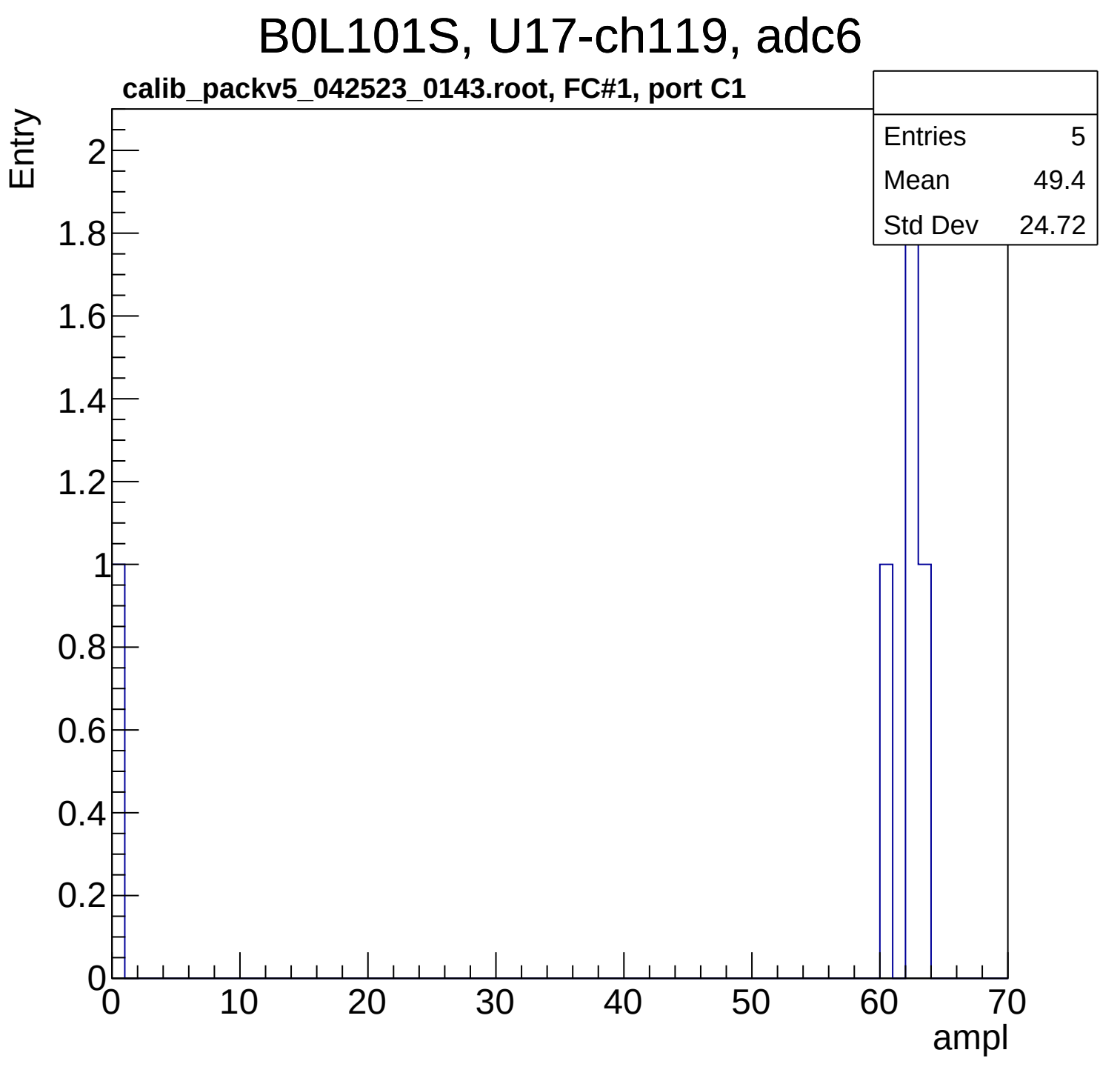

B0L101S, U17-ch119, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 36.86
Std Dev 3.988
Gaus mean : 37.5089
Gaus Width: 3.7867
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

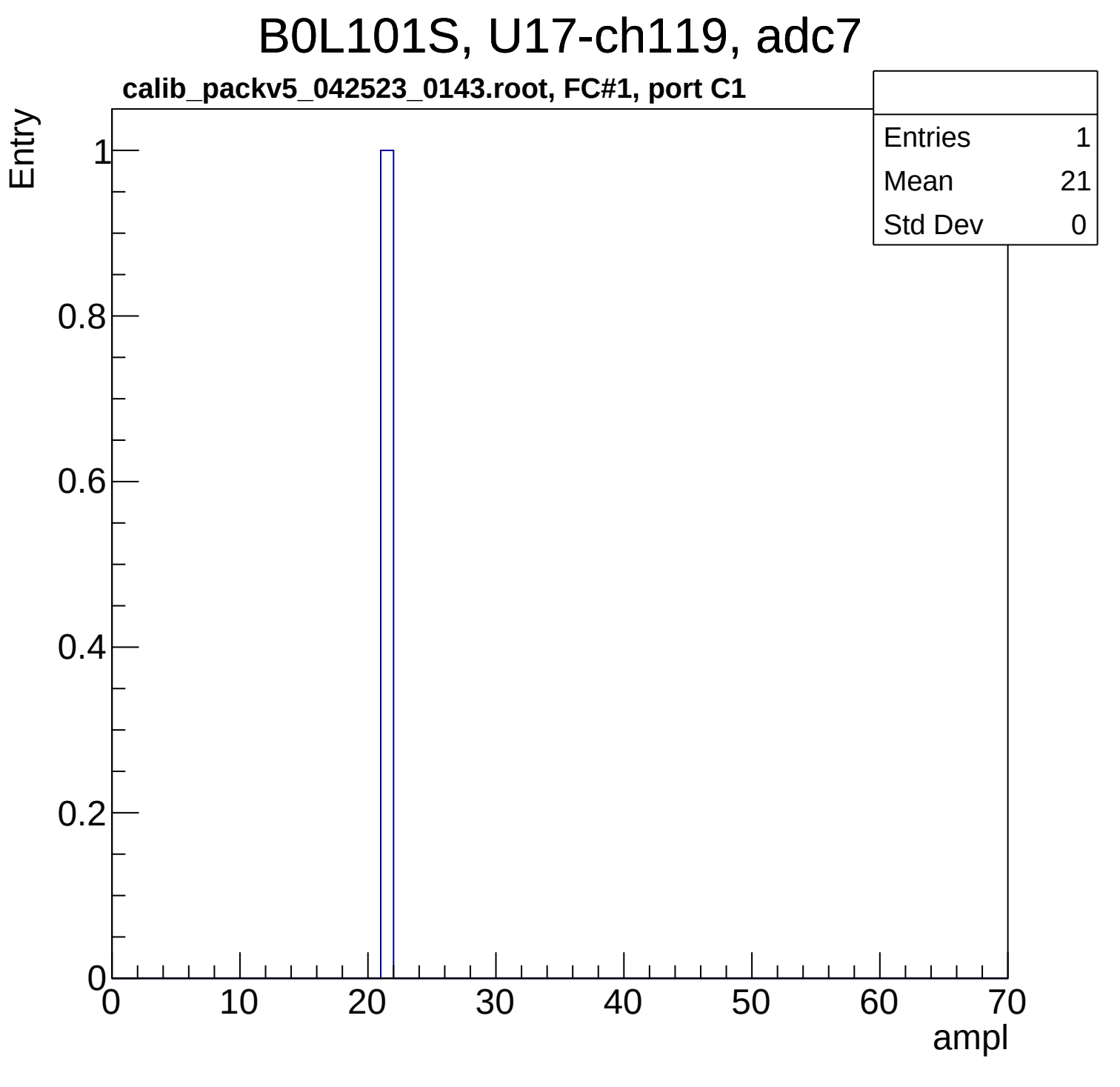

B0L101S, U17-ch119, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 128
Mean 42.84
Std Dev 3.694
Gaus mean : 43.0895
Gaus Width: 3.7225
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

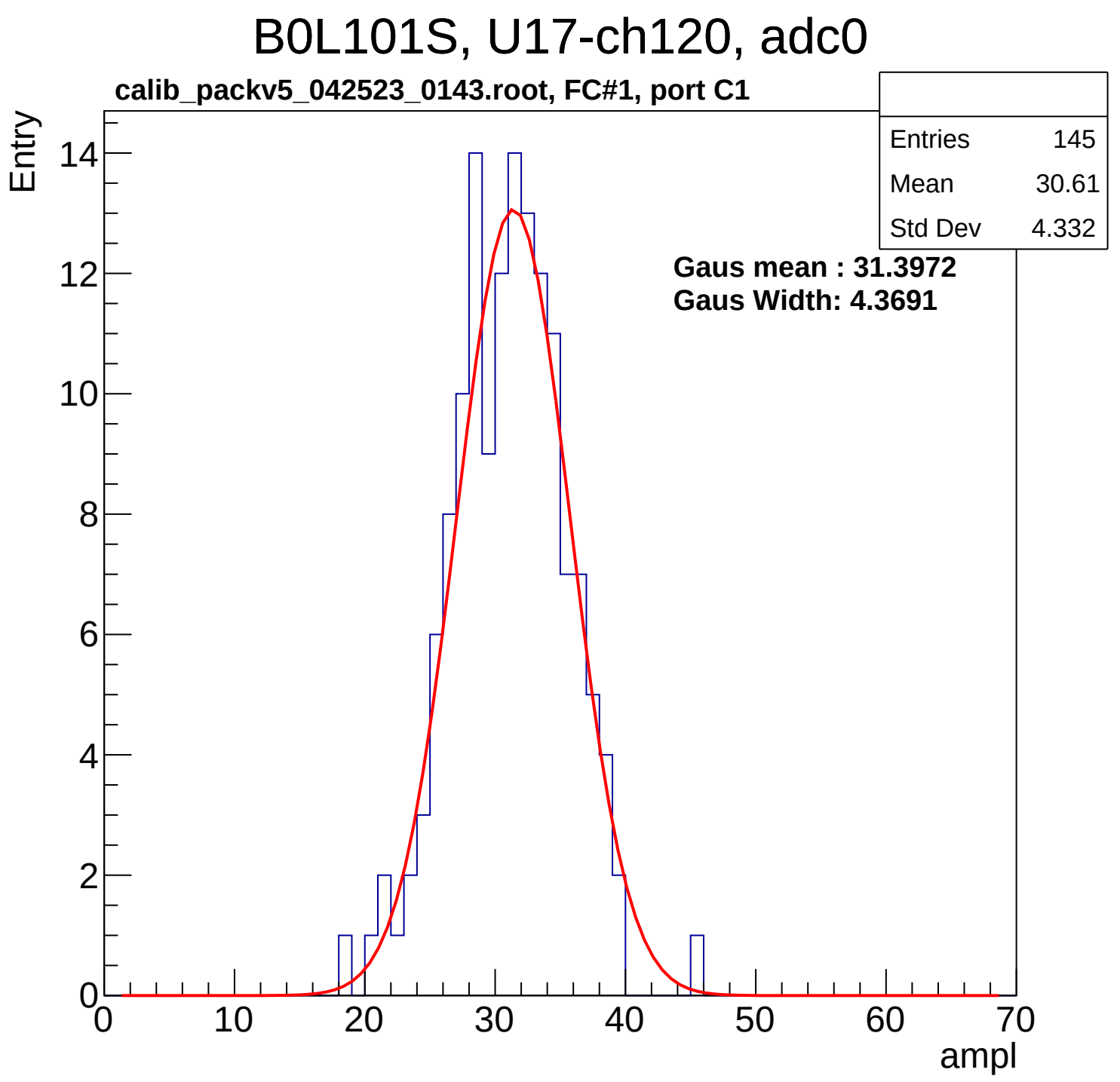

B0L101S, U17-ch119, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 49.74
Std Dev 3.666
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

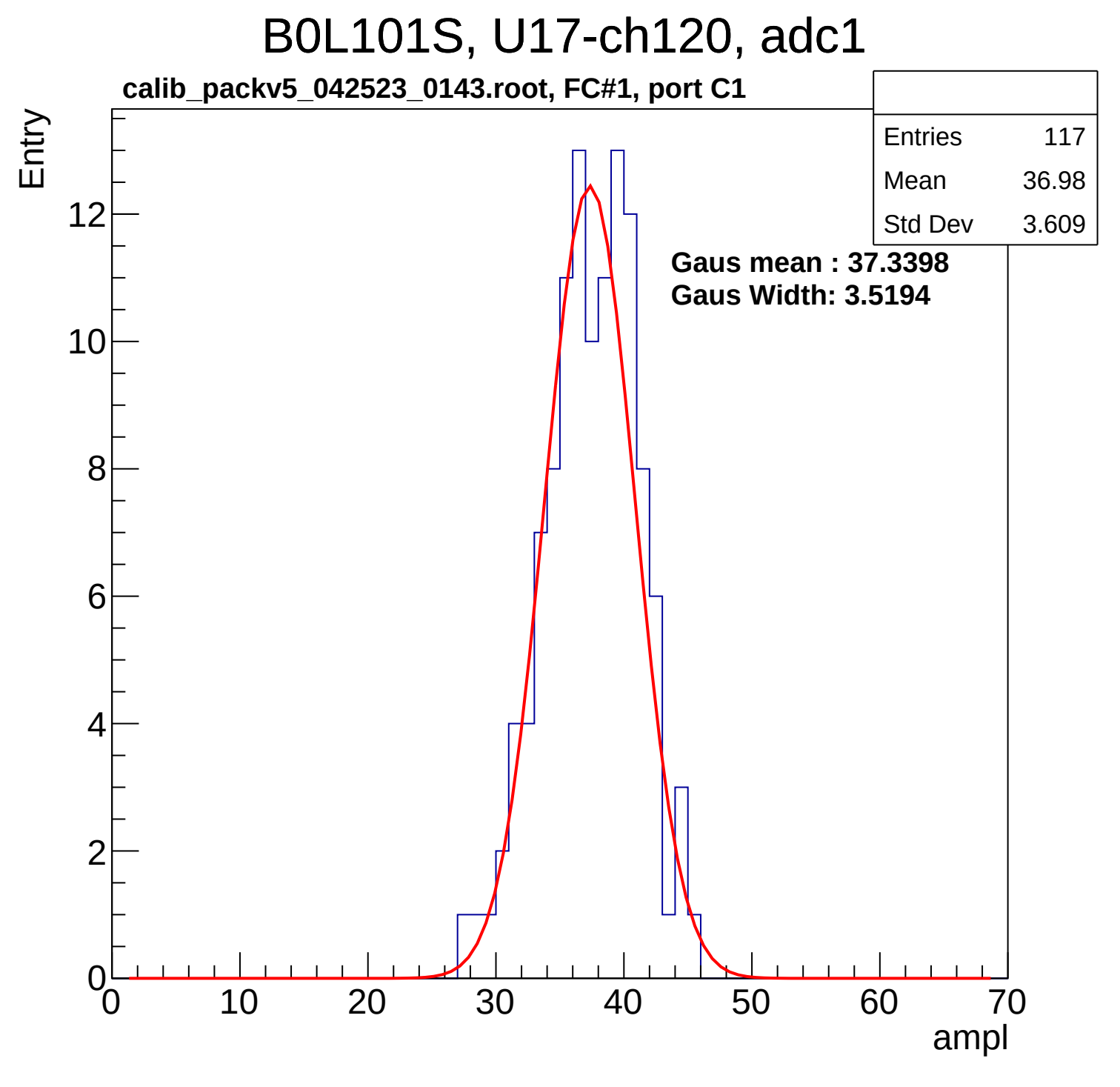

B0L101S, U17-ch119, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 55.75
Std Dev 6.211
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

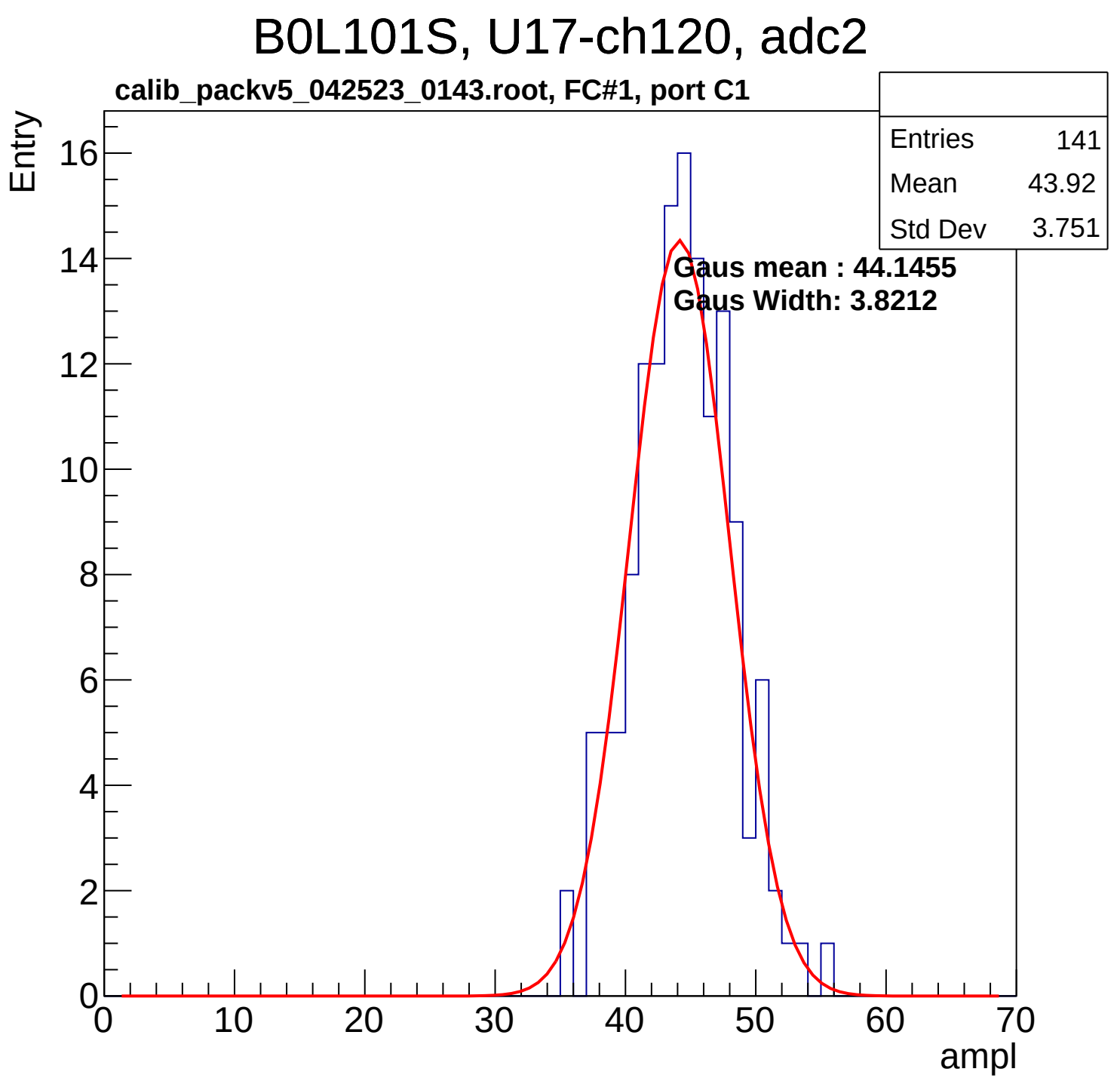

B0L101S, U17-ch119, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 90
Mean 60.36
Std Dev 2.243
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

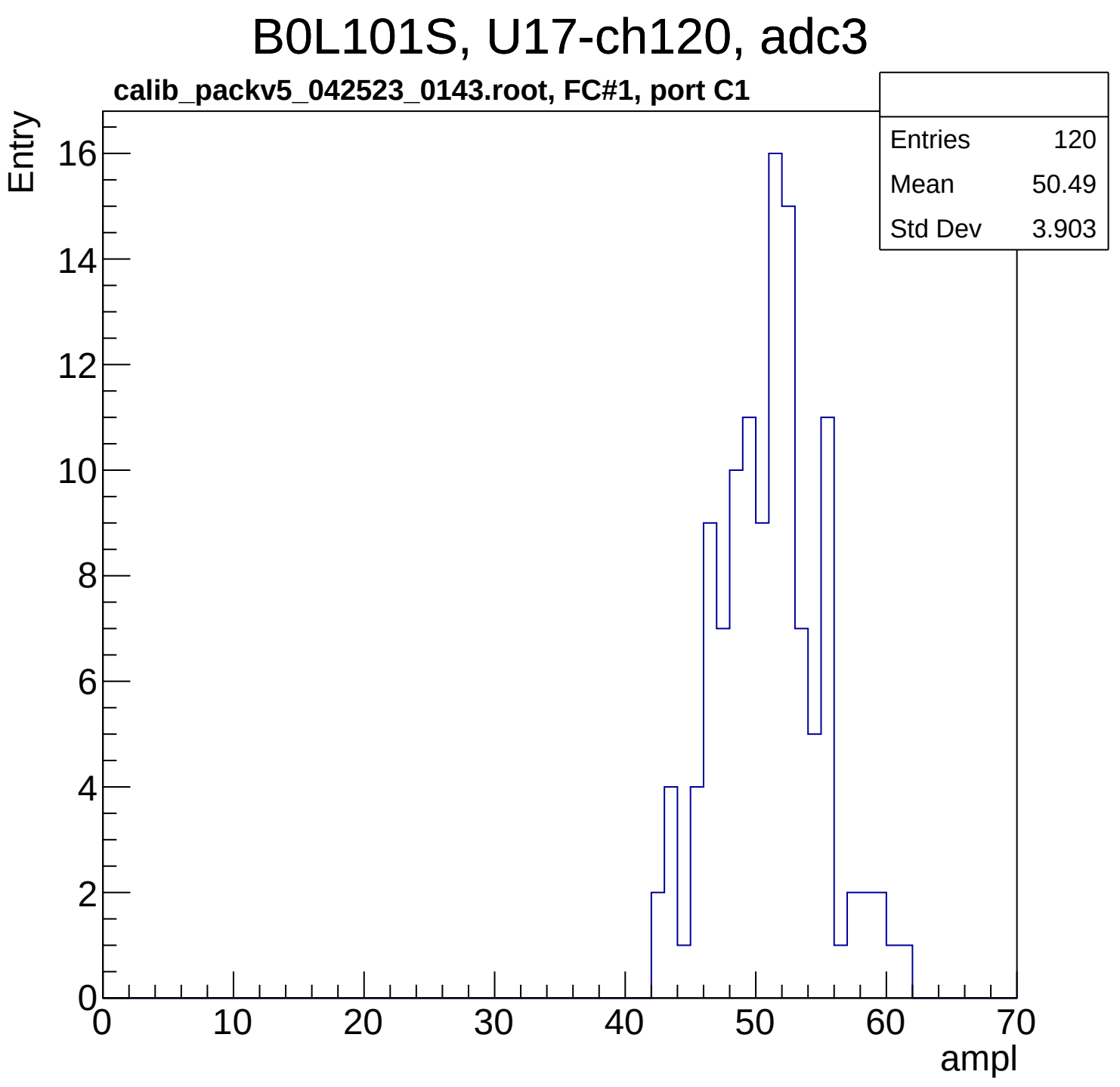

B0L101S, U17-ch119, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 5
Mean 49.4
Std Dev 24.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

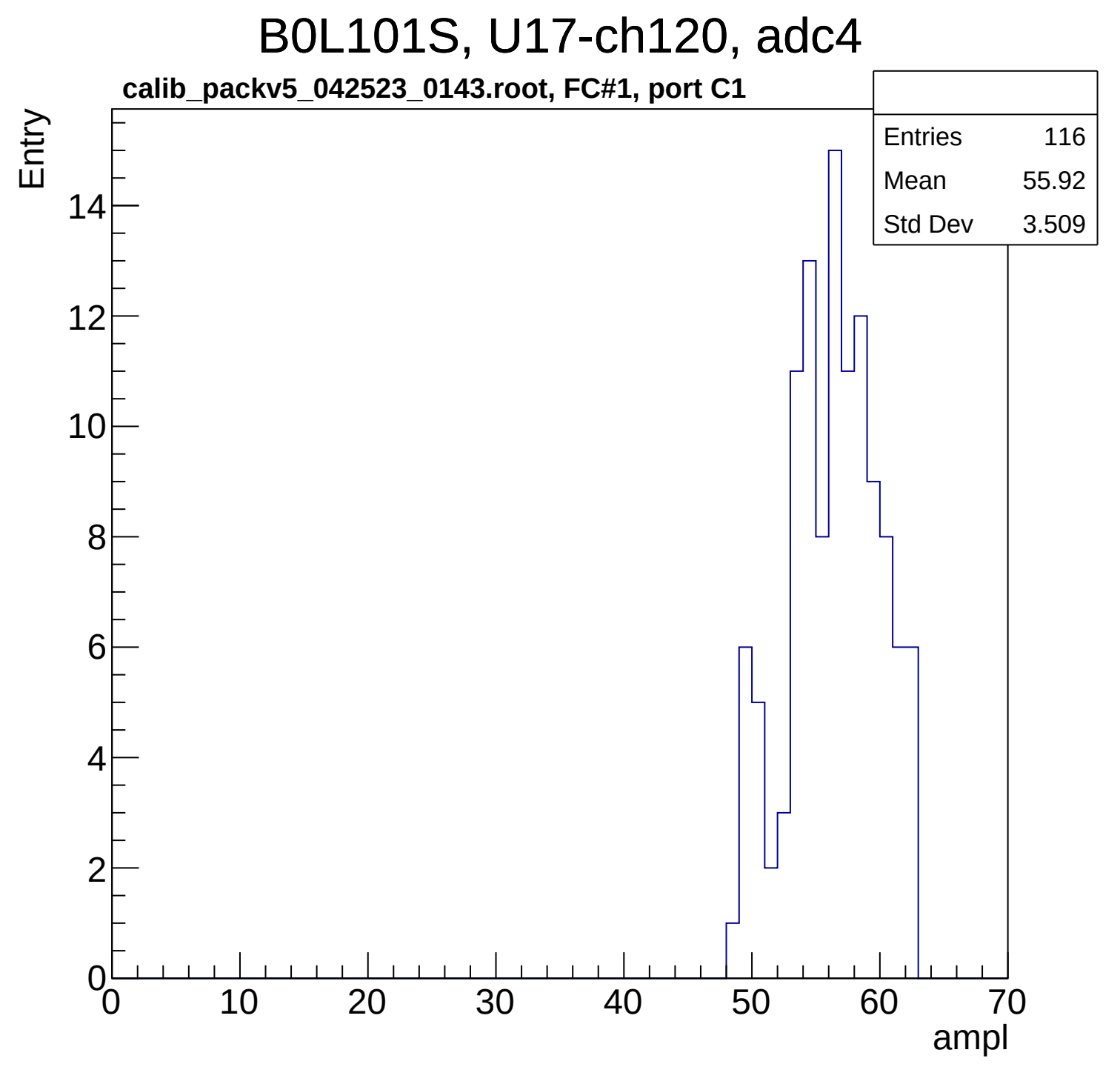

B0L101S, U17-ch119, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

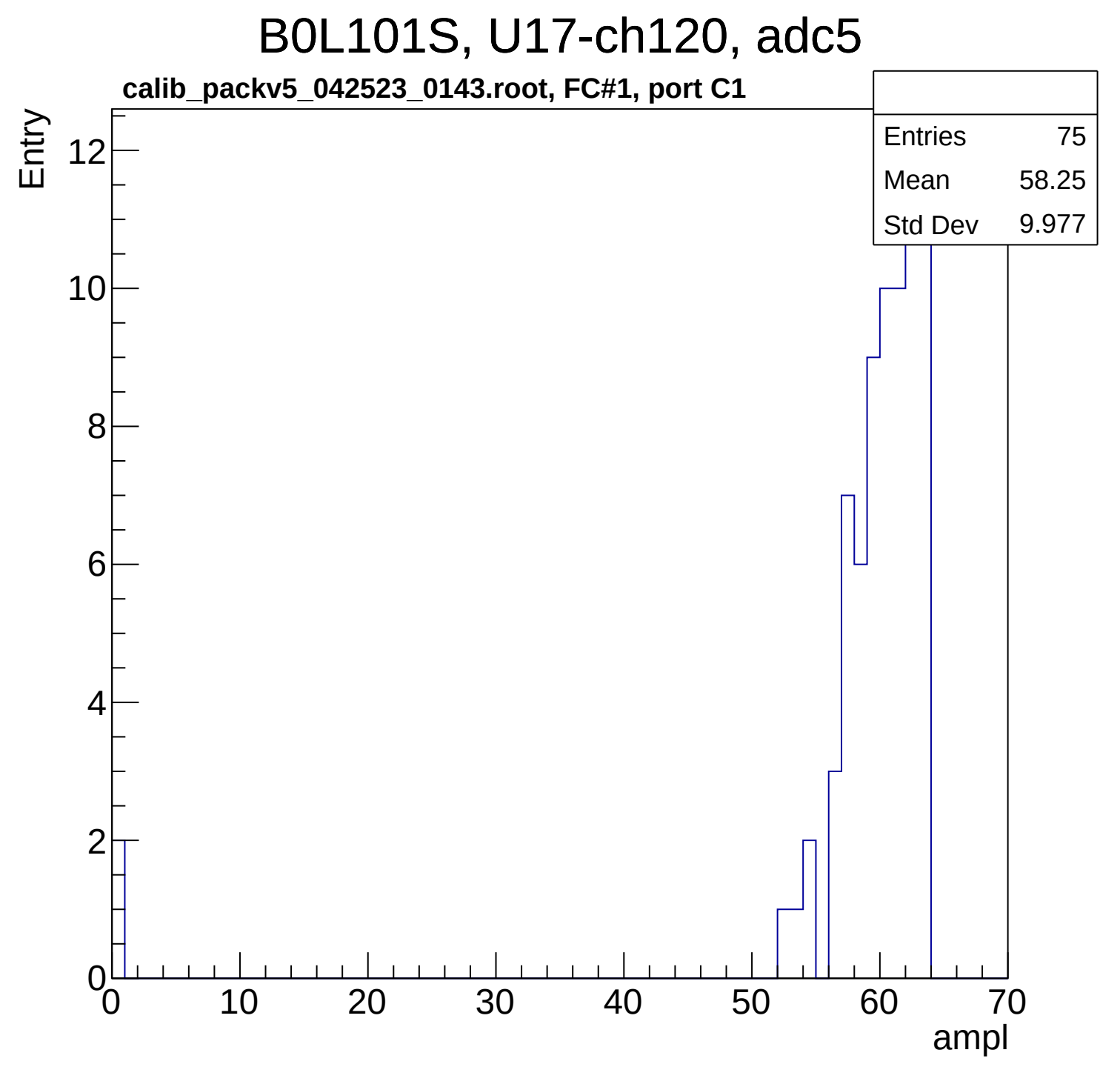

B0L101S, U17-ch120, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 145
Mean 30.61
Std Dev 4.332
Gaus mean : 31.3972
Gaus Width: 4.3691
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

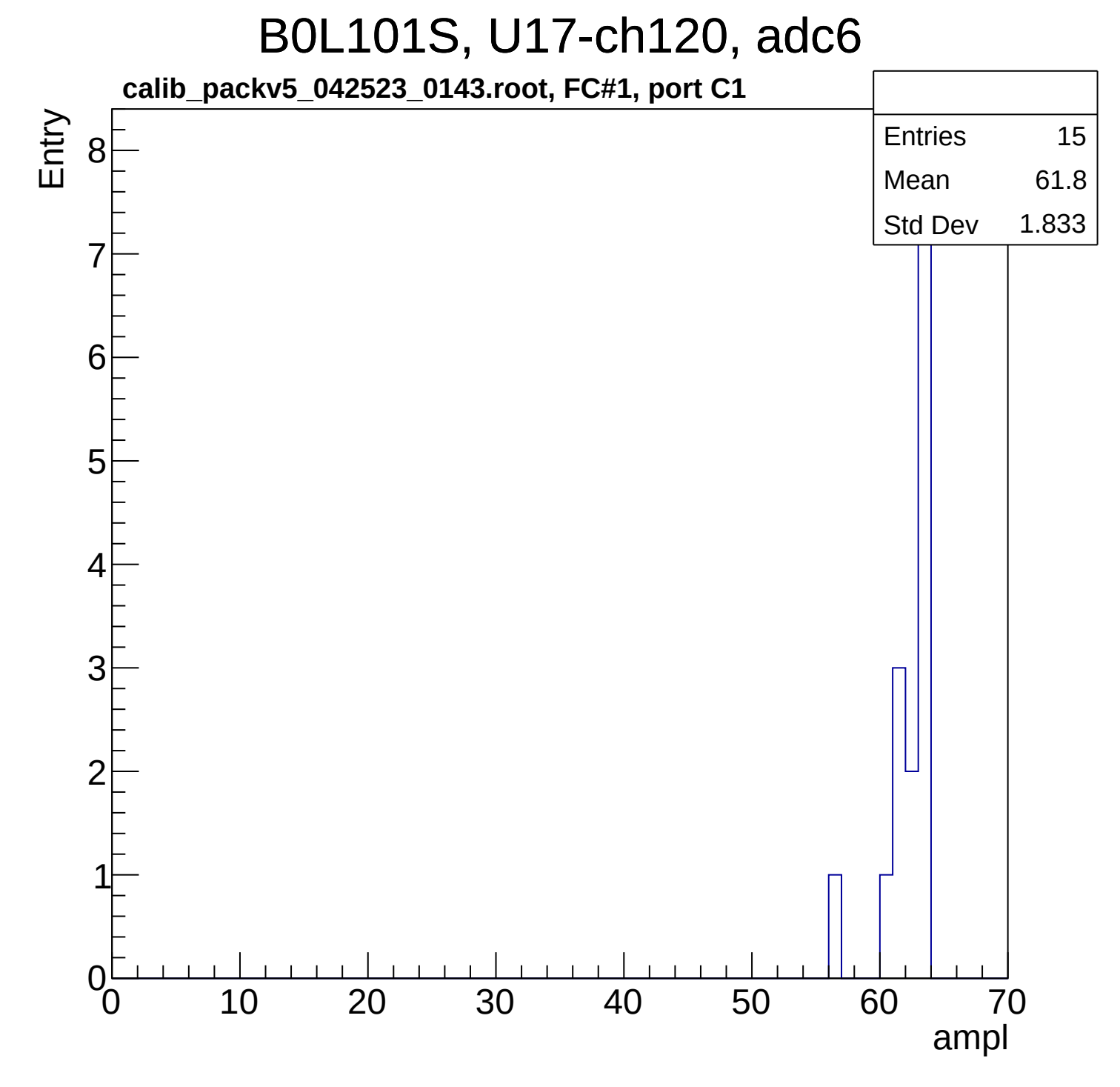

B0L101S, U17-ch120, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 117
Mean 36.98
Std Dev 3.609
Gaus mean : 37.3398
Gaus Width: 3.5194
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

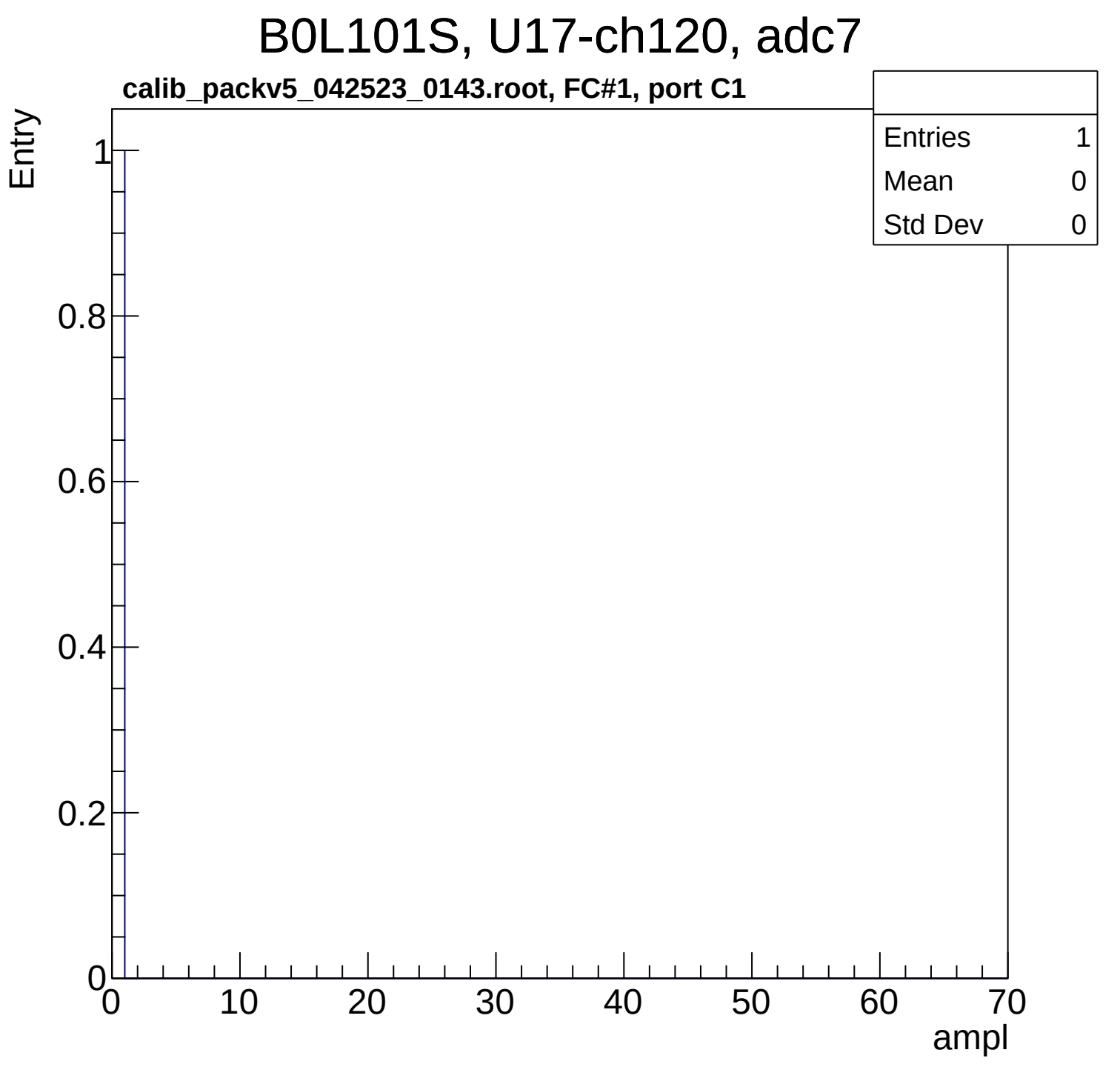

B0L101S, U17-ch120, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 141
Mean 43.92
Std Dev 3.751
Gaus mean : 44.1455
Gaus Width: 3.8212
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

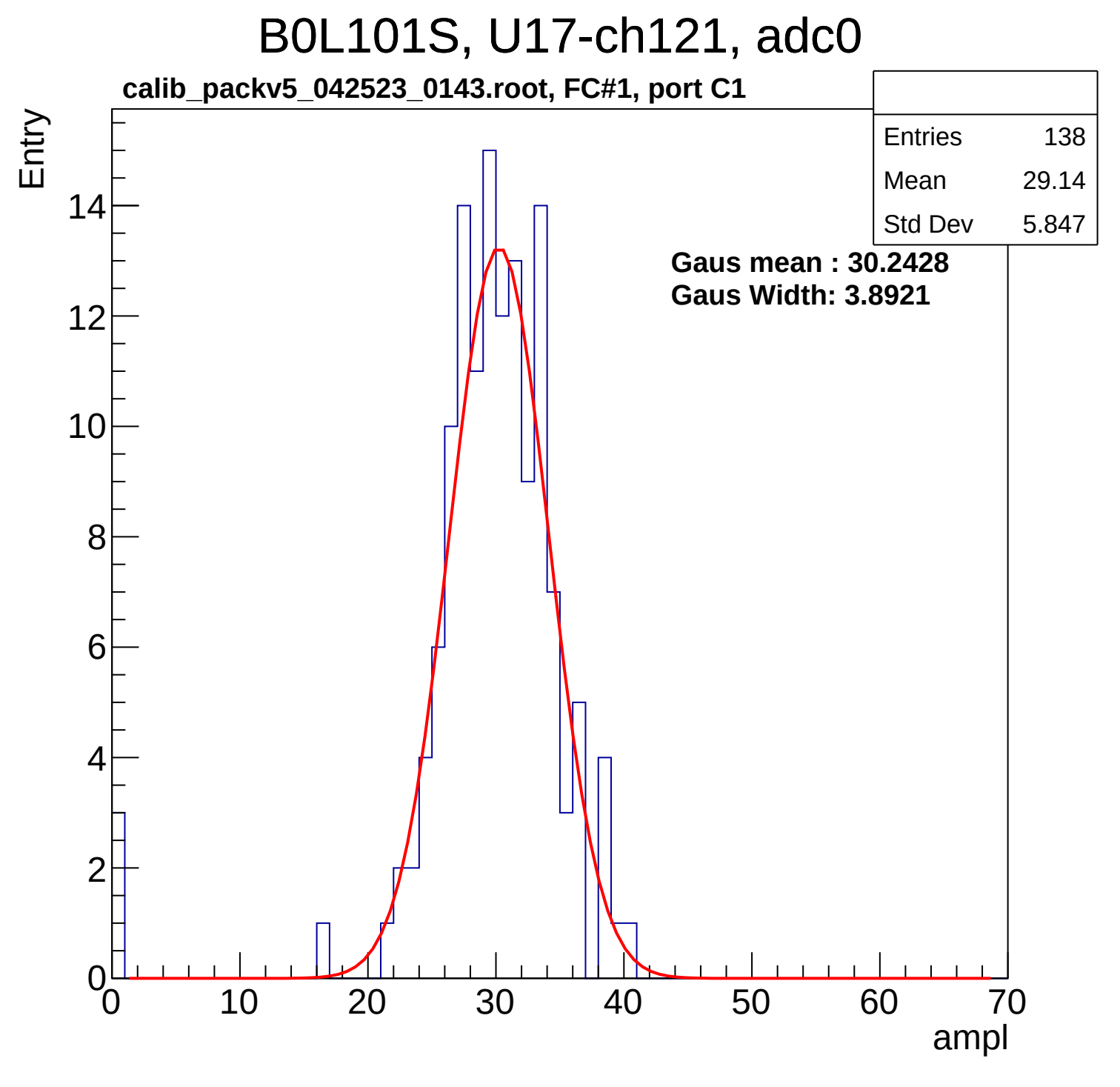

B0L101S, U17-ch120, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 50.49
Std Dev 3.903
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

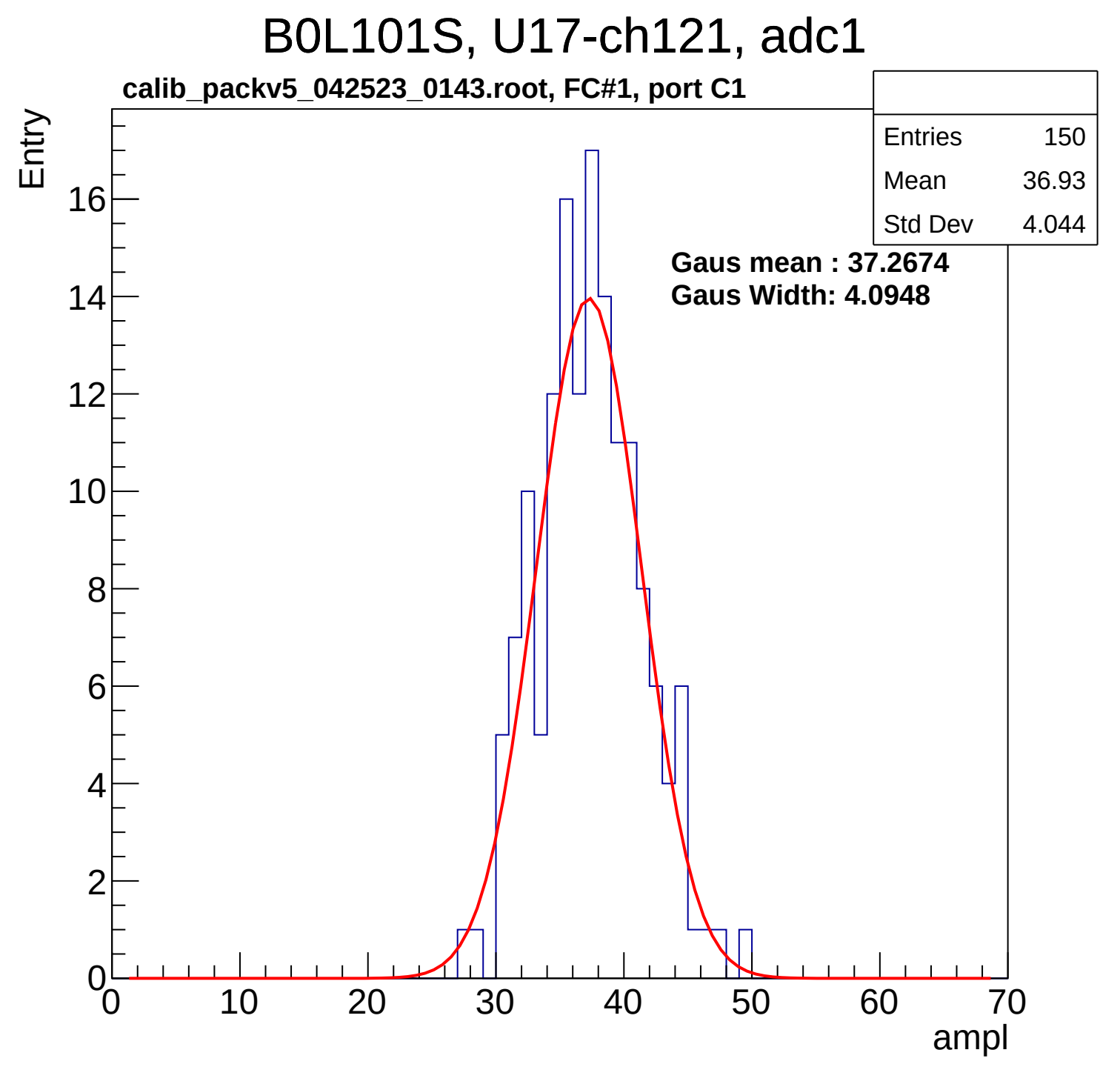

B0L101S, U17-ch120, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 116
Mean 55.92
Std Dev 3.509
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

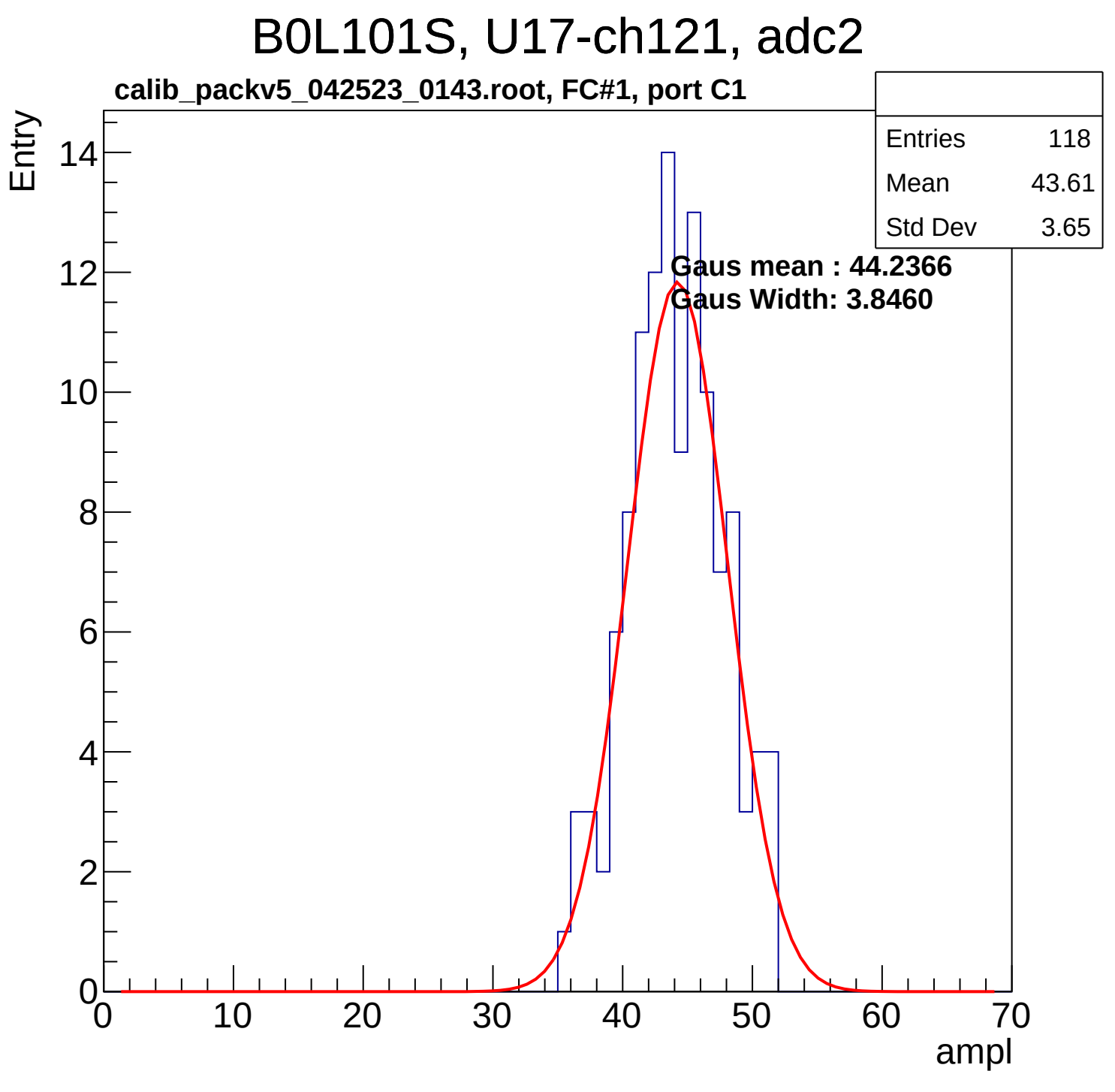

B0L101S, U17-ch120, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 75
Mean 58.25
Std Dev 9.977
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

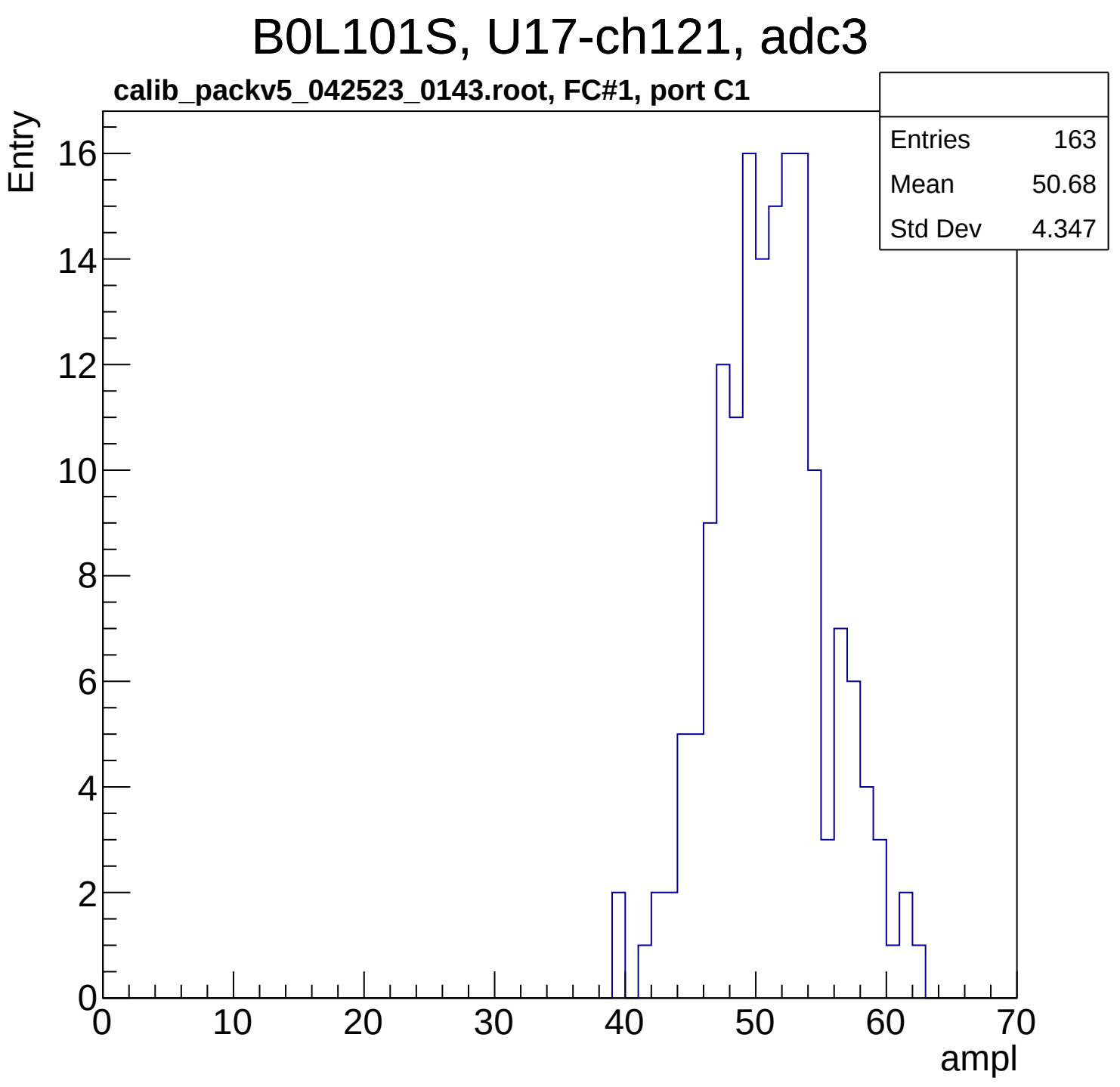

B0L101S, U17-ch120, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 15
Mean 61.8
Std Dev 1.833
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

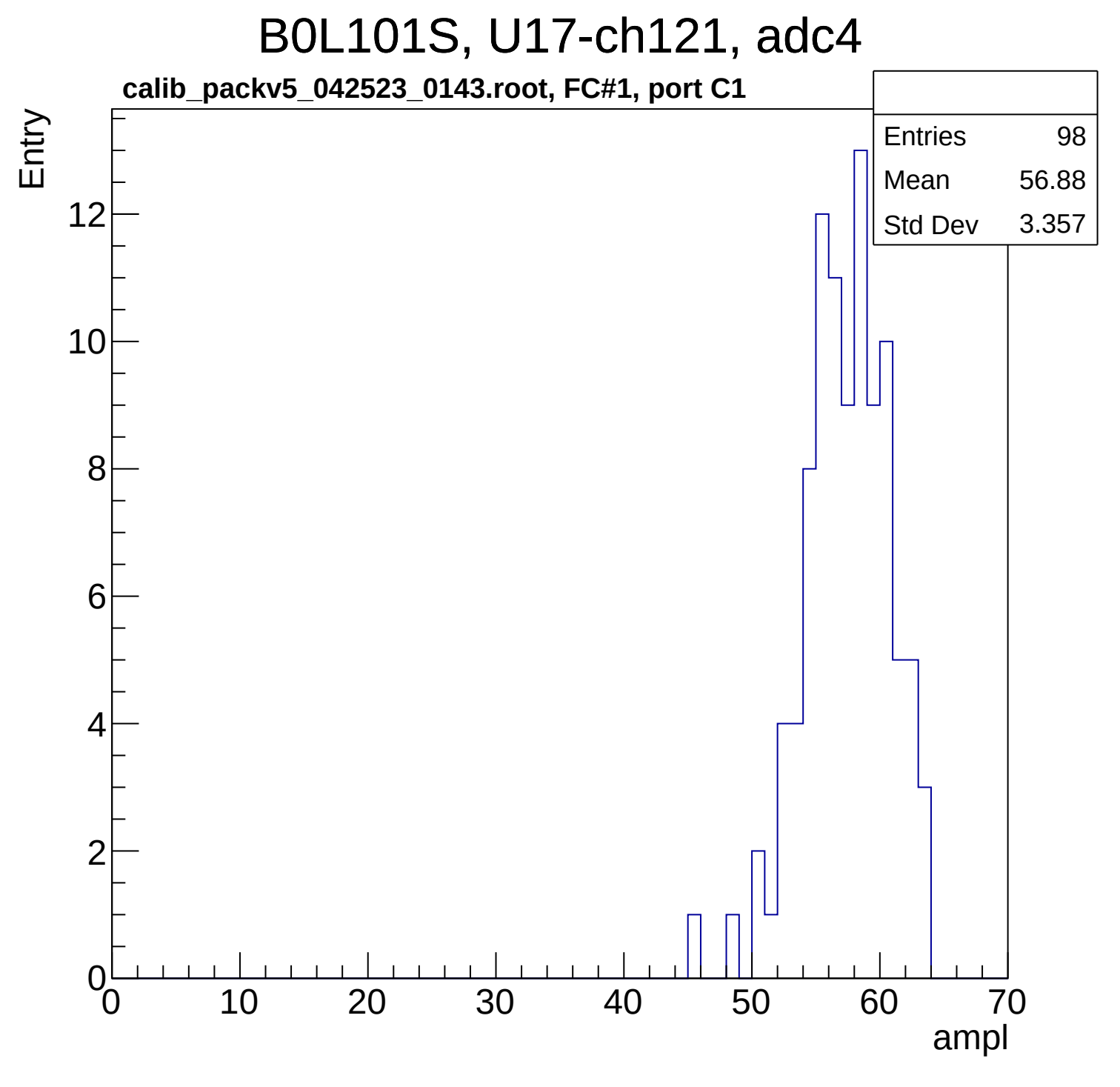

B0L101S, U17-ch120, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

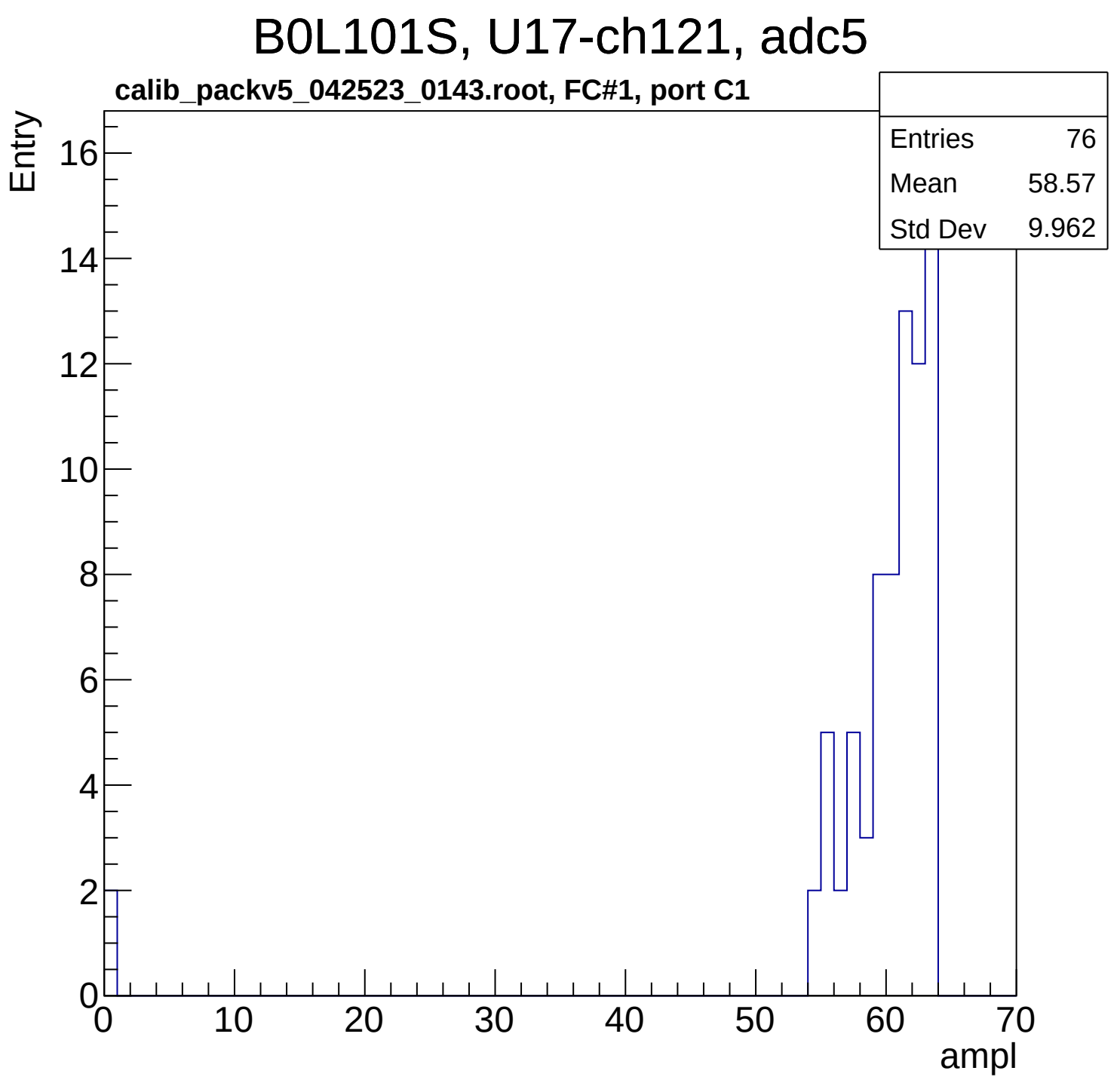

B0L101S, U17-ch121, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 138
Mean 29.14
Std Dev 5.847
Gaus mean : 30.2428
Gaus Width: 3.8921
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

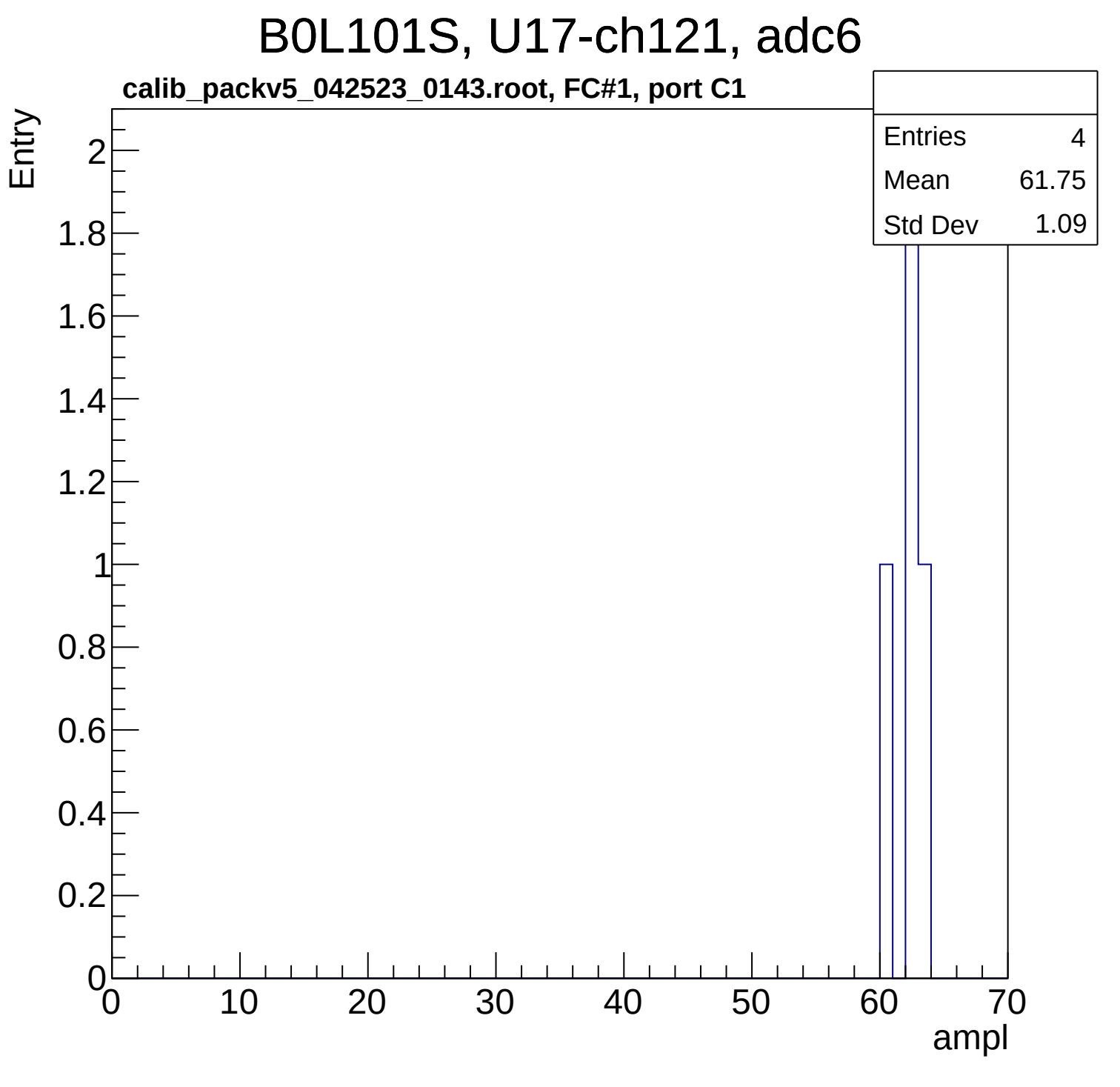

B0L101S, U17-ch121, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 150
Mean 36.93
Std Dev 4.044
Gaus mean : 37.2674
Gaus Width: 4.0948
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

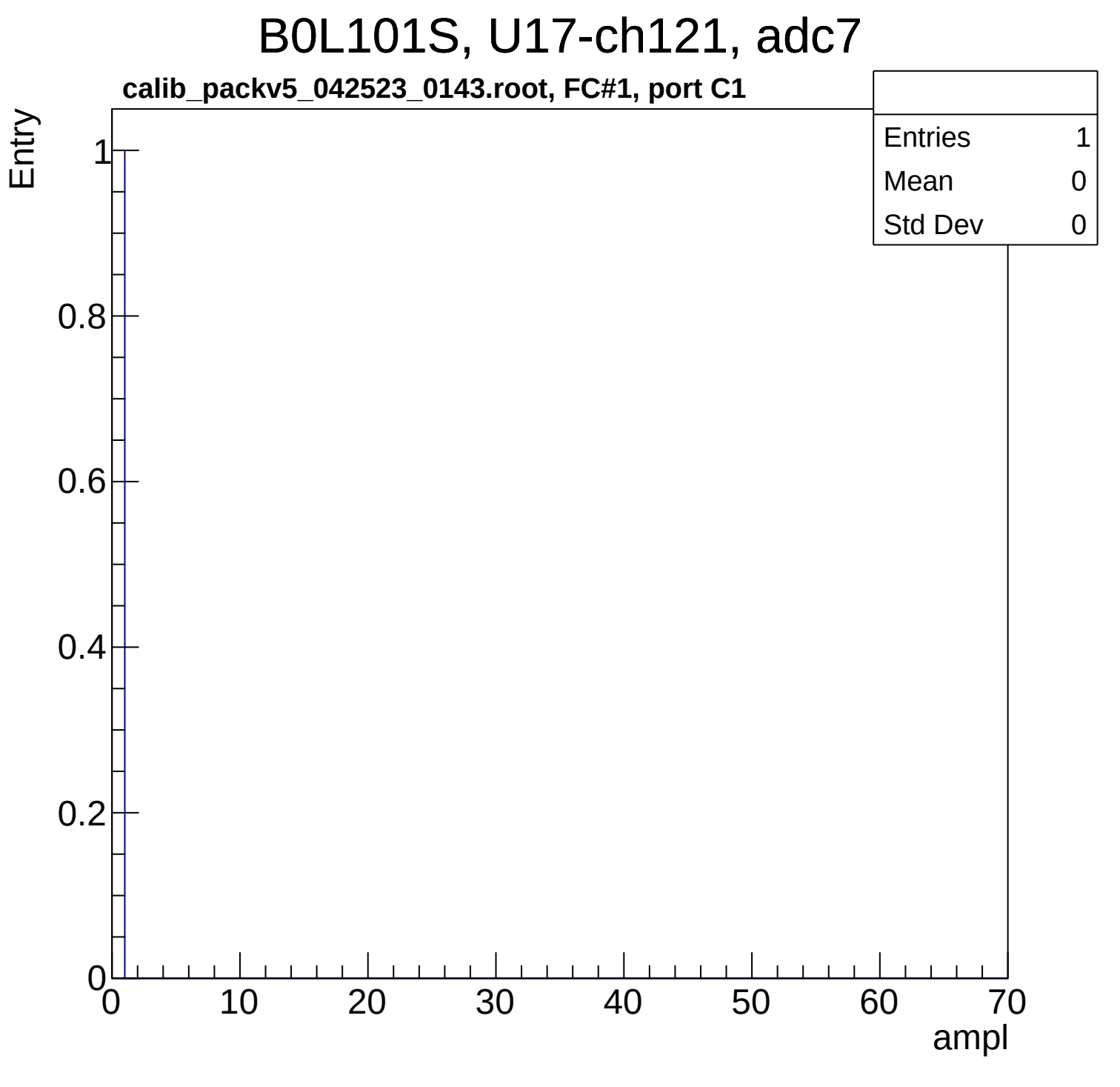

B0L101S, U17-ch121, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 118
Mean 43.61
Std Dev 3.65
Gaus mean : 44.2366
Gaus Width: 3.8460
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

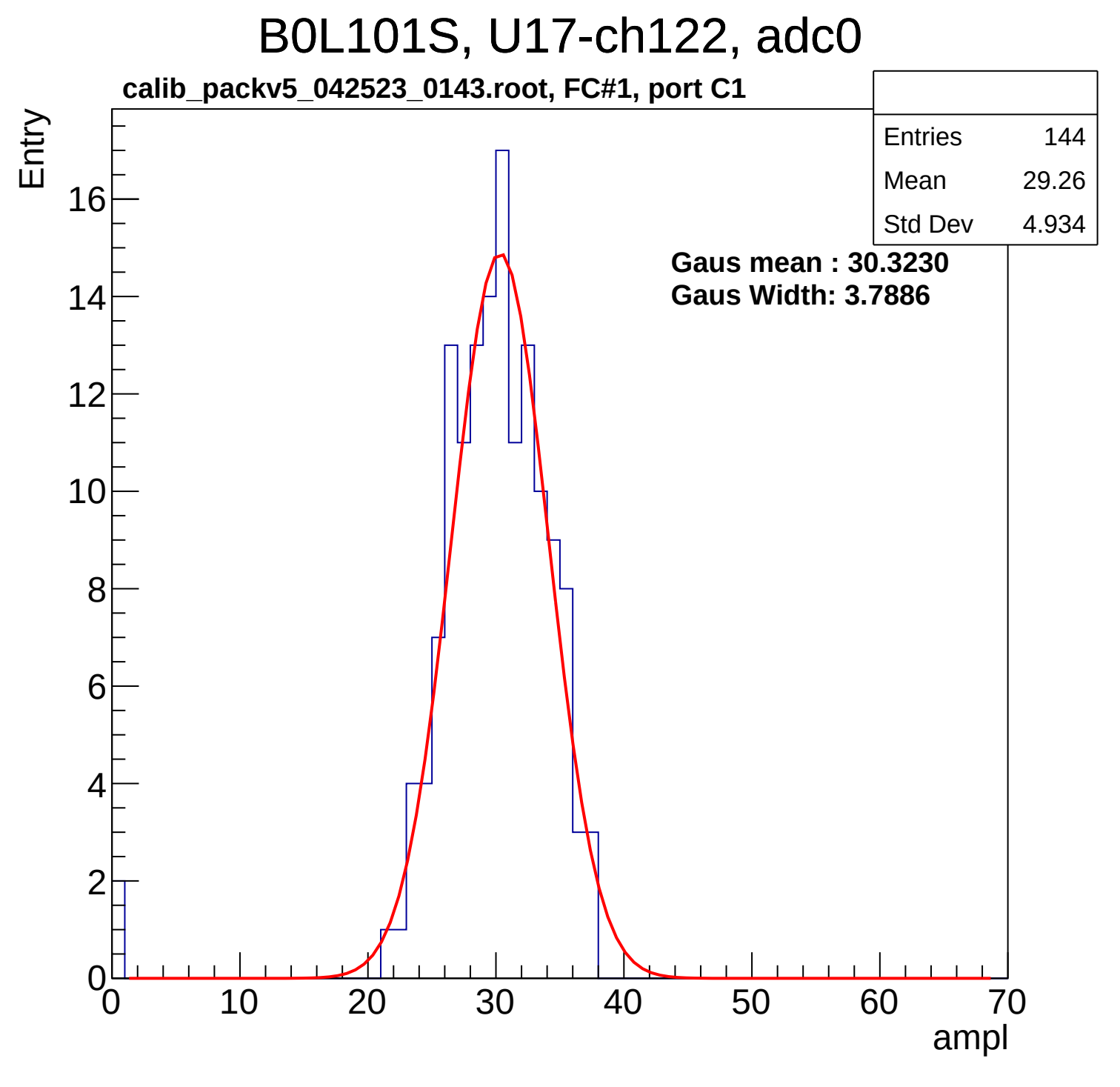

B0L101S, U17-ch121, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 163
Mean 50.68
Std Dev 4.347
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

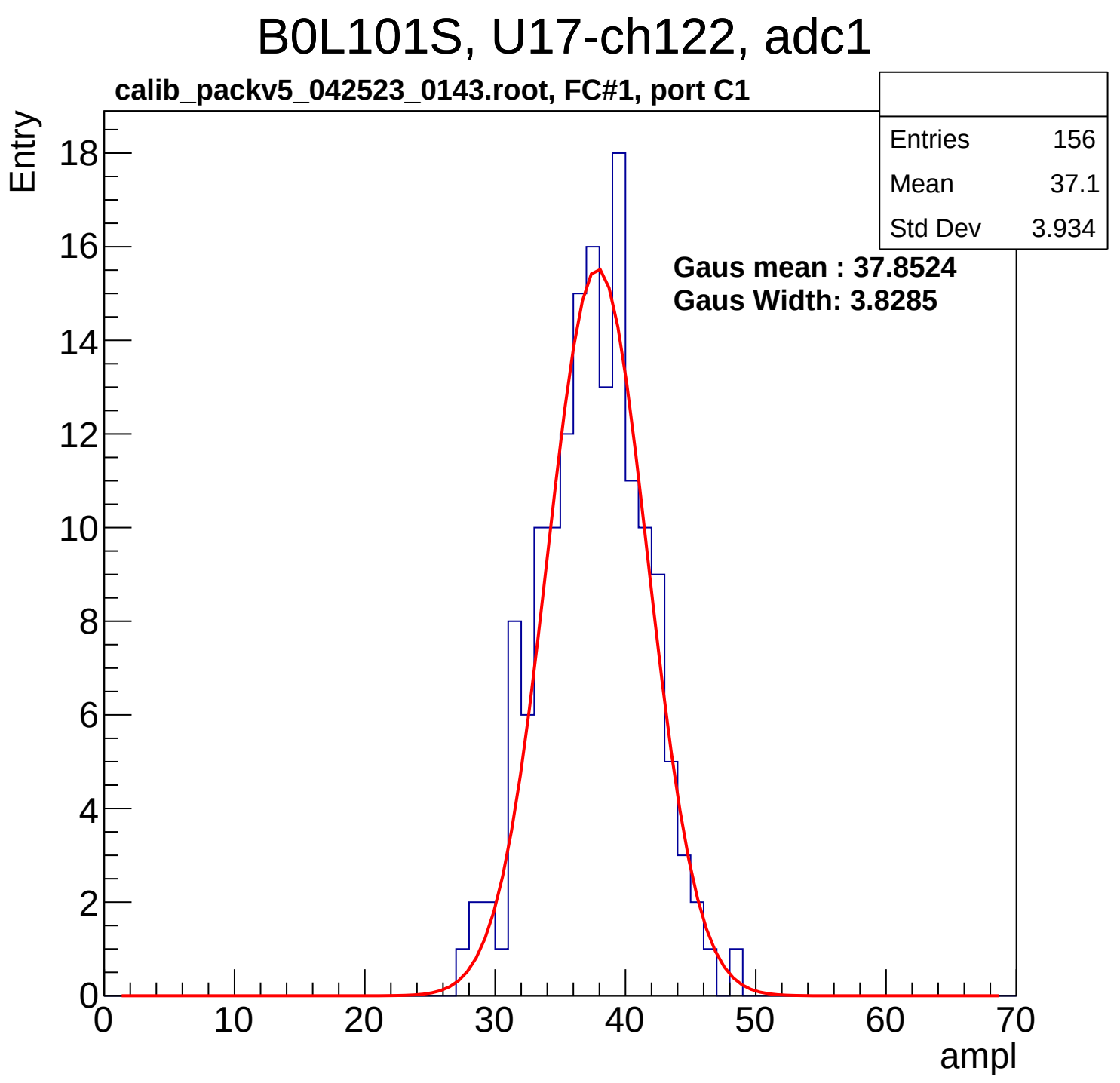

B0L101S, U17-ch121, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 98
Mean 56.88
Std Dev 3.357
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

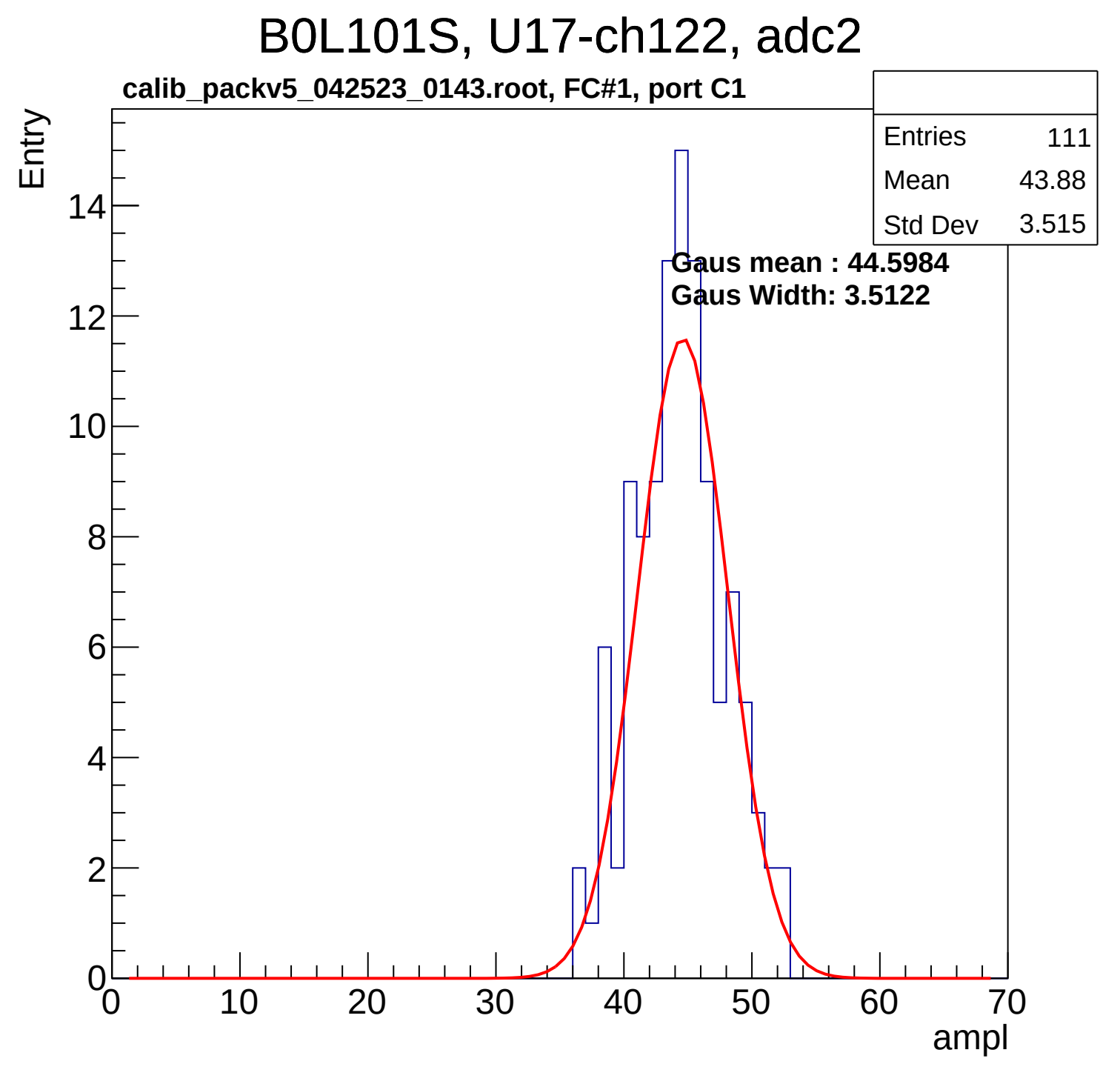

B0L101S, U17-ch121, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 76
Mean 58.57
Std Dev 9.962
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

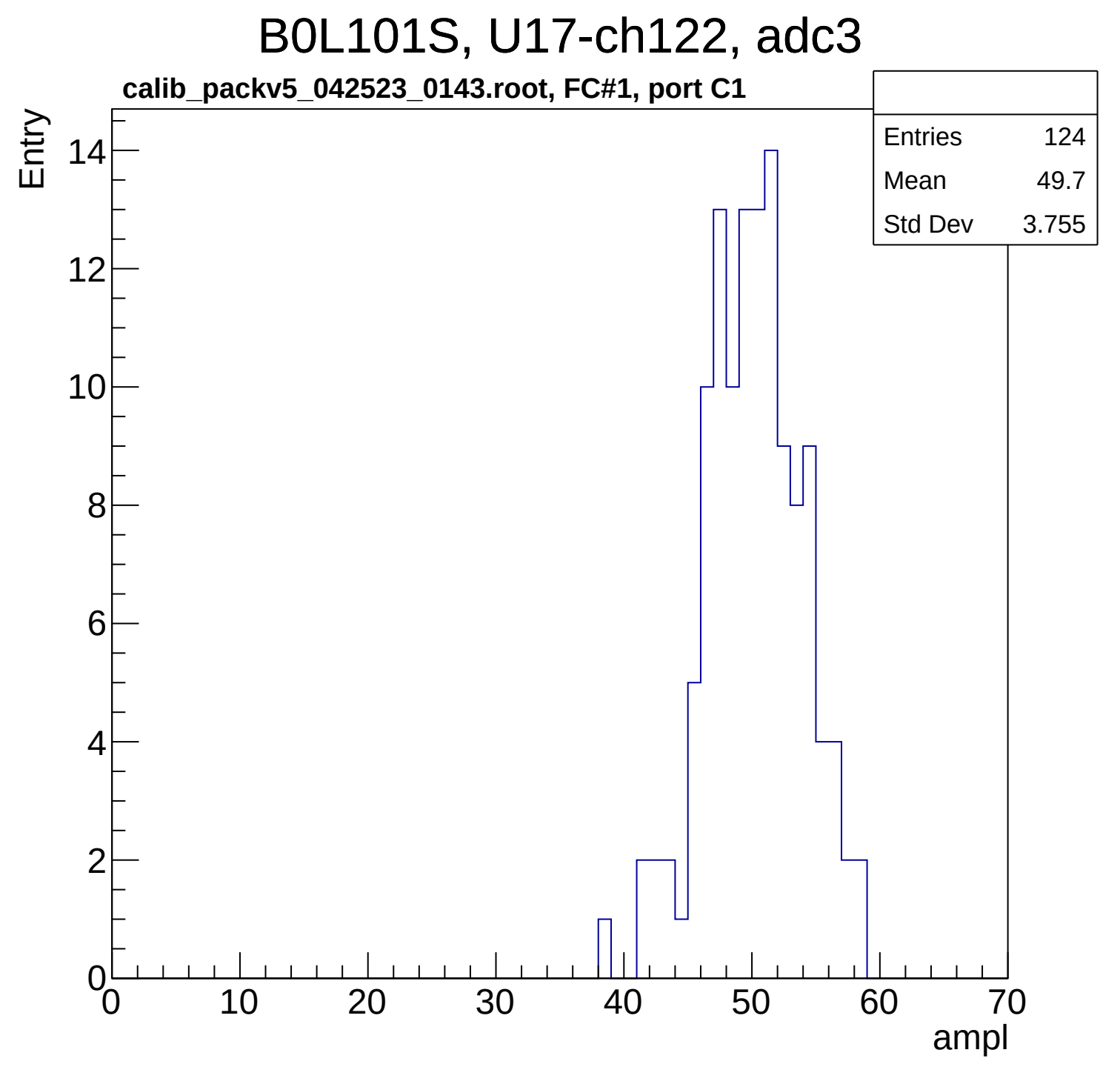

B0L101S, U17-ch121, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

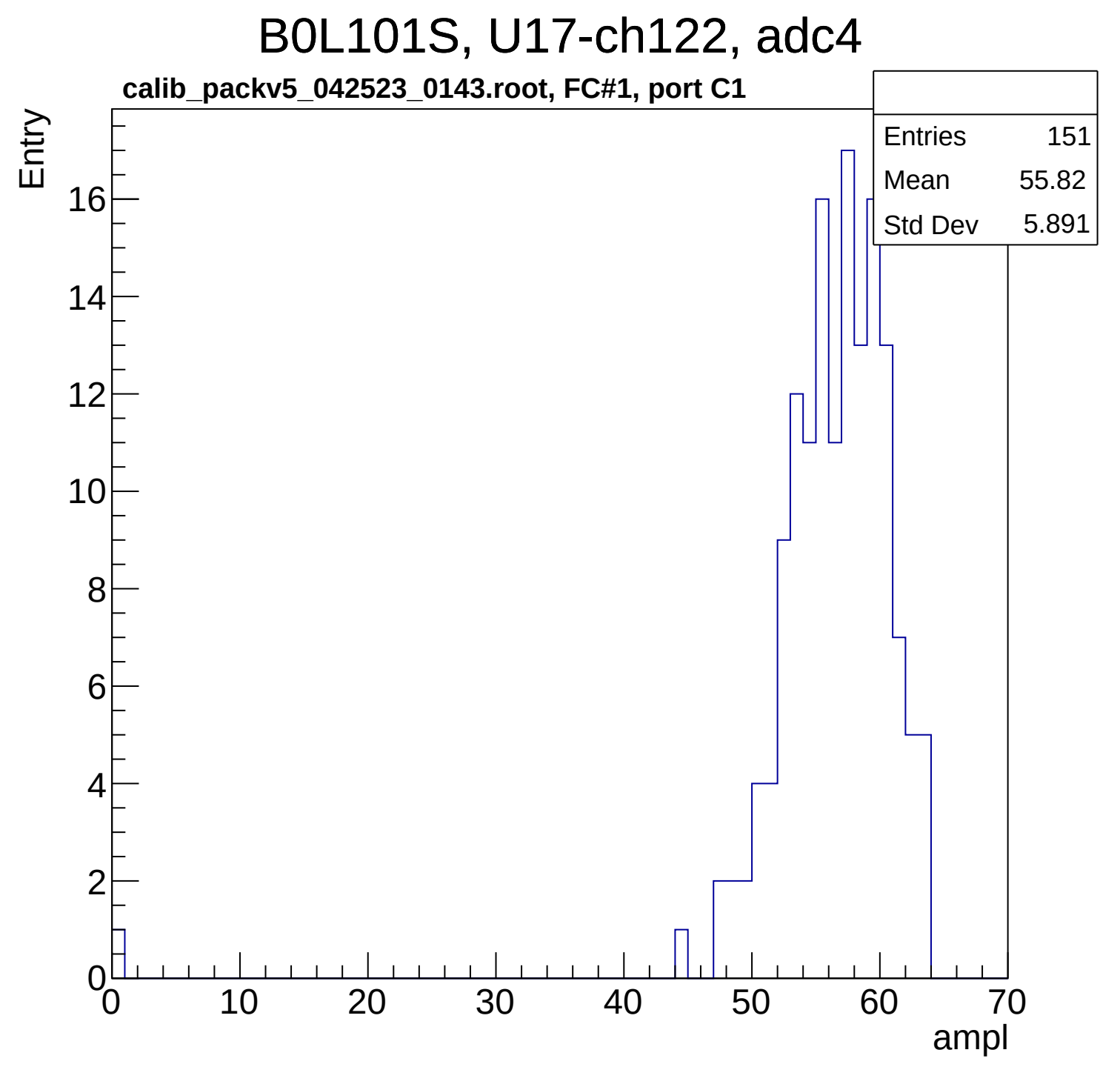

B0L101S, U17-ch121, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

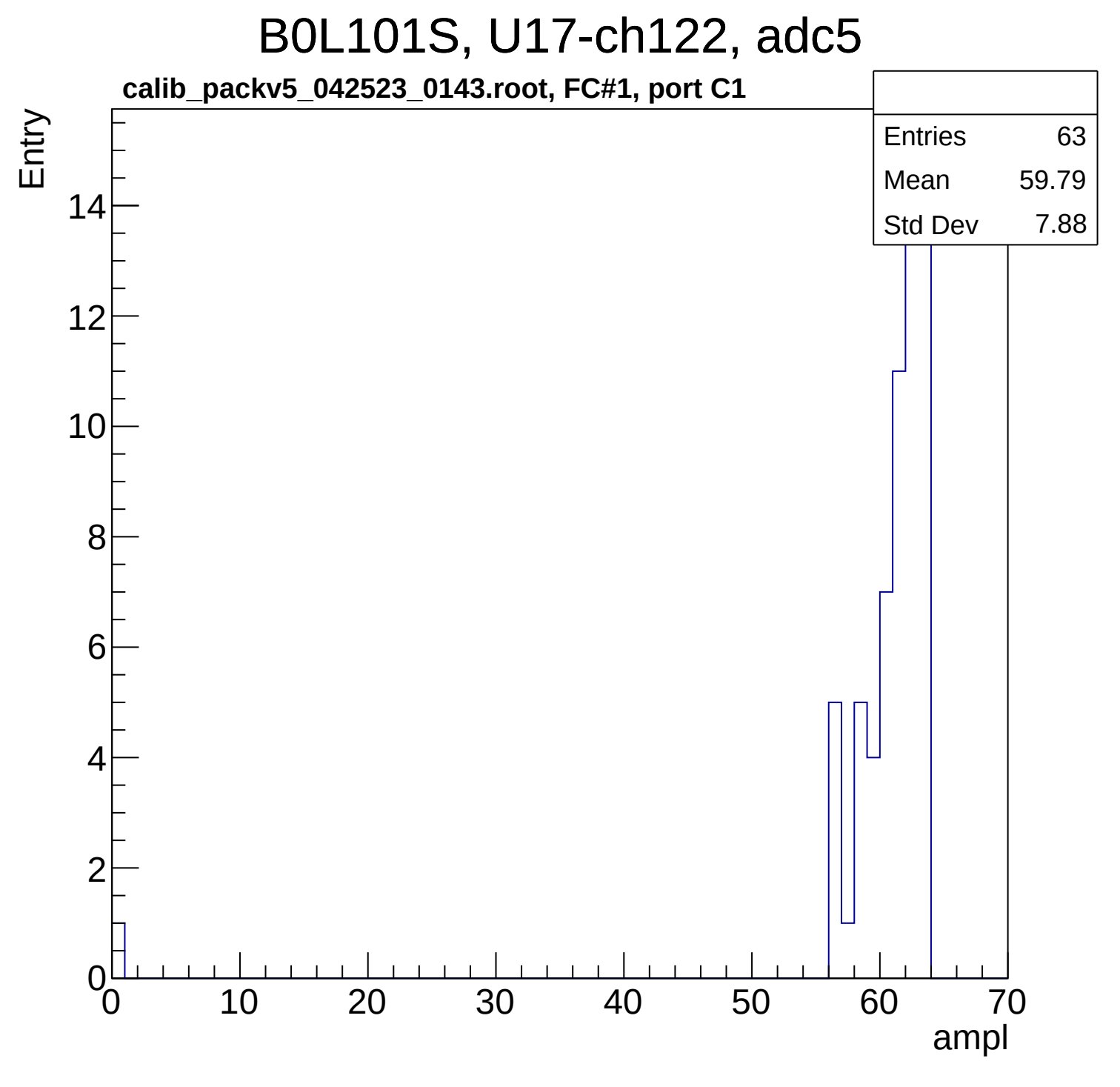

B0L101S, U17-ch122, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 144
Mean 29.26
Std Dev 4.934
Gaus mean : 30.3230
Gaus Width: 3.7886
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

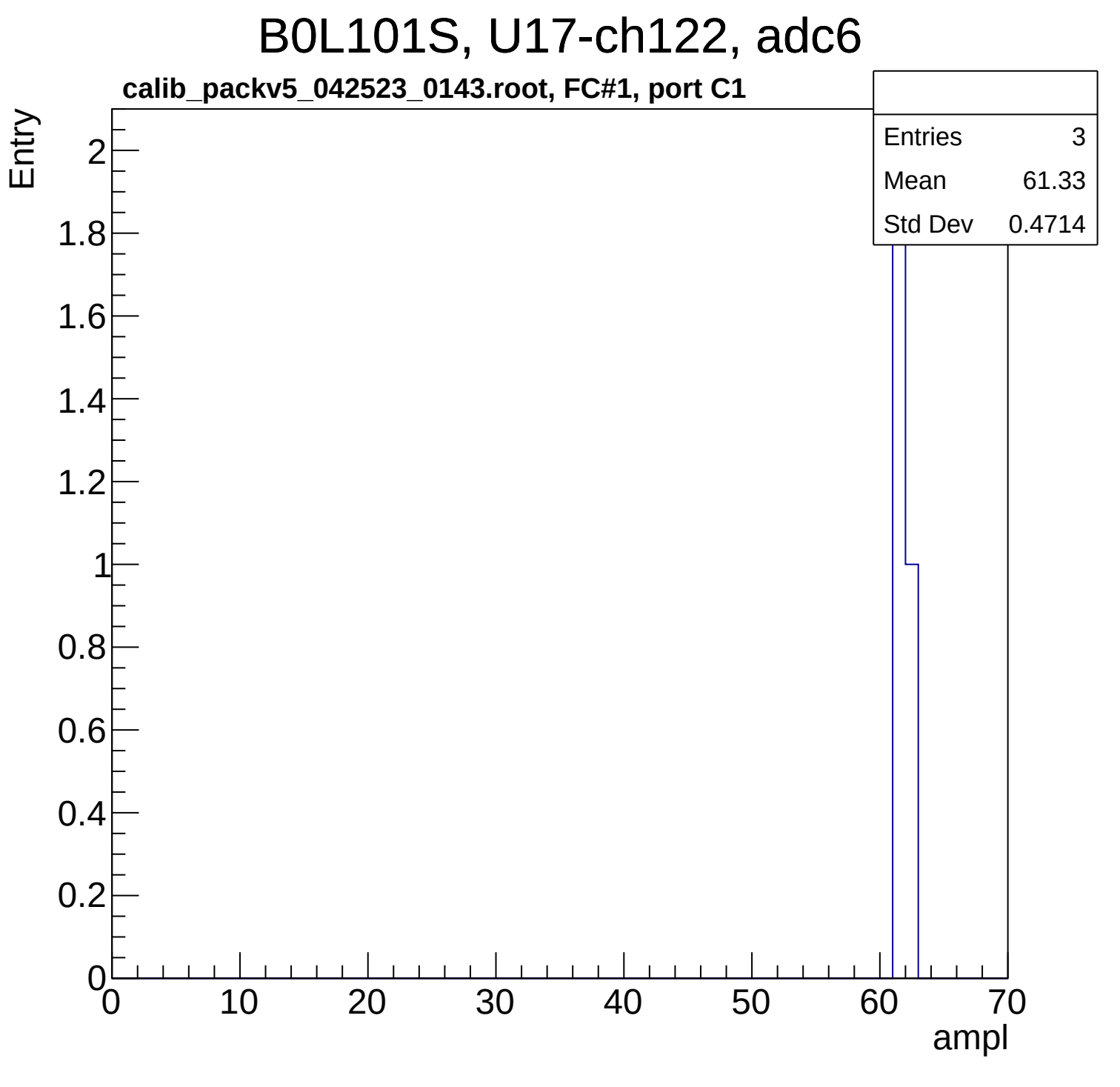

B0L101S, U17-ch122, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 37.1
Std Dev 3.934
Gaus mean : 37.8524
Gaus Width: 3.8285
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

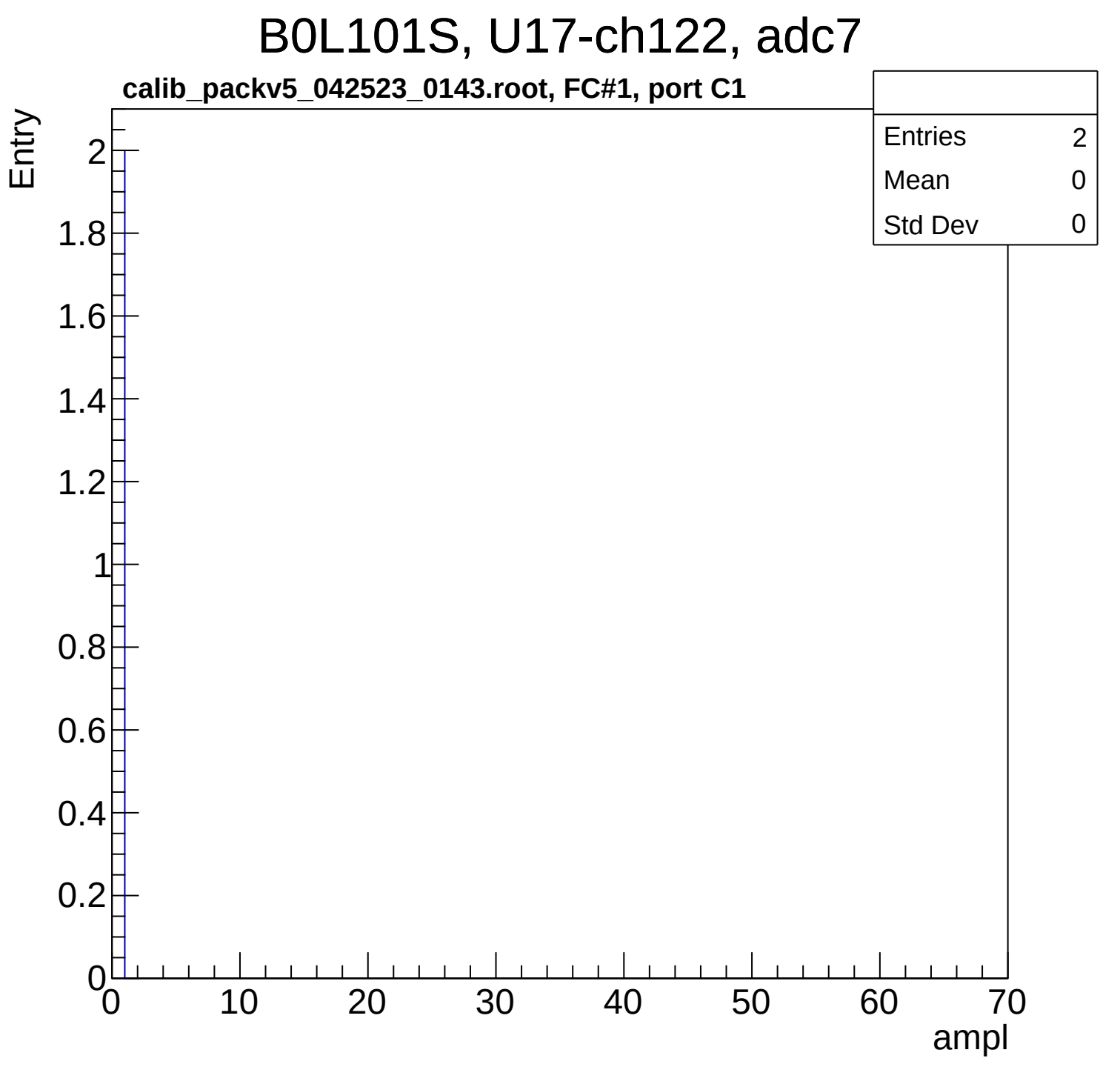

B0L101S, U17-ch122, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 111
Mean 43.88
Std Dev 3.515
Gaus mean : 44.5984
Gaus Width: 3.5122
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

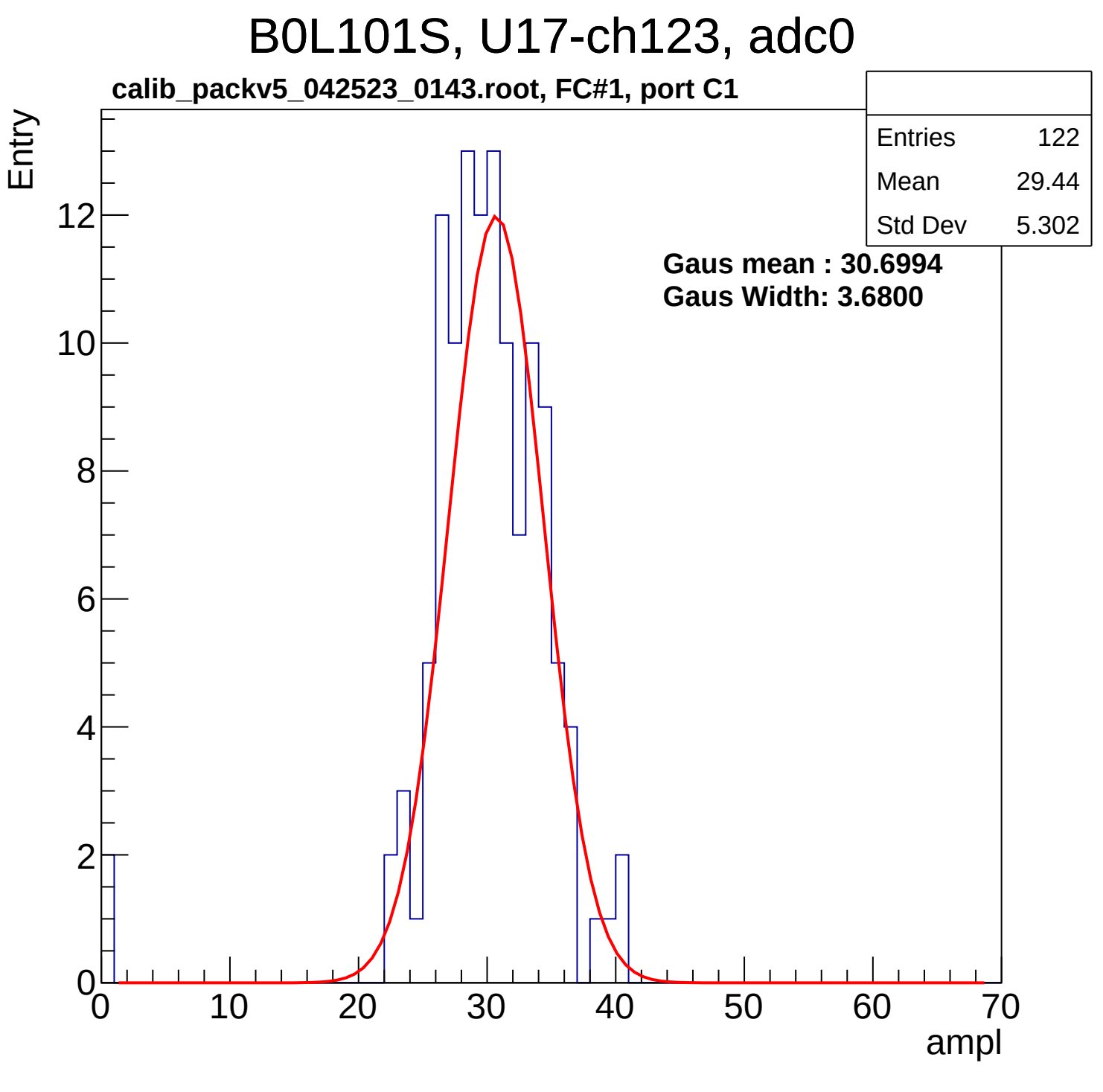

B0L101S, U17-ch122, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 124
Mean 49.7
Std Dev 3.755
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

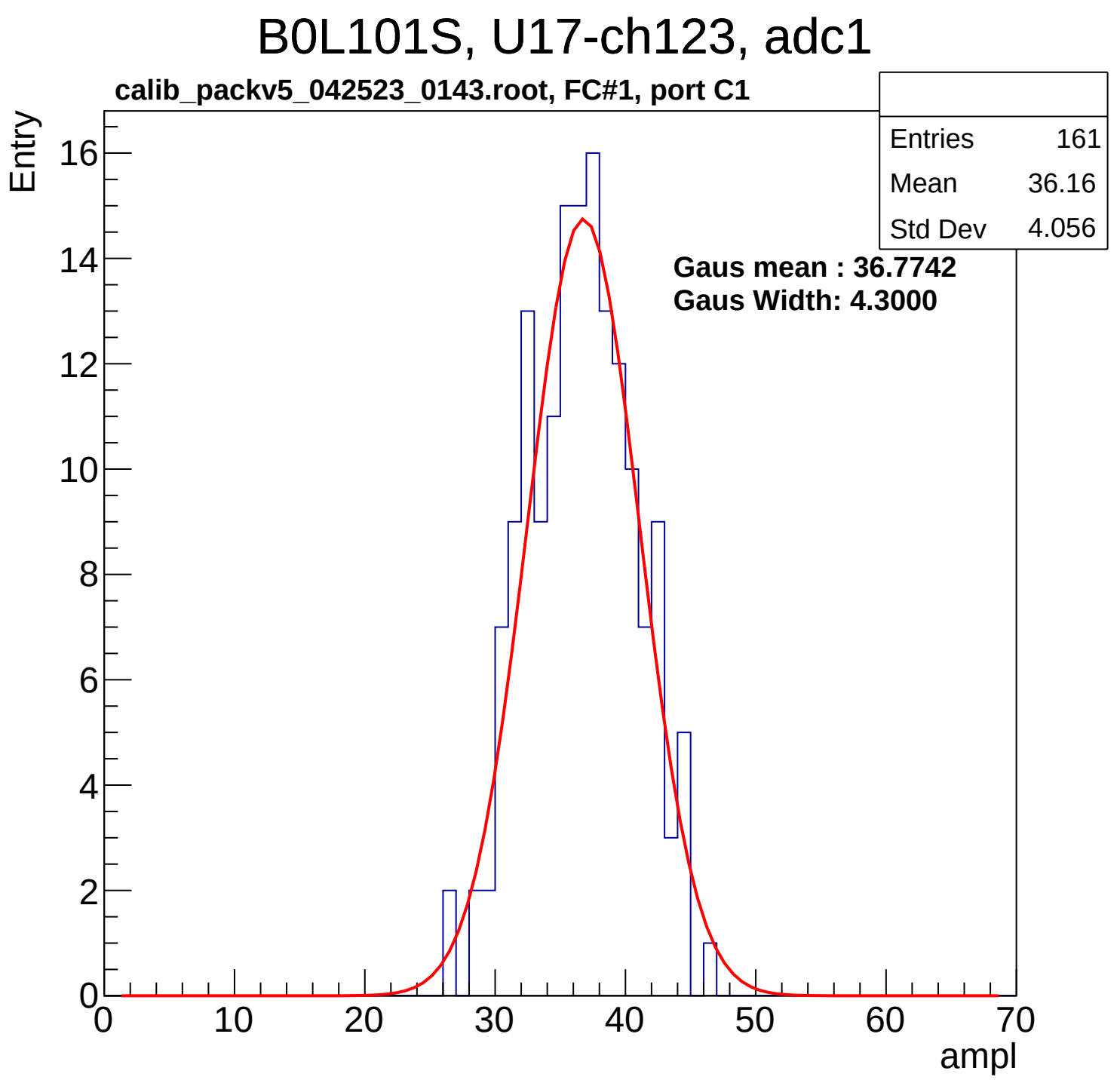

B0L101S, U17-ch122, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 151
Mean 55.82
Std Dev 5.891
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

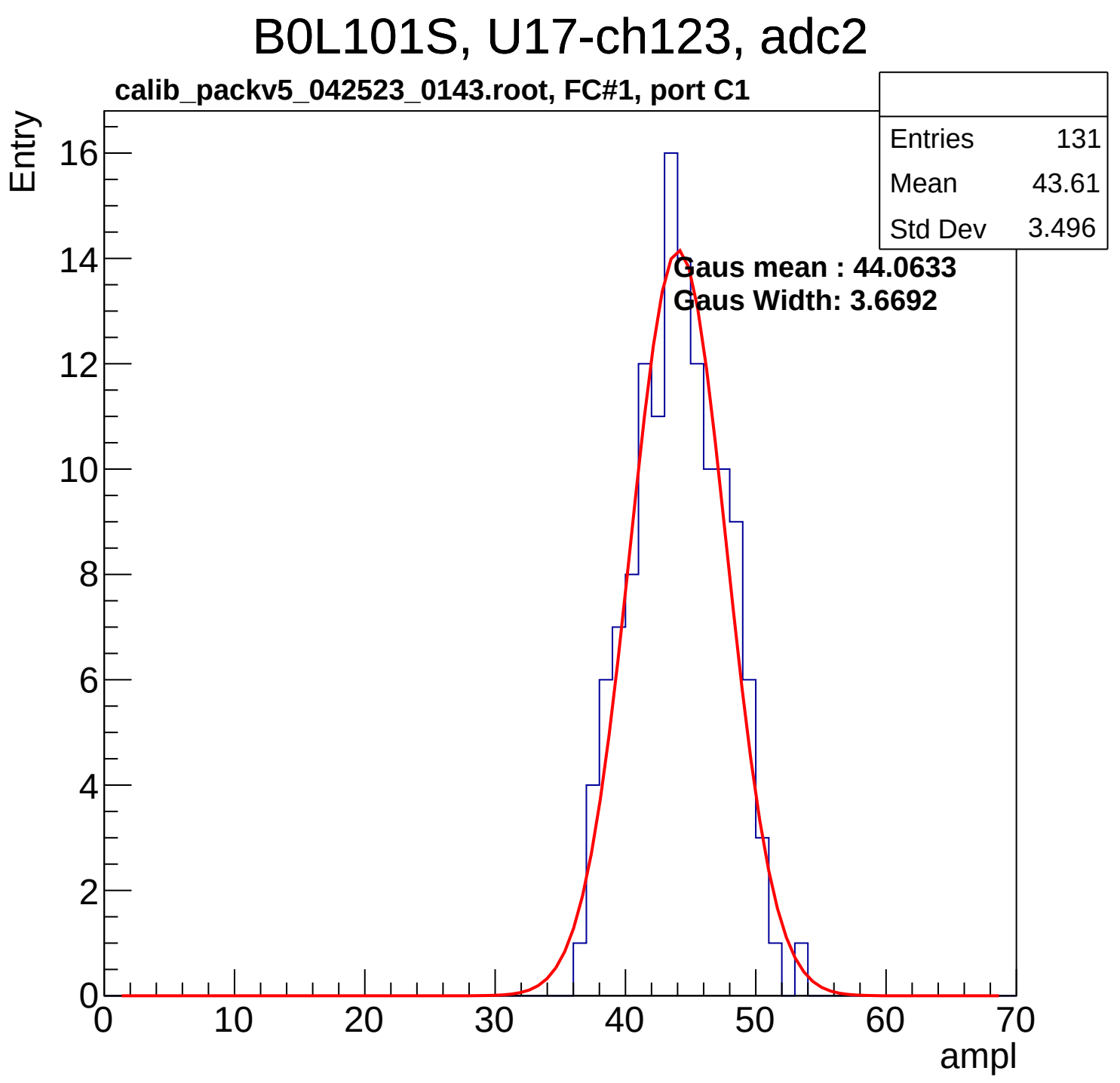

B0L101S, U17-ch122, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 63
Mean 59.79
Std Dev 7.88
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

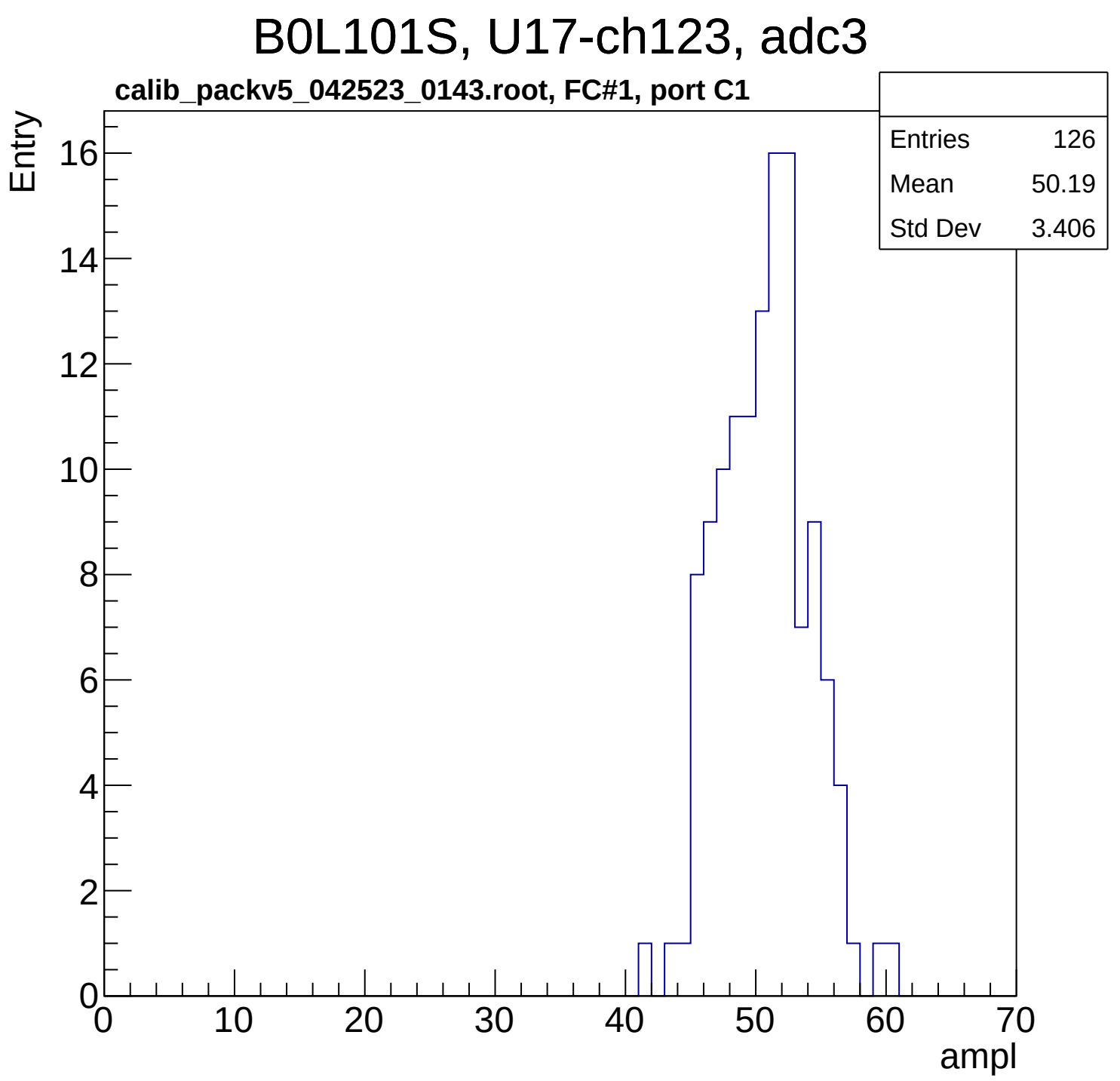

B0L101S, U17-ch122, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

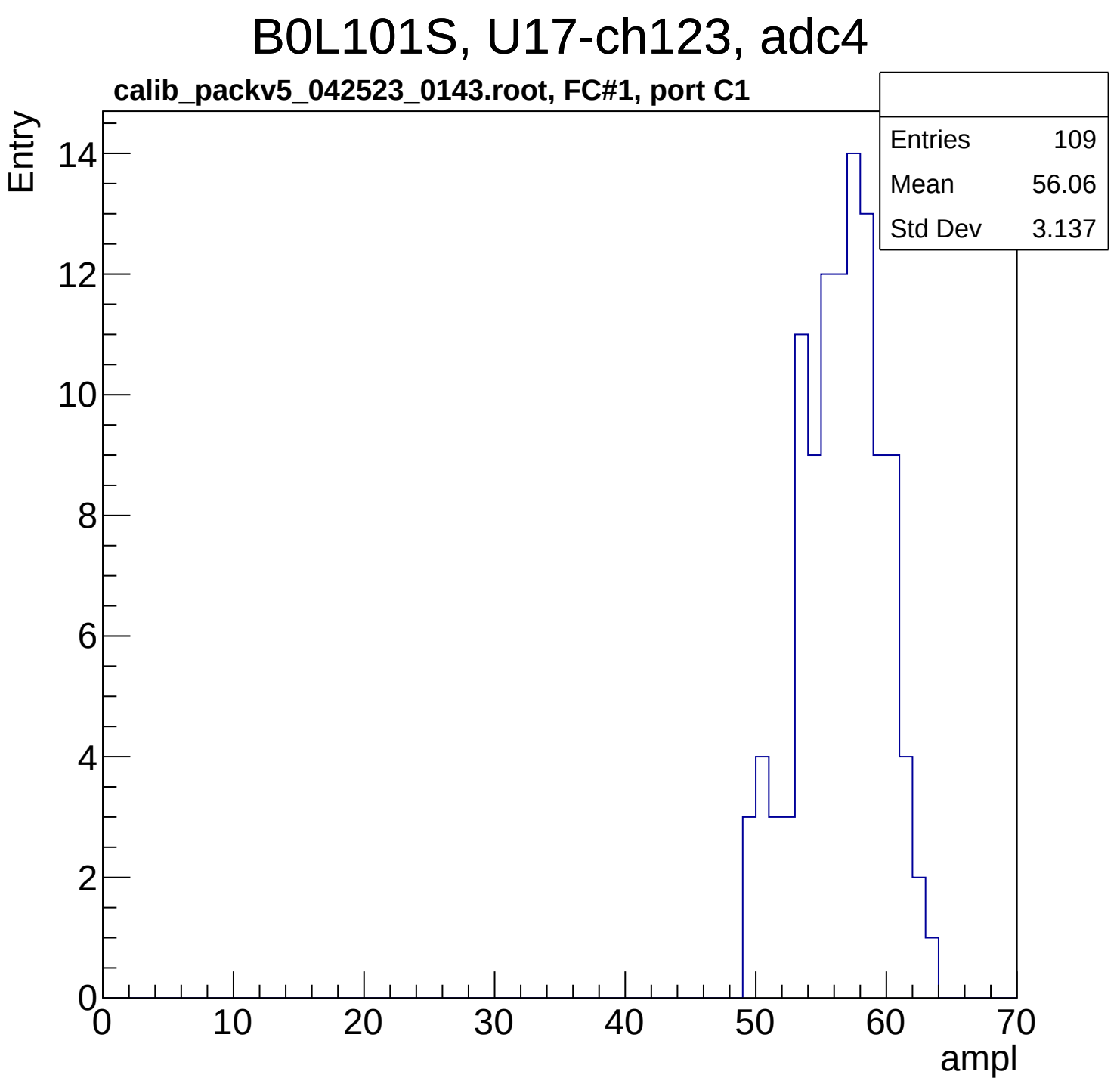

B0L101S, U17-ch122, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

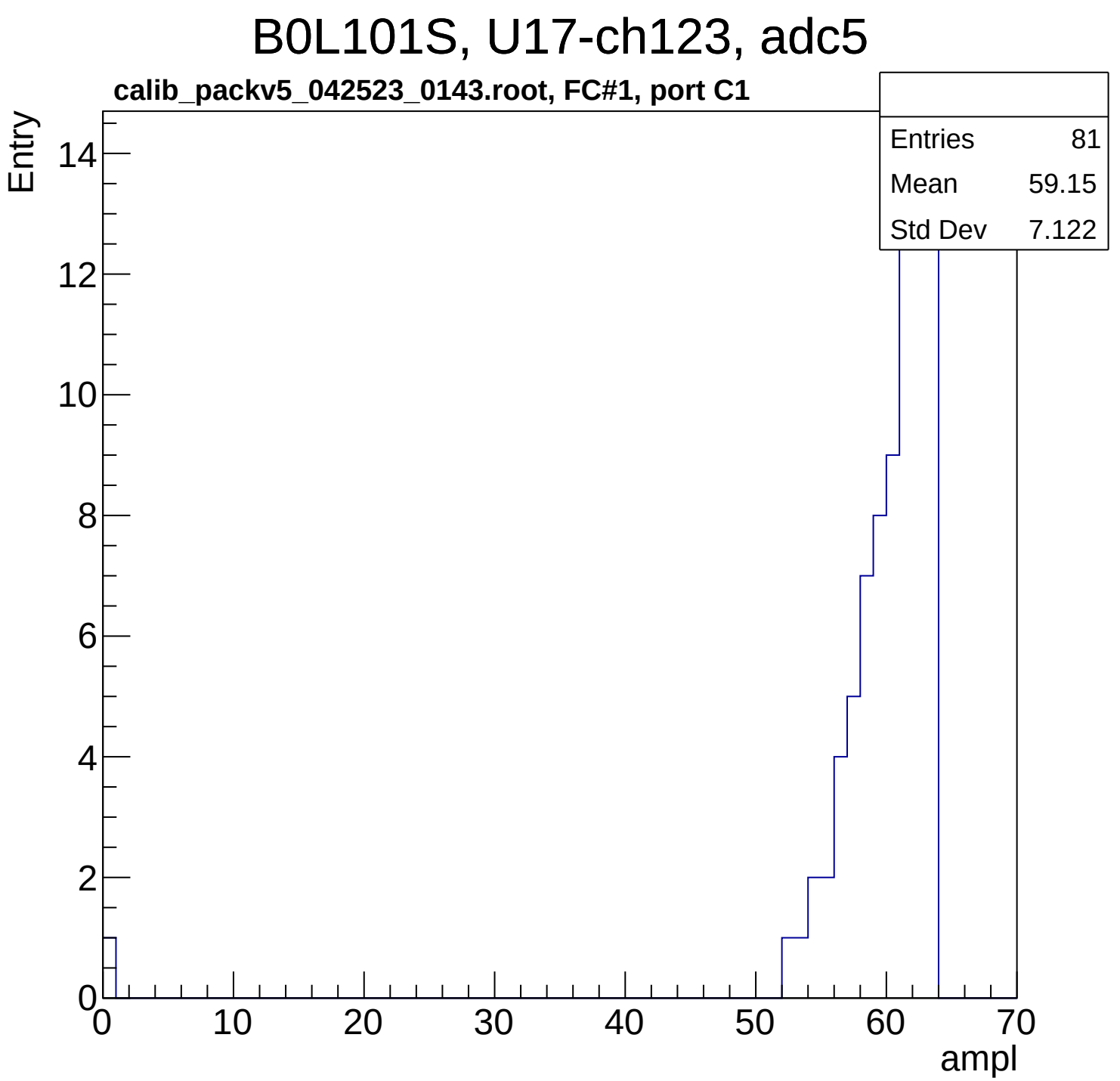

B0L101S, U17-ch123, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 122
Mean 29.44
Std Dev 5.302
Gaus mean : 30.6994
Gaus Width: 3.6800
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

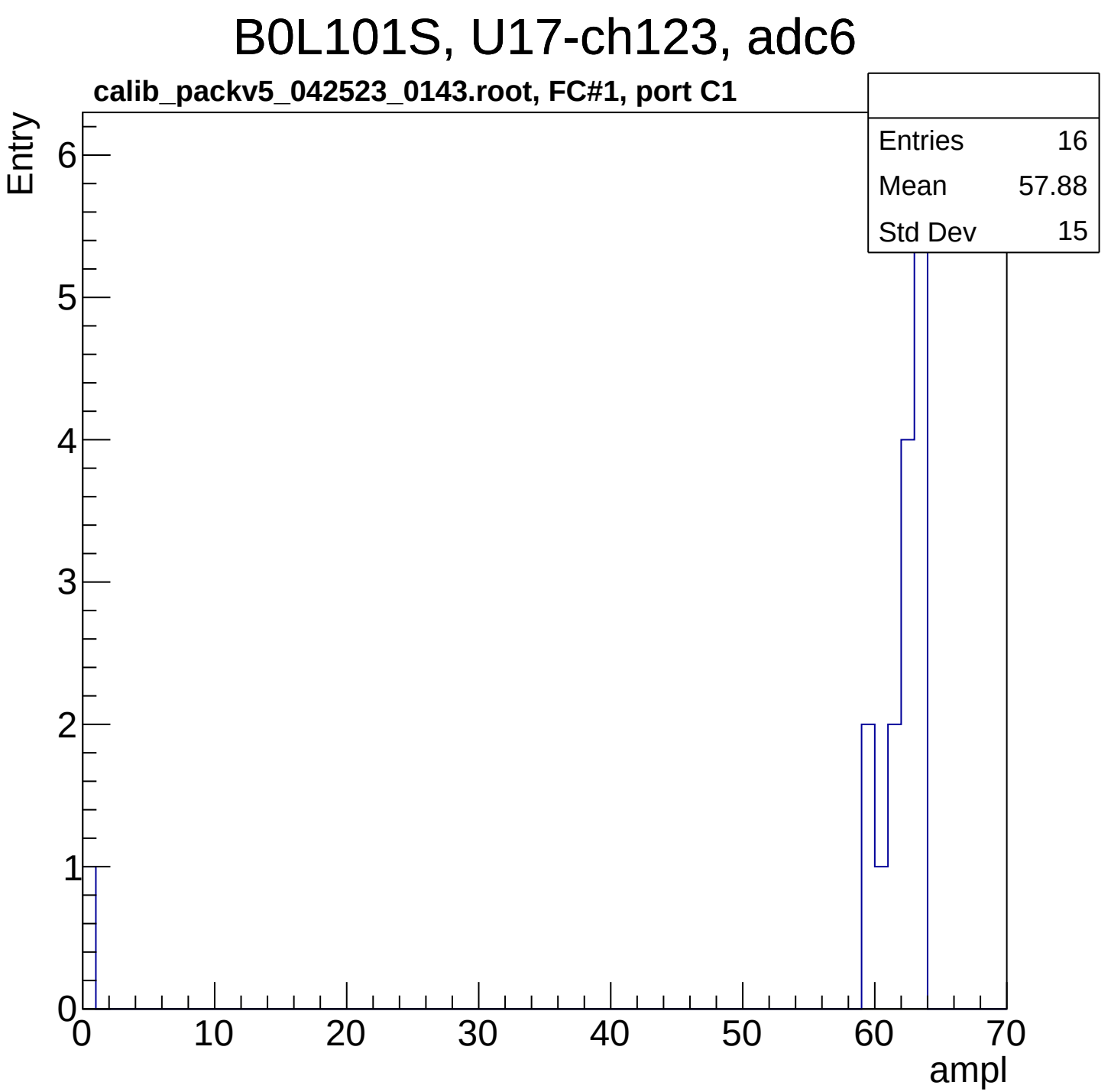

B0L101S, U17-ch123, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 161
Mean 36.16
Std Dev 4.056
Gaus mean : 36.7742
Gaus Width: 4.3000
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

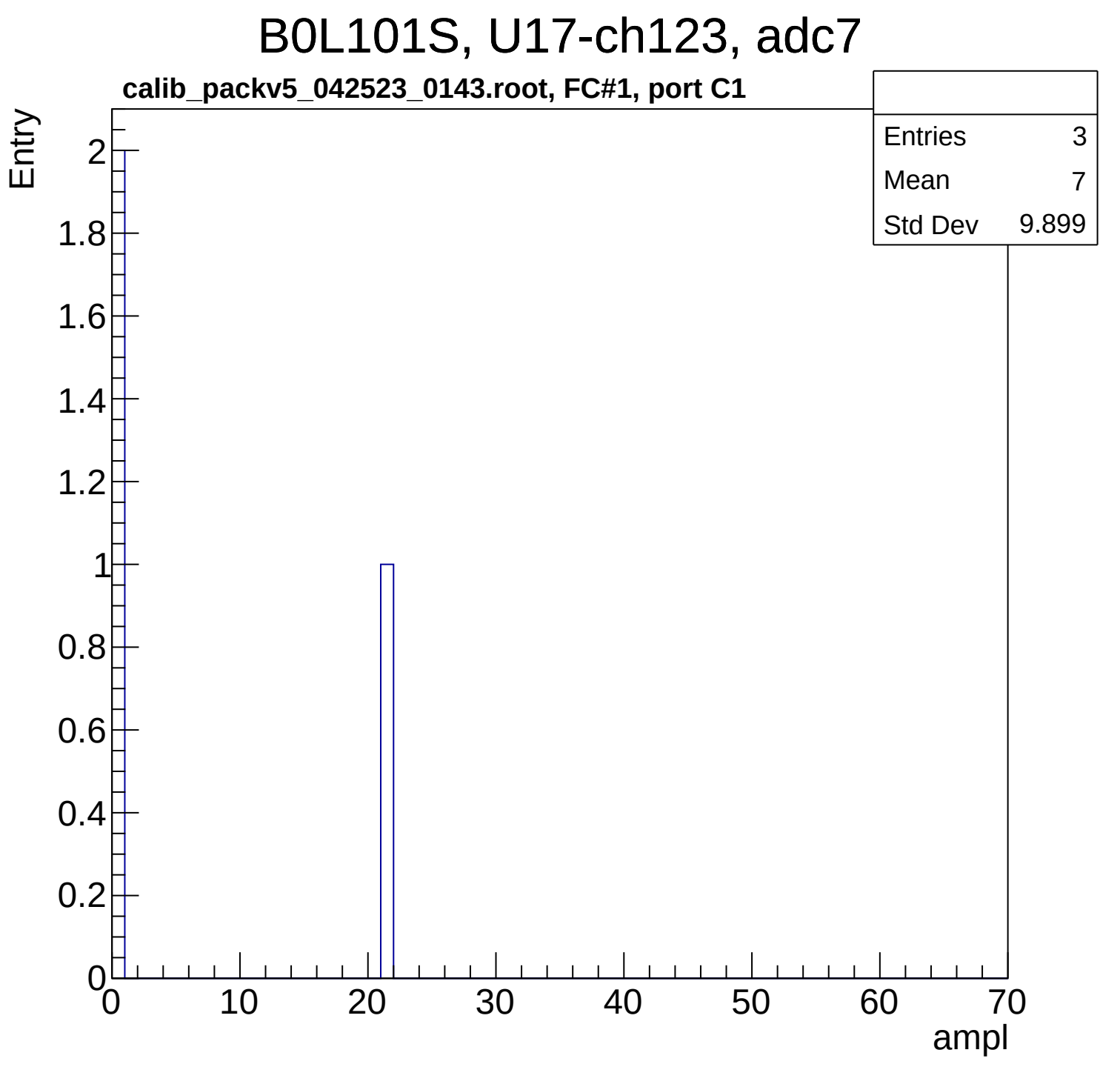

B0L101S, U17-ch123, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 43.61
Std Dev 3.496
Gaus mean : 44.0633
Gaus Width: 3.6692
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

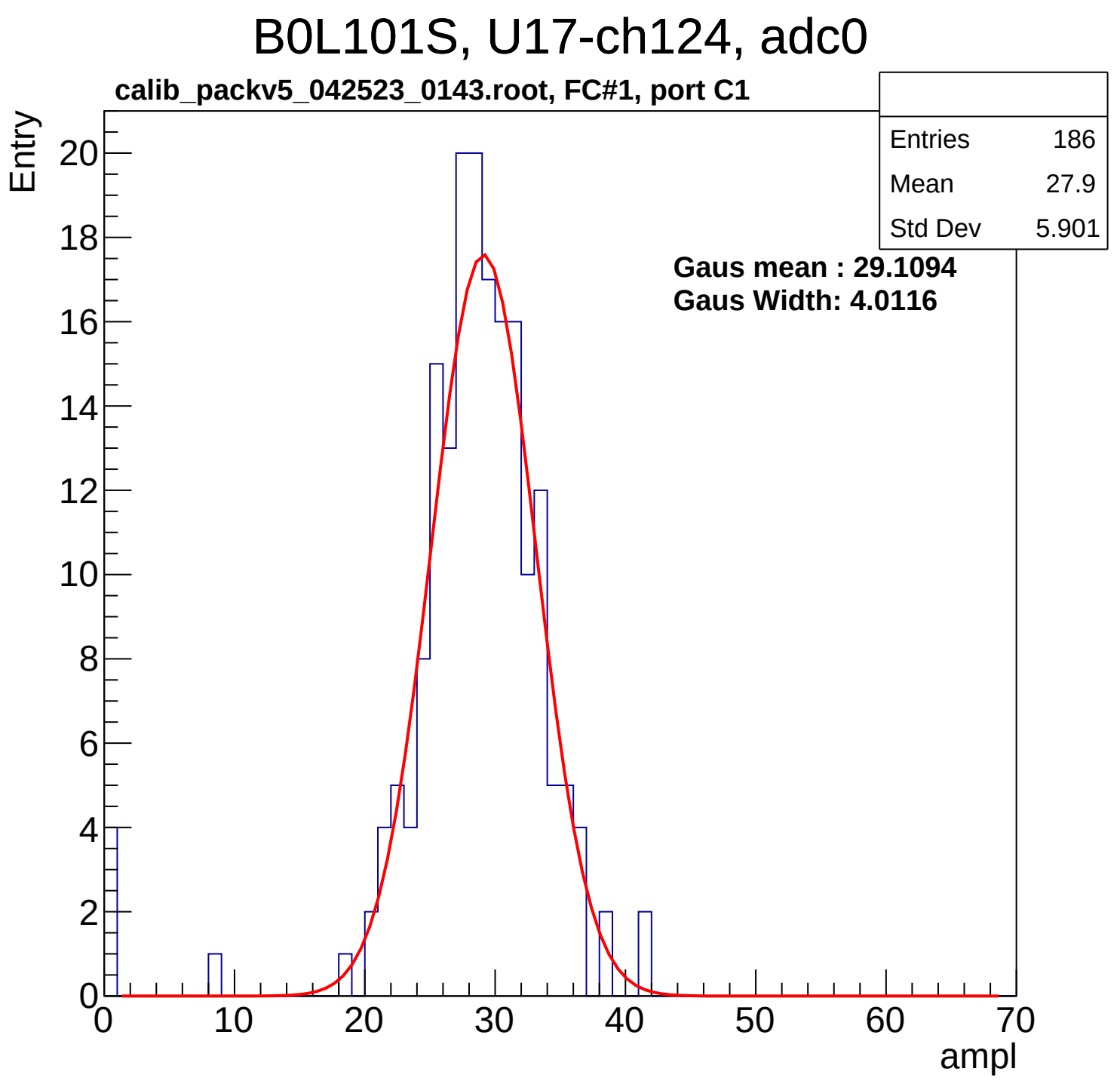

B0L101S, U17-ch123, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 50.19
Std Dev 3.406
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

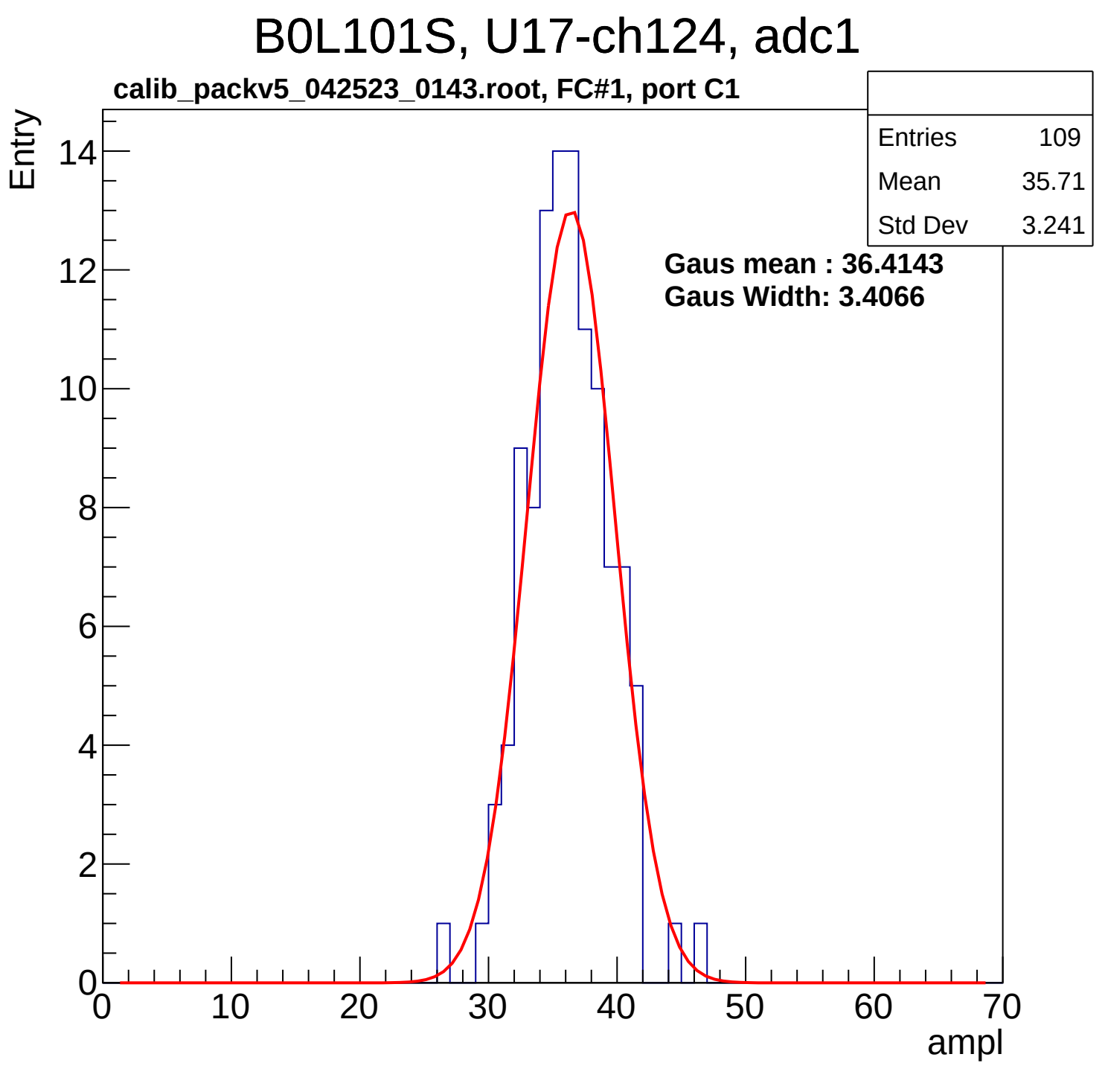

B0L101S, U17-ch123, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 56.06
Std Dev 3.137
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

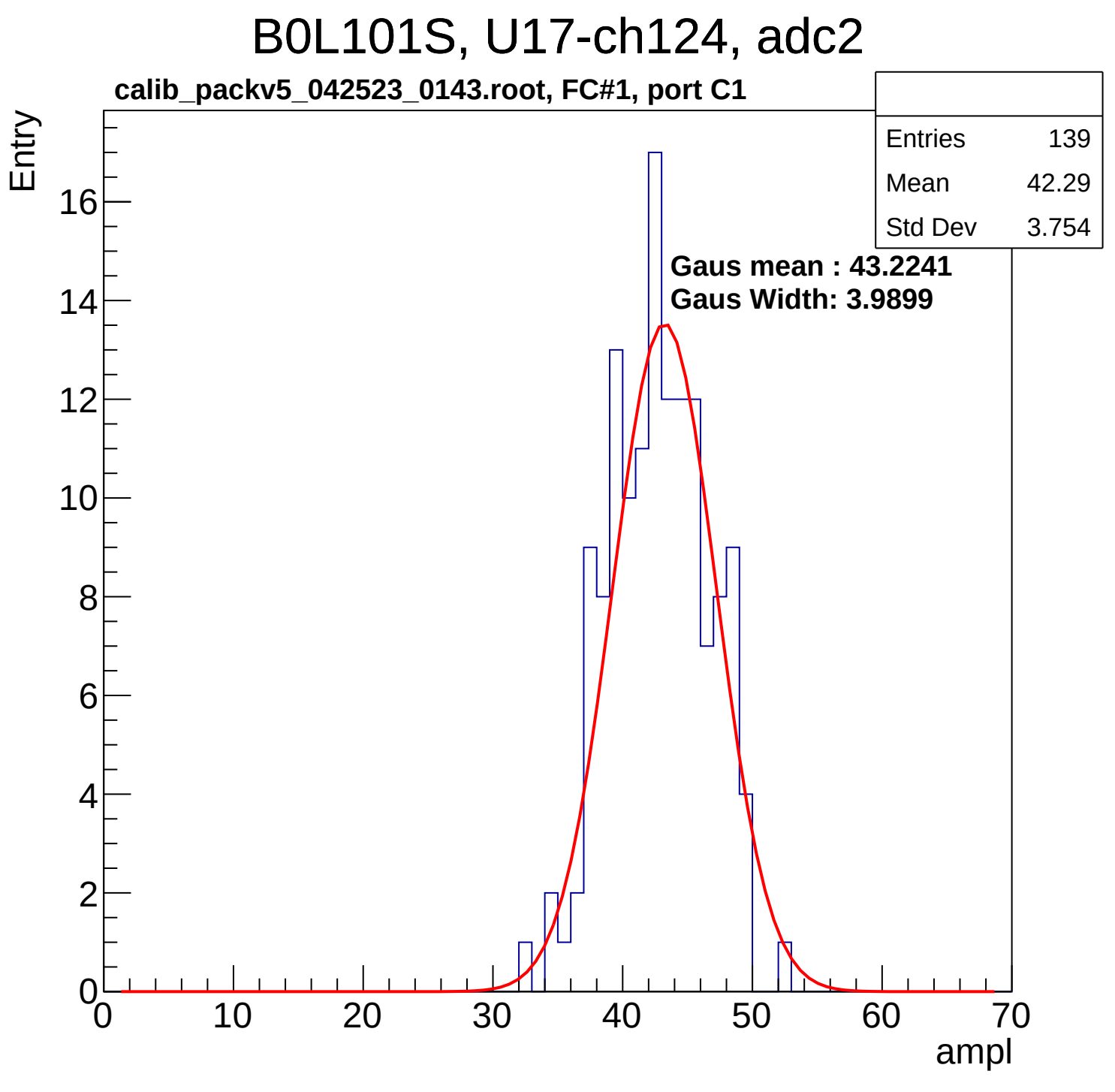

B0L101S, U17-ch123, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 81
Mean 59.15
Std Dev 7.122
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

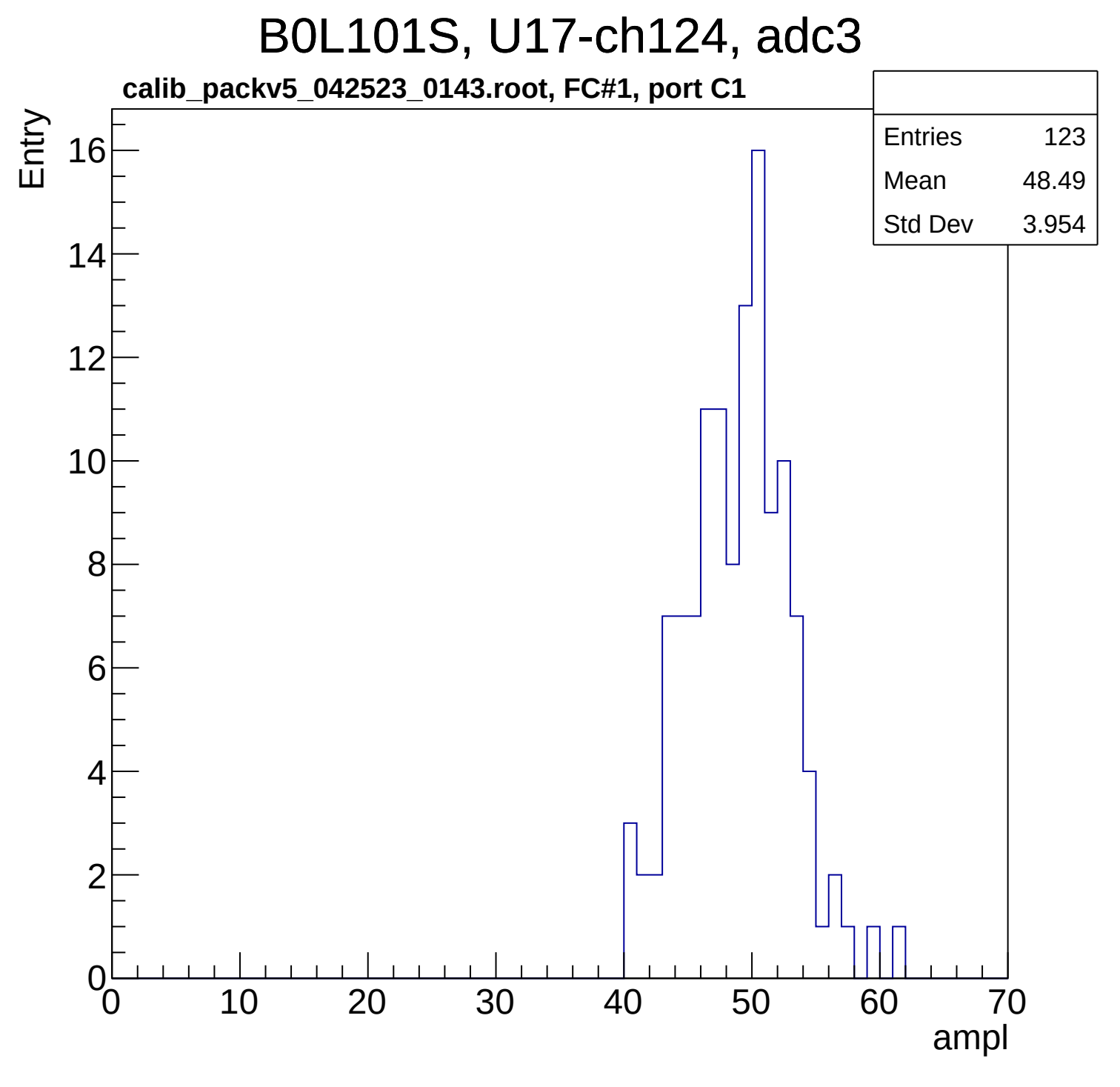

B0L101S, U17-ch123, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 57.88
Std Dev 15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

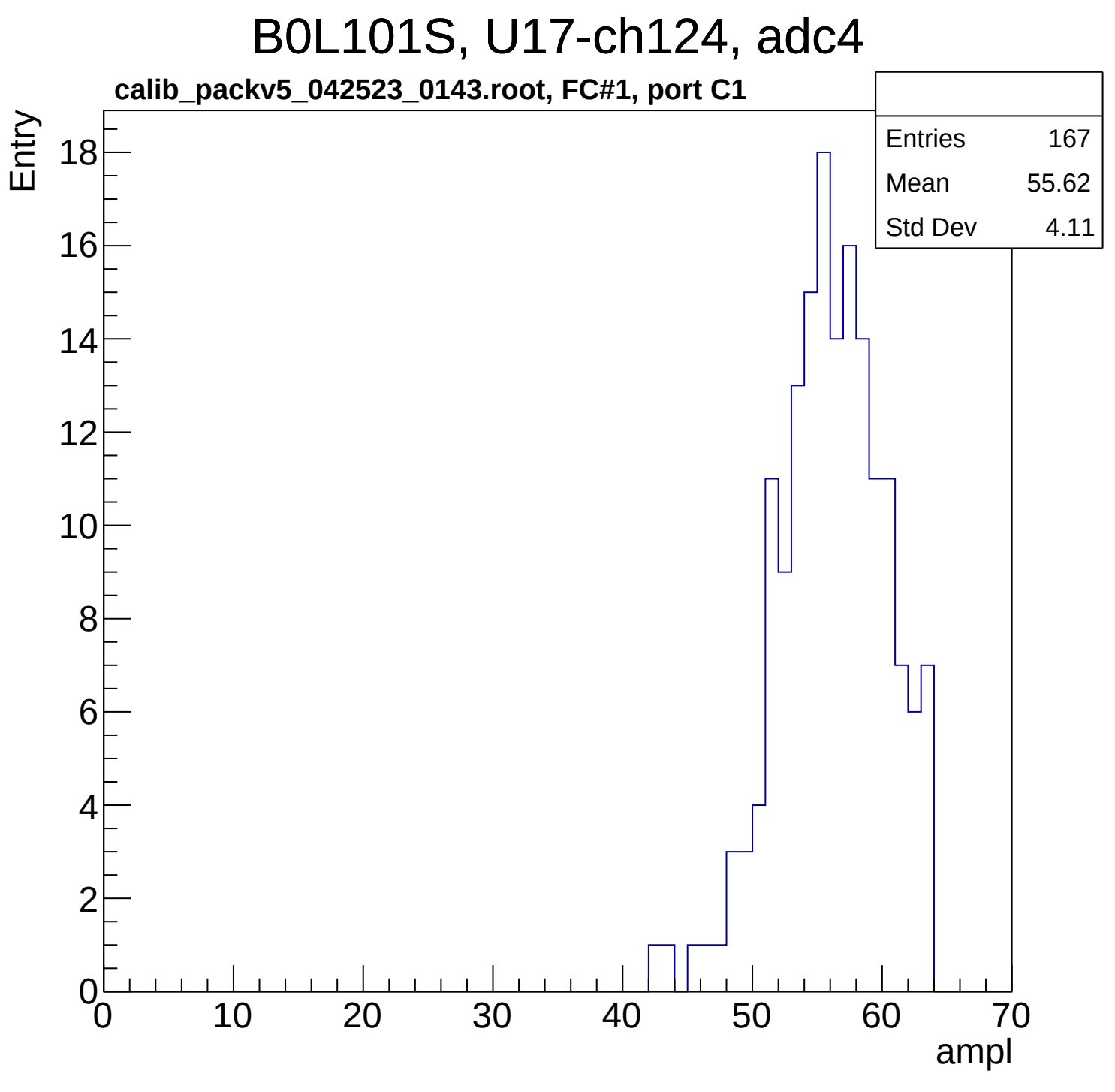

B0L101S, U17-ch123, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

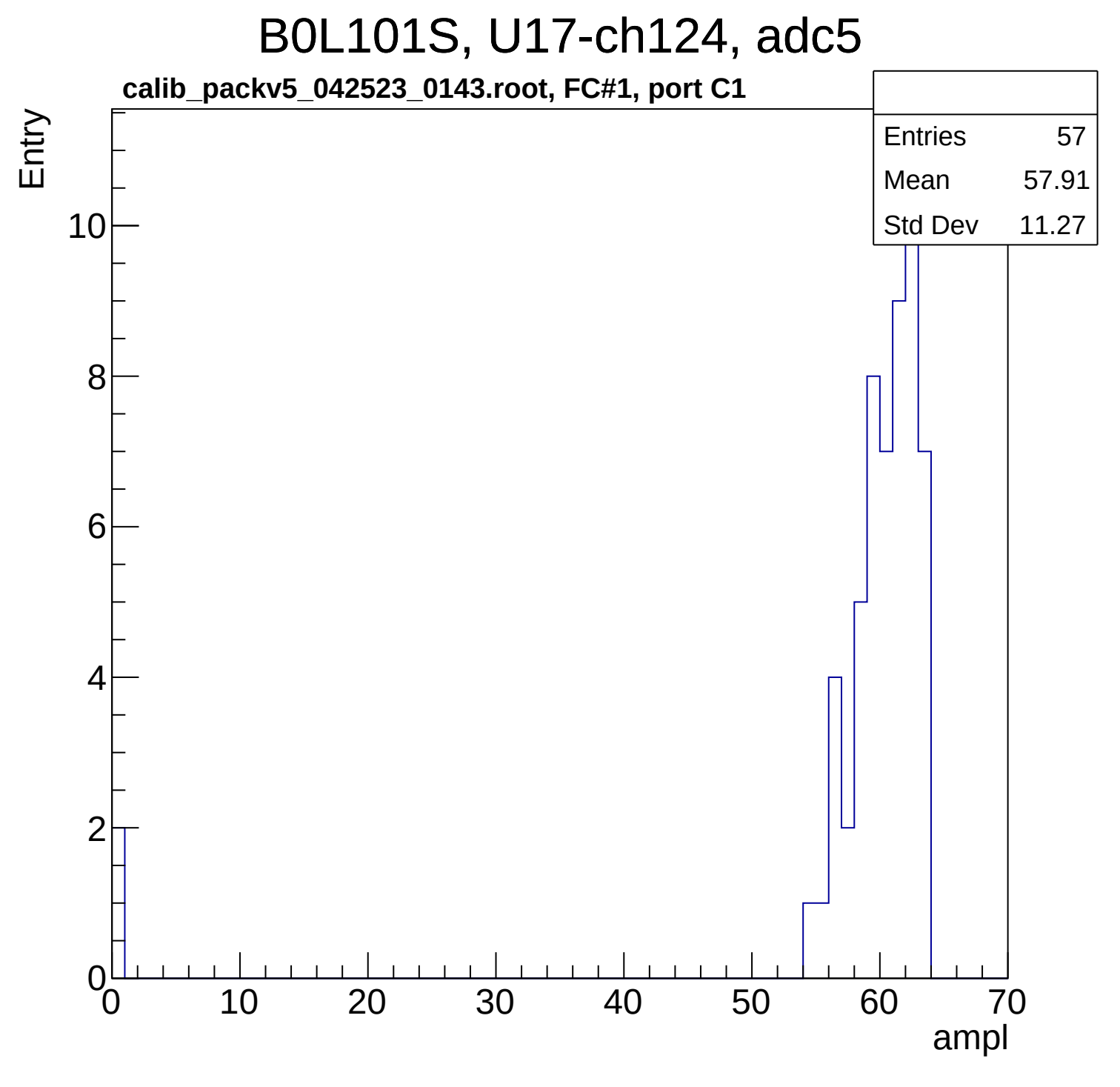

B0L101S, U17-ch124, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 186
Mean 27.9
Std Dev 5.901
Gaus mean : 29.1094
Gaus Width: 4.0116
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70

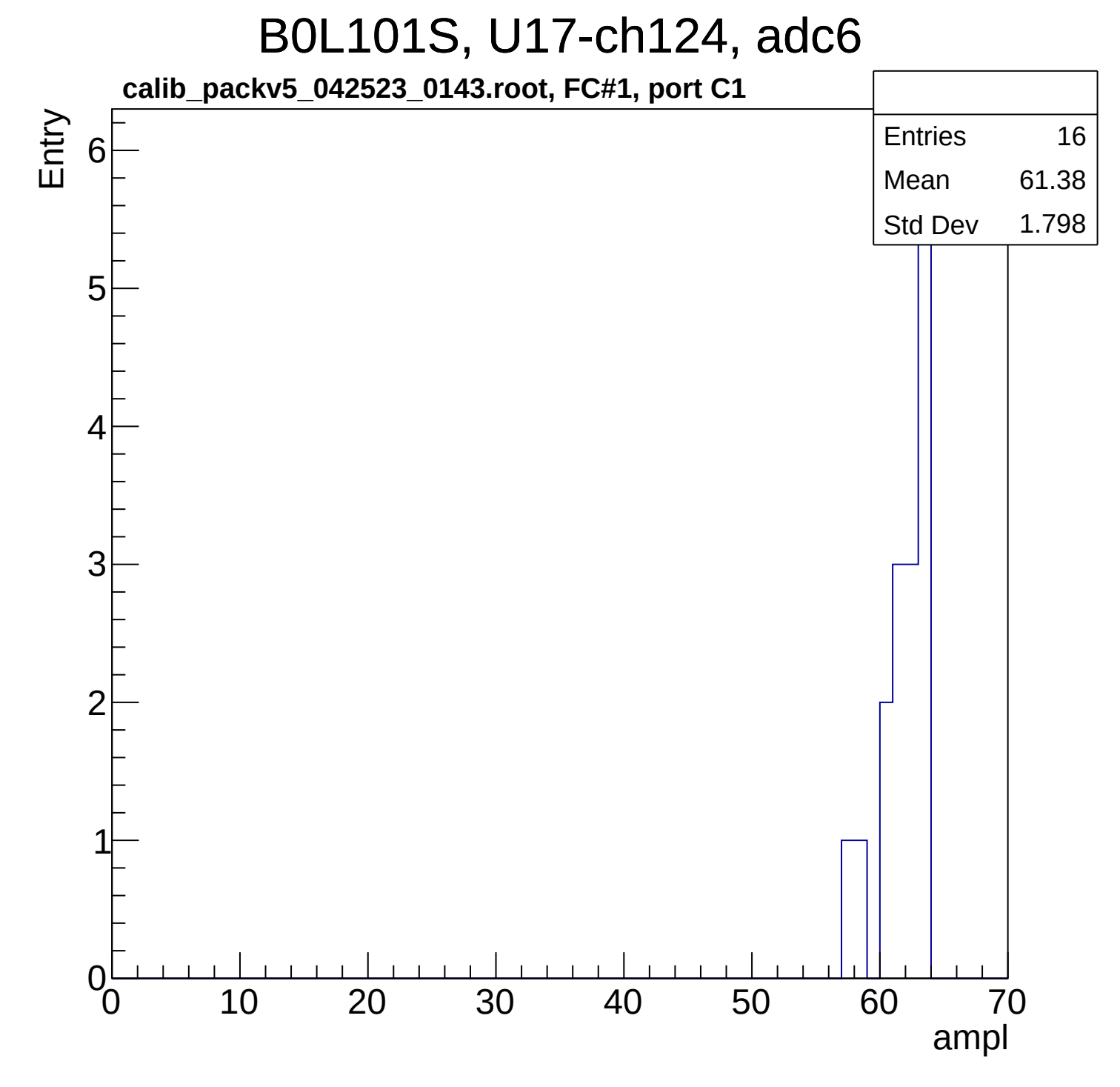

B0L101S, U17-ch124, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 109
Mean 35.71
Std Dev 3.241
Gaus mean : 36.4143
Gaus Width: 3.4066
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

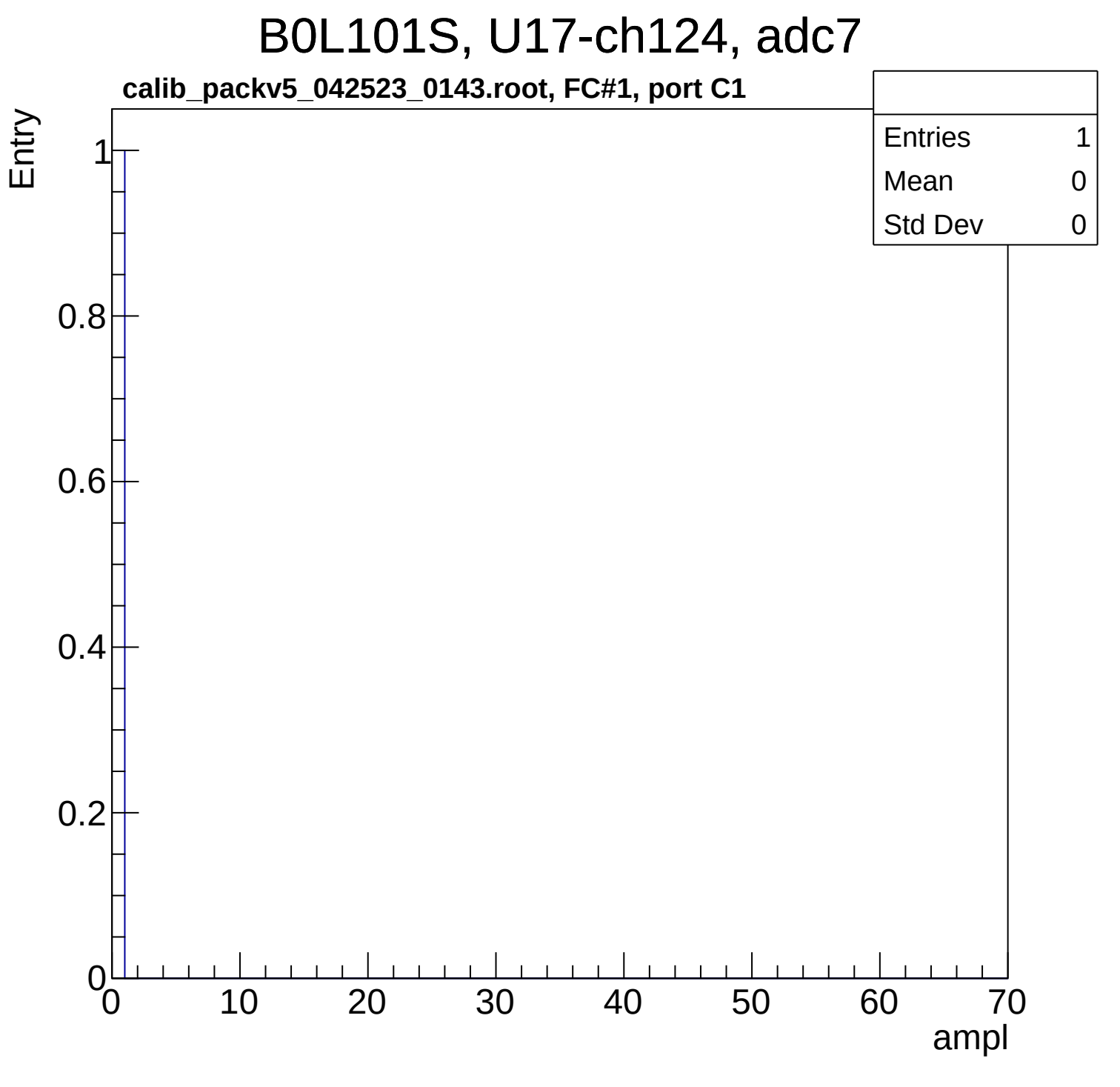

B0L101S, U17-ch124, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 42.29
Std Dev 3.754
Gaus mean : 43.2241
Gaus Width: 3.9899
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

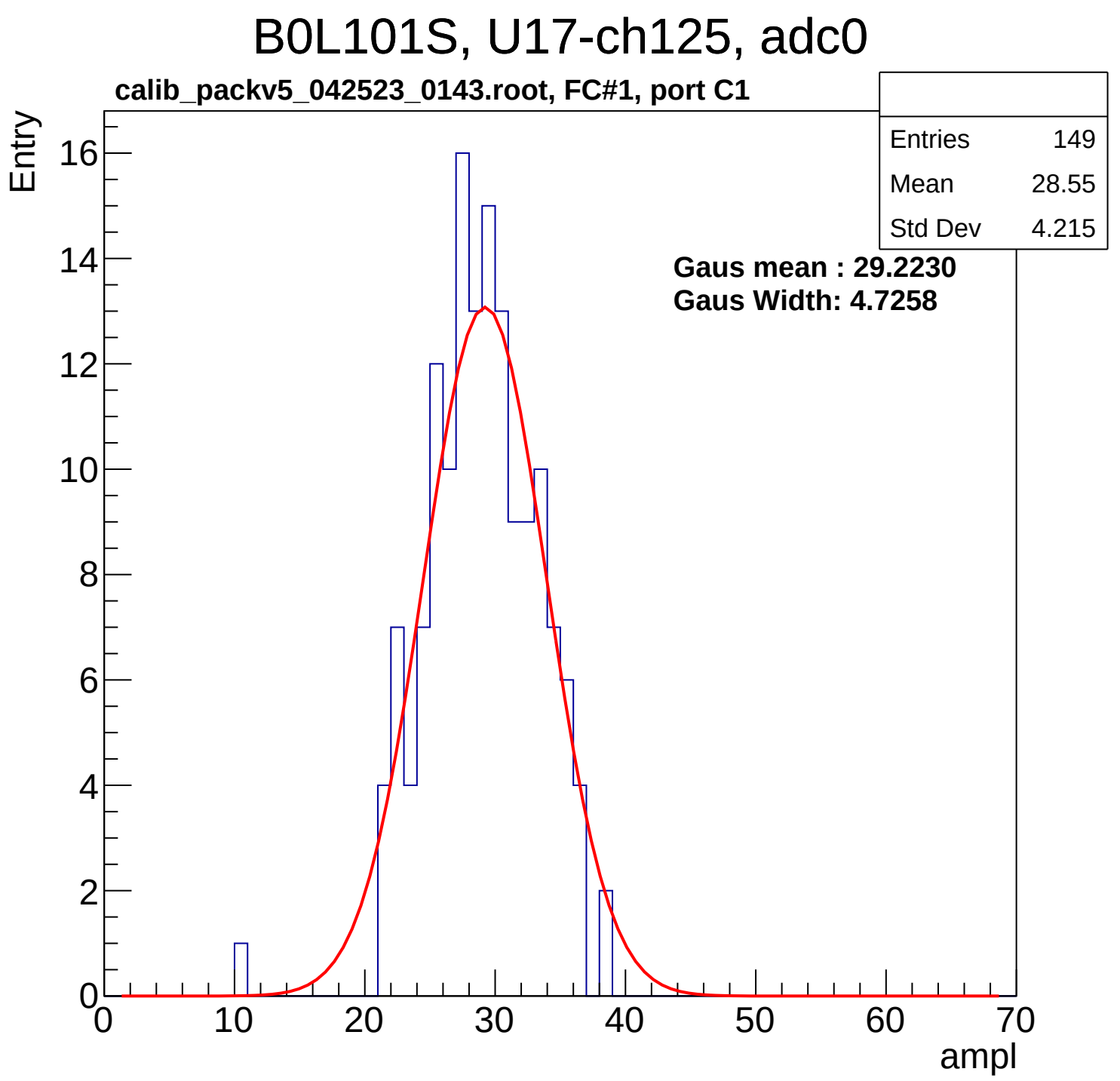

B0L101S, U17-ch124, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 48.49
Std Dev 3.954
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

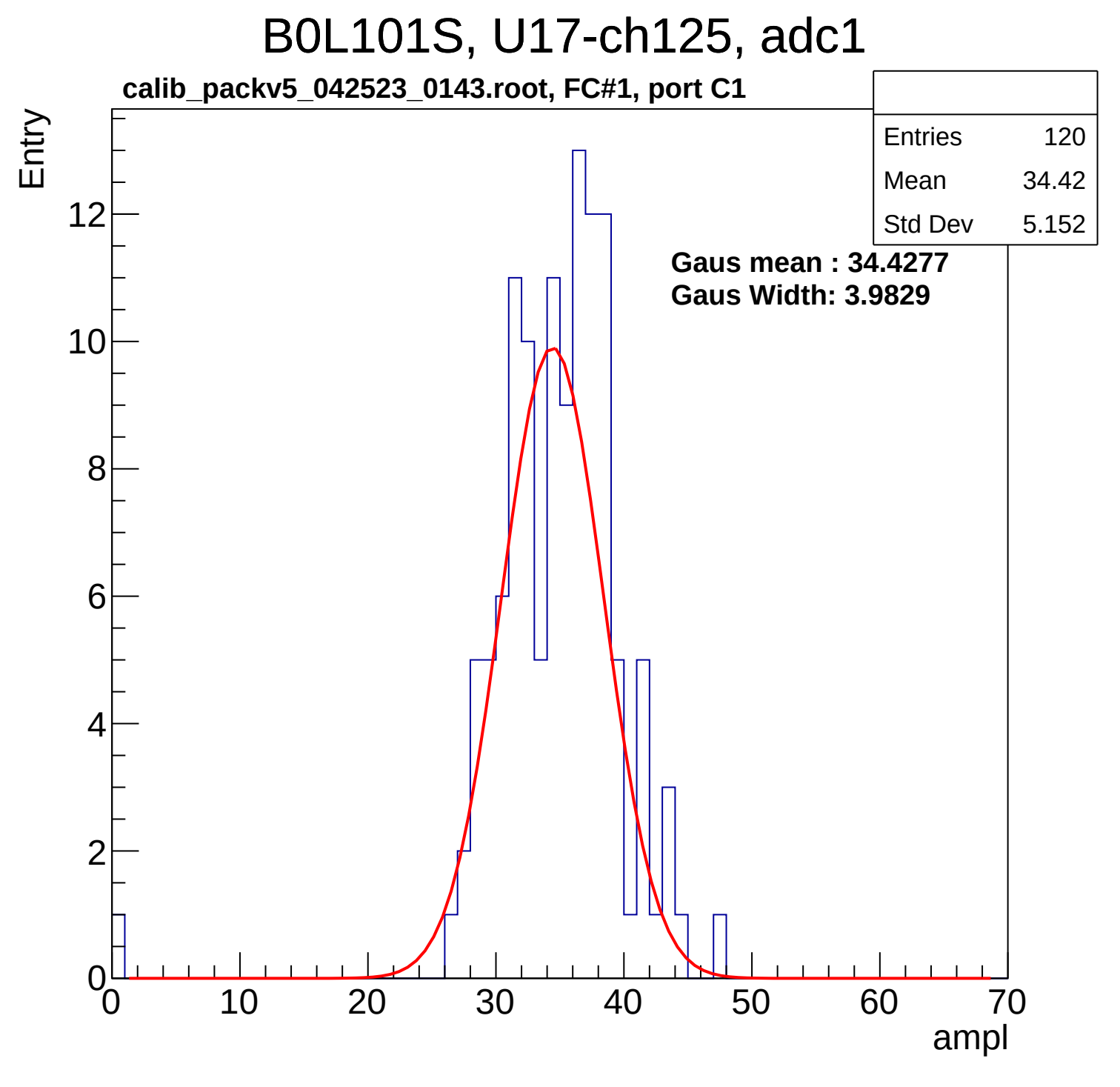

B0L101S, U17-ch124, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 167
Mean 55.62
Std Dev 4.11
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

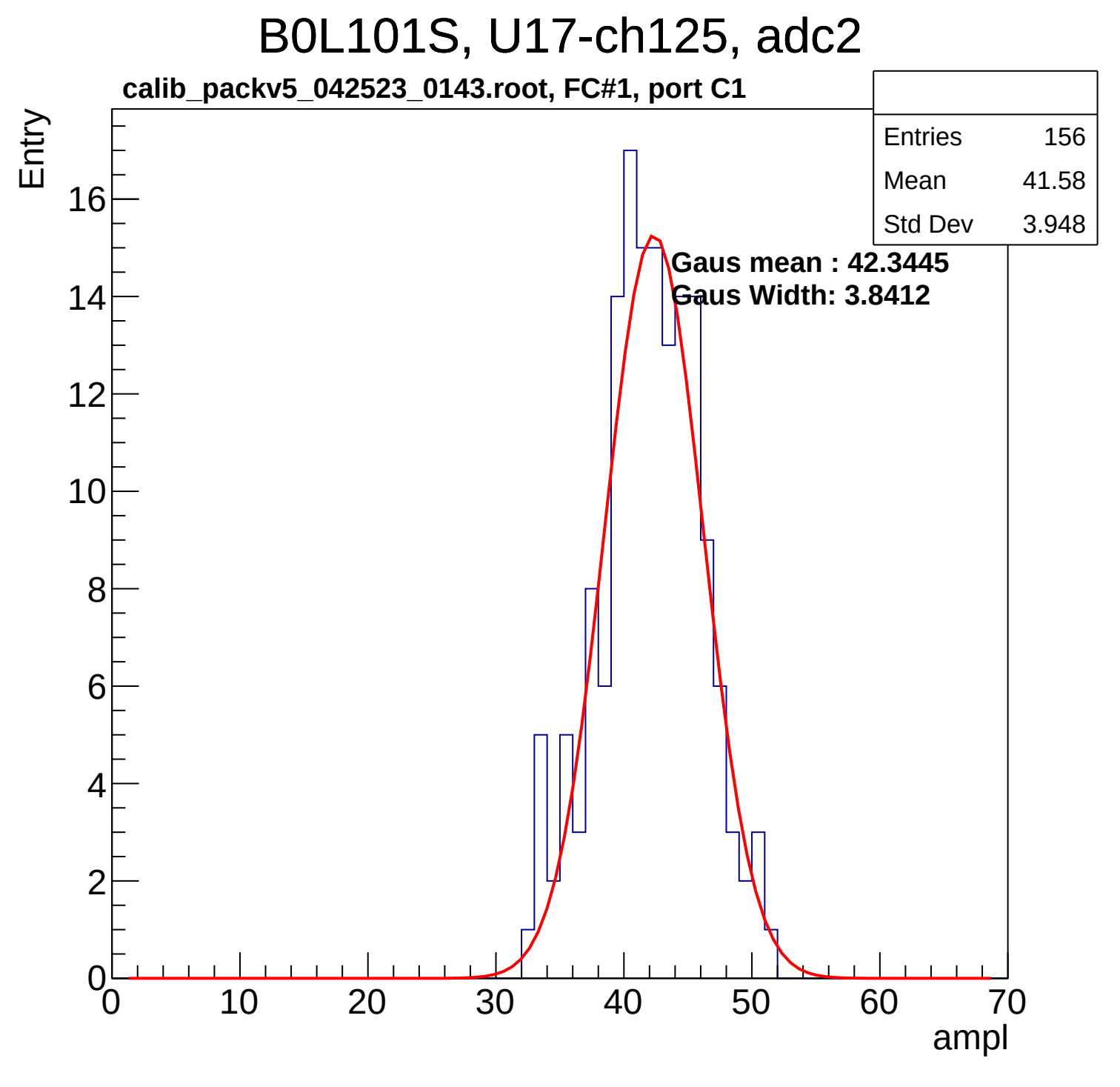

B0L101S, U17-ch124, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 57
Mean 57.91
Std Dev 11.27
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

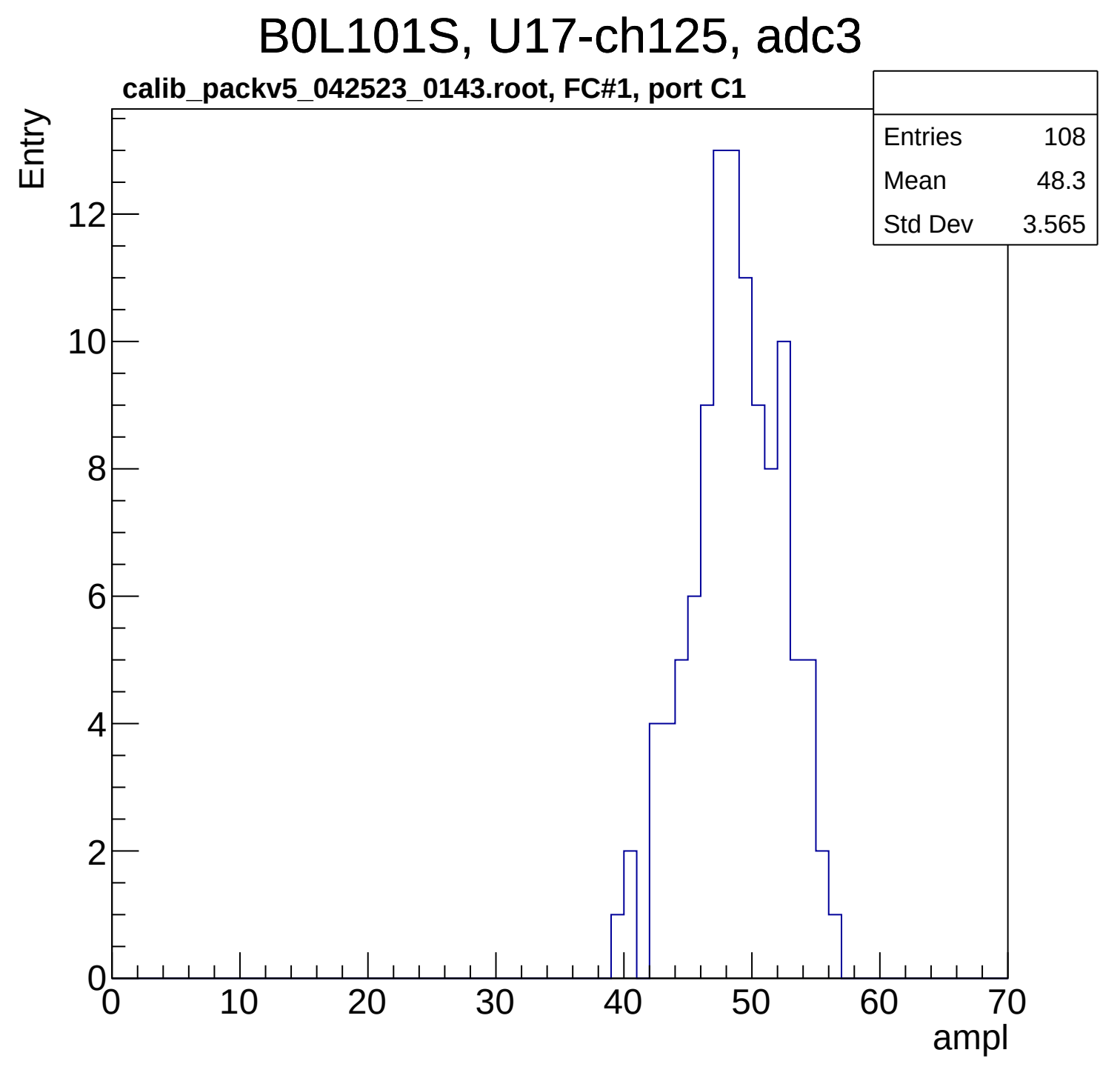

B0L101S, U17-ch124, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 16
Mean 61.38
Std Dev 1.798
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

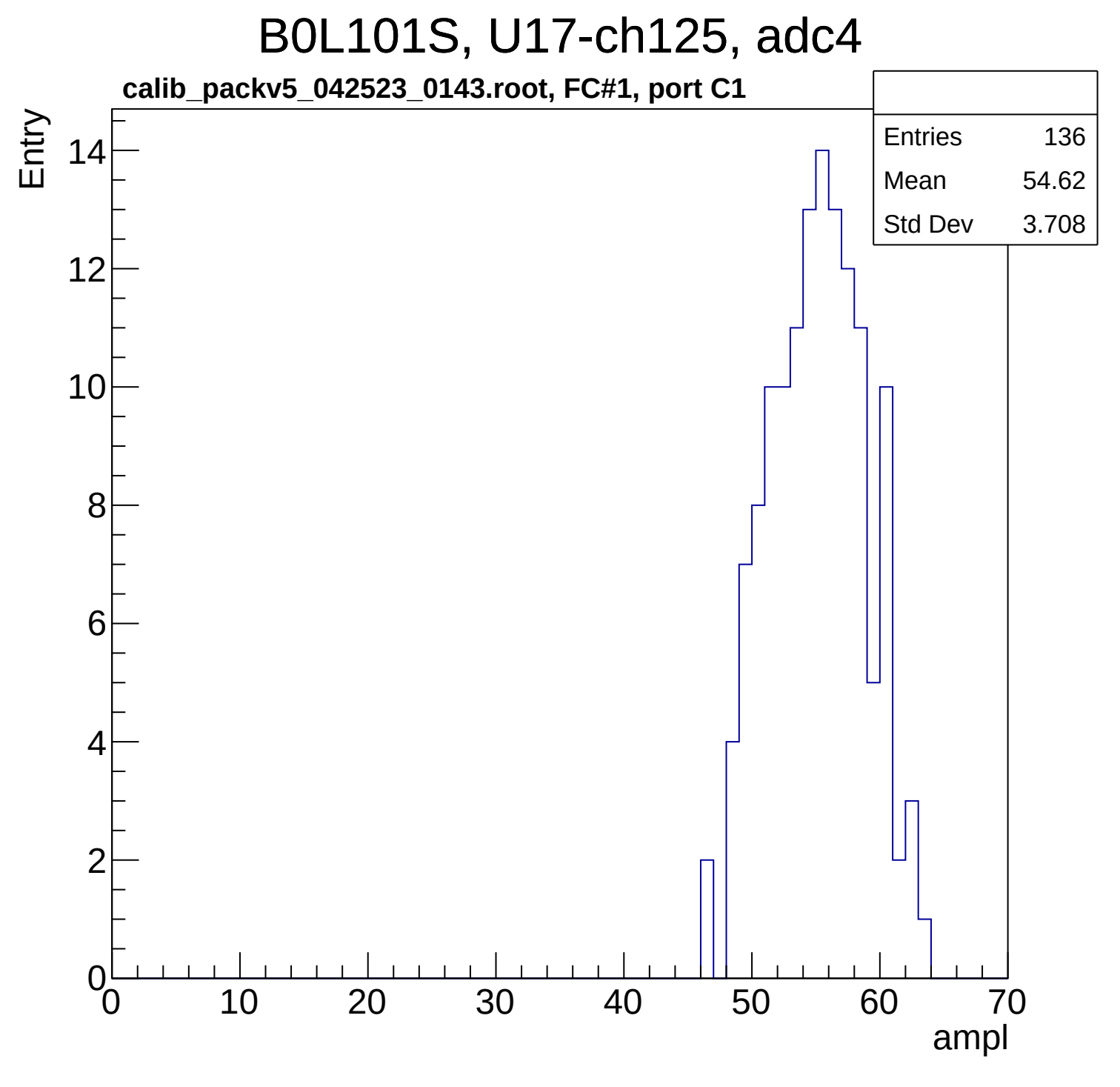

B0L101S, U17-ch124, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

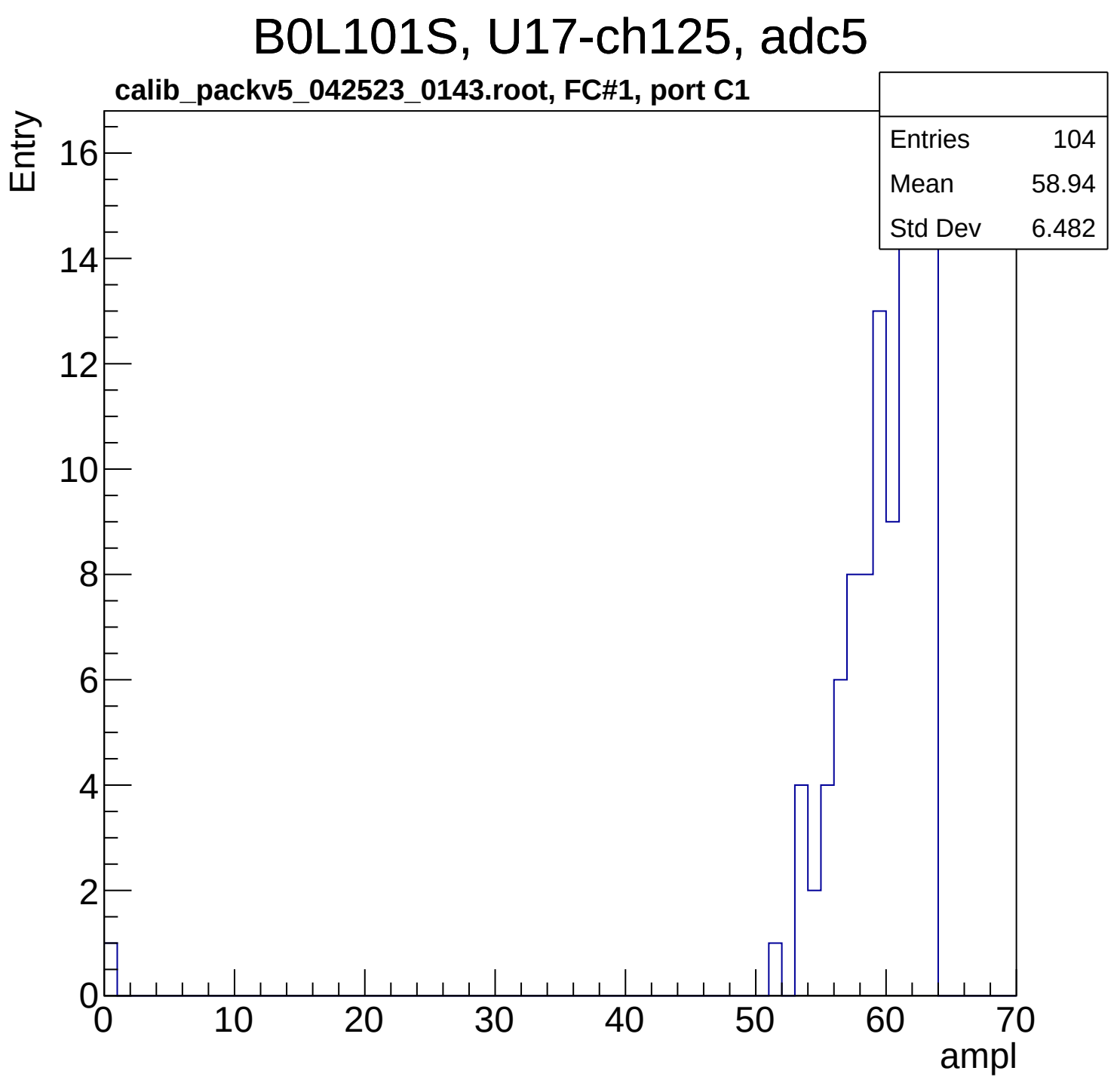

B0L101S, U17-ch125, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 149
Mean 28.55
Std Dev 4.215
Gaus mean : 29.2230
Gaus Width: 4.7258
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

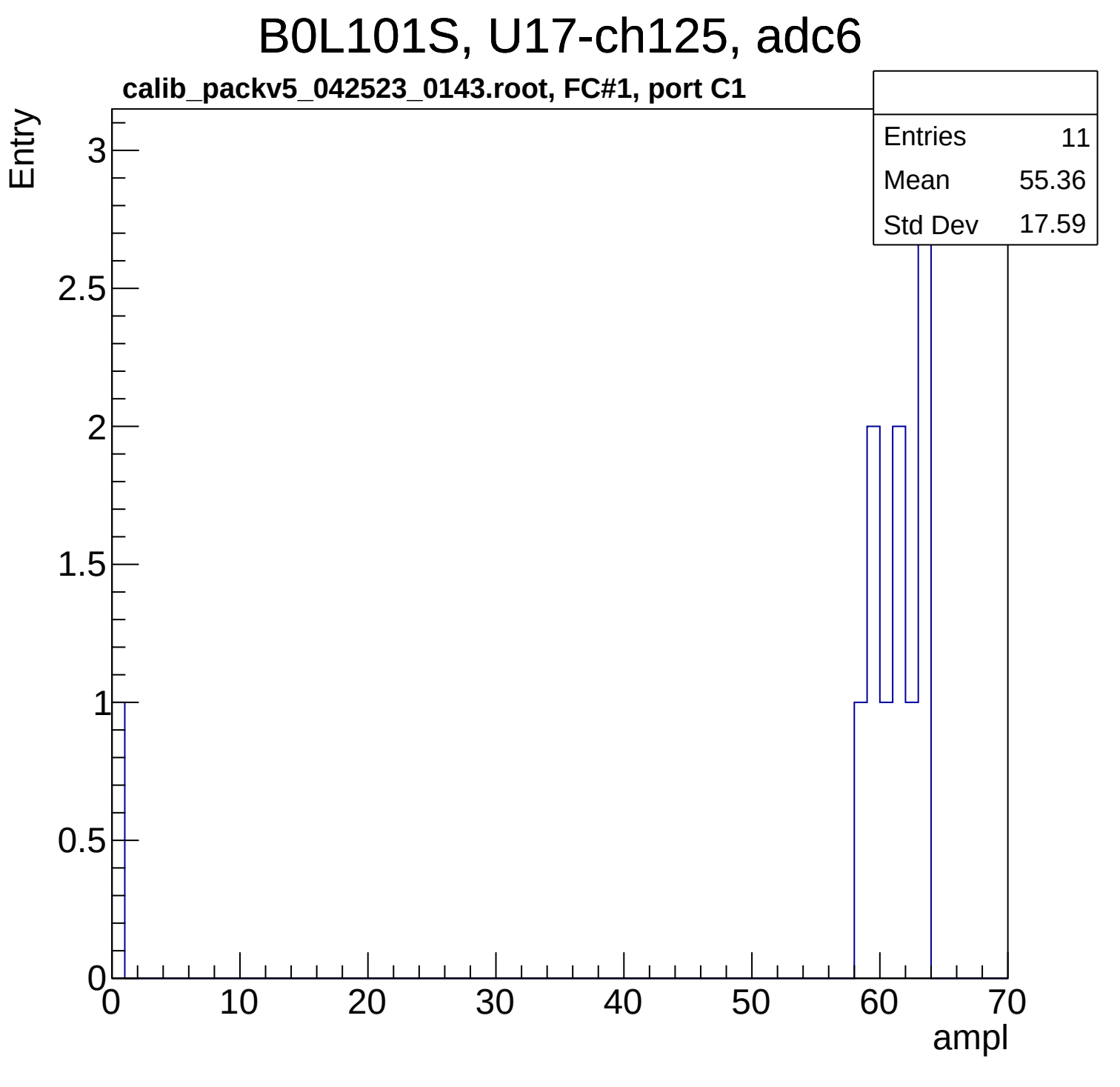

B0L101S, U17-ch125, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 120
Mean 34.42
Std Dev 5.152
Gaus mean : 34.4277
Gaus Width: 3.9829
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

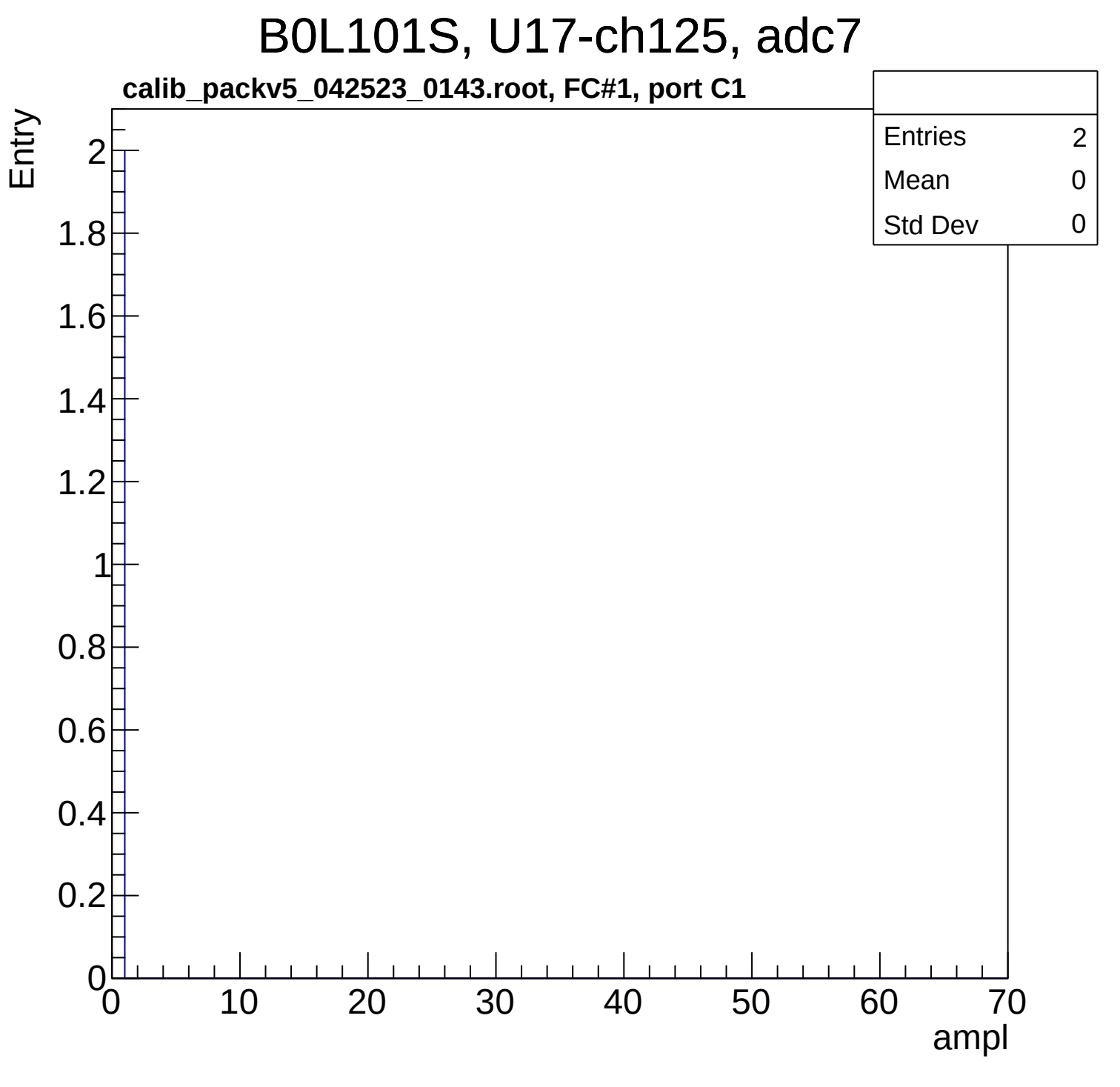

B0L101S, U17-ch125, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 156
Mean 41.58
Std Dev 3.948
Gaus mean : 42.3445
Gaus Width: 3.8412
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

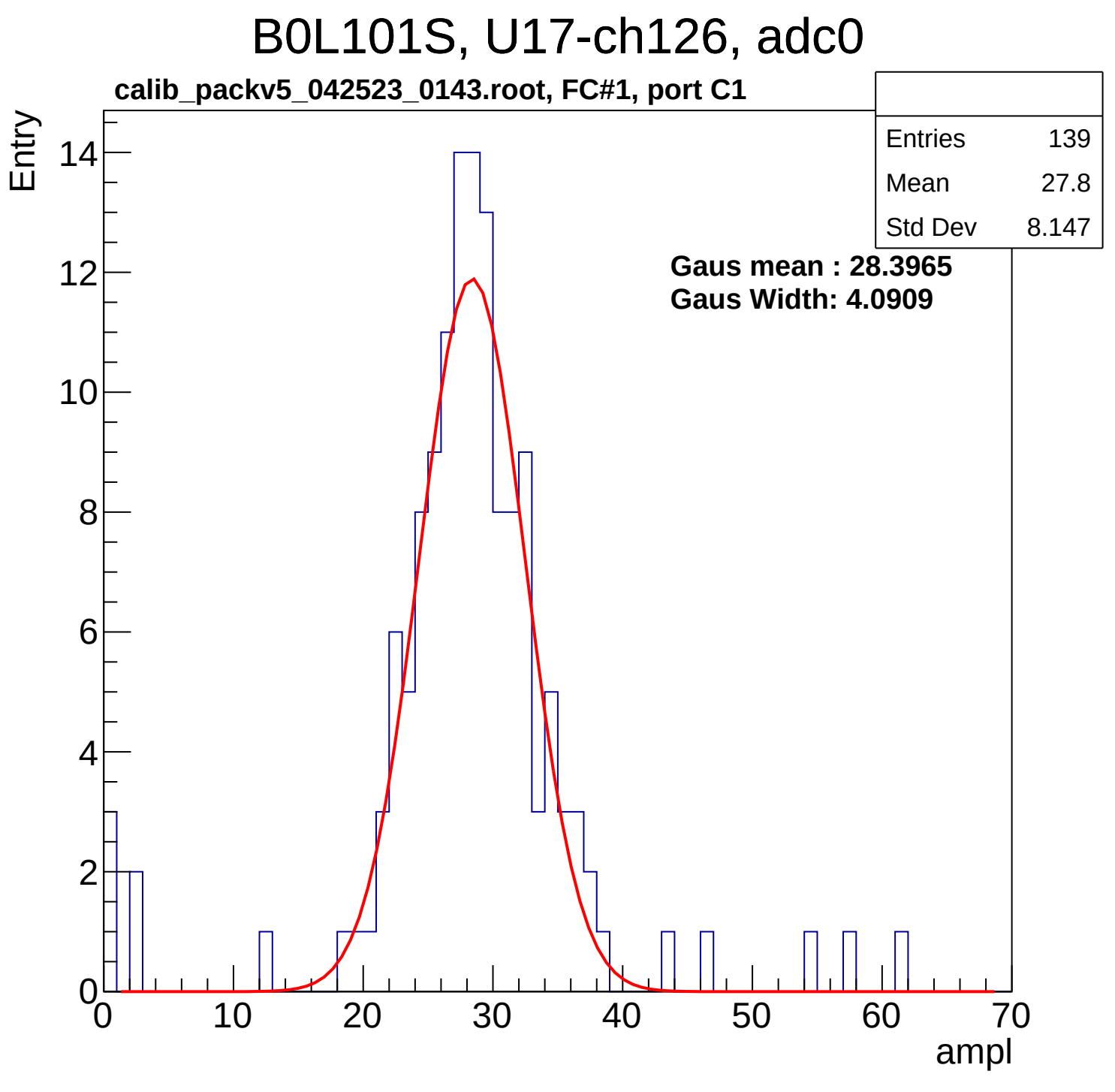

B0L101S, U17-ch125, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 108
Mean 48.3
Std Dev 3.565
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70

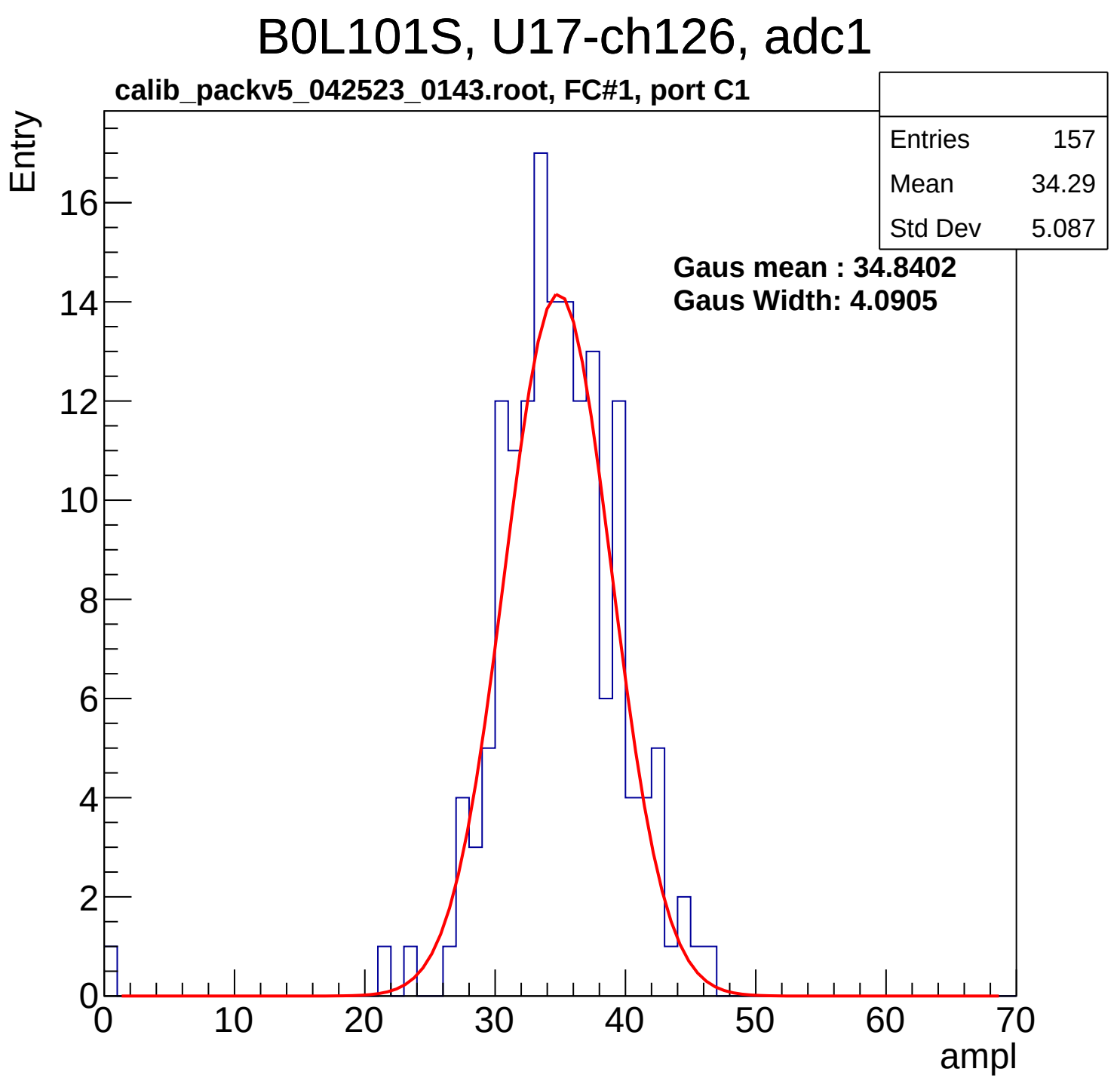

B0L101S, U17-ch125, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 136
Mean 54.62
Std Dev 3.708
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

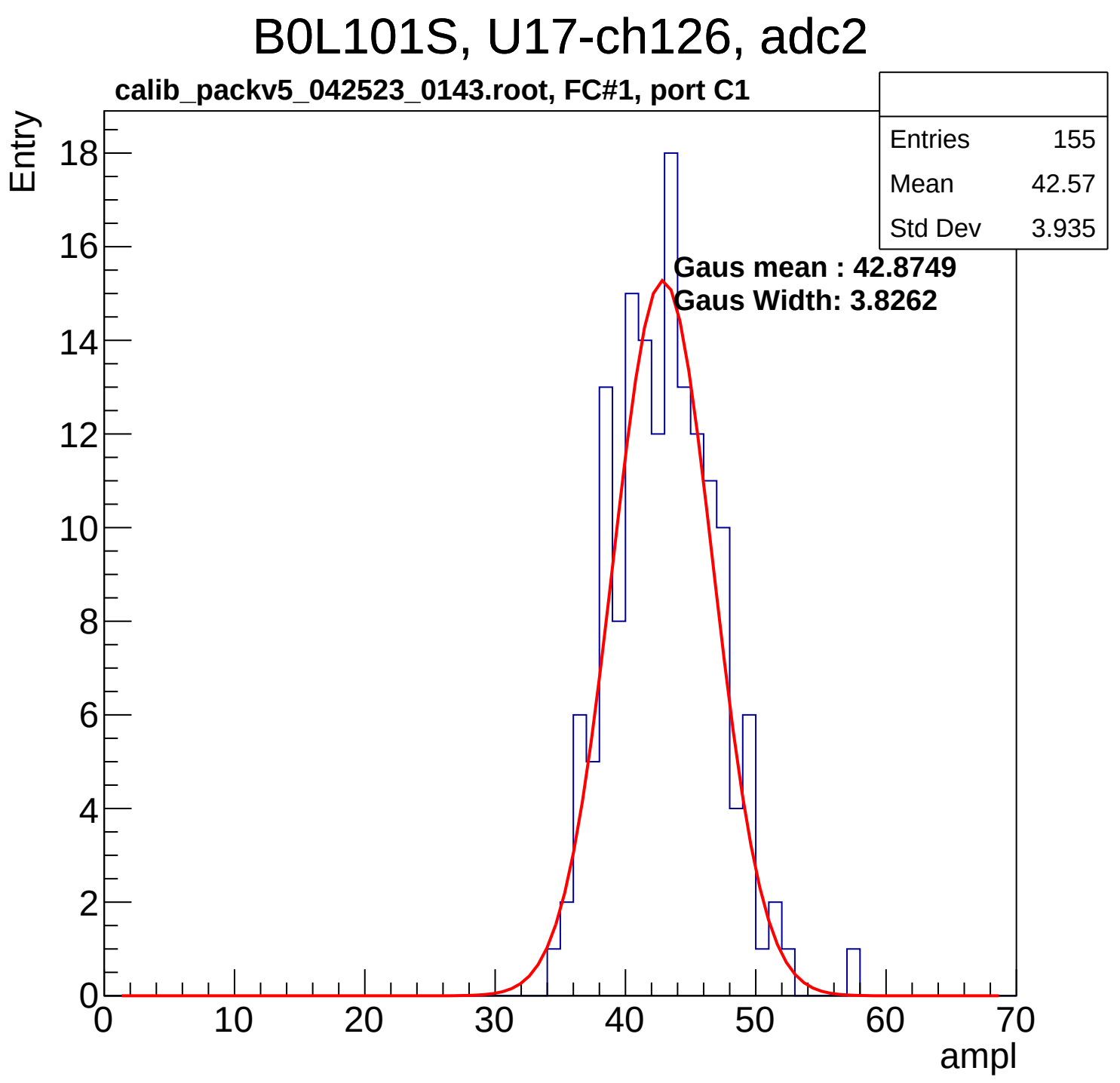

B0L101S, U17-ch125, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.94
Std Dev 6.482
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

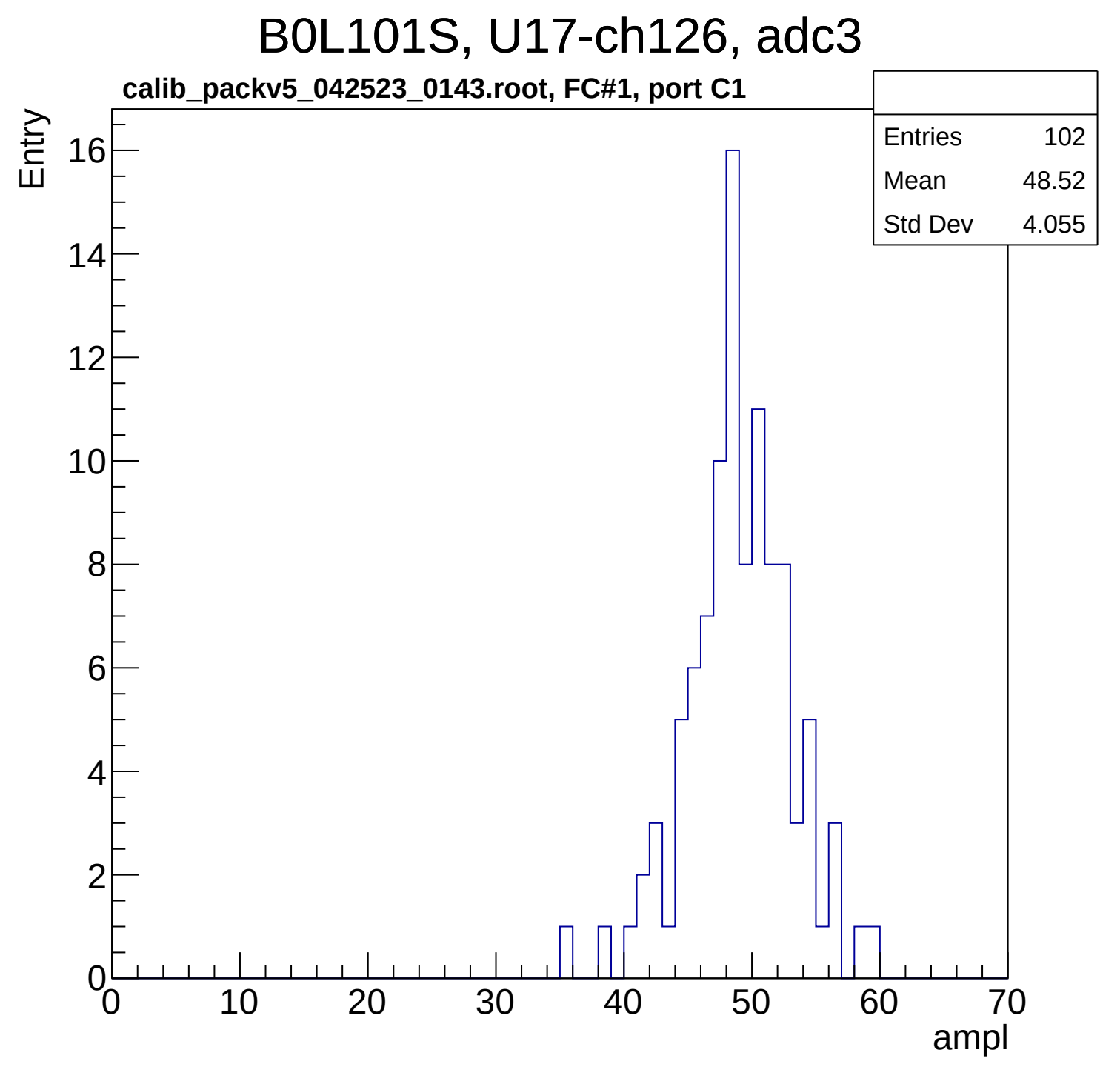

B0L101S, U17-ch125, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 11
Mean 55.36
Std Dev 17.59
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

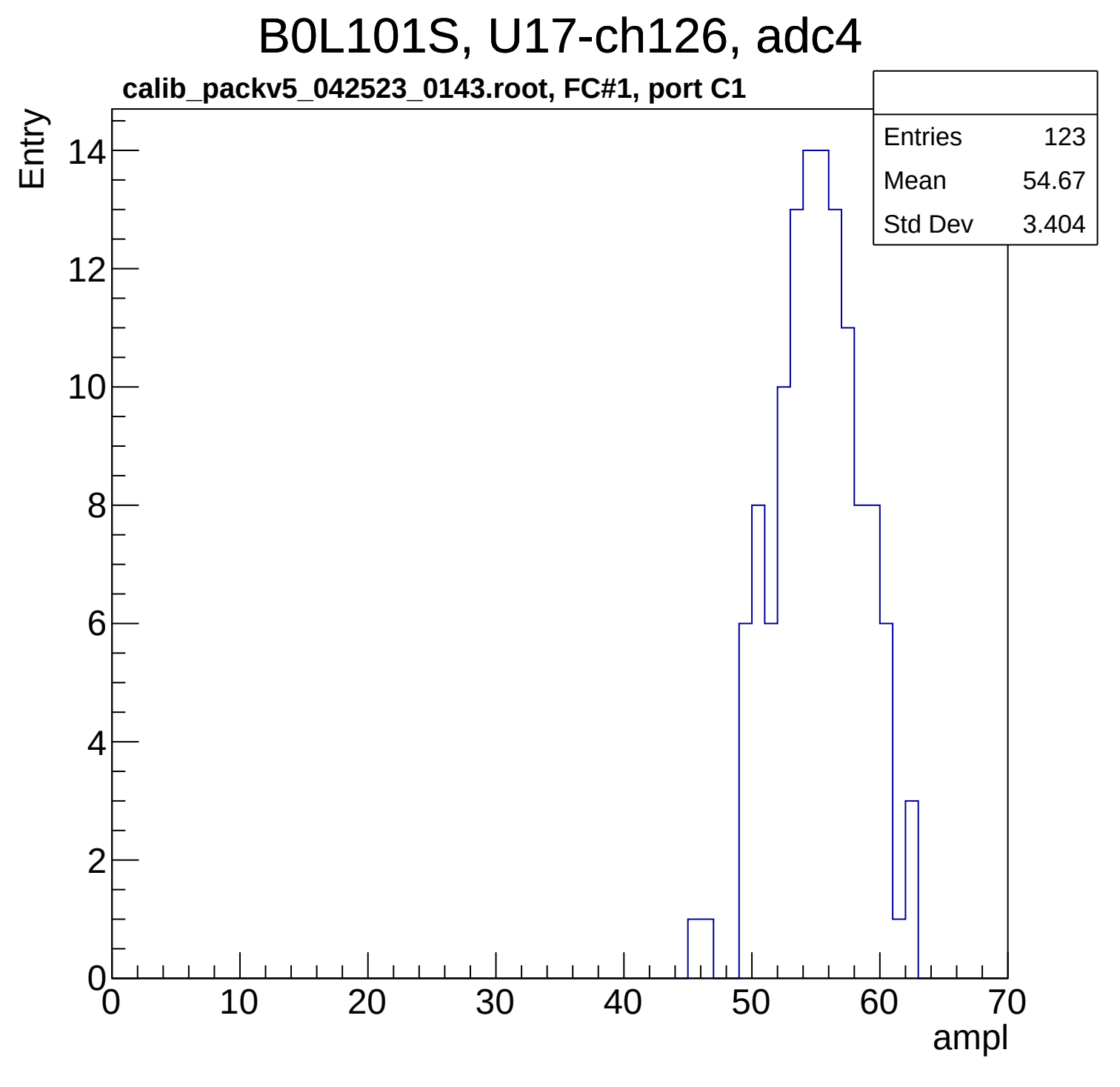

B0L101S, U17-ch125, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

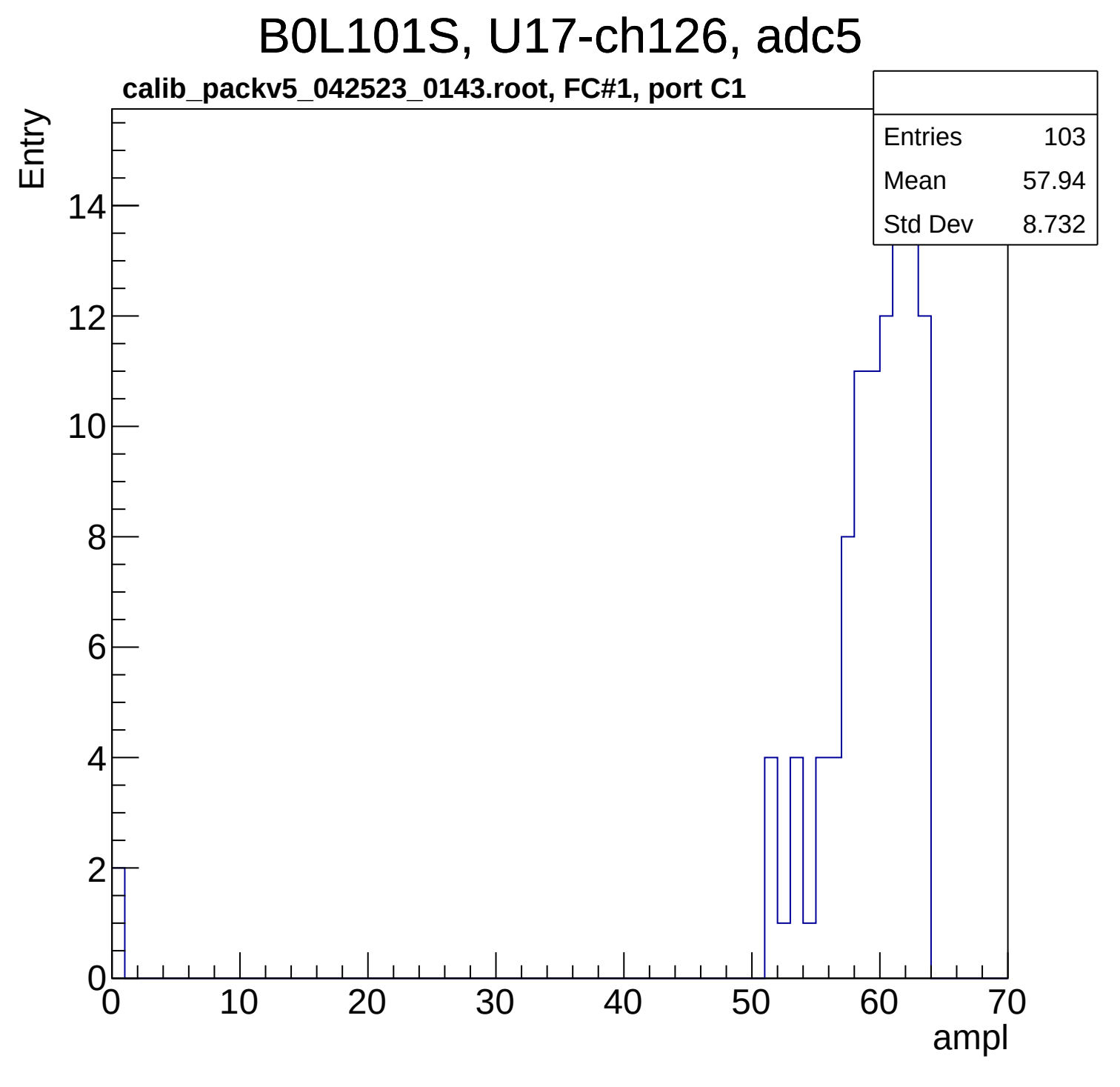

B0L101S, U17-ch126, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 139
Mean 27.8
Std Dev 8.147
Gaus mean : 28.3965
Gaus Width: 4.0909
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

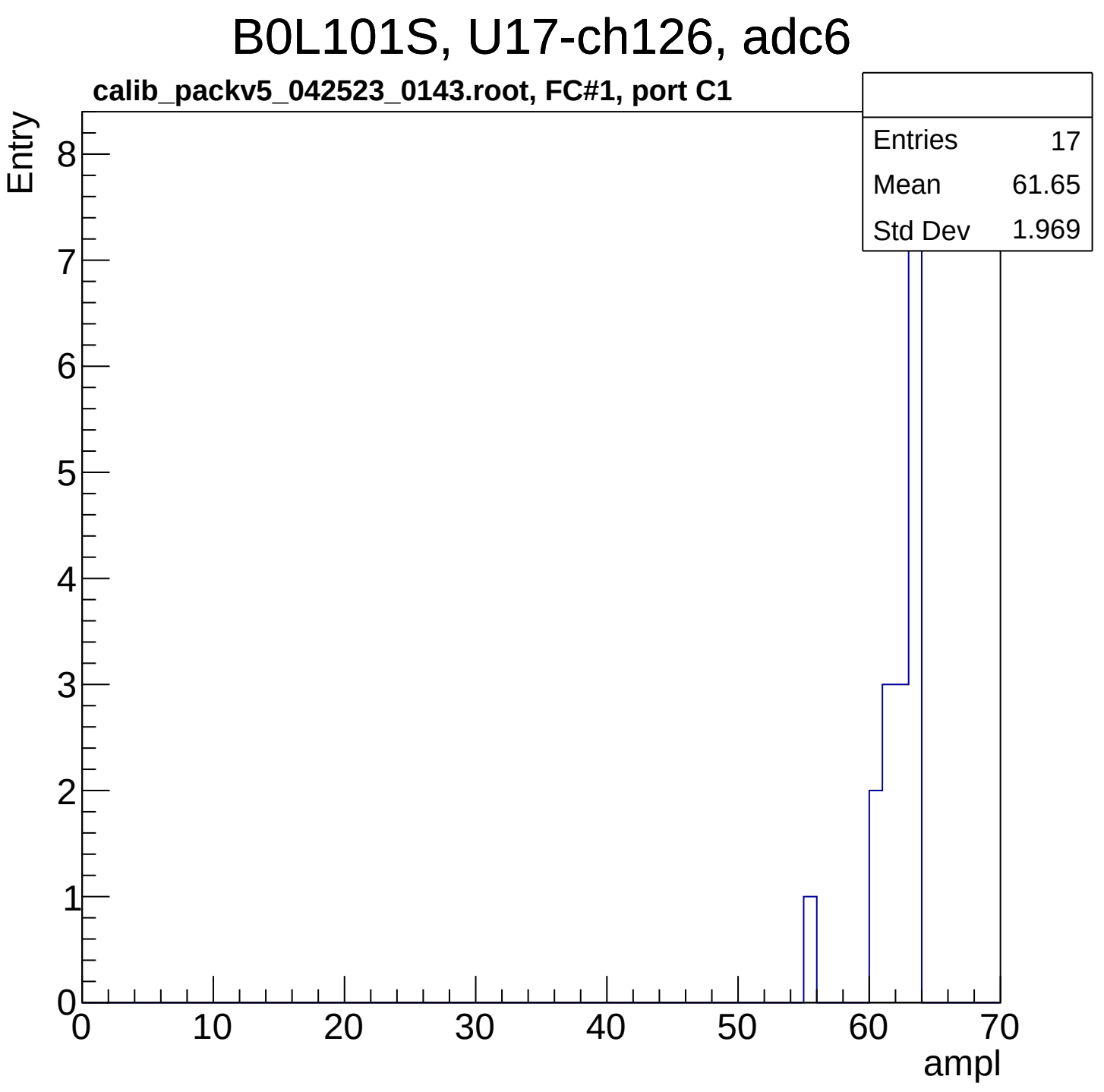

B0L101S, U17-ch126, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 157
Mean 34.29
Std Dev 5.087
Gaus mean : 34.8402
Gaus Width: 4.0905
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

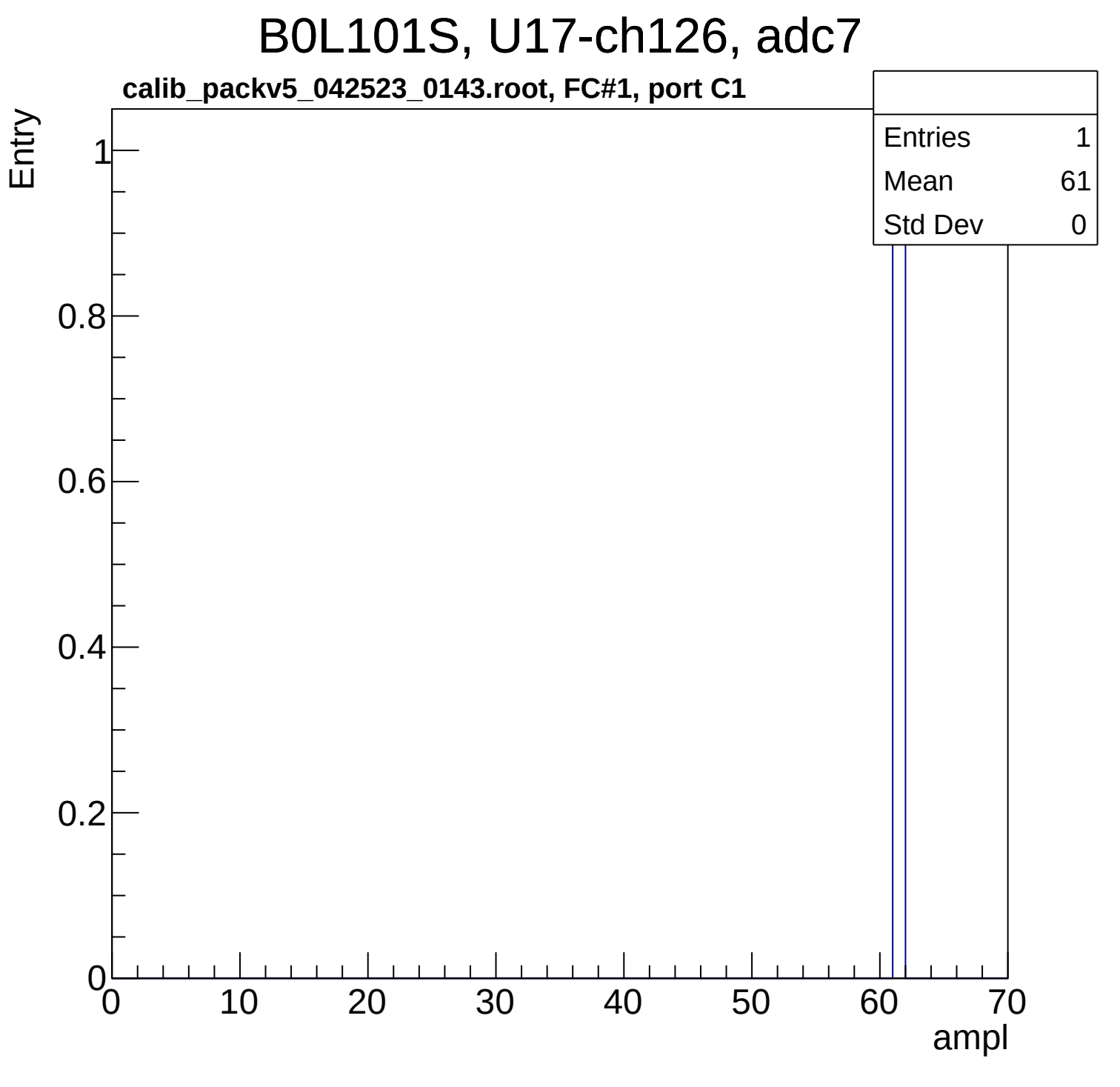

B0L101S, U17-ch126, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 155
Mean 42.57
Std Dev 3.935
Gaus mean : 42.8749
Gaus Width: 3.8262
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70

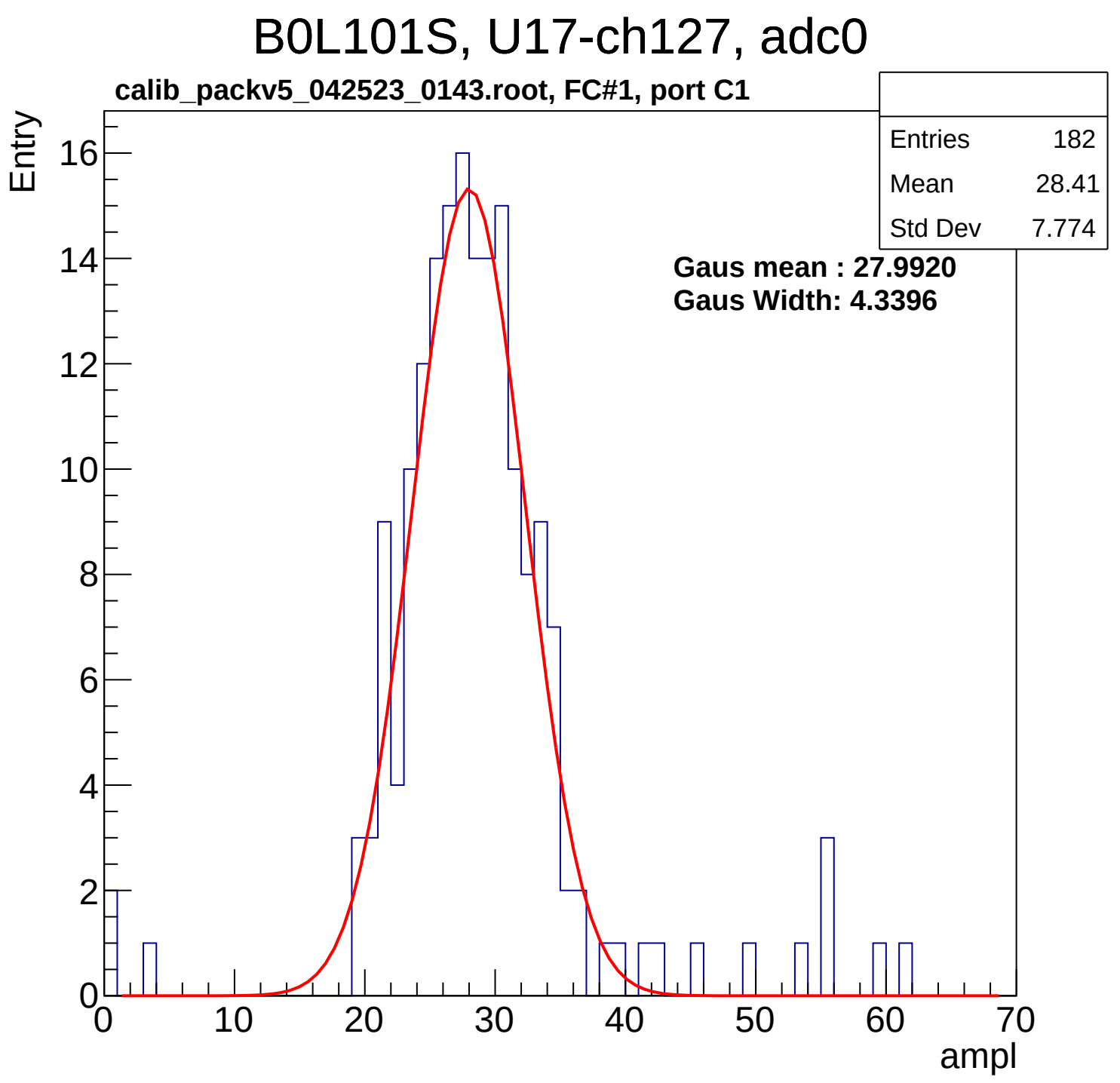

B0L101S, U17-ch126, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 102
Mean 48.52
Std Dev 4.055
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

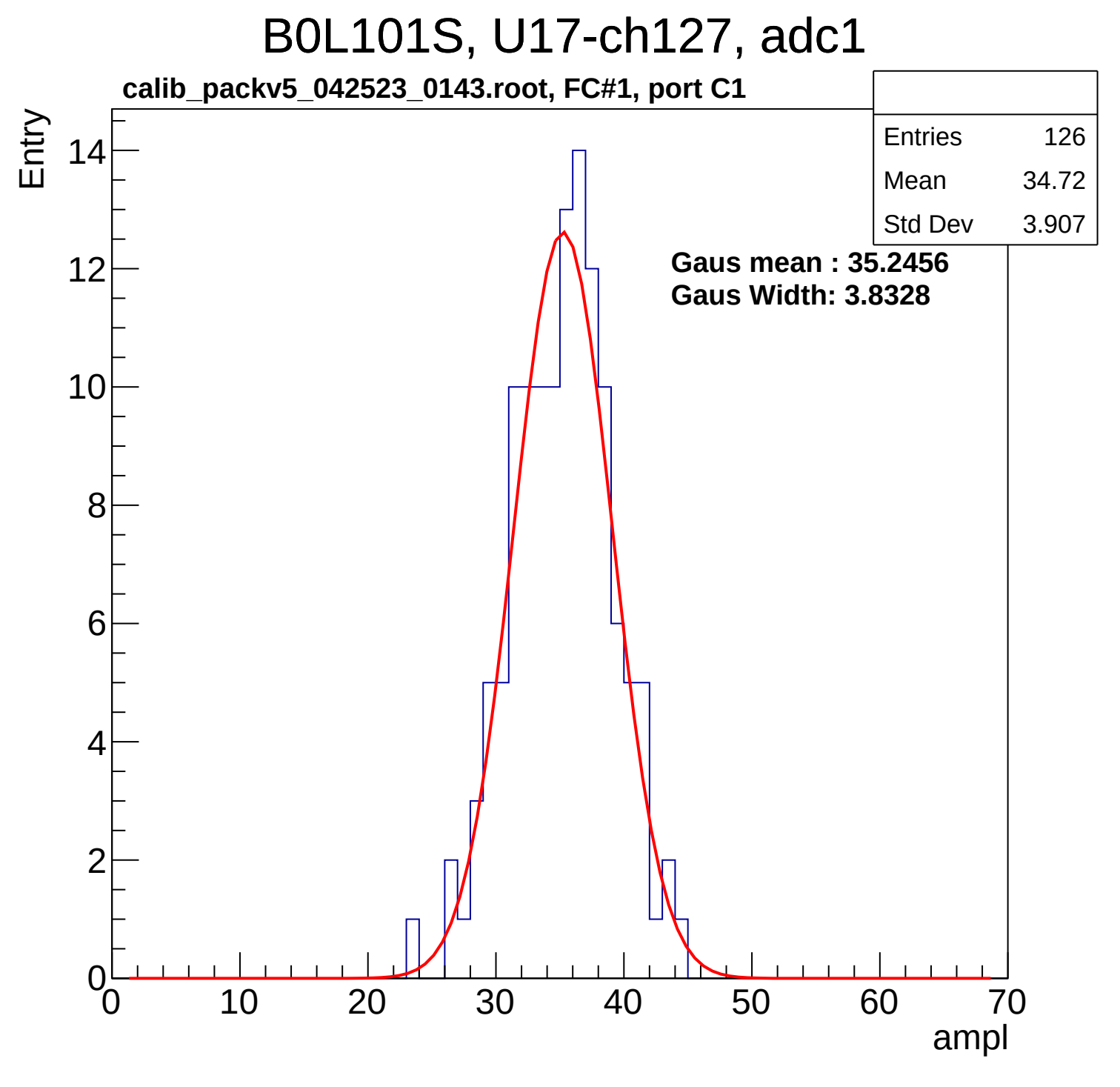

B0L101S, U17-ch126, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 123
Mean 54.67
Std Dev 3.404
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

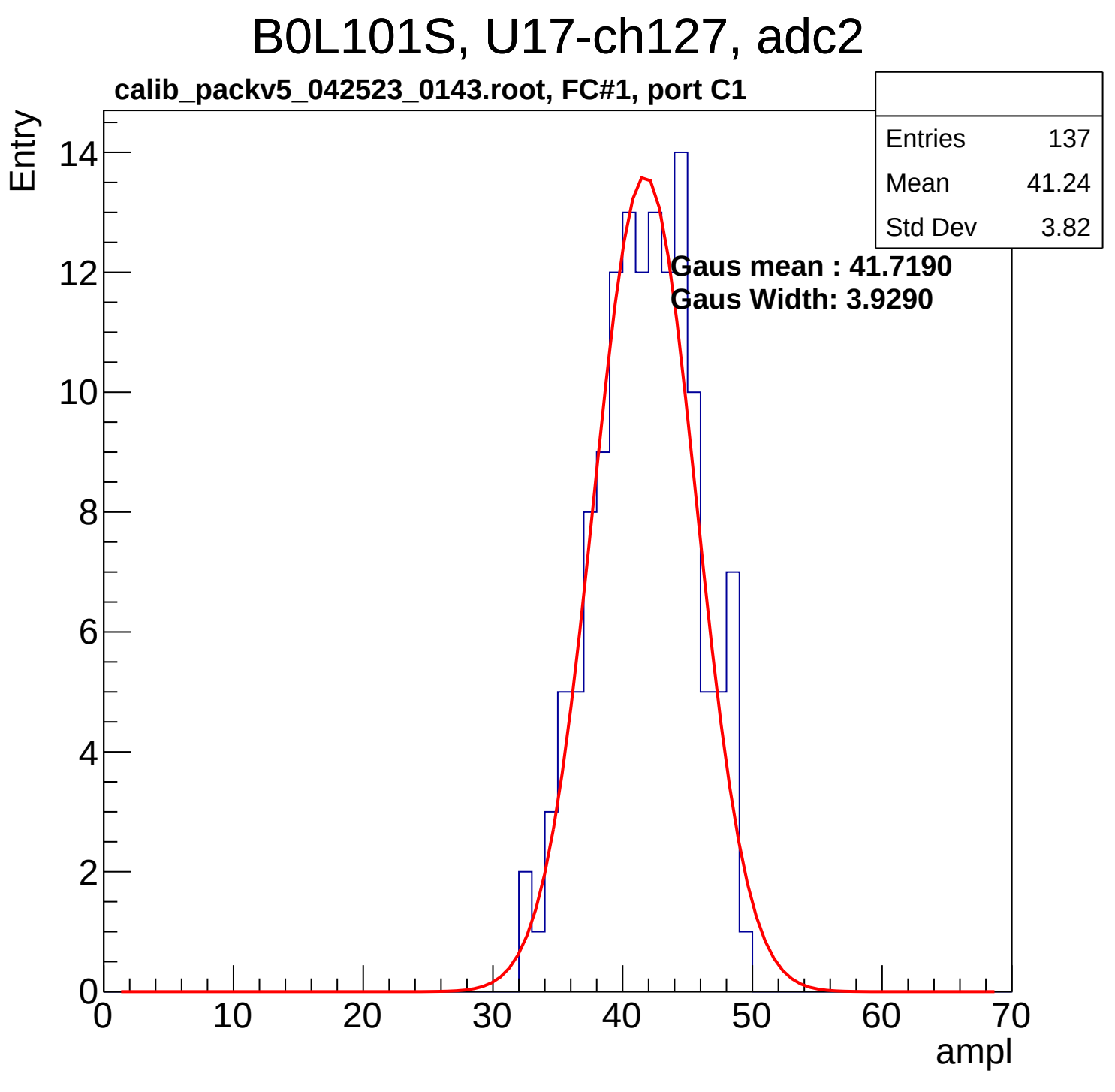

B0L101S, U17-ch126, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 103
Mean 57.94
Std Dev 8.732
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

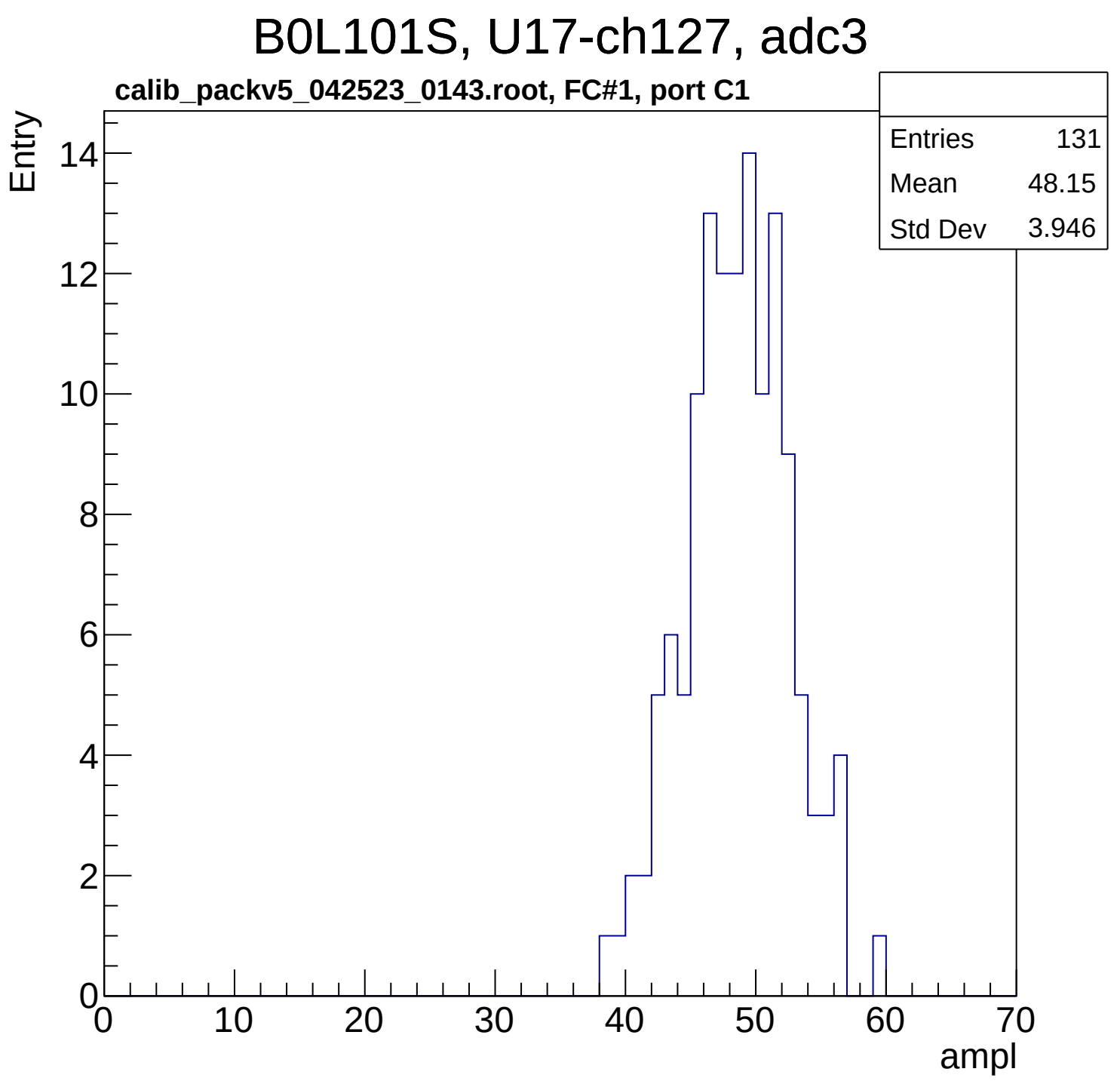

B0L101S, U17-ch126, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 17
Mean 61.65
Std Dev 1.969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

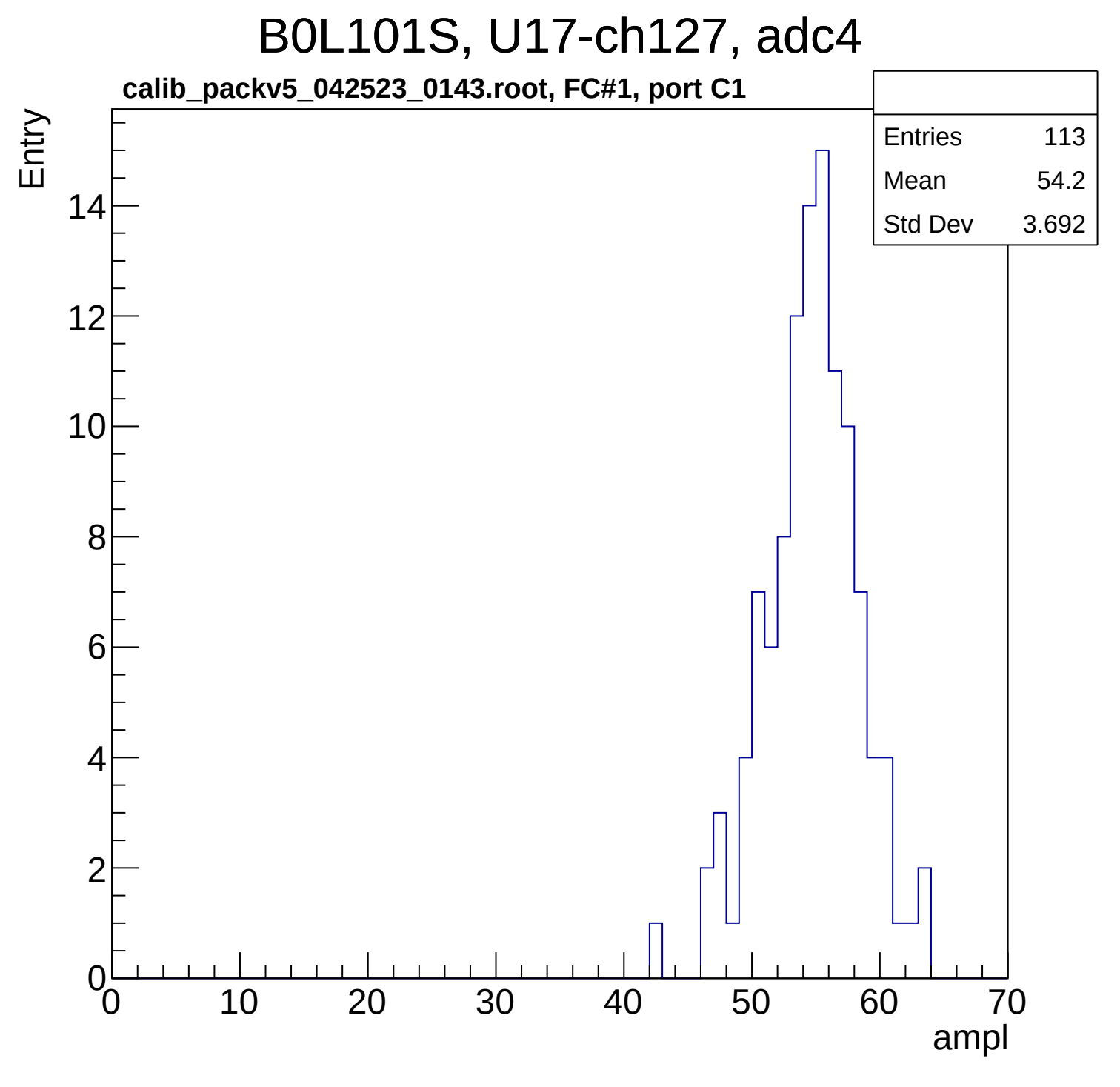

B0L101S, U17-ch126, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

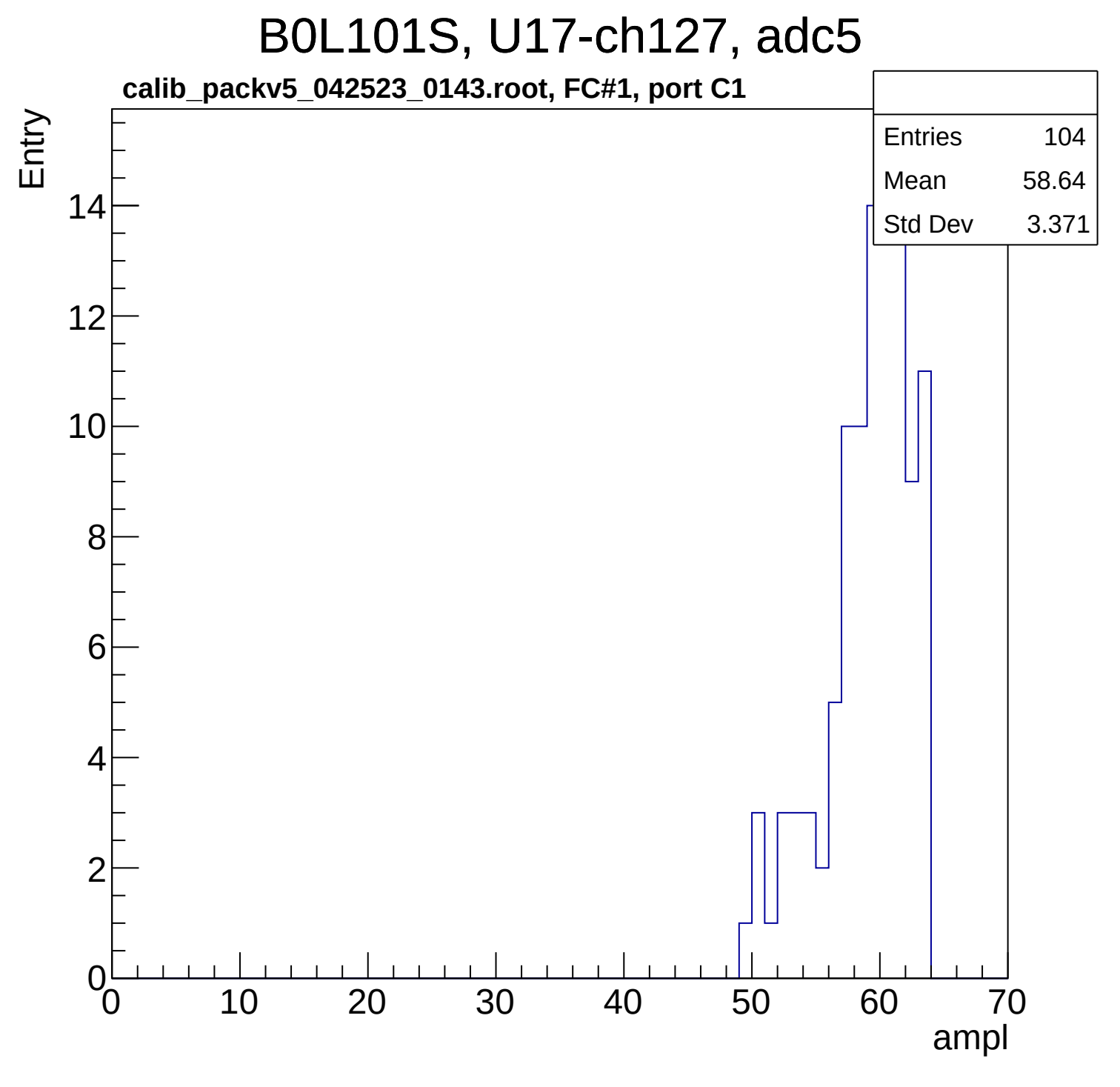

B0L101S, U17-ch127, adc0
calib_packv5_042523_0143.root, FC#1, port C1
Entries 182
Mean 28.41
Std Dev 7.774
Gaus mean : 27.9920
Gaus Width: 4.3396
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

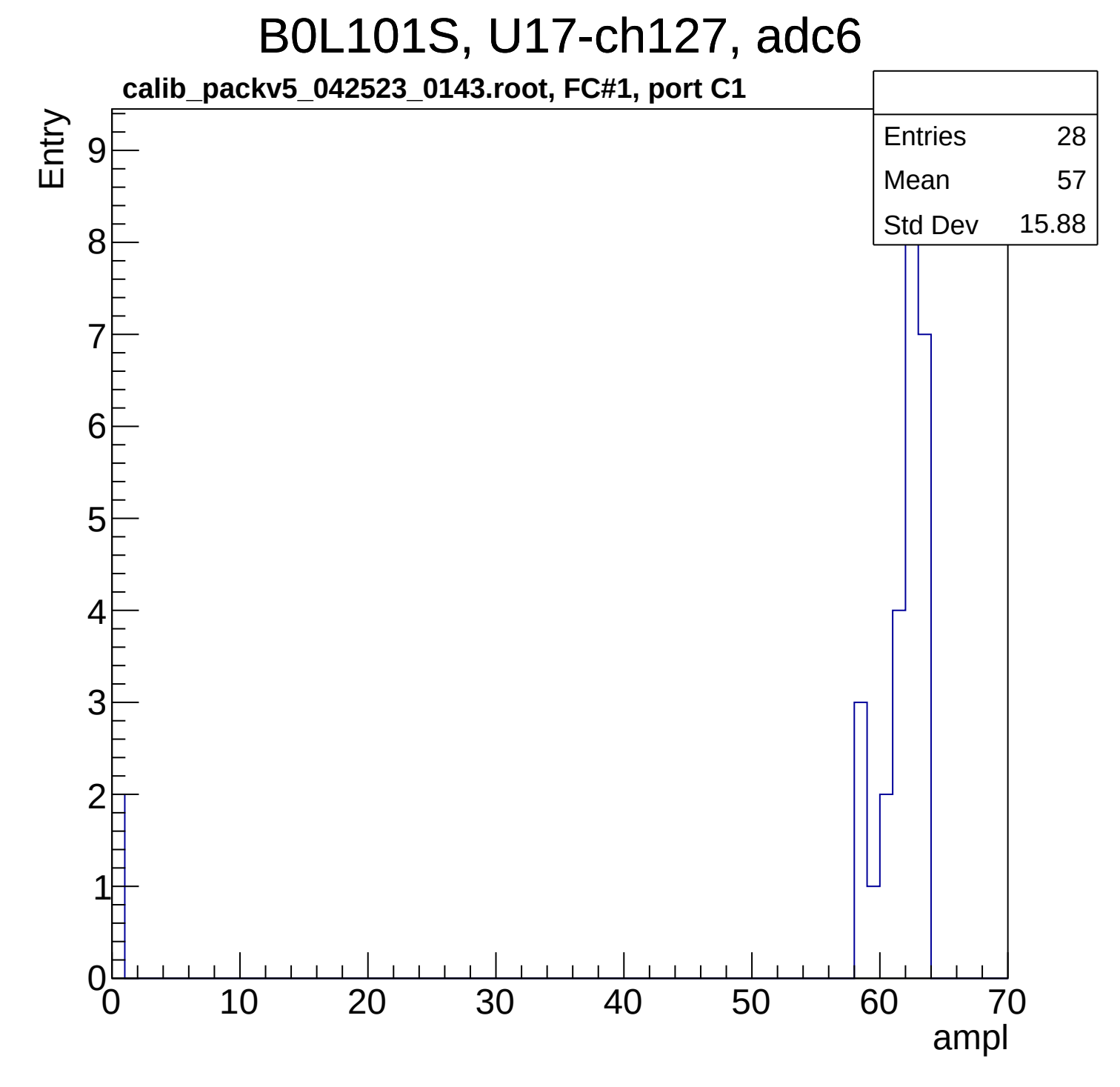

B0L101S, U17-ch127, adc1
calib_packv5_042523_0143.root, FC#1, port C1
Entries 126
Mean 34.72
Std Dev 3.907
Gaus mean : 35.2456
Gaus Width: 3.8328
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

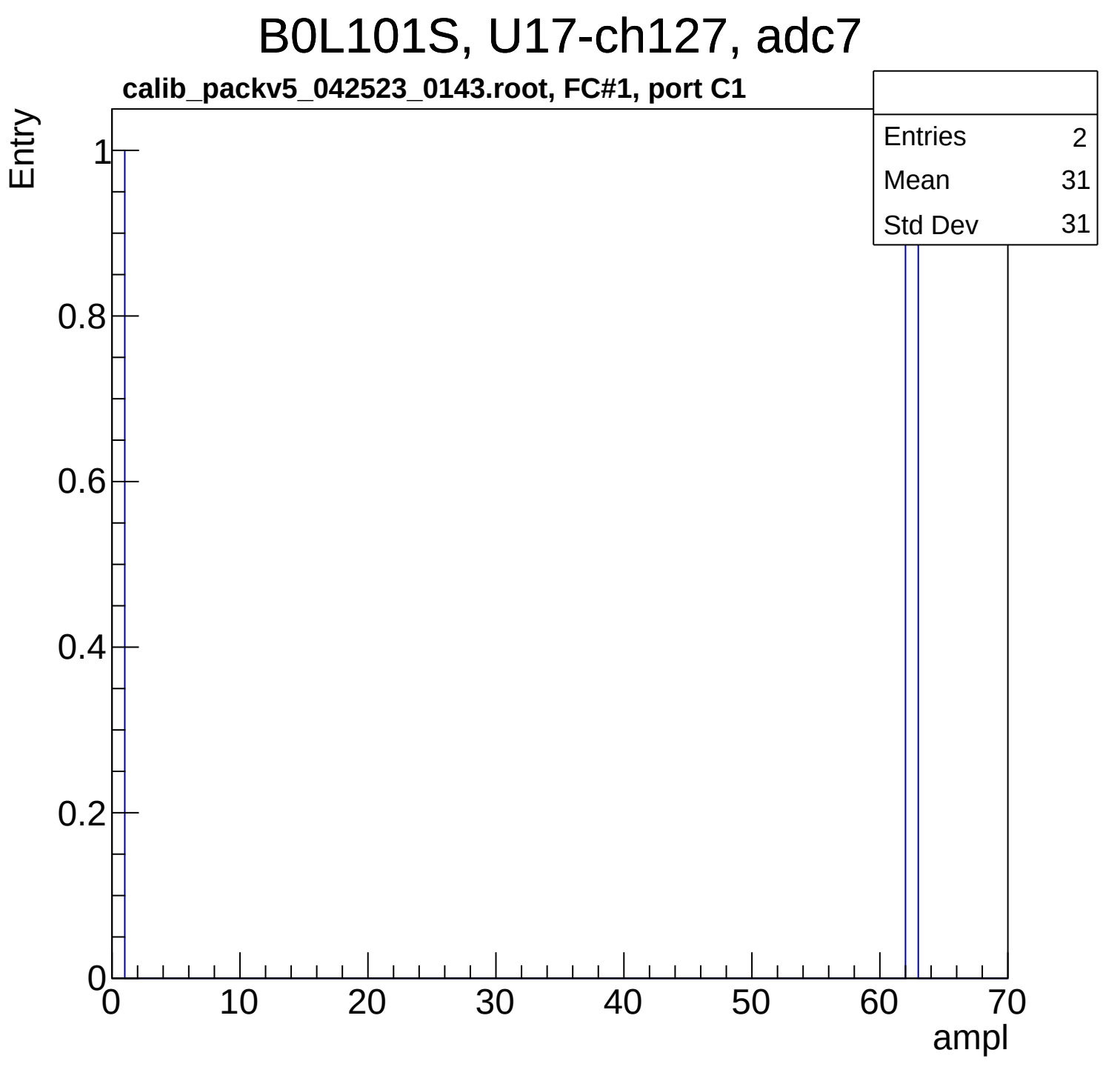

B0L101S, U17-ch127, adc2
calib_packv5_042523_0143.root, FC#1, port C1
Entries 137
Mean 41.24
Std Dev 3.82
Gaus mean : 41.7190
Gaus Width: 3.9290
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

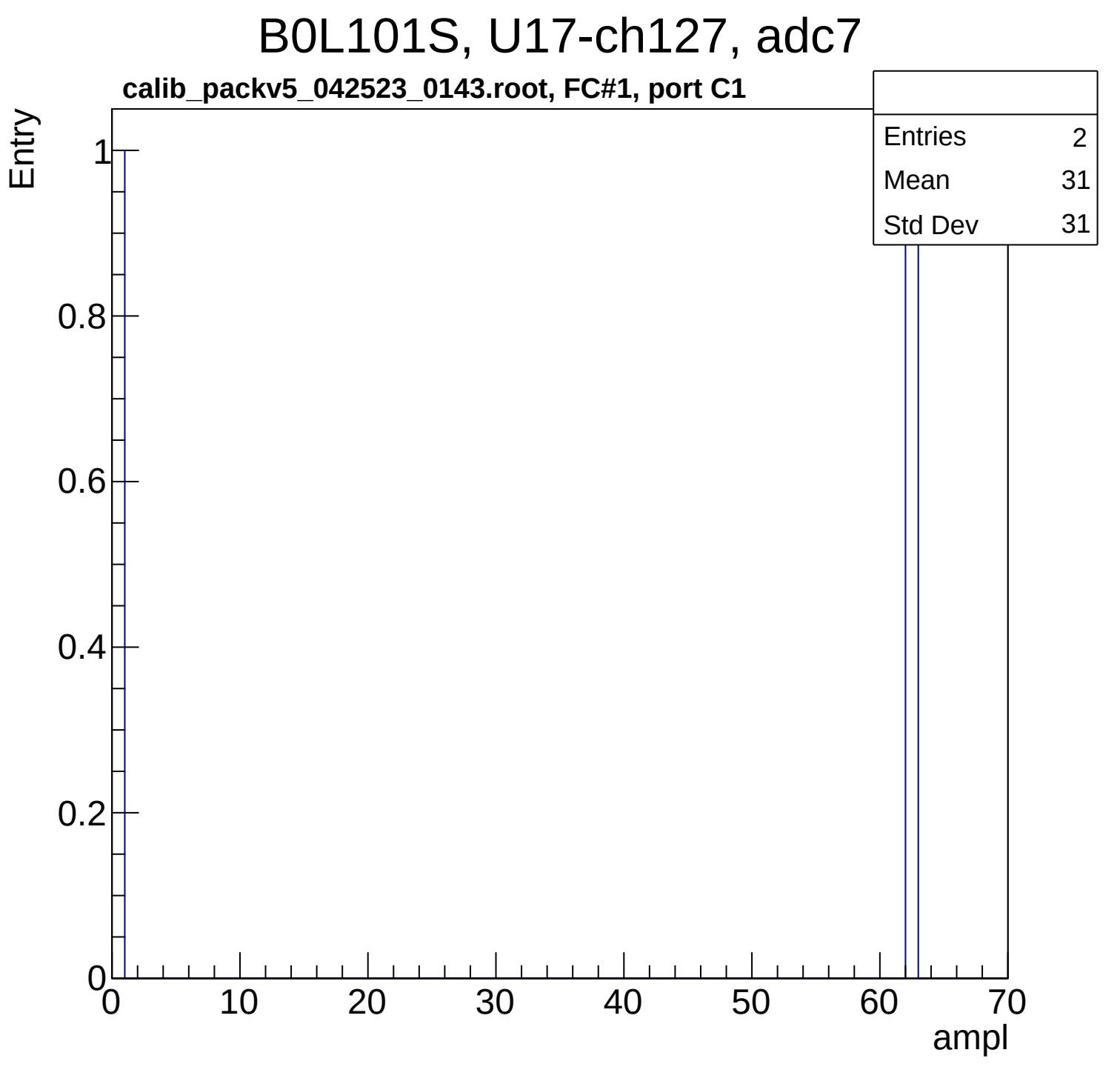

B0L101S, U17-ch127, adc3
calib_packv5_042523_0143.root, FC#1, port C1
Entries 131
Mean 48.15
Std Dev 3.946
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B0L101S, U17-ch127, adc4
calib_packv5_042523_0143.root, FC#1, port C1
Entries 113
Mean 54.2
Std Dev 3.692
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B0L101S, U17-ch127, adc5
calib_packv5_042523_0143.root, FC#1, port C1
Entries 104
Mean 58.64
Std Dev 3.371
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B0L101S, U17-ch127, adc6
calib_packv5_042523_0143.root, FC#1, port C1
Entries 28
Mean 57
Std Dev 15.88
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L101S, U17-ch127, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L101S, U17-ch127, adc7
calib_packv5_042523_0143.root, FC#1, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl