

B1L002S, U9-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.1
Std Dev	11.87

Turn on : 26.4486

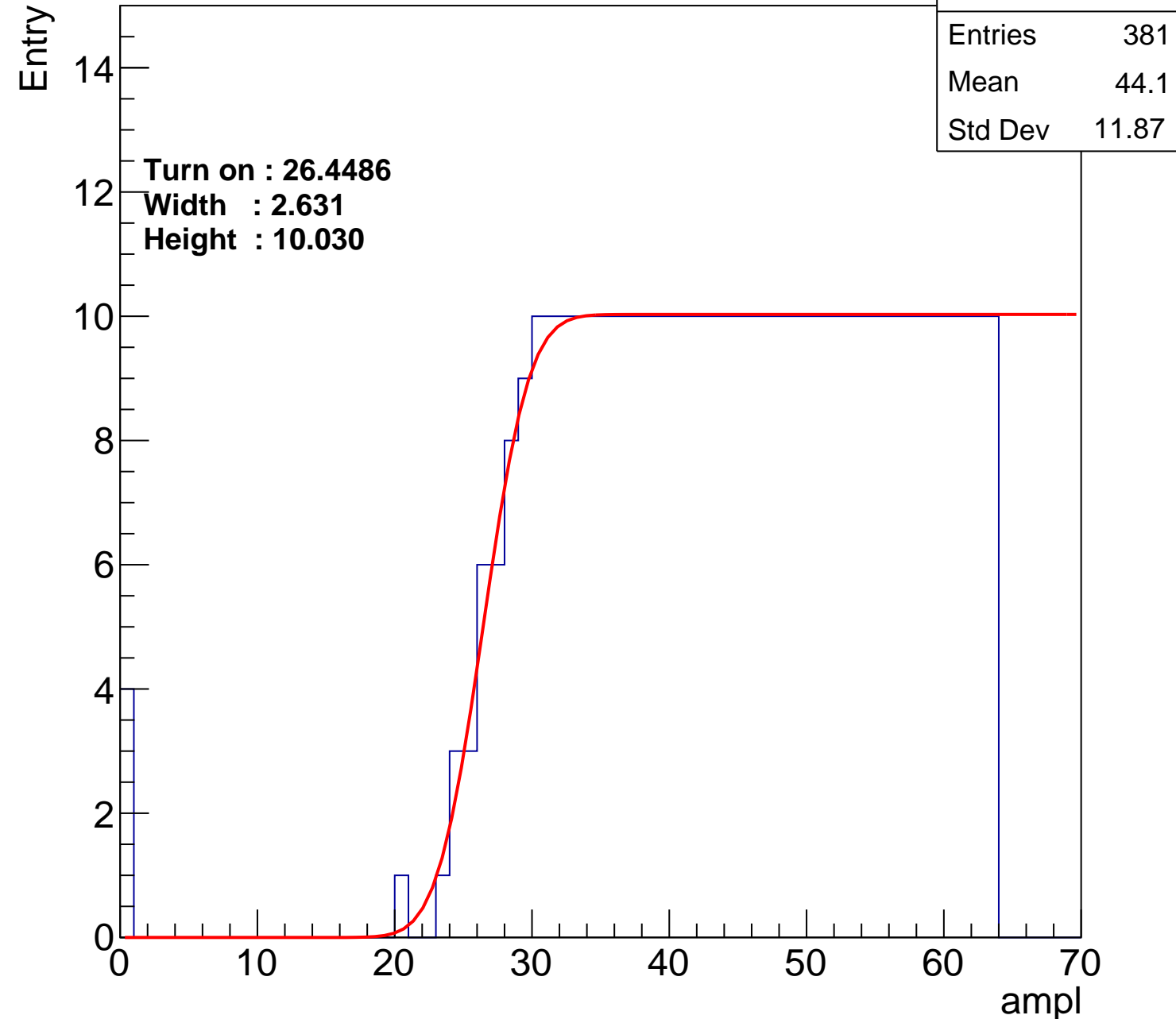
Width : 2.631

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.35
Std Dev	12.09

Turn on : 24.8288

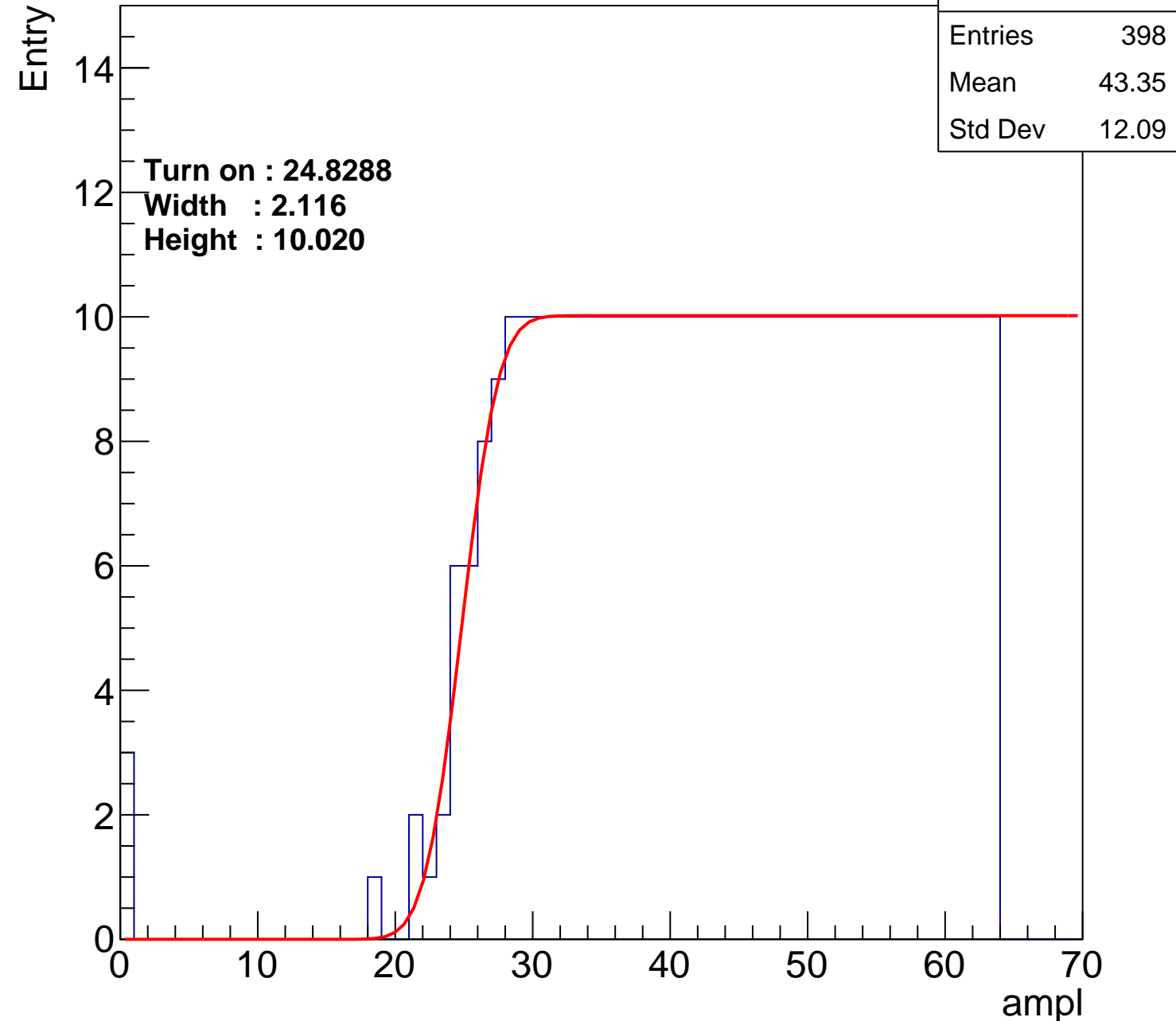
Width : 2.116

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch2

calib_packv5_042523_0143.root, FC#10, port B3

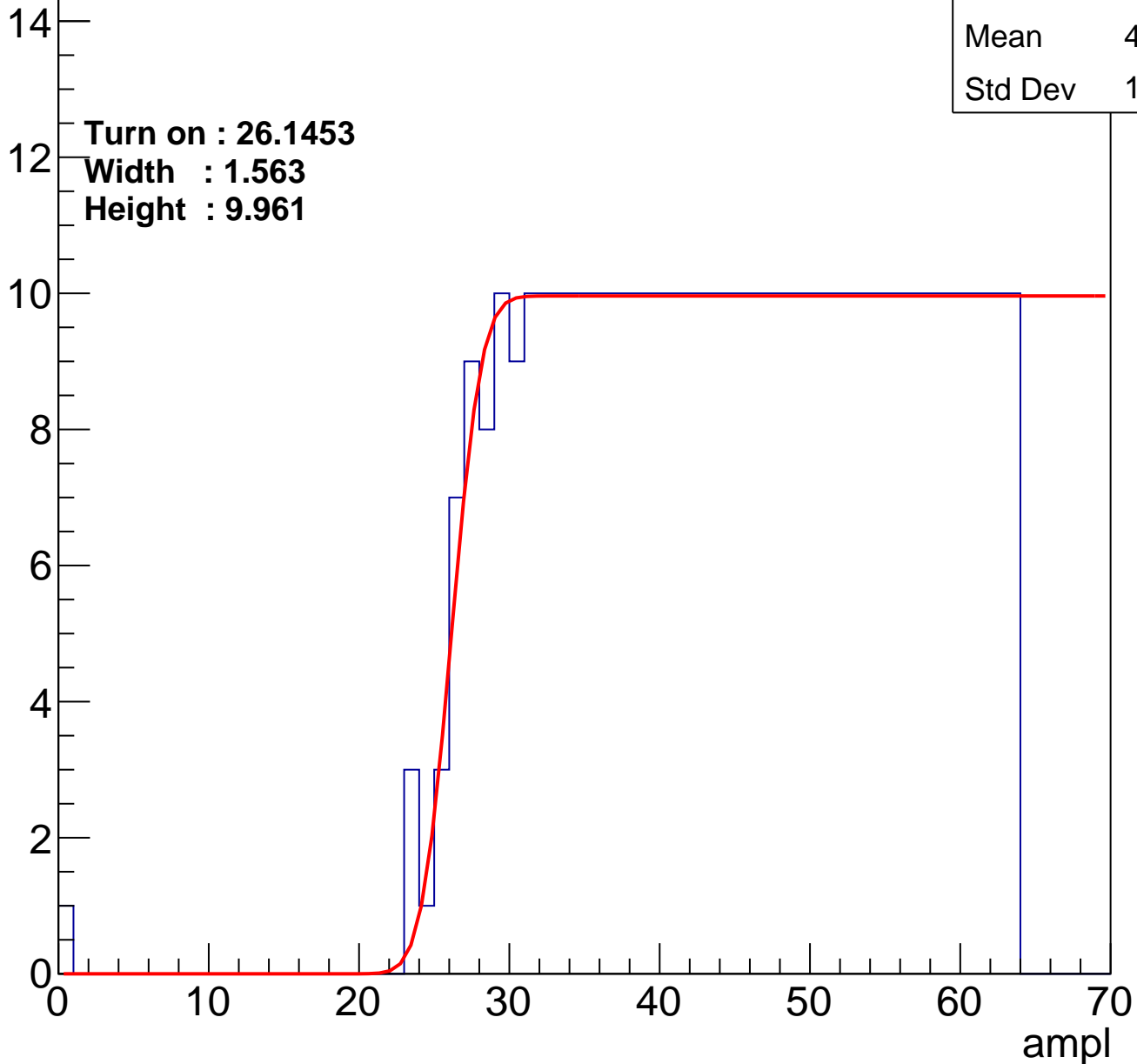
Entry

Entries	381
Mean	44.32
Std Dev	11.29

Turn on : 26.1453

Width : 1.563

Height : 9.961



B1L002S, U9-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.55
Std Dev	12.03

Turn on : 26.2256

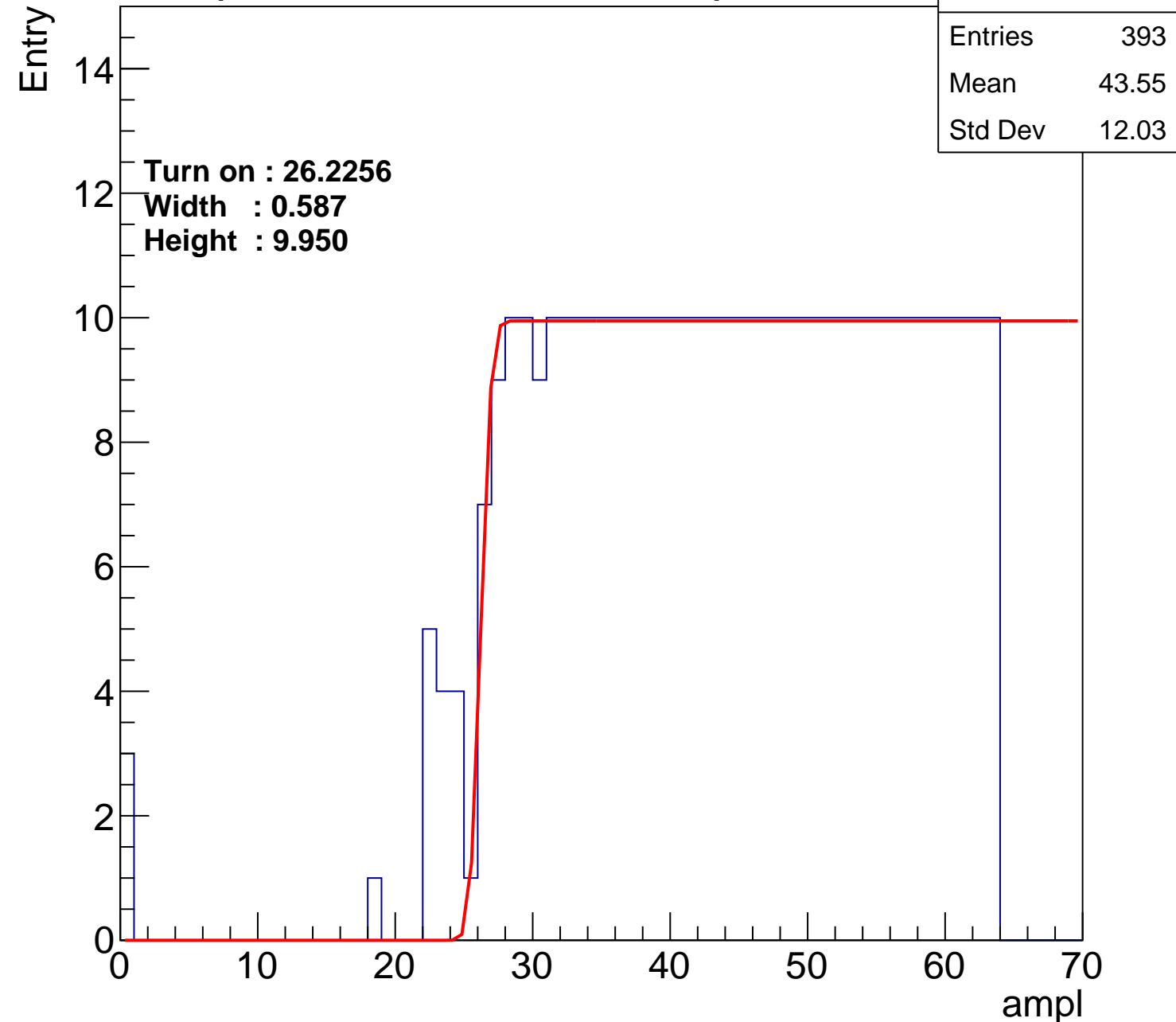
Width : 0.587

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch4

calib_packv5_042523_0143.root, FC#10, port B3

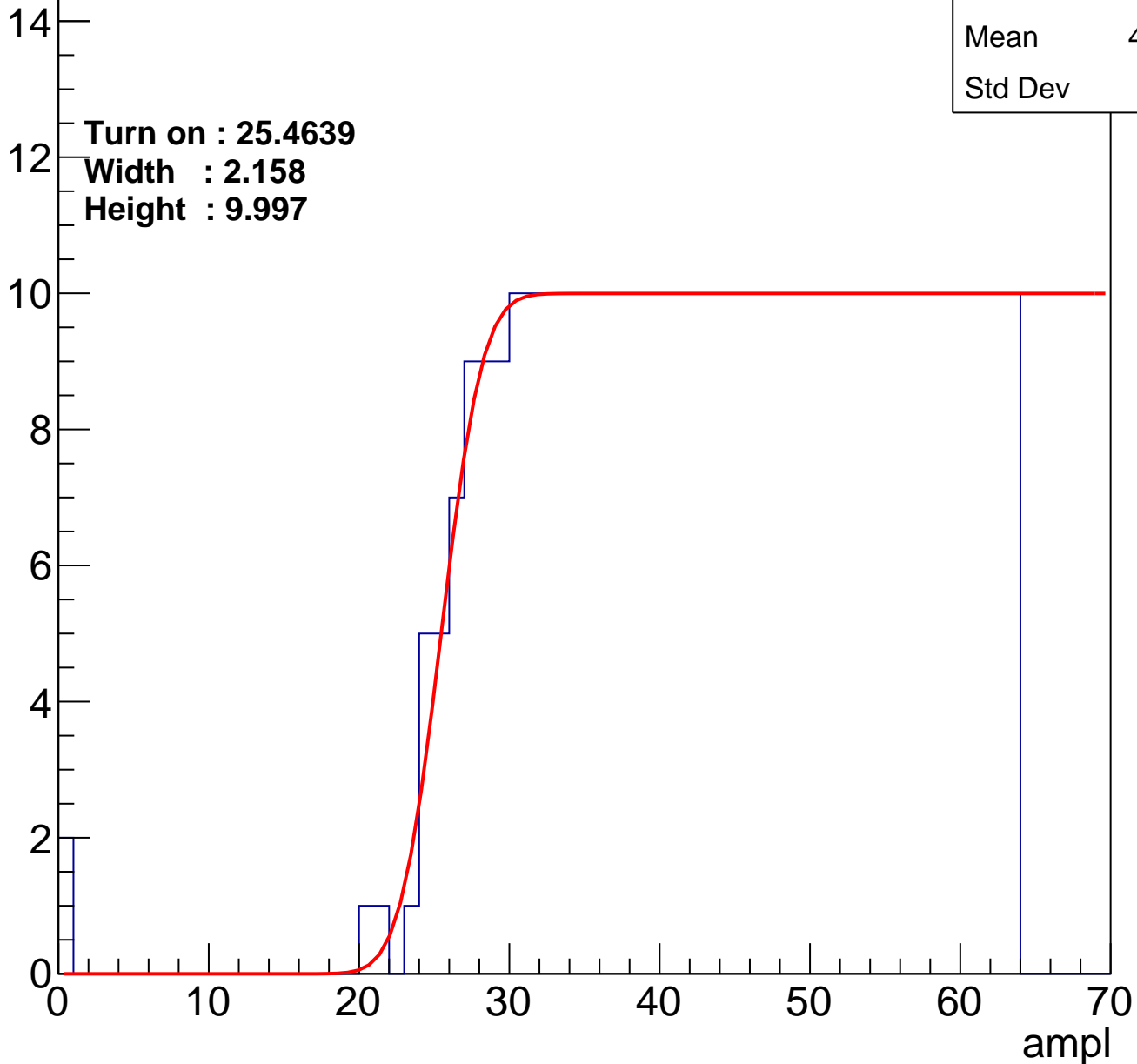
Entry

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.4639

Width : 2.158

Height : 9.997



B1L002S, U9-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.47
Std Dev	11.76

Turn on : 23.9423

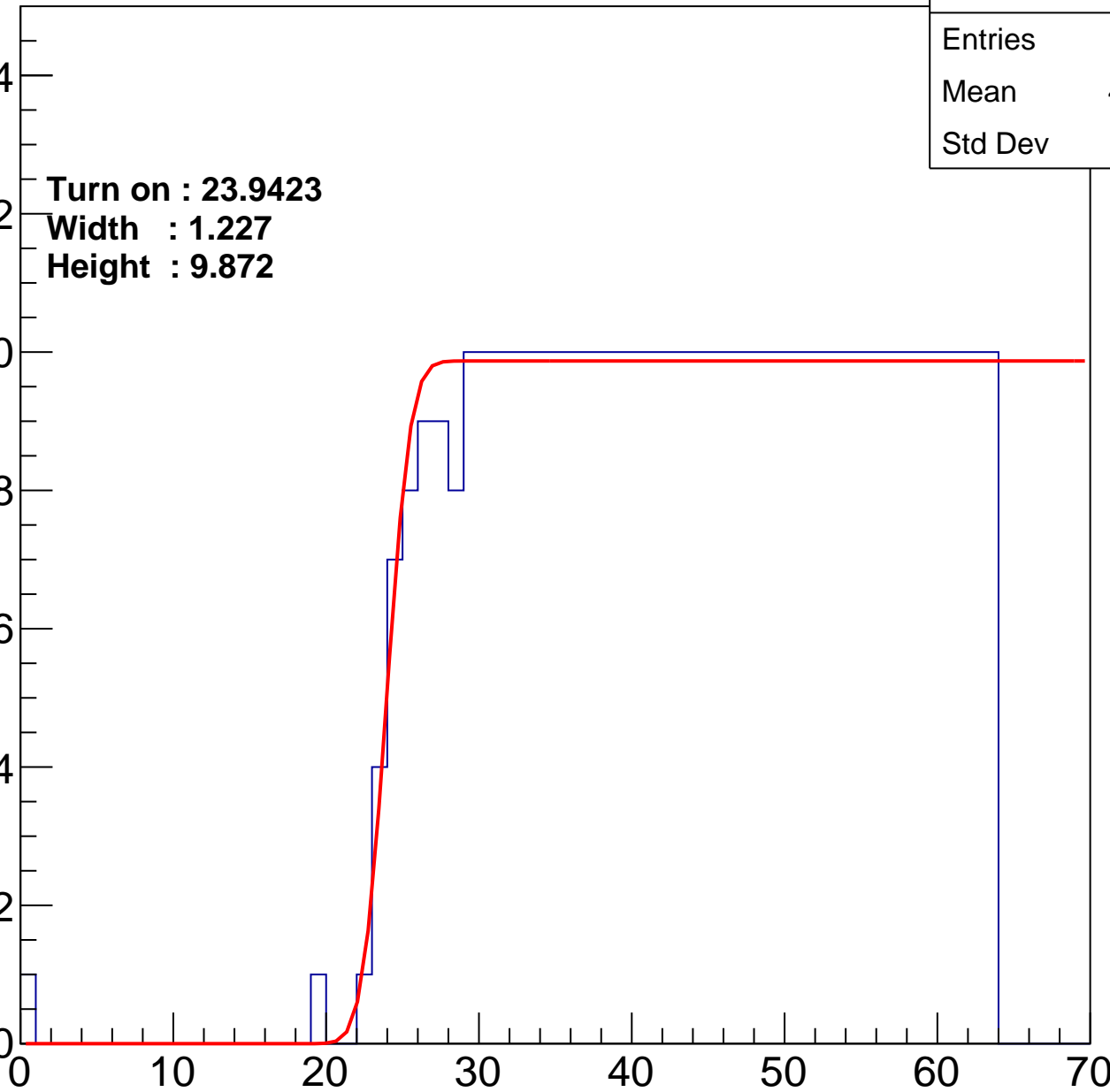
Width : 1.227

Height : 9.872

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.47
Std Dev	11.21

Turn on : 26.2045

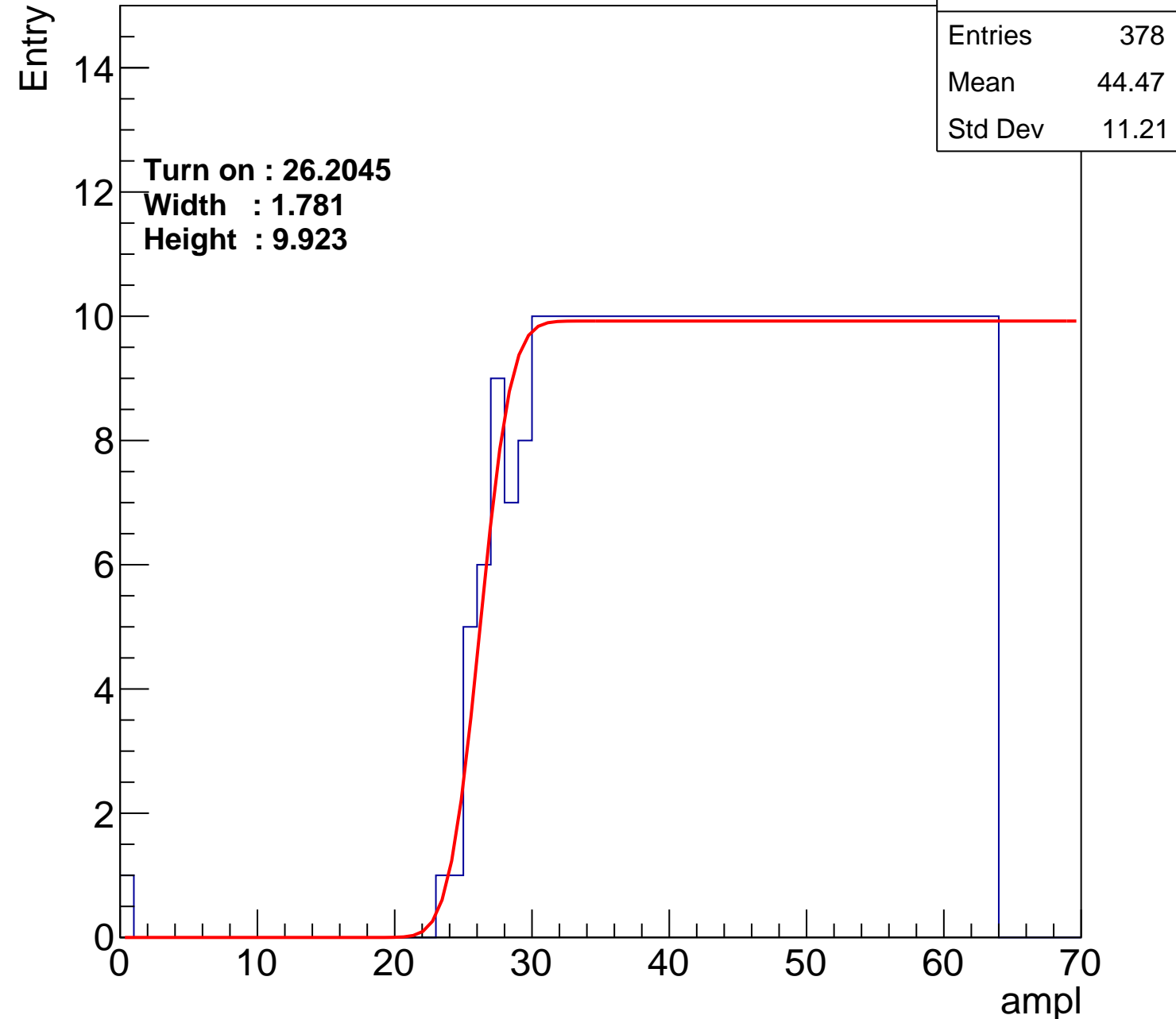
Width : 1.781

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch7

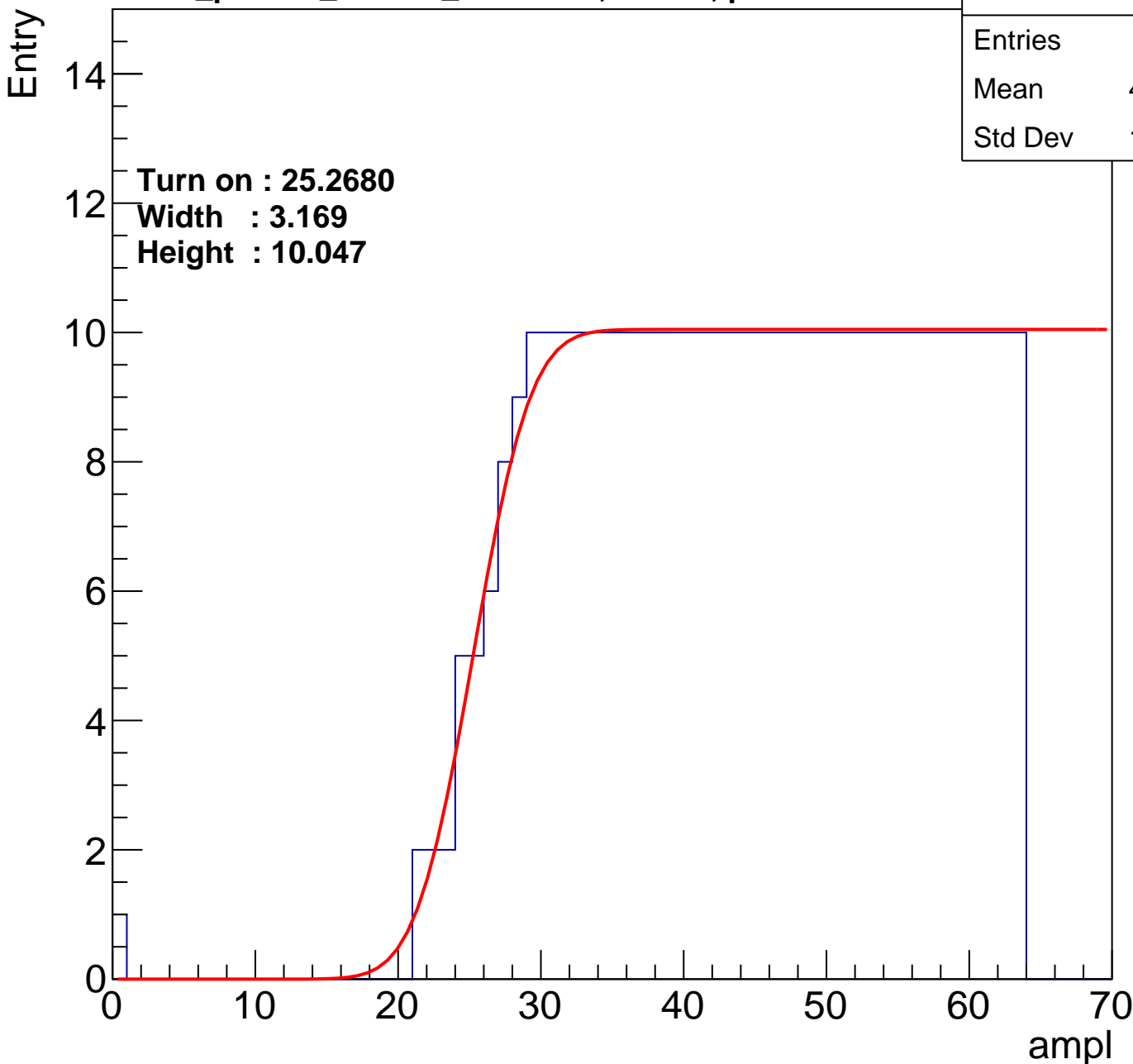
calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.2680

Width : 3.169

Height : 10.047



B1L002S, U9-ch8

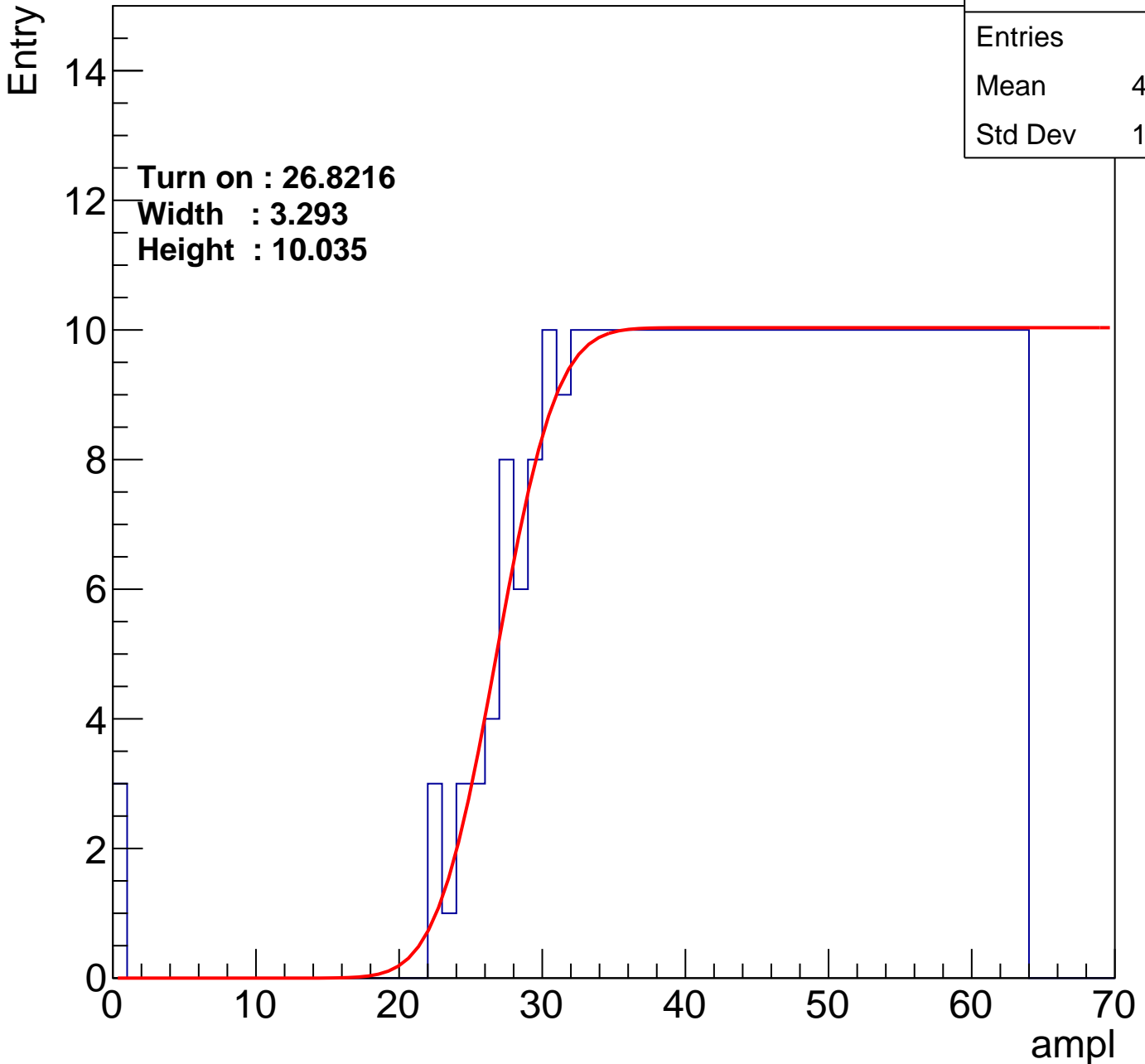
calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 26.8216

Width : 3.293

Height : 10.035



B1L002S, U9-ch9

calib_packv5_042523_0143.root, FC#10, port B3

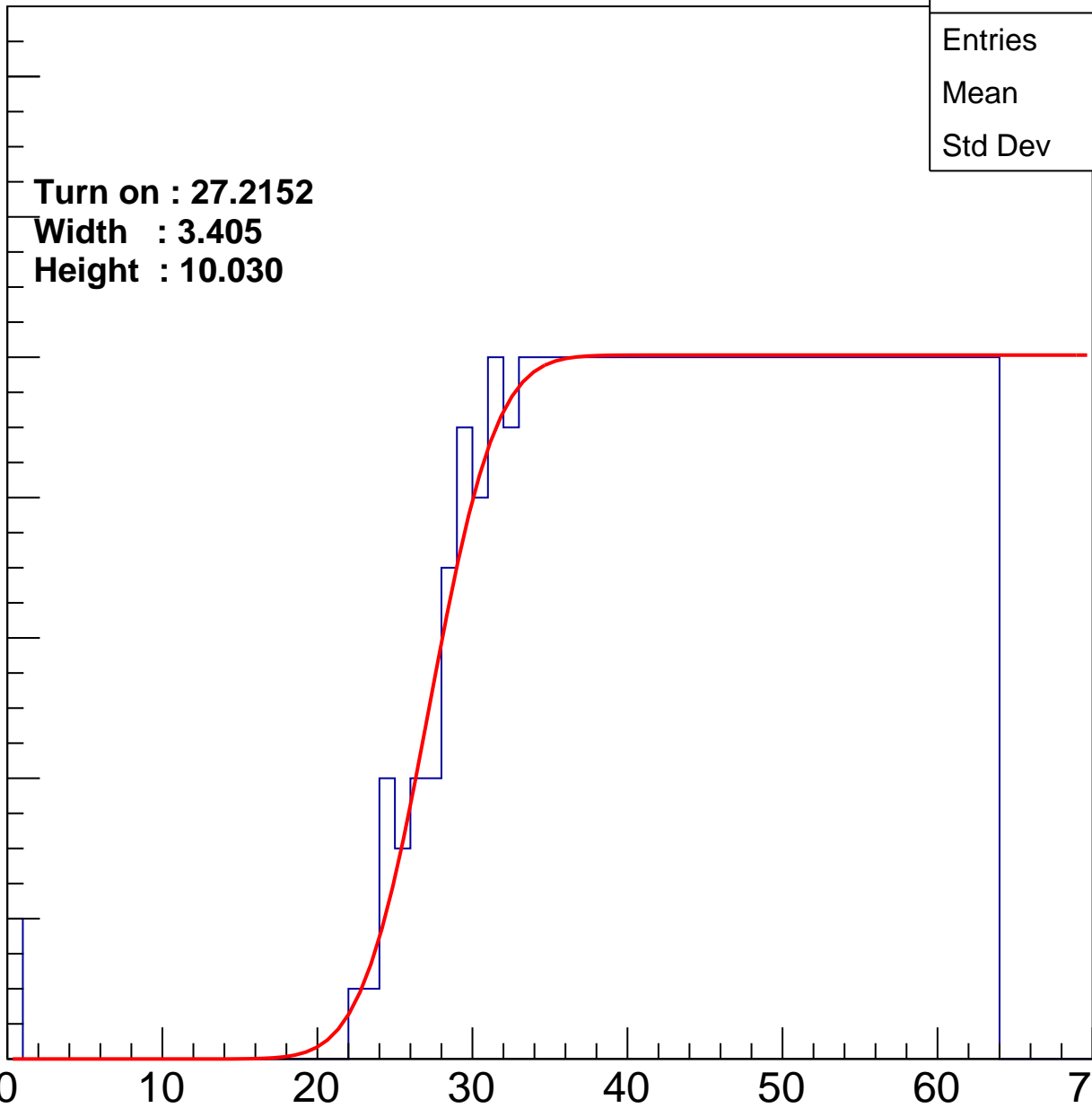
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2152
Width : 3.405
Height : 10.030

Entries	372
Mean	44.63
Std Dev	11.35

ampl



B1L002S, U9-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.4
Std Dev	11.54

Turn on : 26.7413

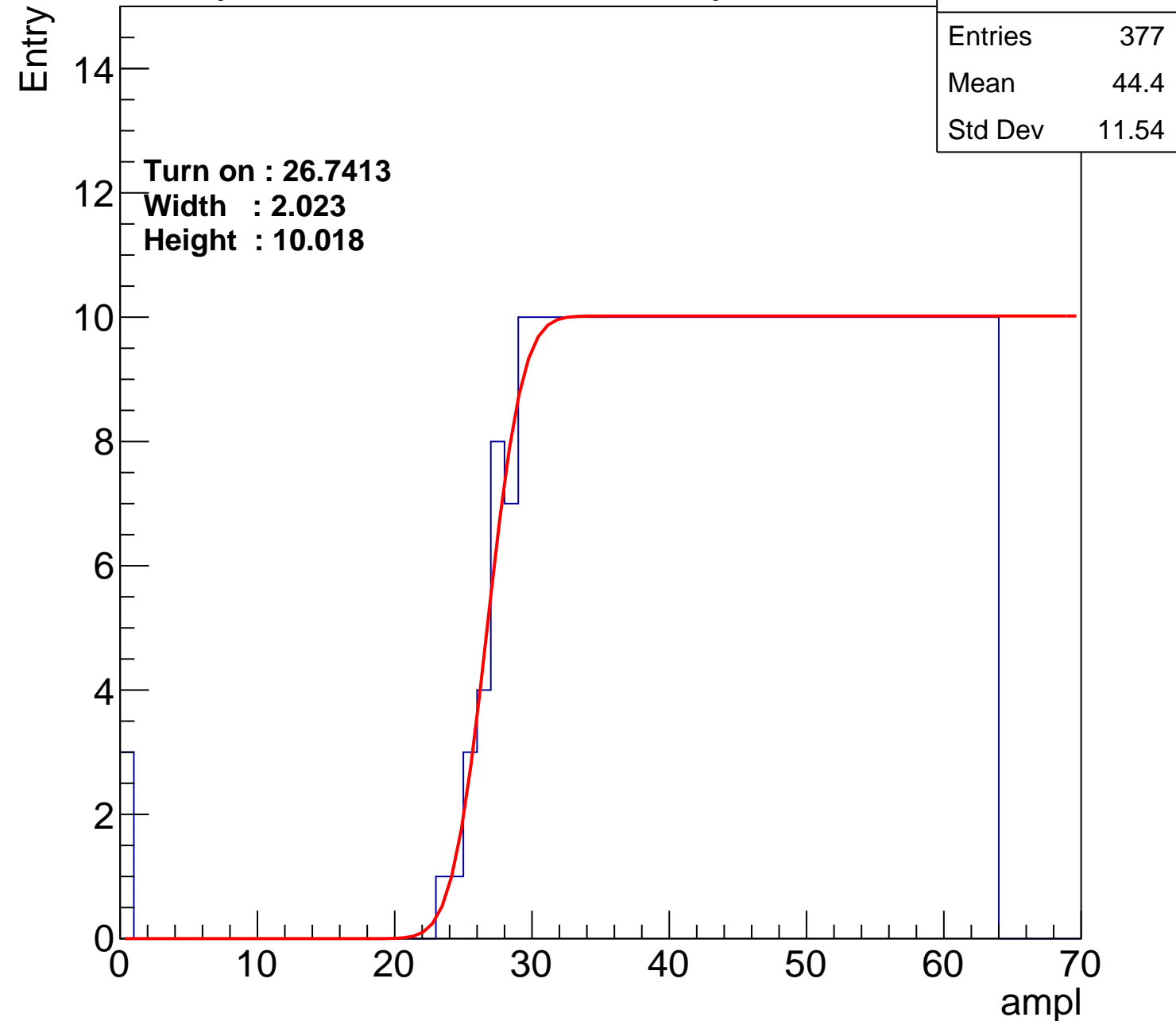
Width : 2.023

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.4081

Width : 3.017

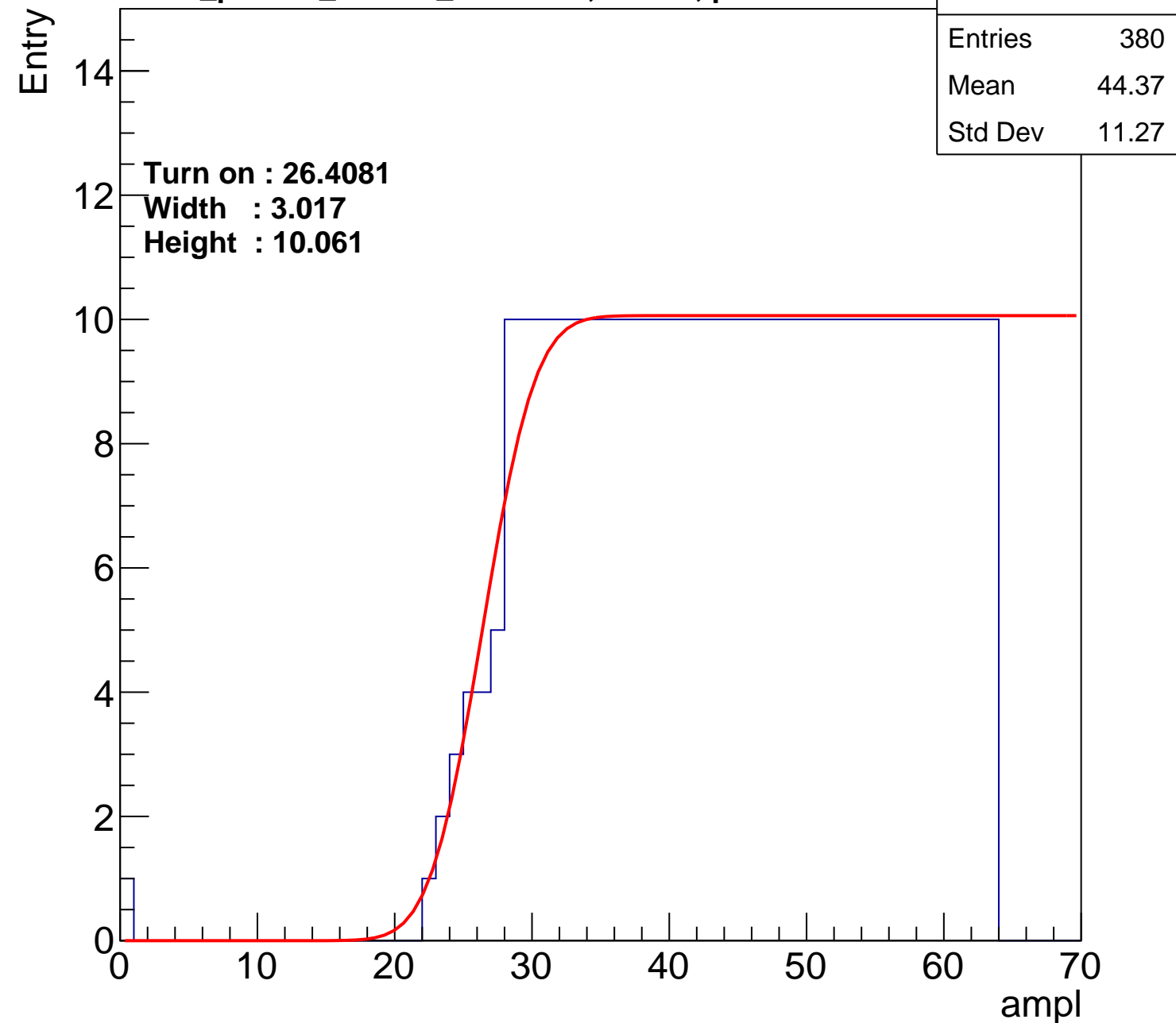
Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U9-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.52
Std Dev	11.18

Turn on : 26.0889

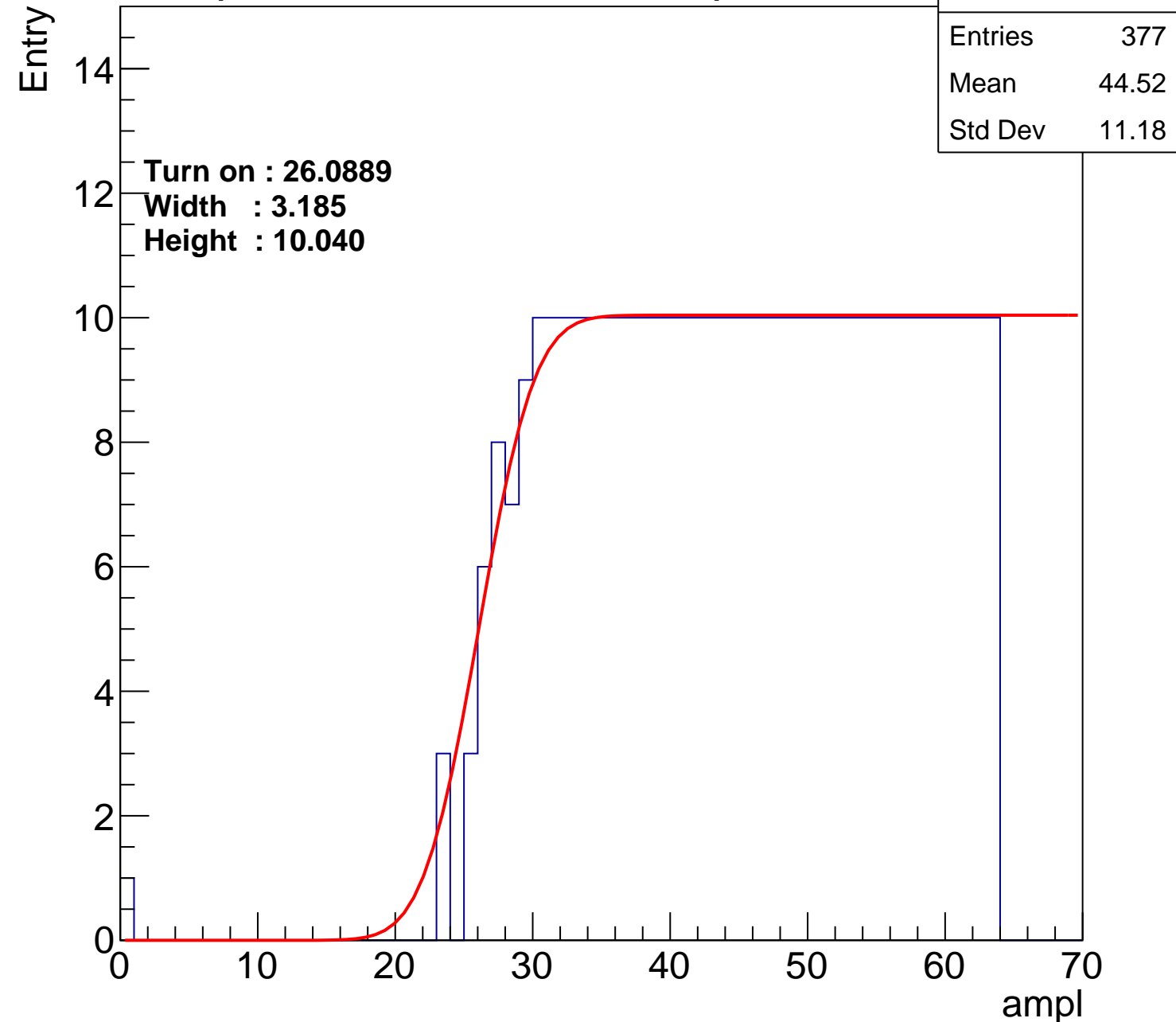
Width : 3.185

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.25
Std Dev	11.8

Turn on : 27.0385

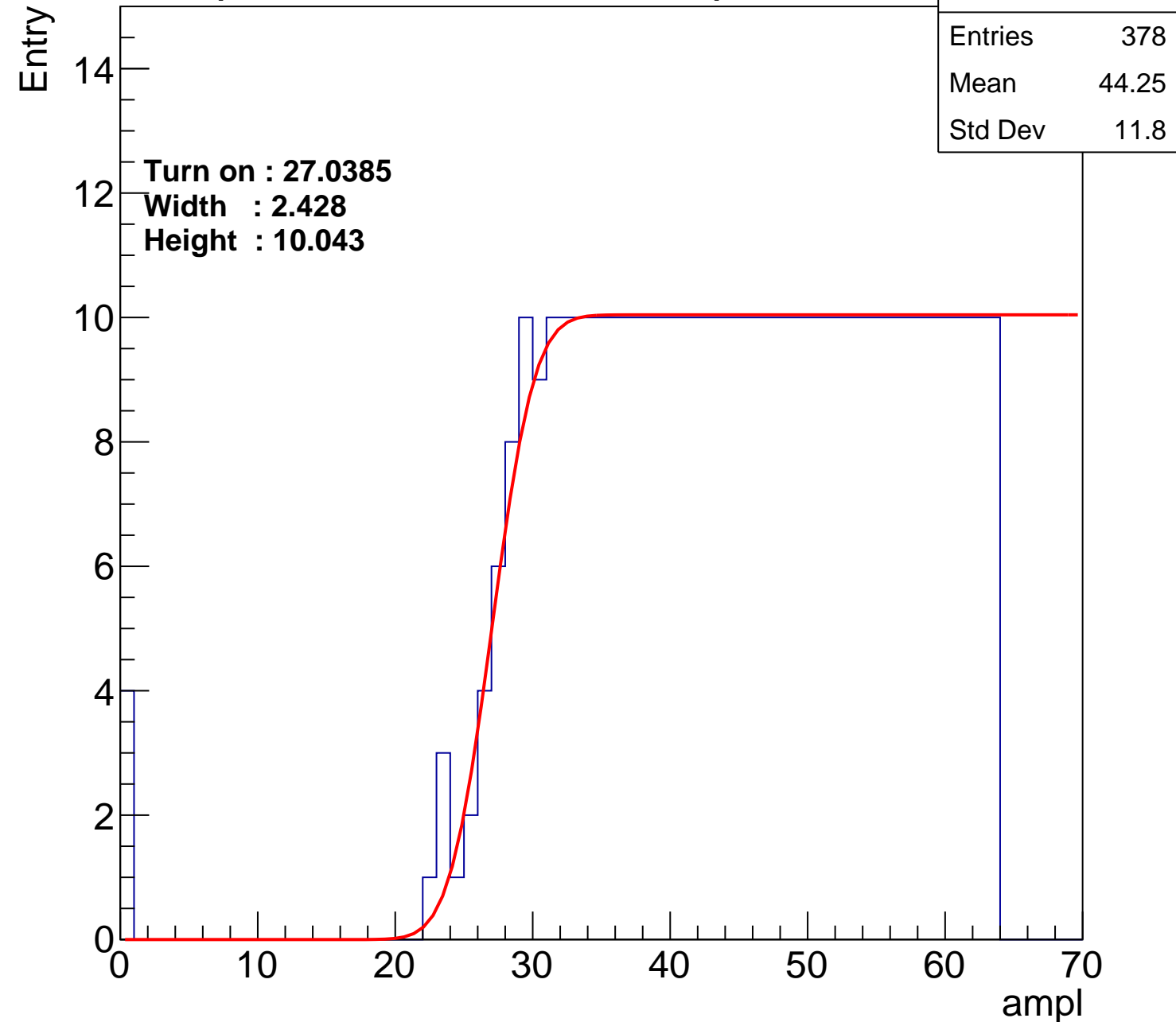
Width : 2.428

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.67

Turn on : 25.6127

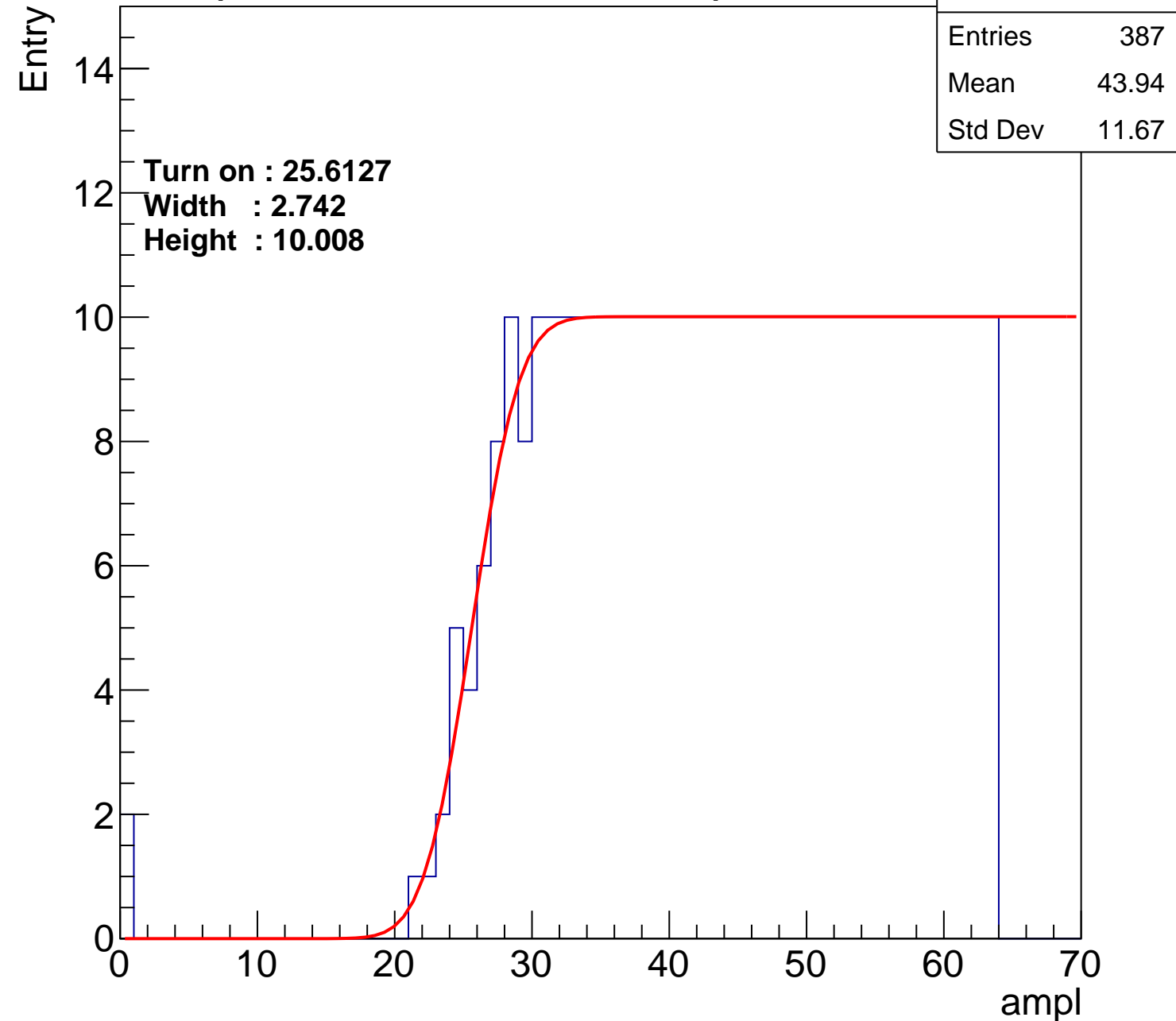
Width : 2.742

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch15

calib_packv5_042523_0143.root, FC#10, port B3

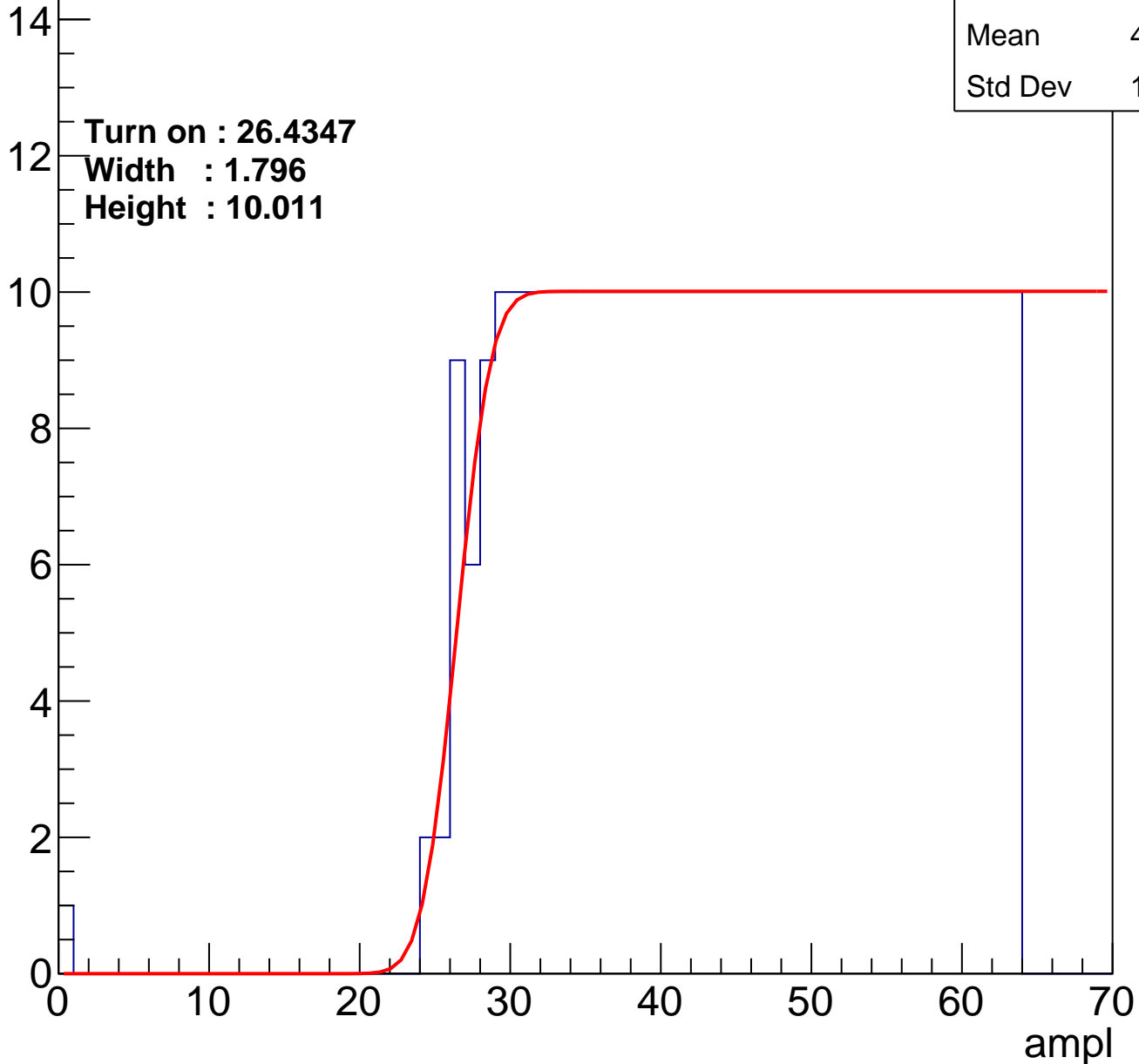
Entries	379
Mean	44.45
Std Dev	11.19

Turn on : 26.4347

Width : 1.796

Height : 10.011

Entry



B1L002S, U9-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 24.8945

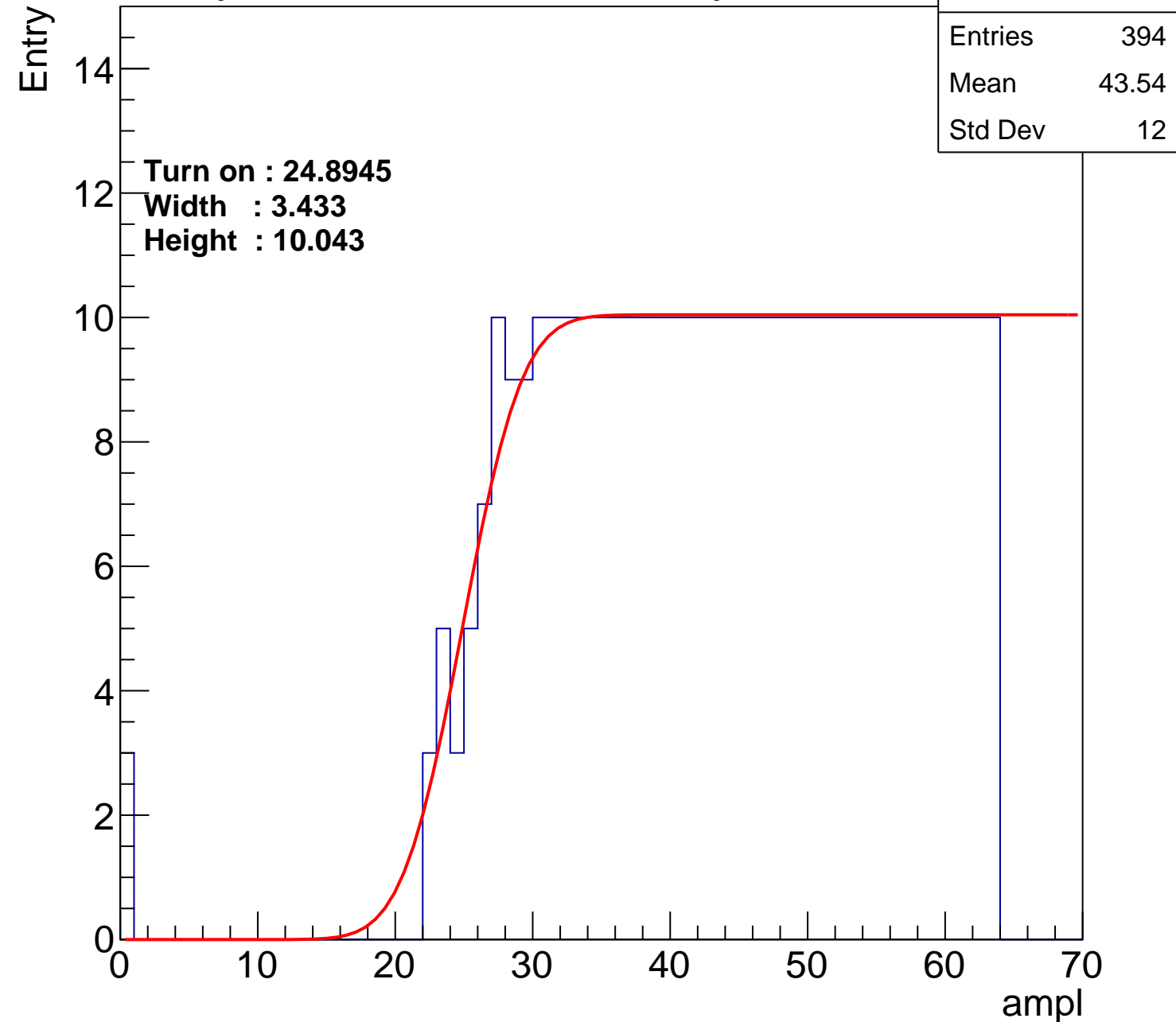
Width : 3.433

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.6604

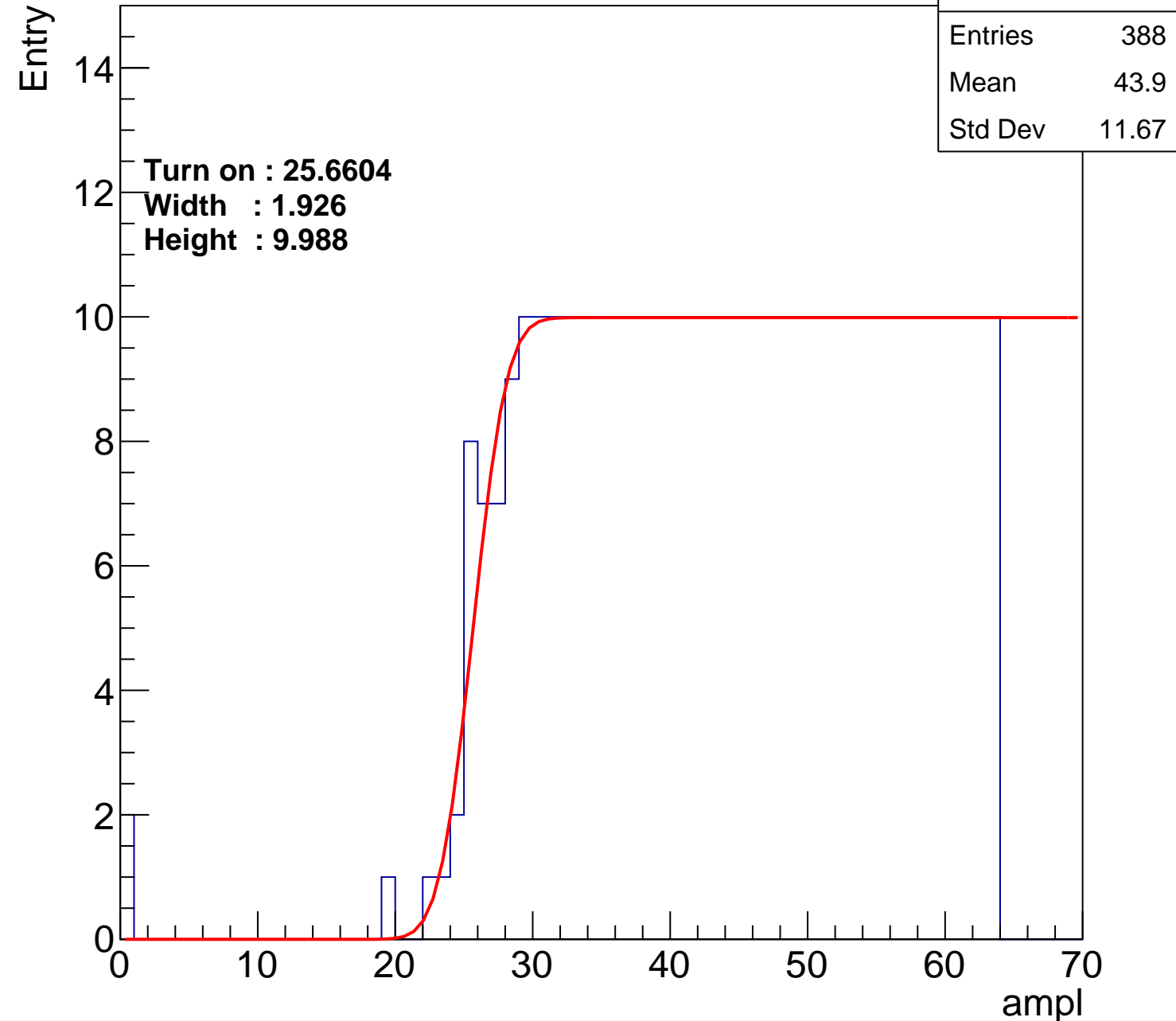
Width : 1.926

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.97
Std Dev	11.49

Turn on : 25.7021

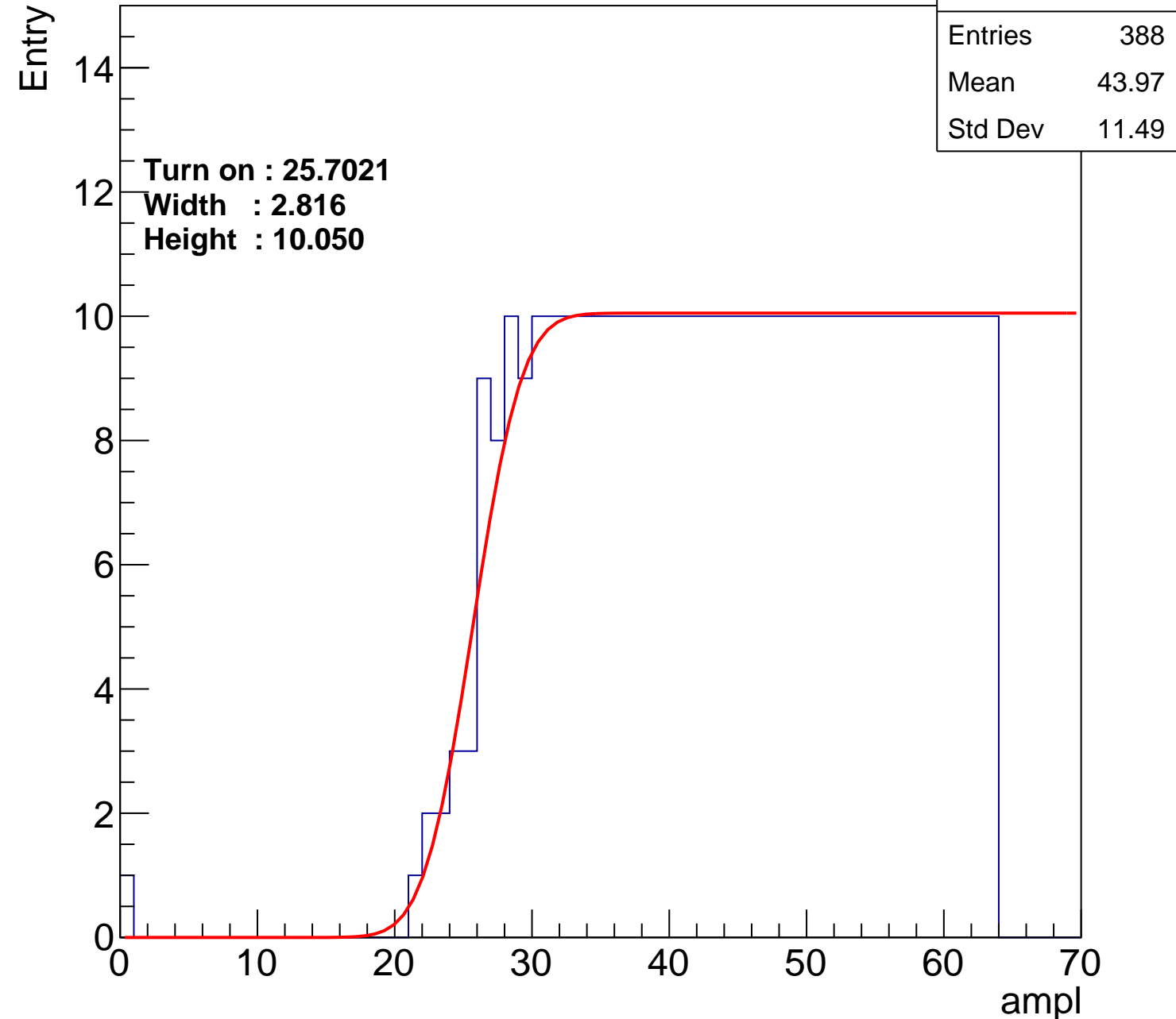
Width : 2.816

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch19

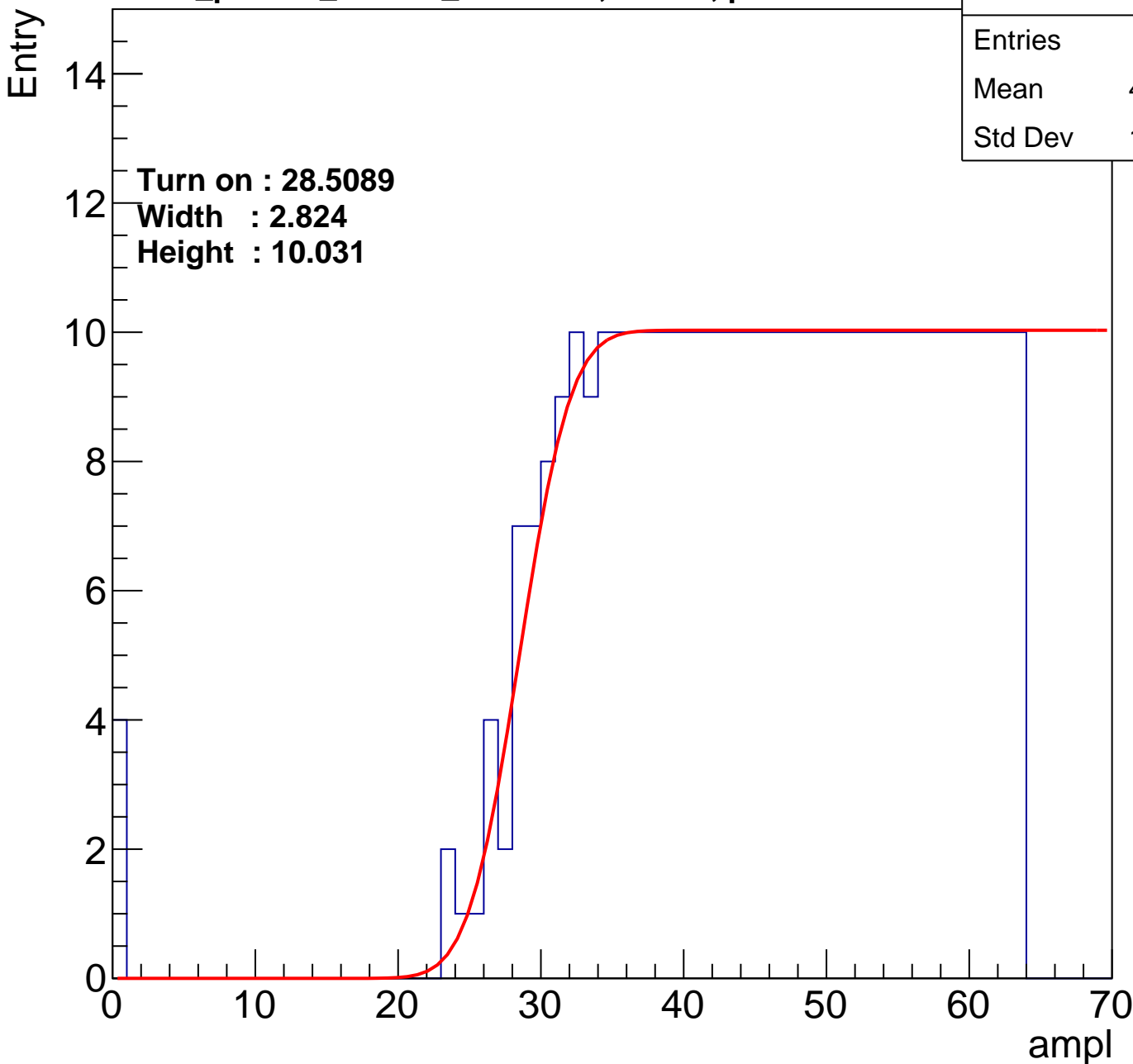
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 28.5089

Width : 2.824

Height : 10.031

Entries	364
Mean	44.88
Std Dev	11.54



B1L002S, U9-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	45
Std Dev	10.94

Turn on : 27.5628

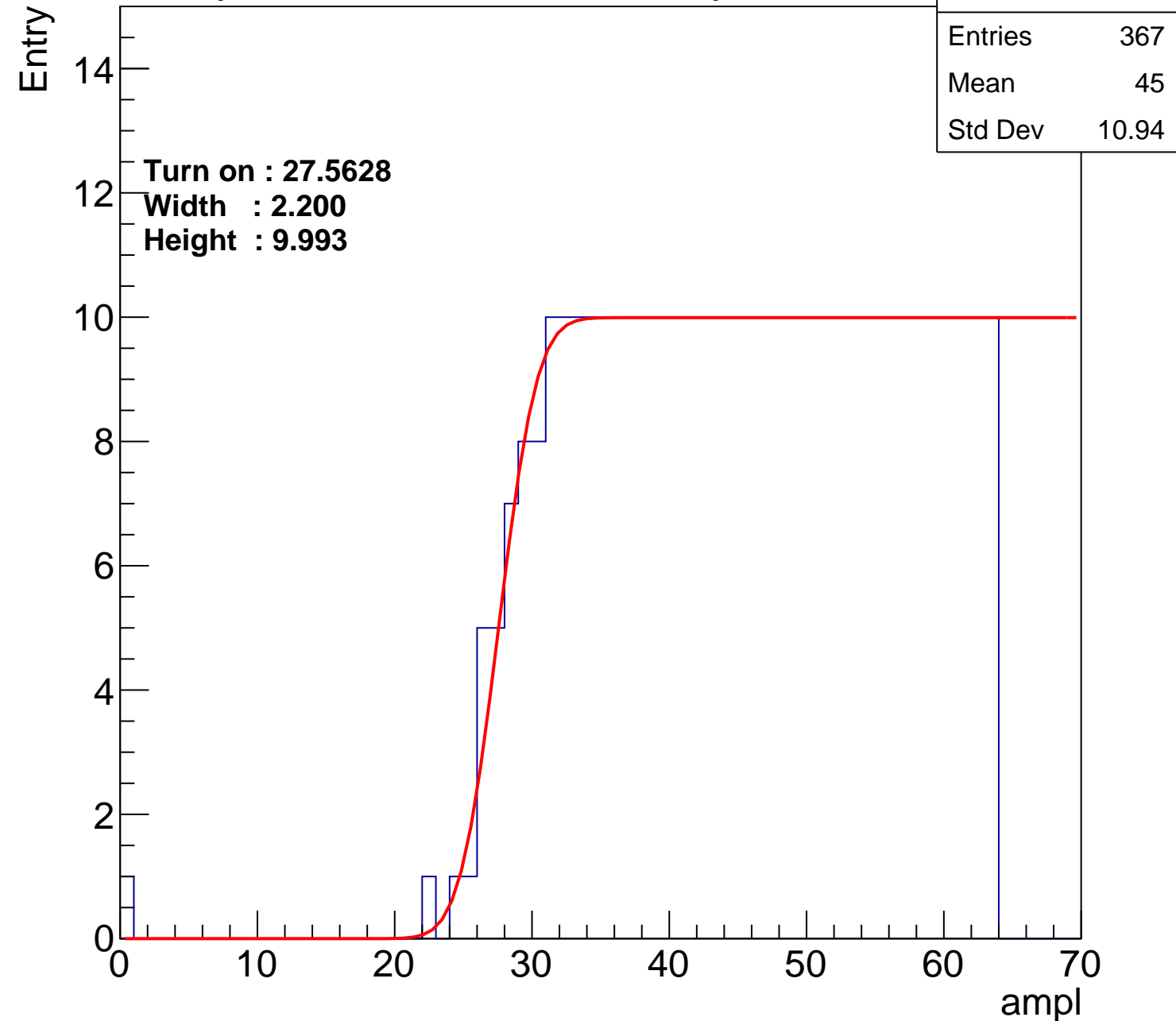
Width : 2.200

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.1974

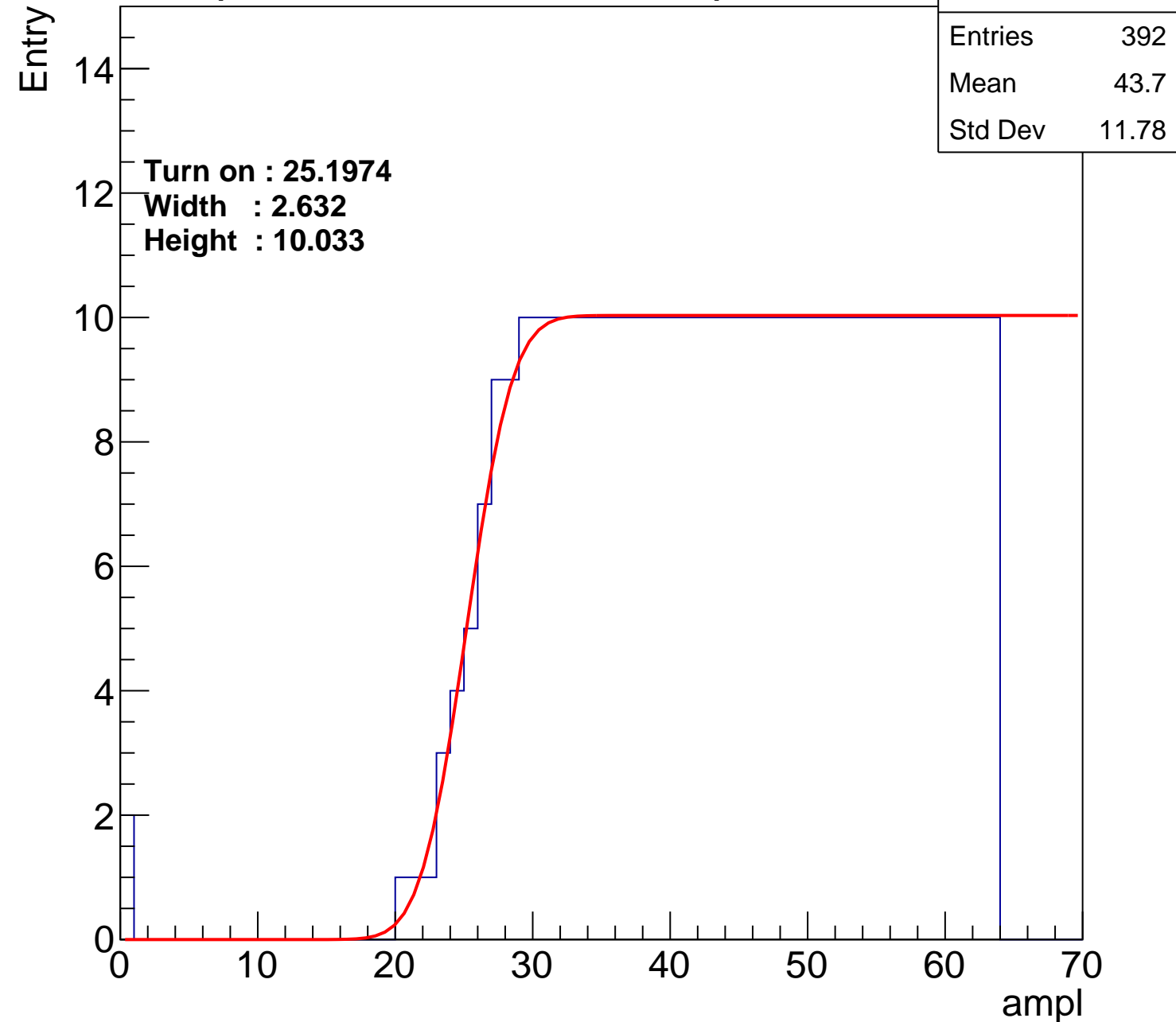
Width : 2.632

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.78
Std Dev	12.36

Turn on : 23.3077

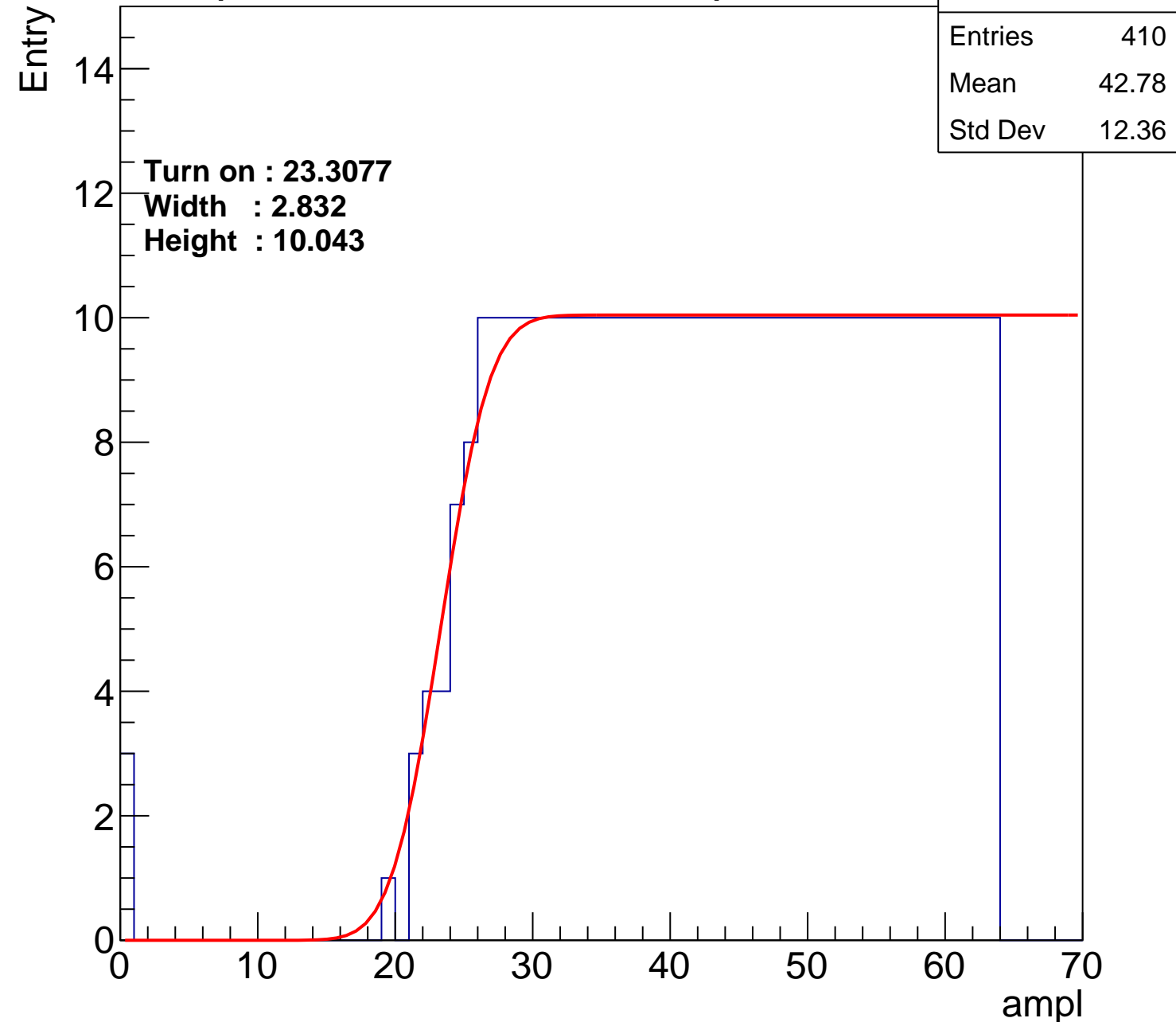
Width : 2.832

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 25.6570

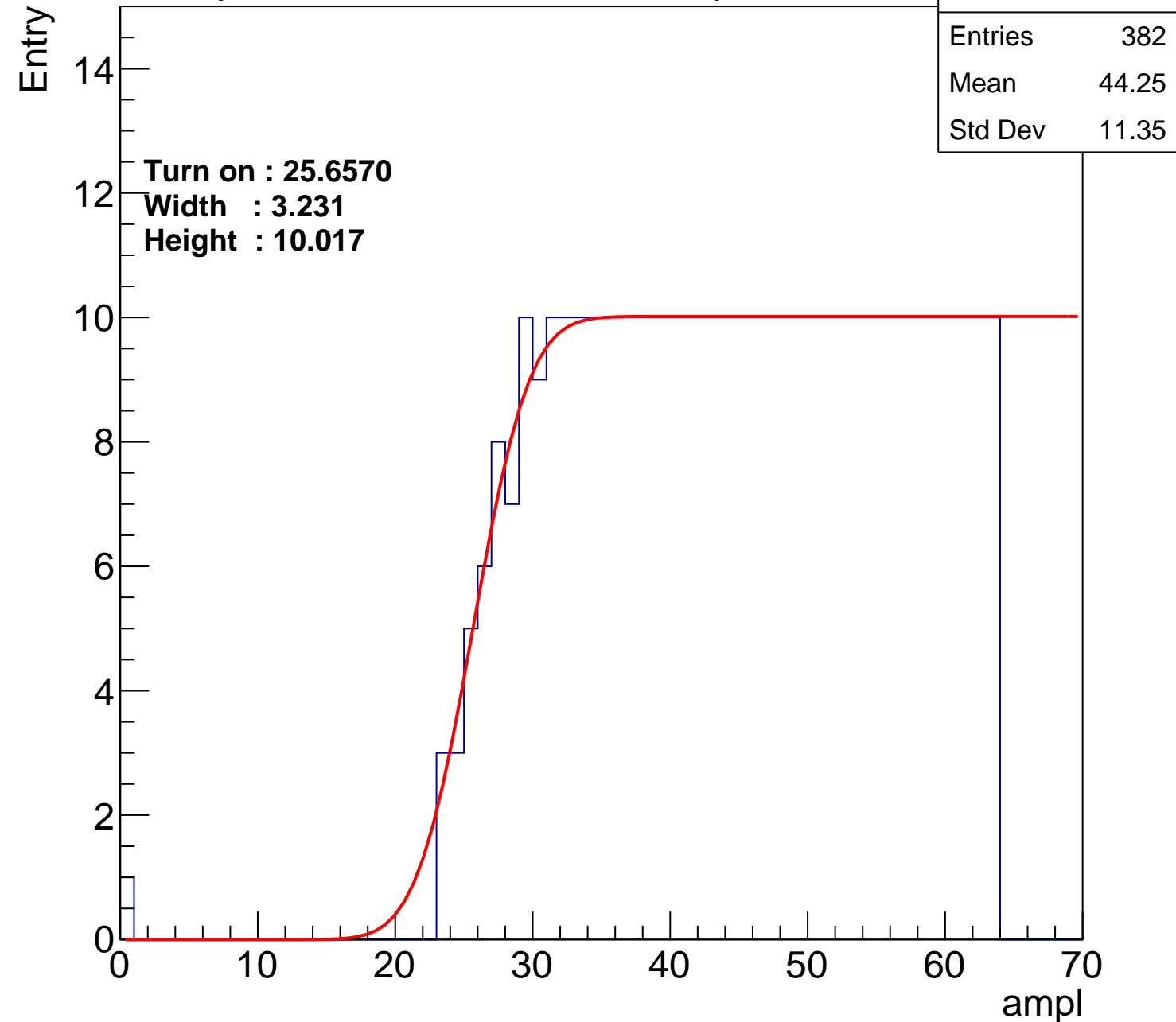
Width : 3.231

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.2513

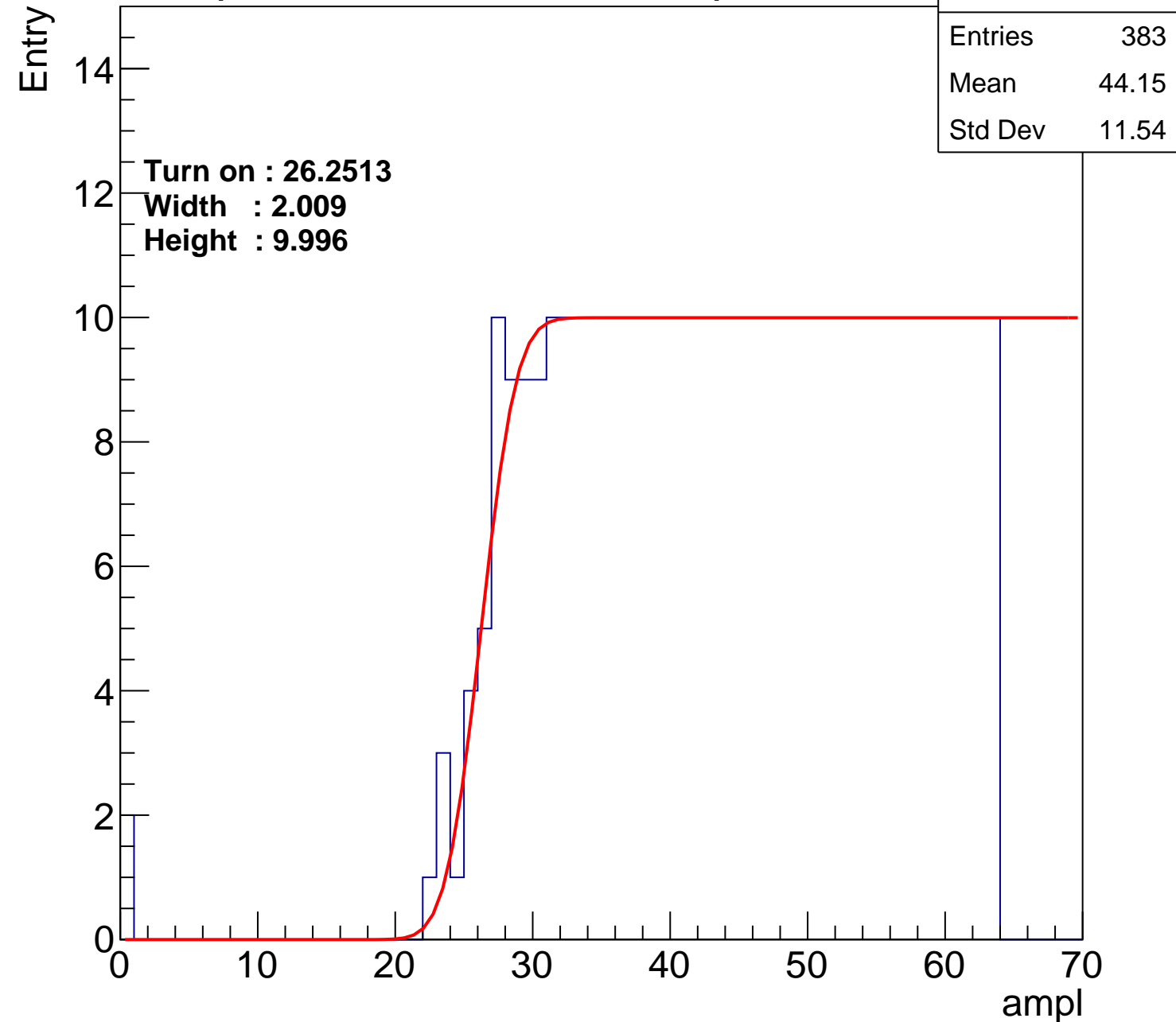
Width : 2.009

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.62
Std Dev	11.12

Turn on : 27.1237

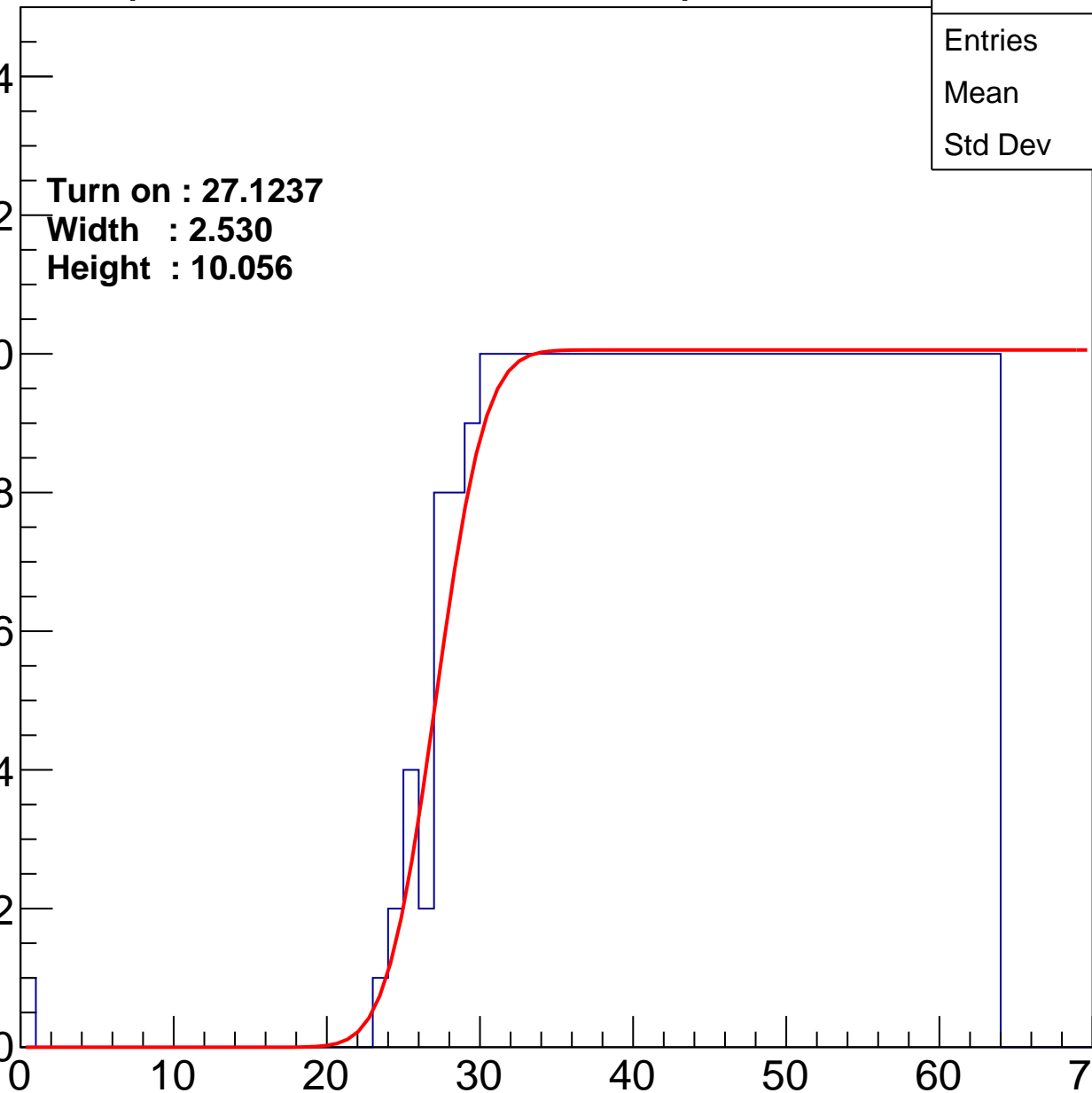
Width : 2.530

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.8792

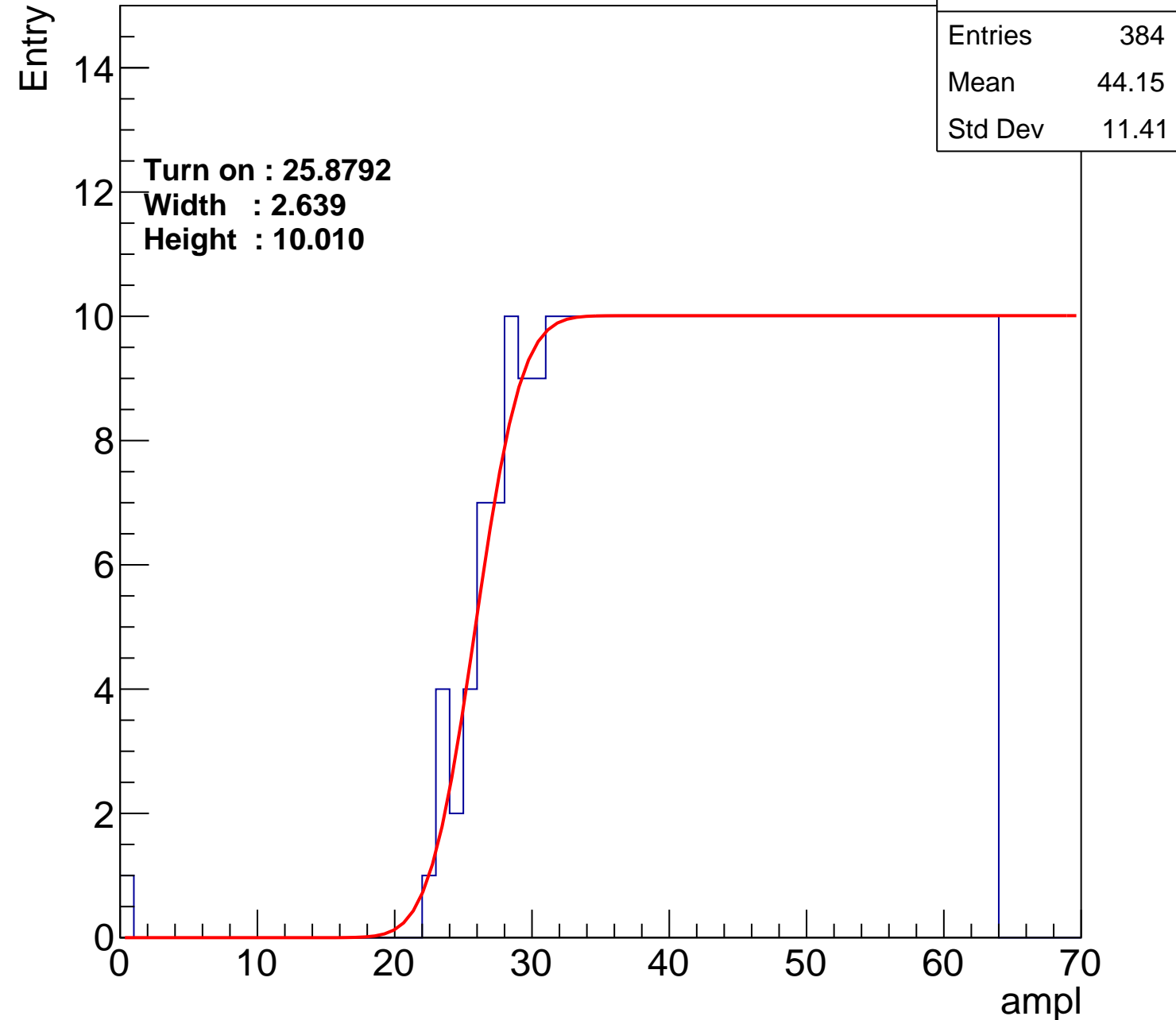
Width : 2.639

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.39
Std Dev	12.07

Turn on : 24.5362

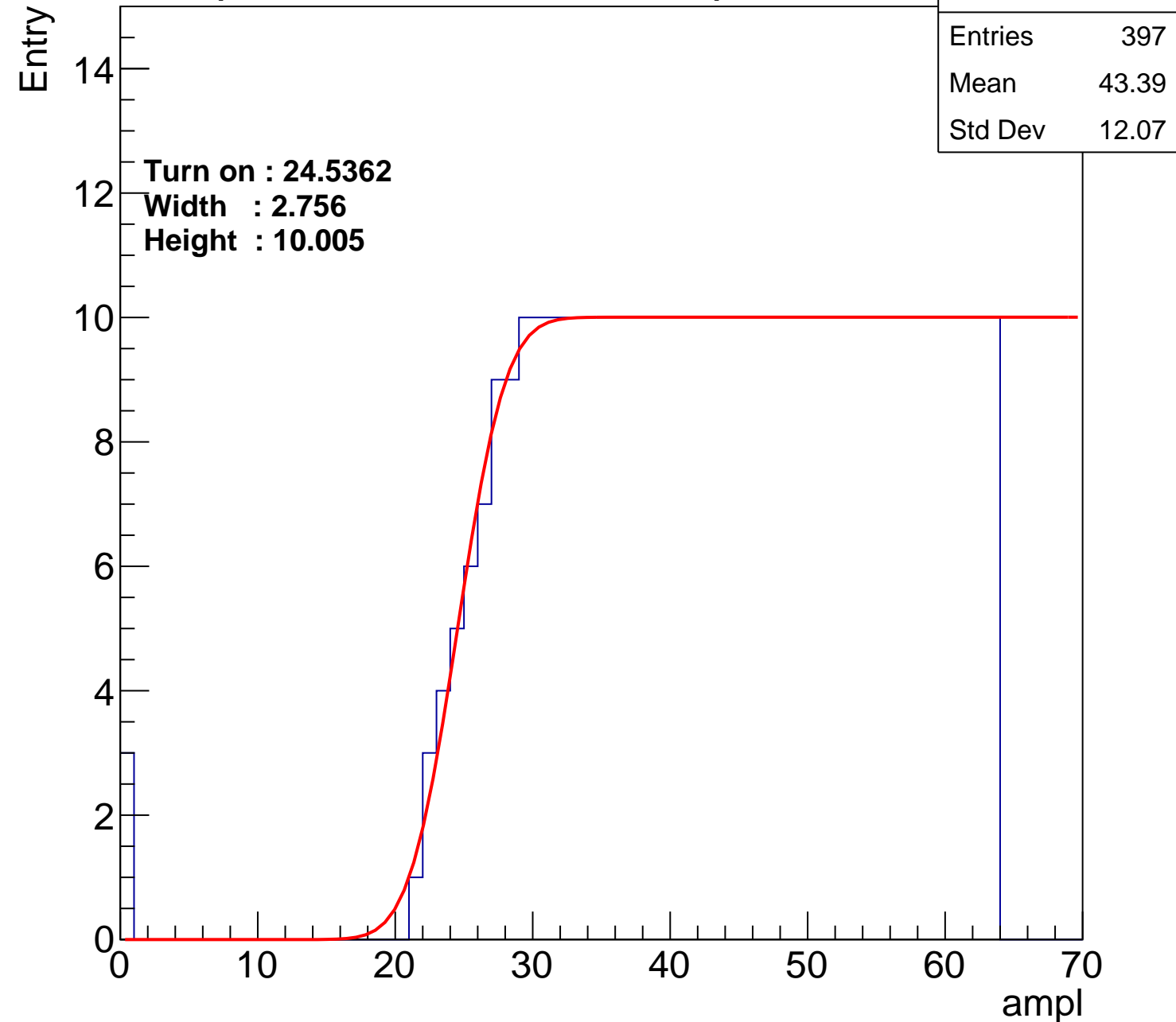
Width : 2.756

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.38
Std Dev	11.25

Turn on : 25.8593

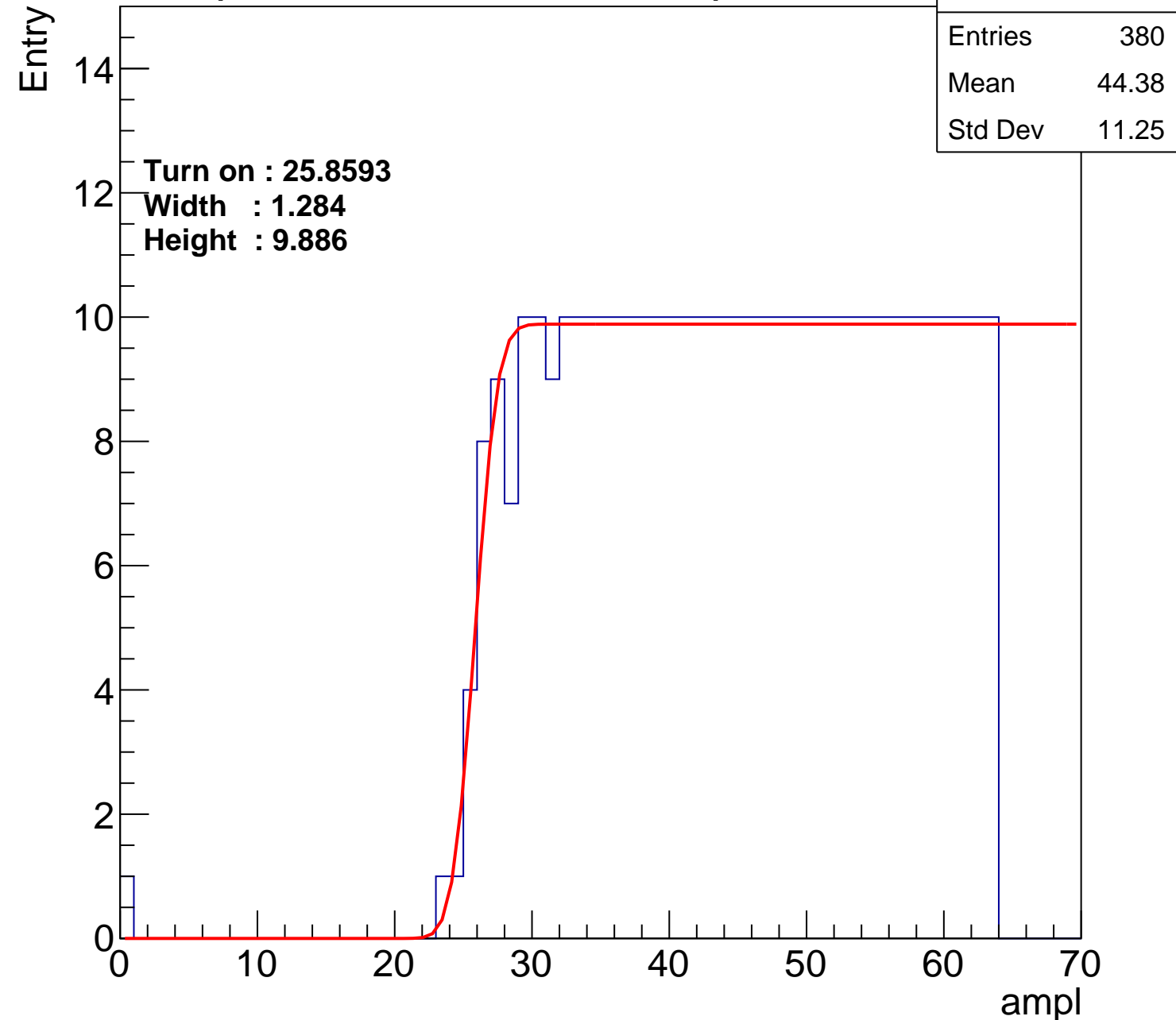
Width : 1.284

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.32
Std Dev	11.29

Turn on : 25.9417

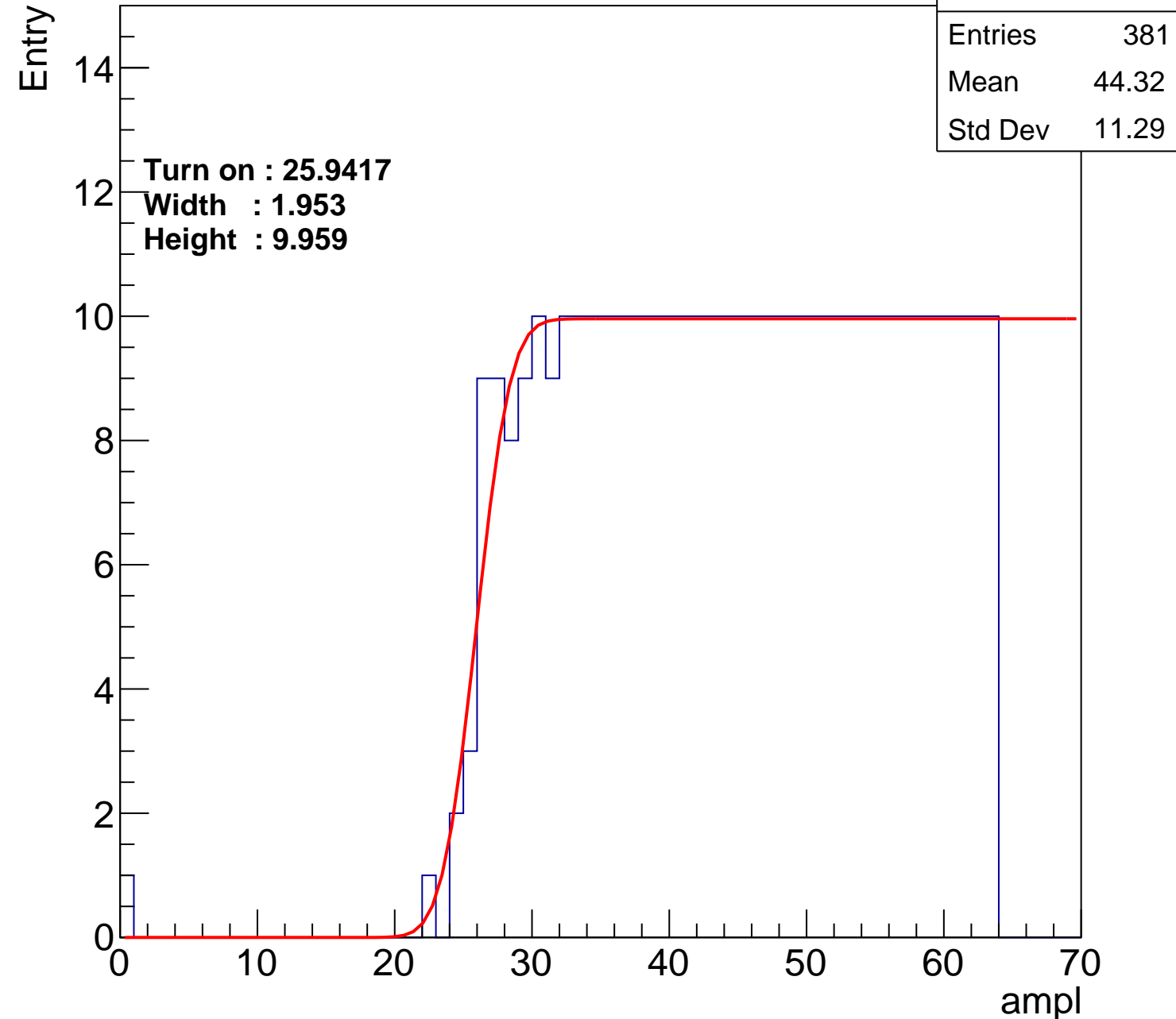
Width : 1.953

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.44
Std Dev	11.77

Turn on : 24.1012

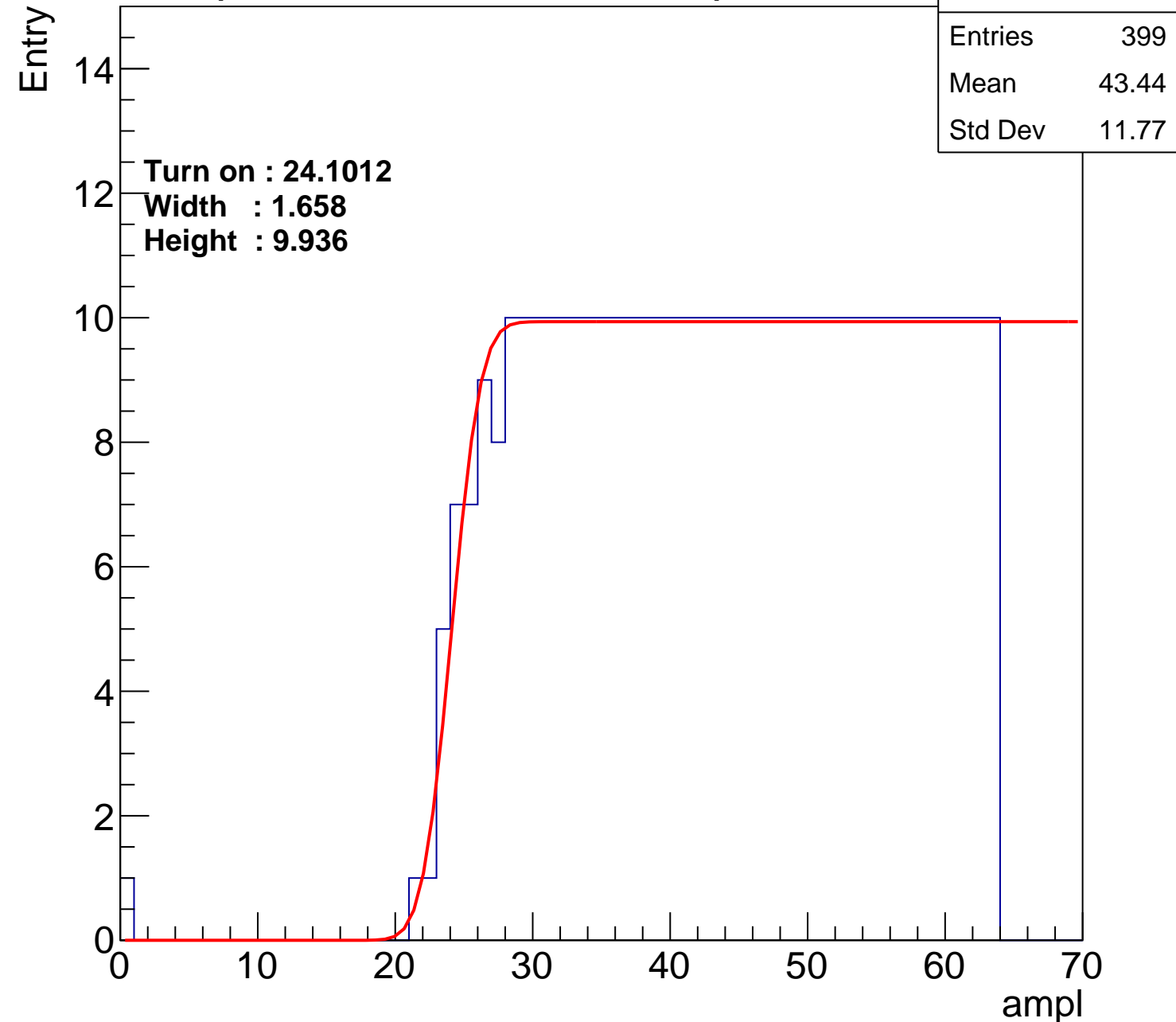
Width : 1.658

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.5404

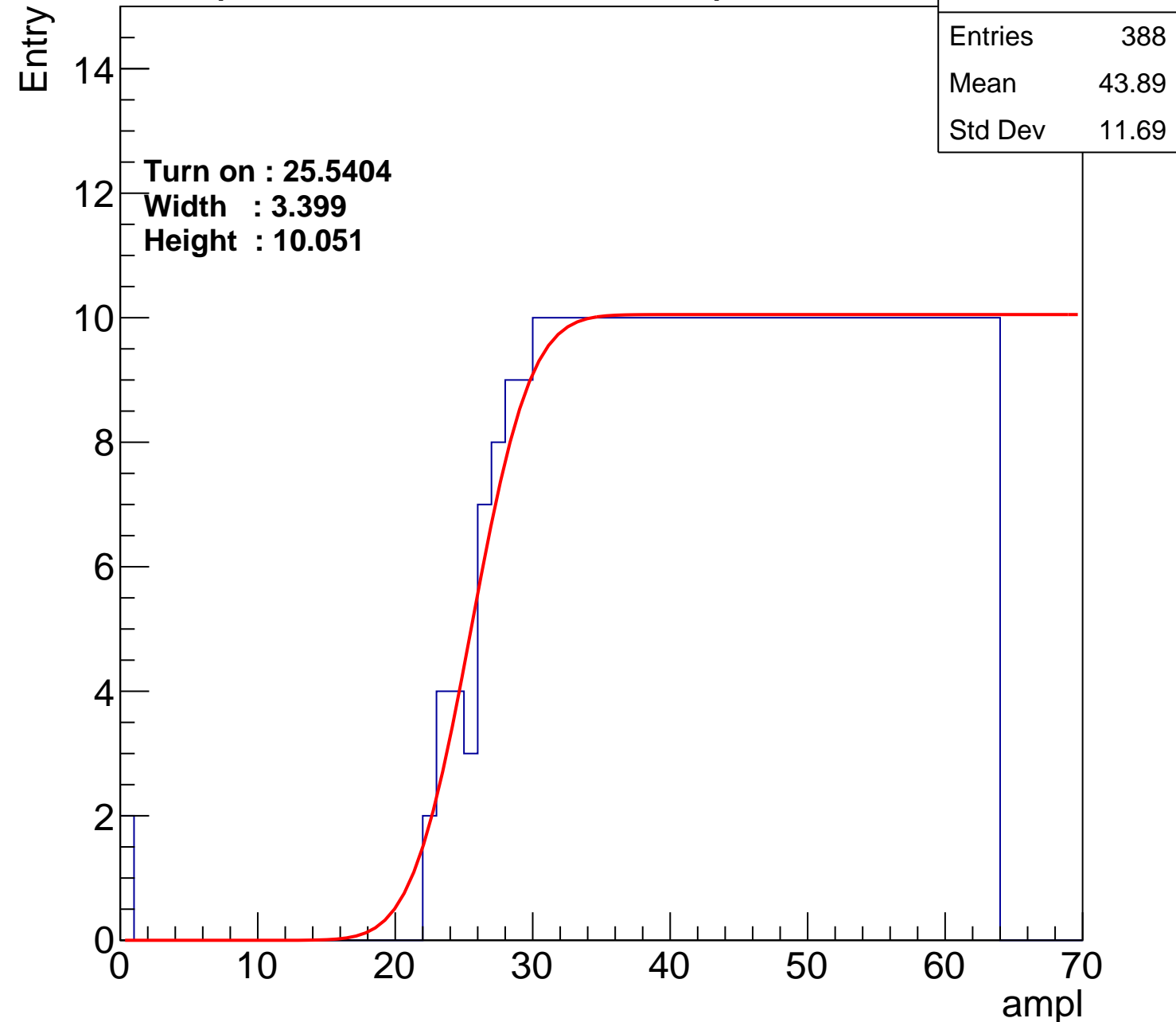
Width : 3.399

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.43
Std Dev	11.22

Turn on : 26.3895

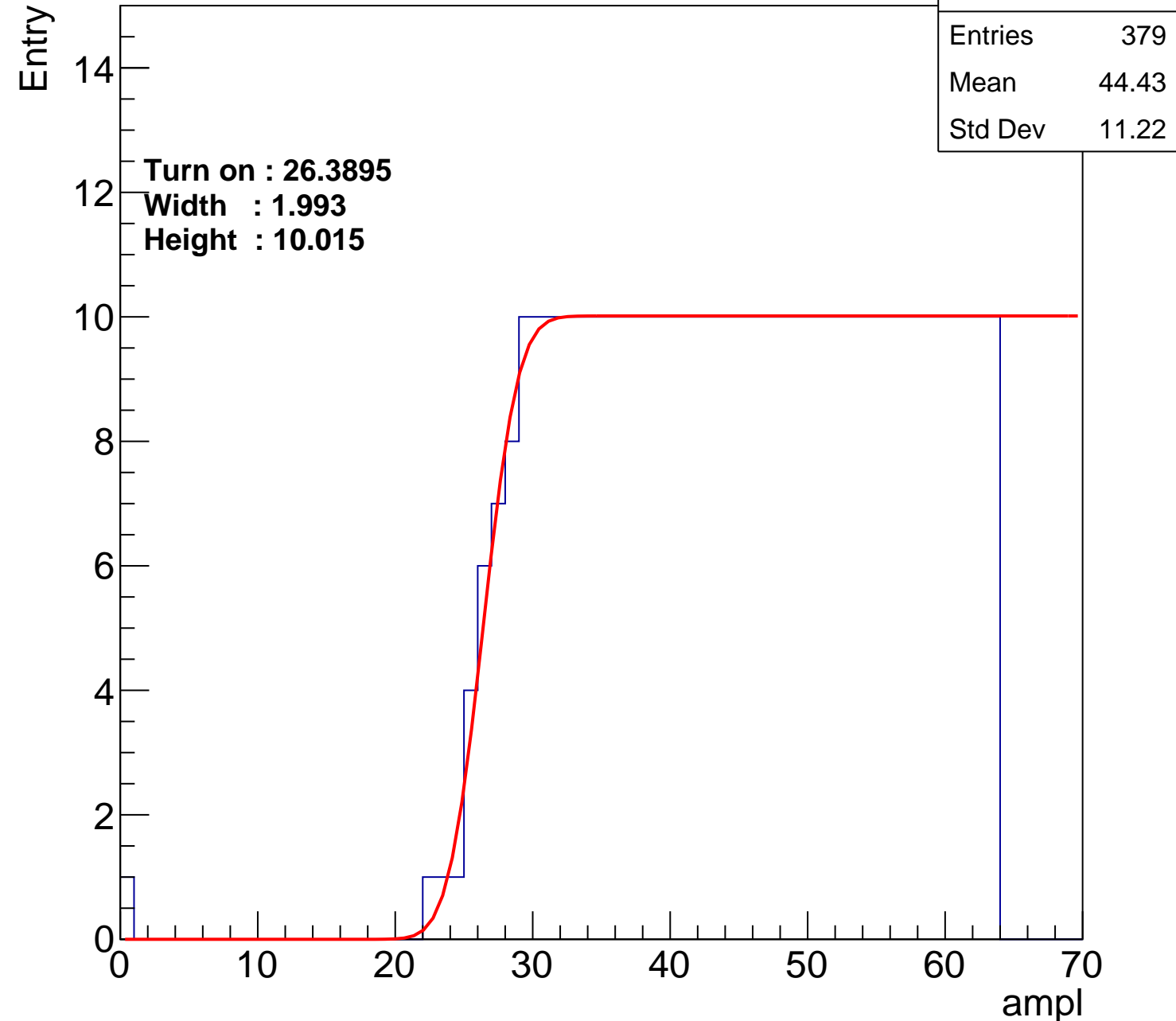
Width : 1.993

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch33

calib_packv5_042523_0143.root, FC#10, port B3

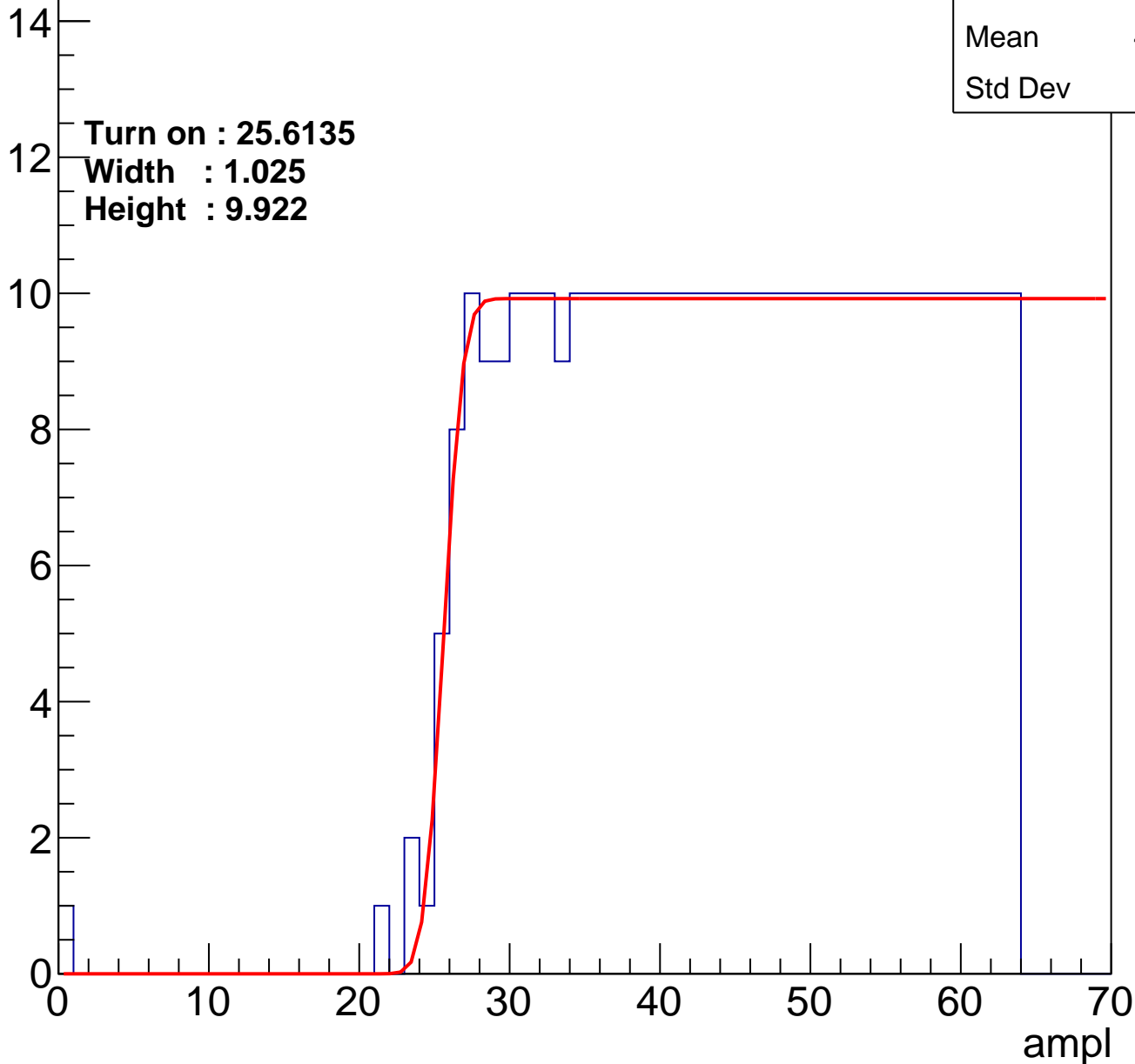
Entry

Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.6135

Width : 1.025

Height : 9.922



B1L002S, U9-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.03
Std Dev	12.38

Turn on : 24.3601

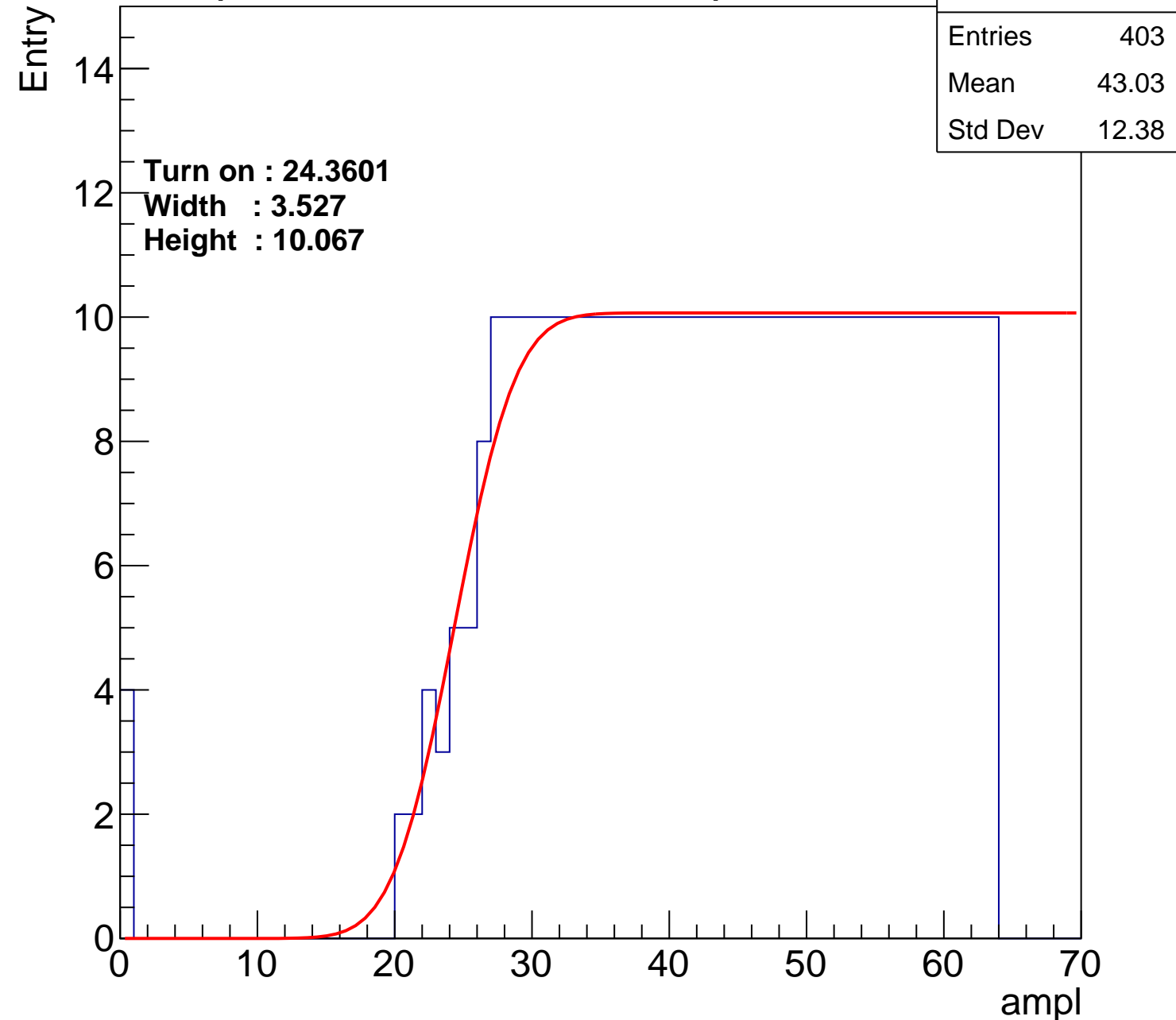
Width : 3.527

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.1004

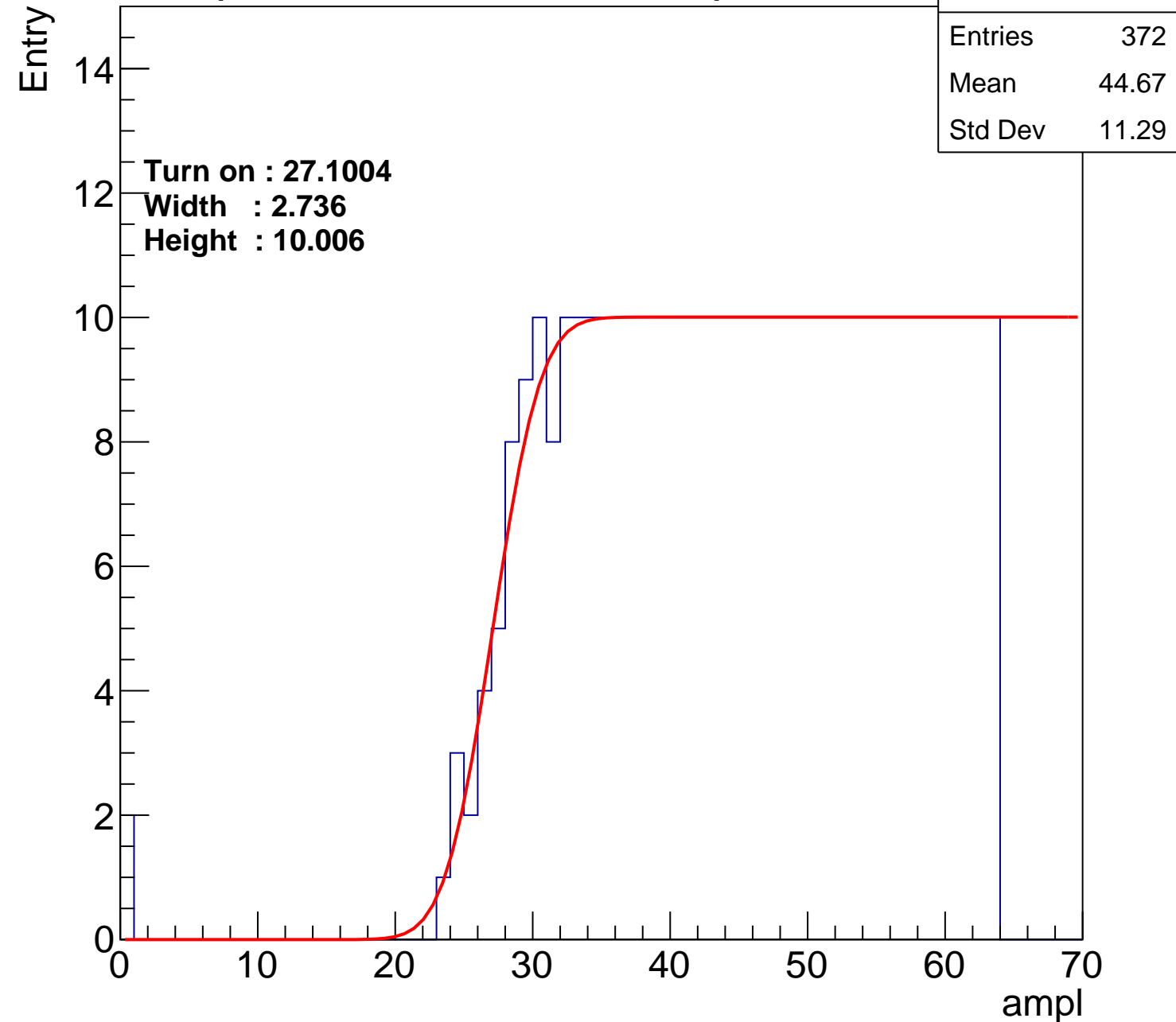
Width : 2.736

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.57
Std Dev	11.95

Turn on : 24.8298

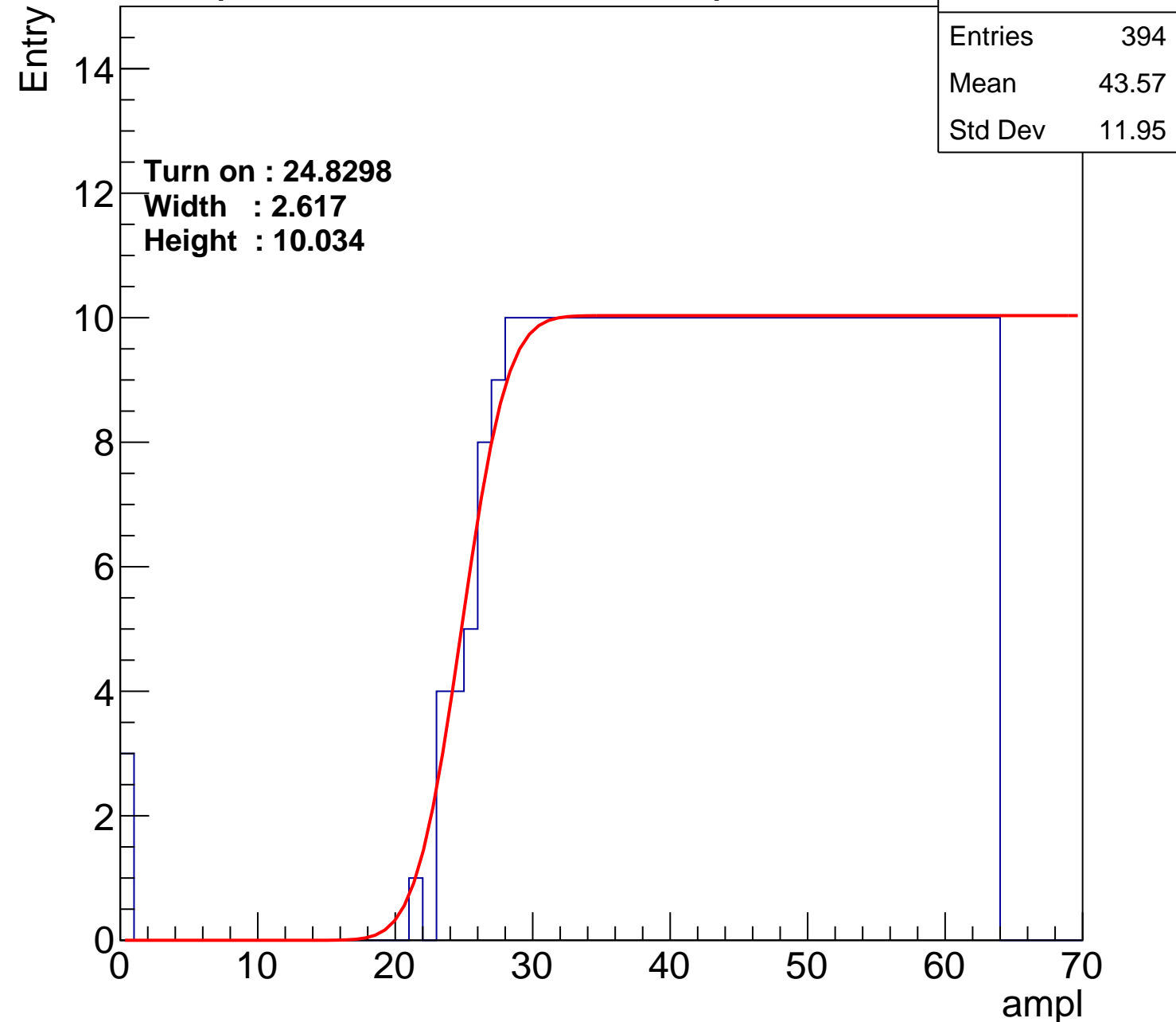
Width : 2.617

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.7774

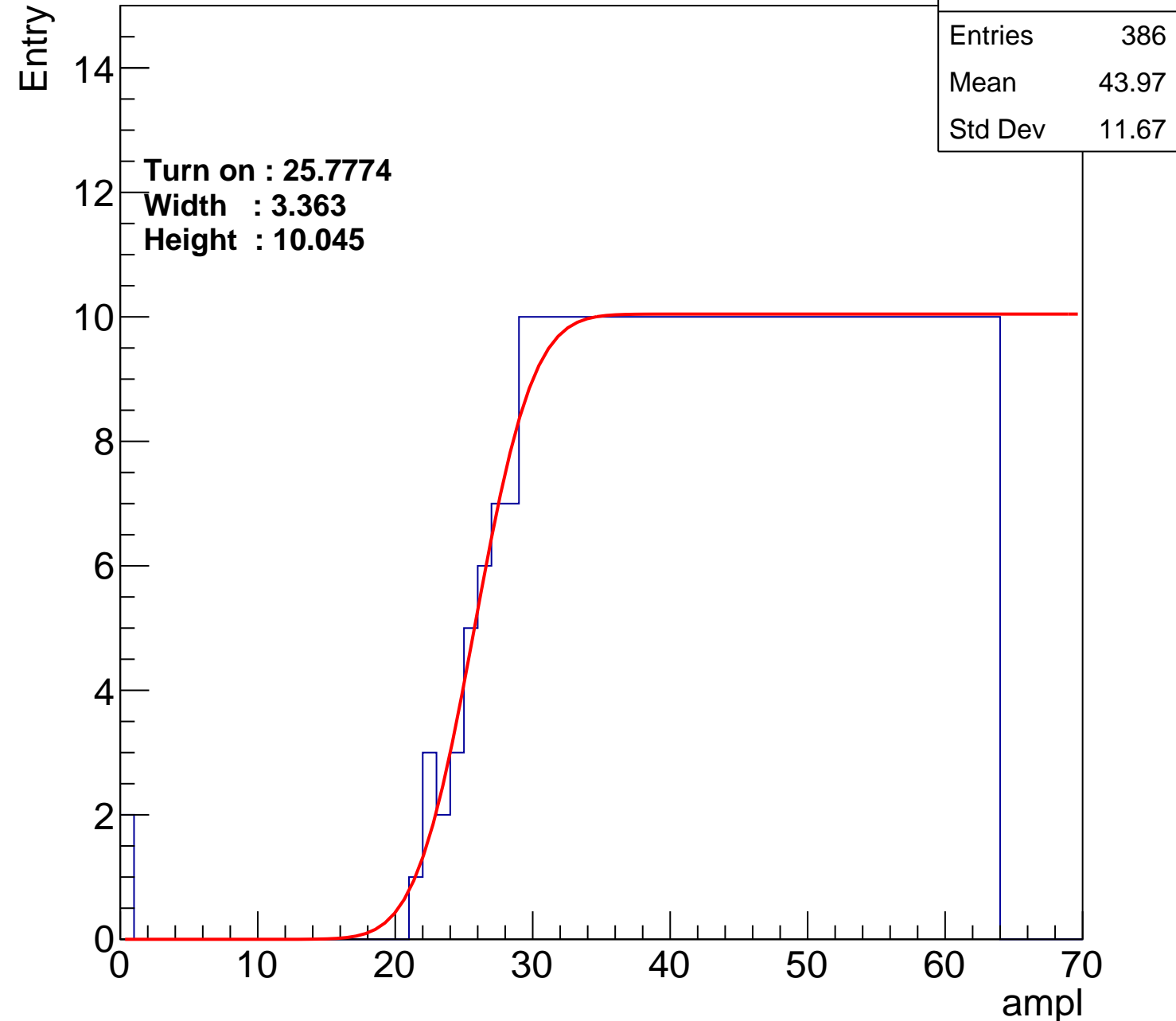
Width : 3.363

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 25.8768

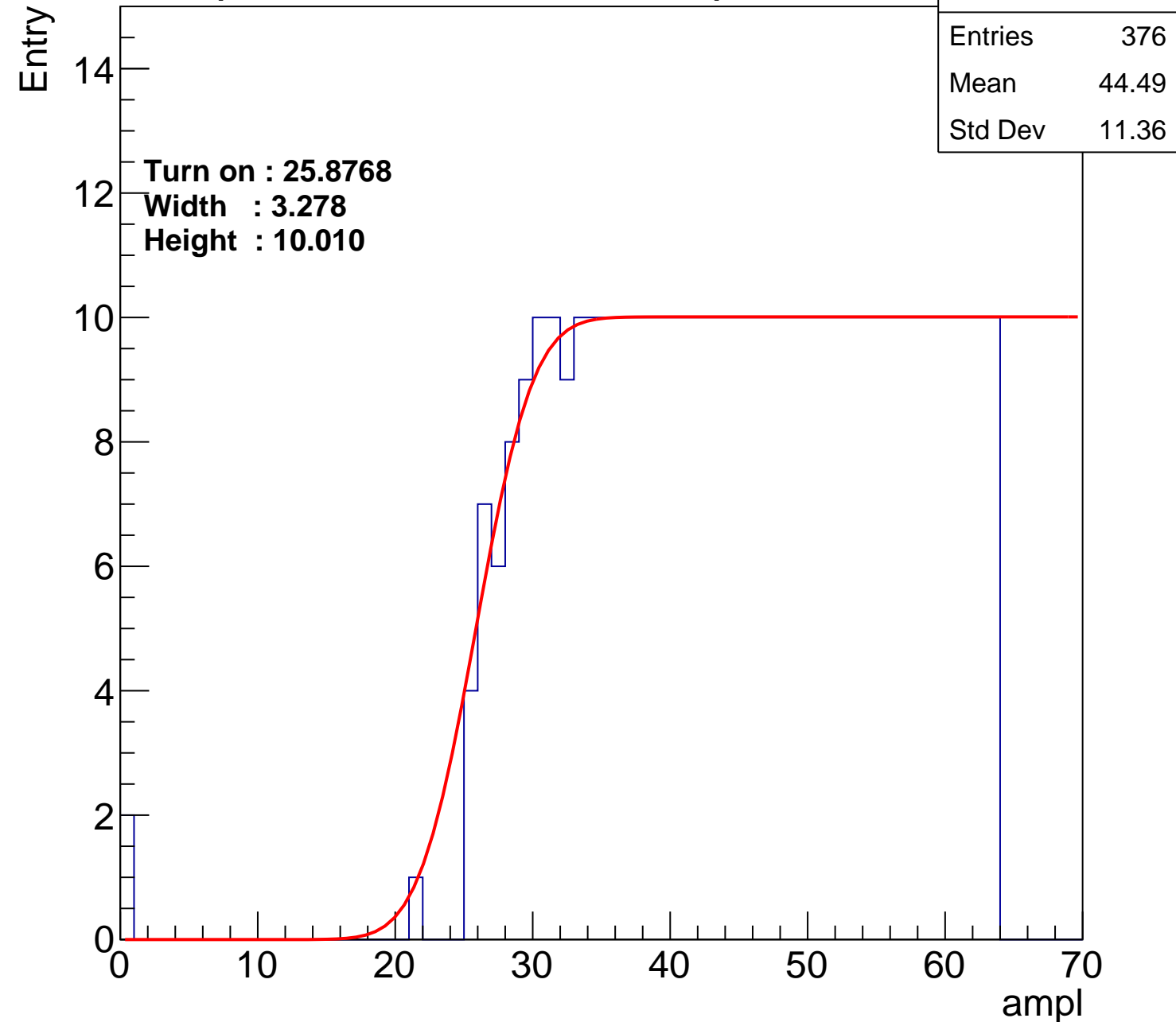
Width : 3.278

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.23
Std Dev	11.5

Turn on : 26.2675

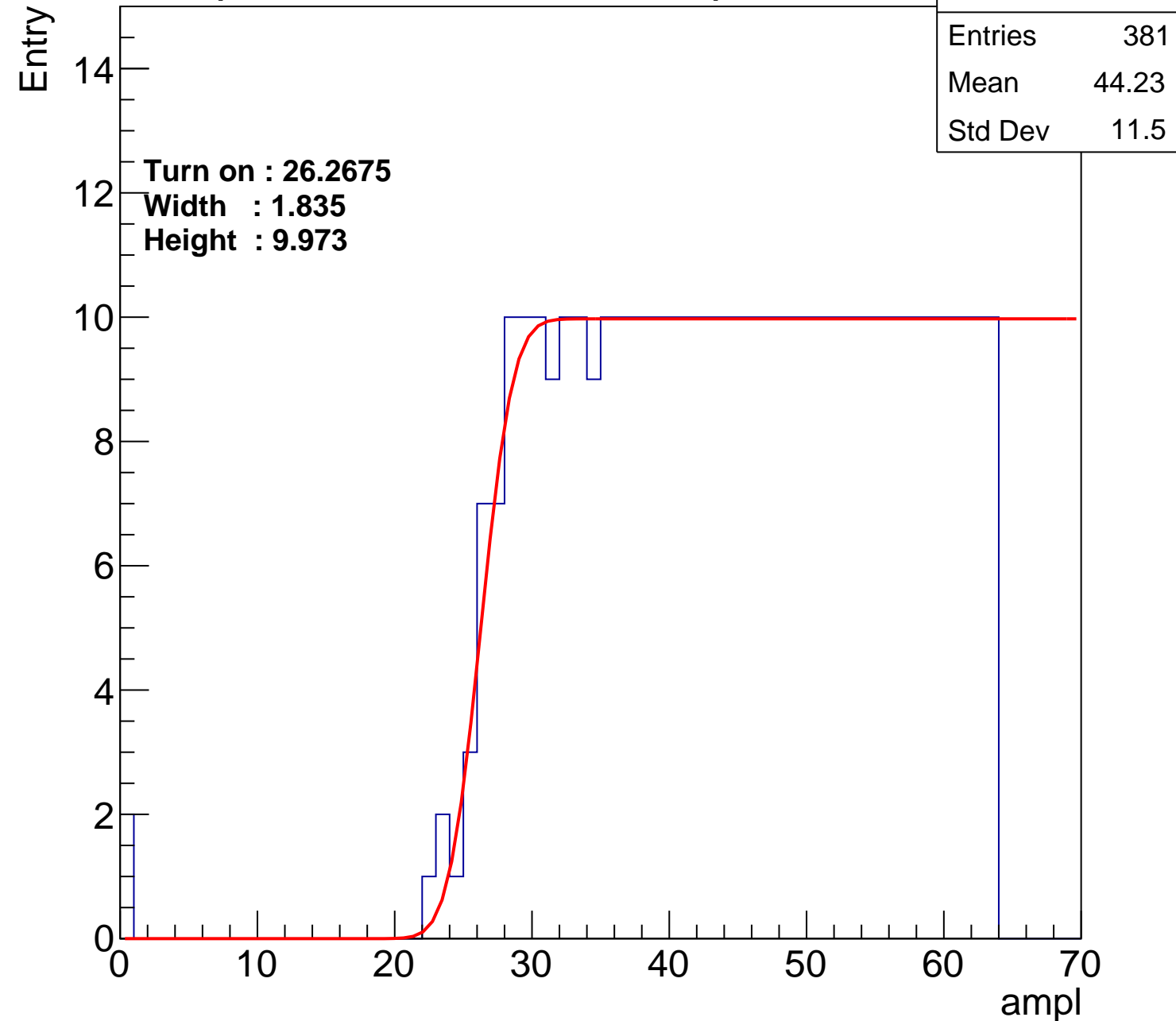
Width : 1.835

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch40

calib_packv5_042523_0143.root, FC#10, port B3

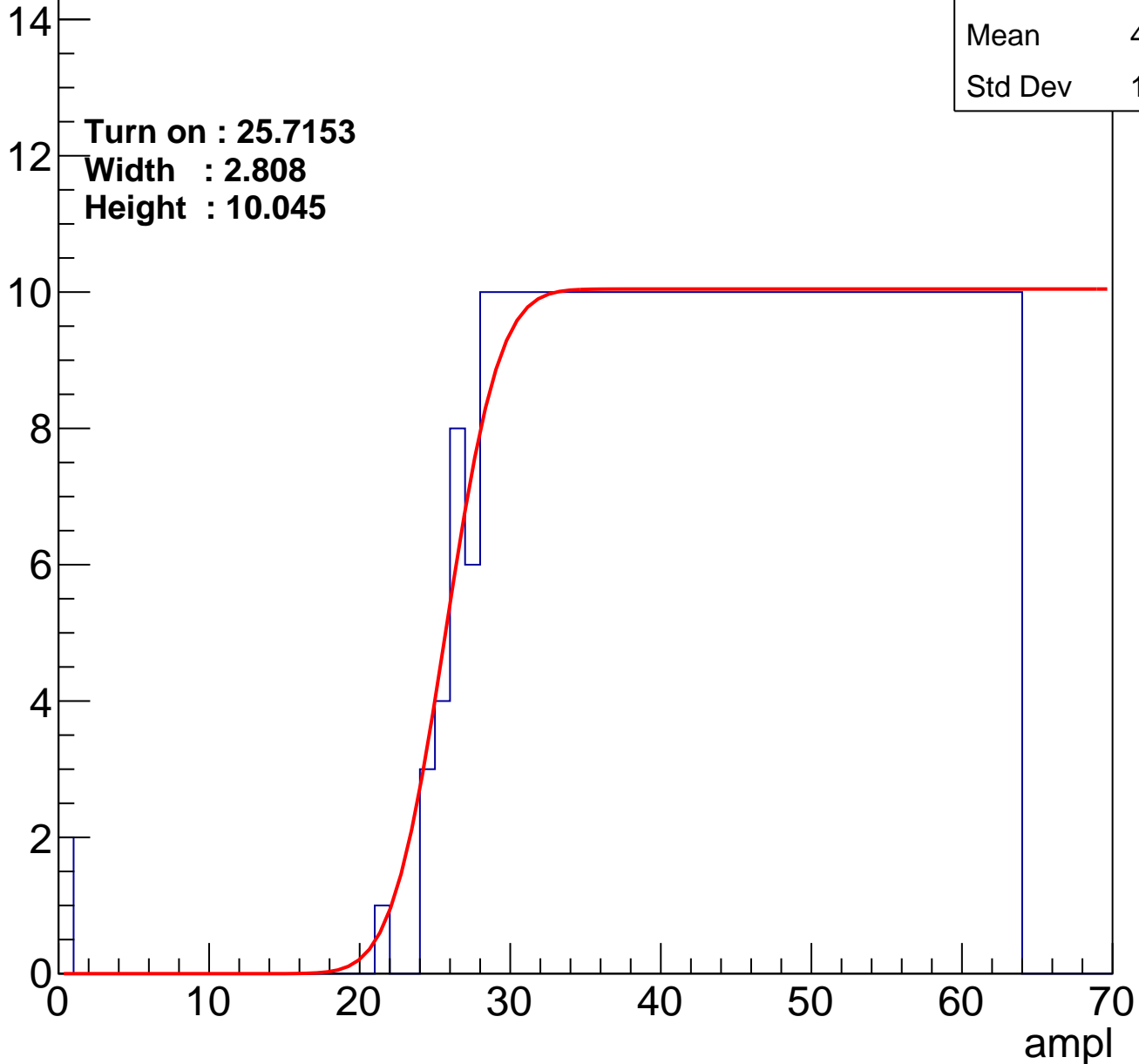
Entries	384
Mean	44.12
Std Dev	11.53

Turn on : 25.7153

Width : 2.808

Height : 10.045

Entry



B1L002S, U9-ch41

calib_packv5_042523_0143.root, FC#10, port B3

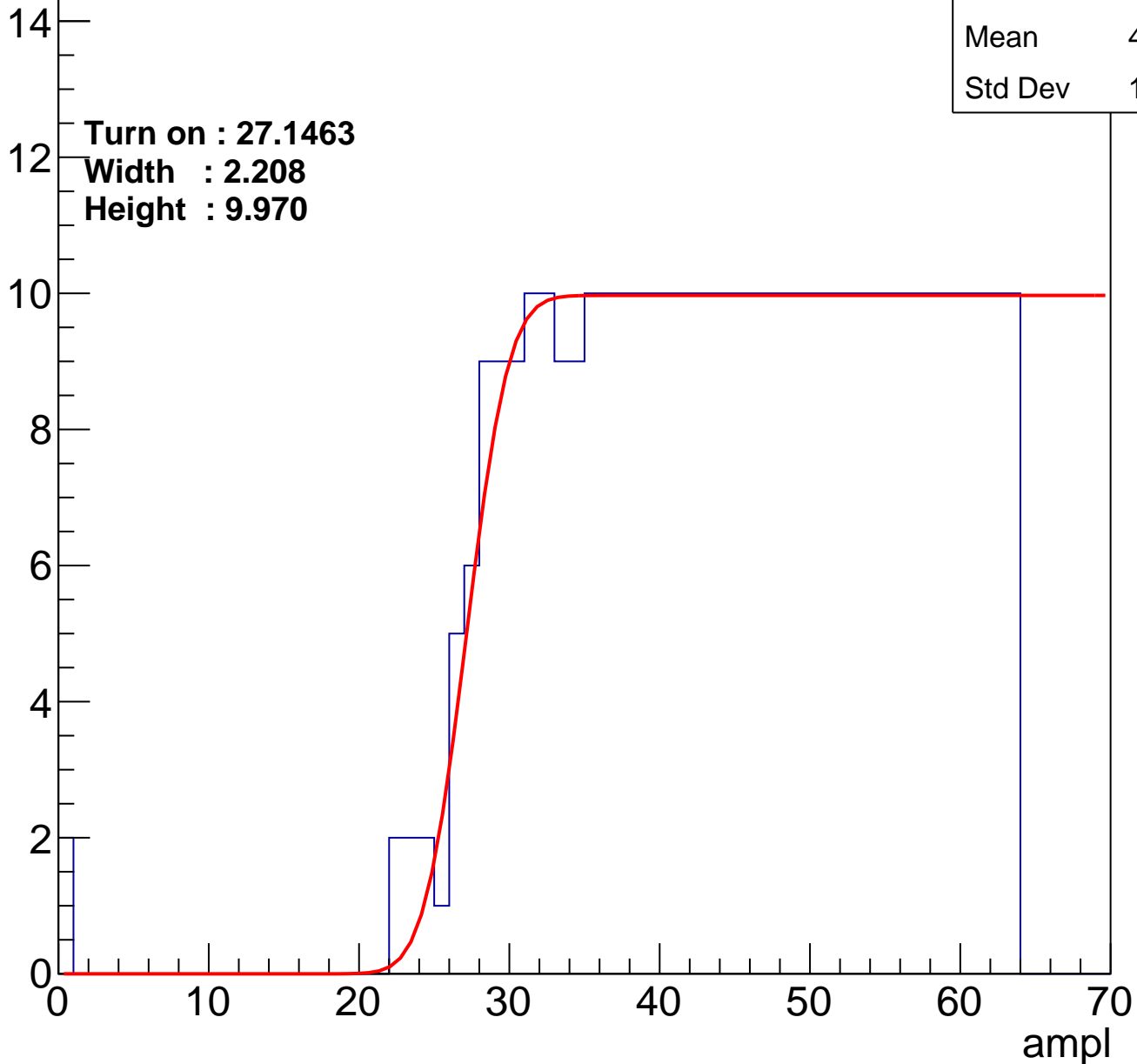
Entries	375
Mean	44.48
Std Dev	11.42

Turn on : 27.1463

Width : 2.208

Height : 9.970

Entry



B1L002S, U9-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	417
Mean	42.4
Std Dev	12.59

Turn on : 23.4098

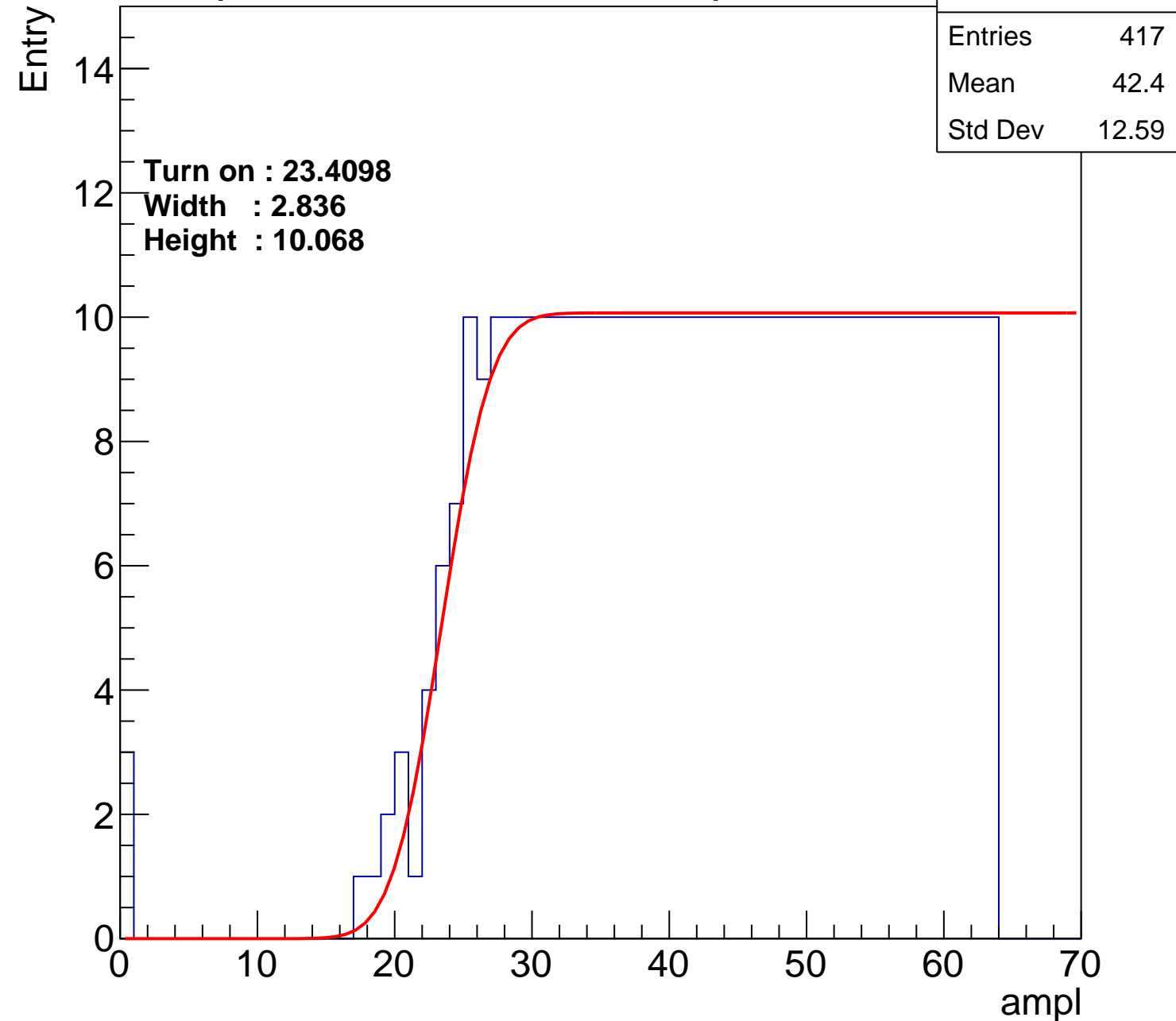
Width : 2.836

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.84

Turn on : 24.3632

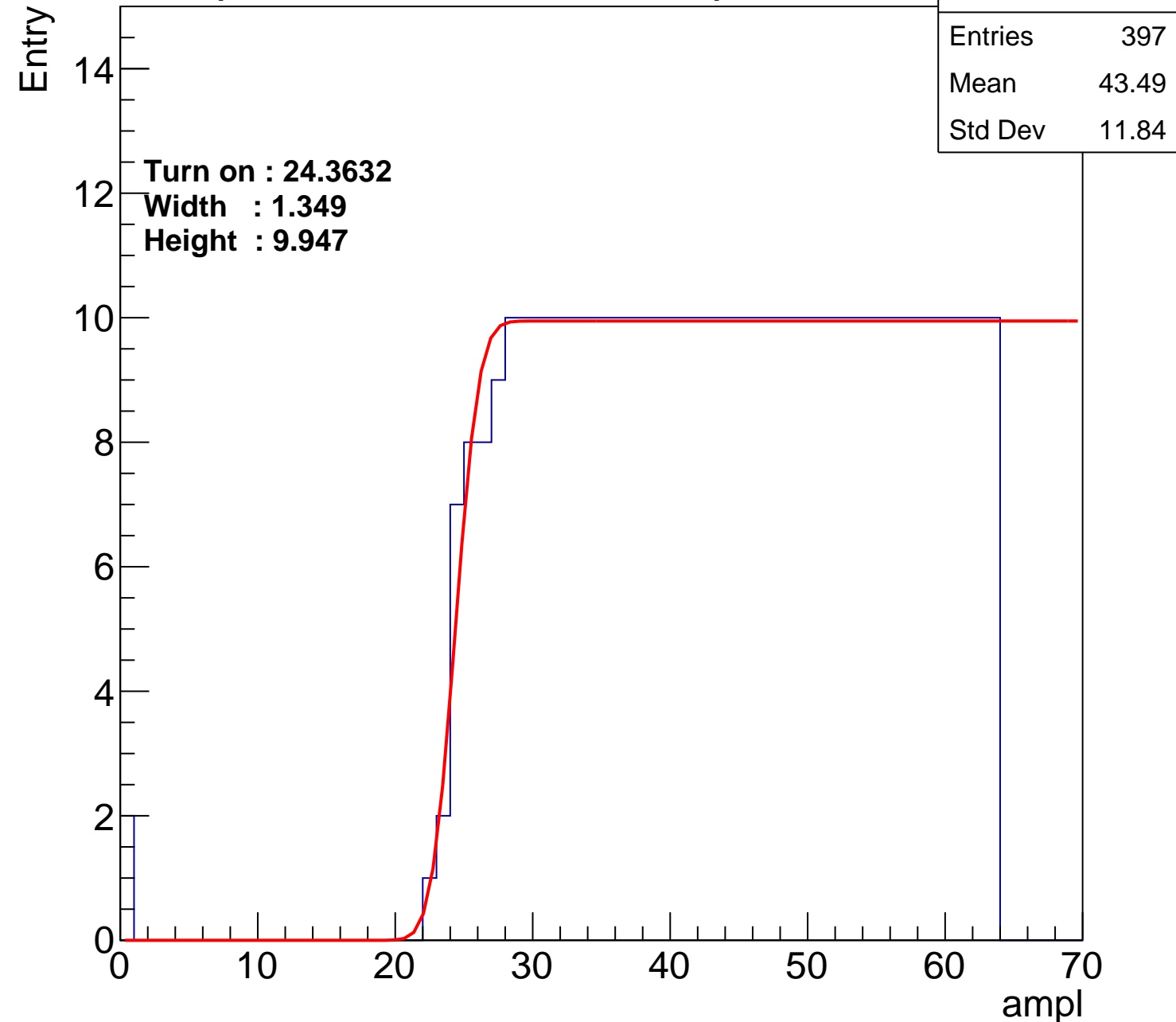
Width : 1.349

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.7
Std Dev	11.77

Turn on : 24.9316

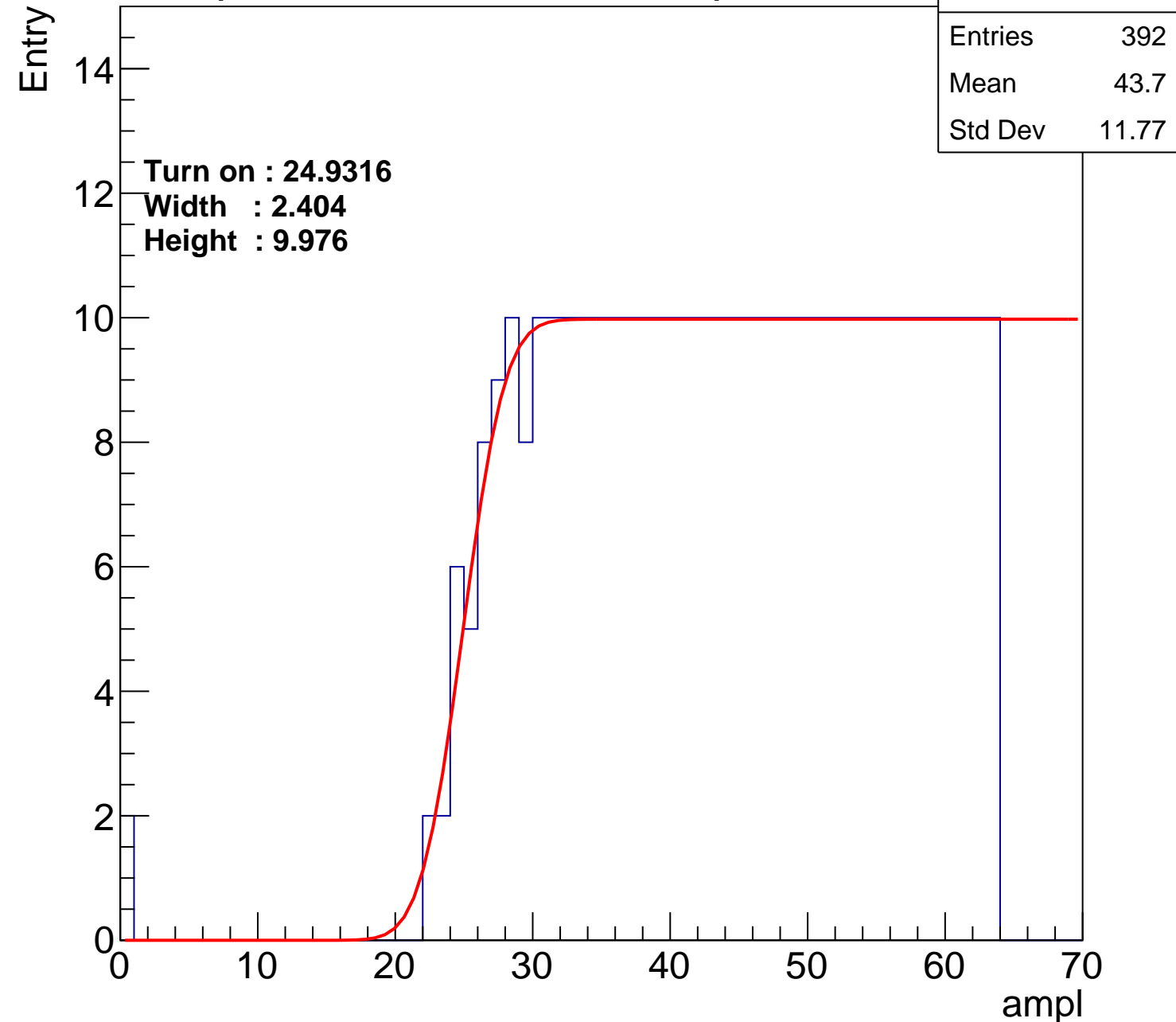
Width : 2.404

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.33
Std Dev	12.1

Turn on : 24.0270

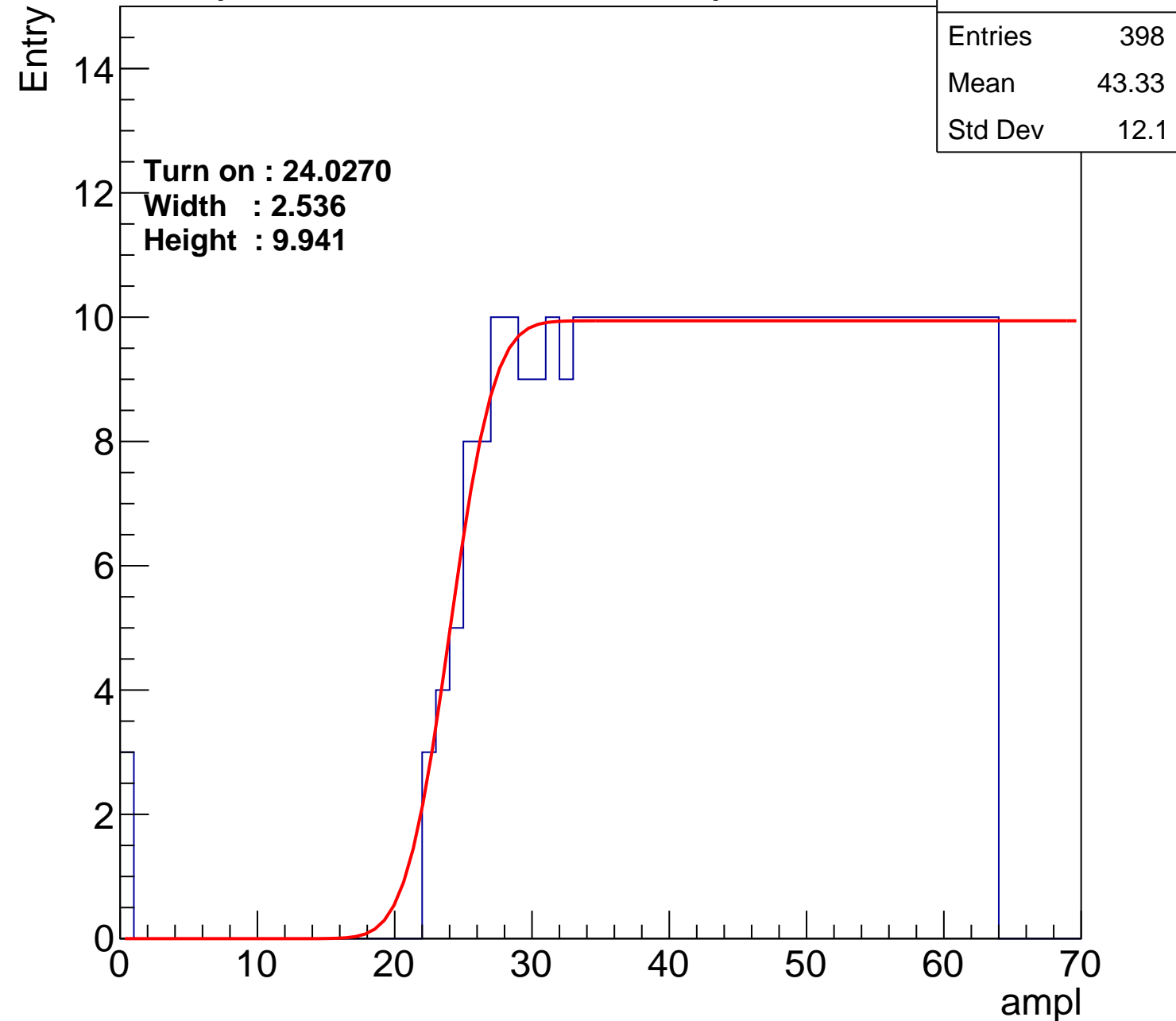
Width : 2.536

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.12
Std Dev	12.24

Turn on : 24.5004

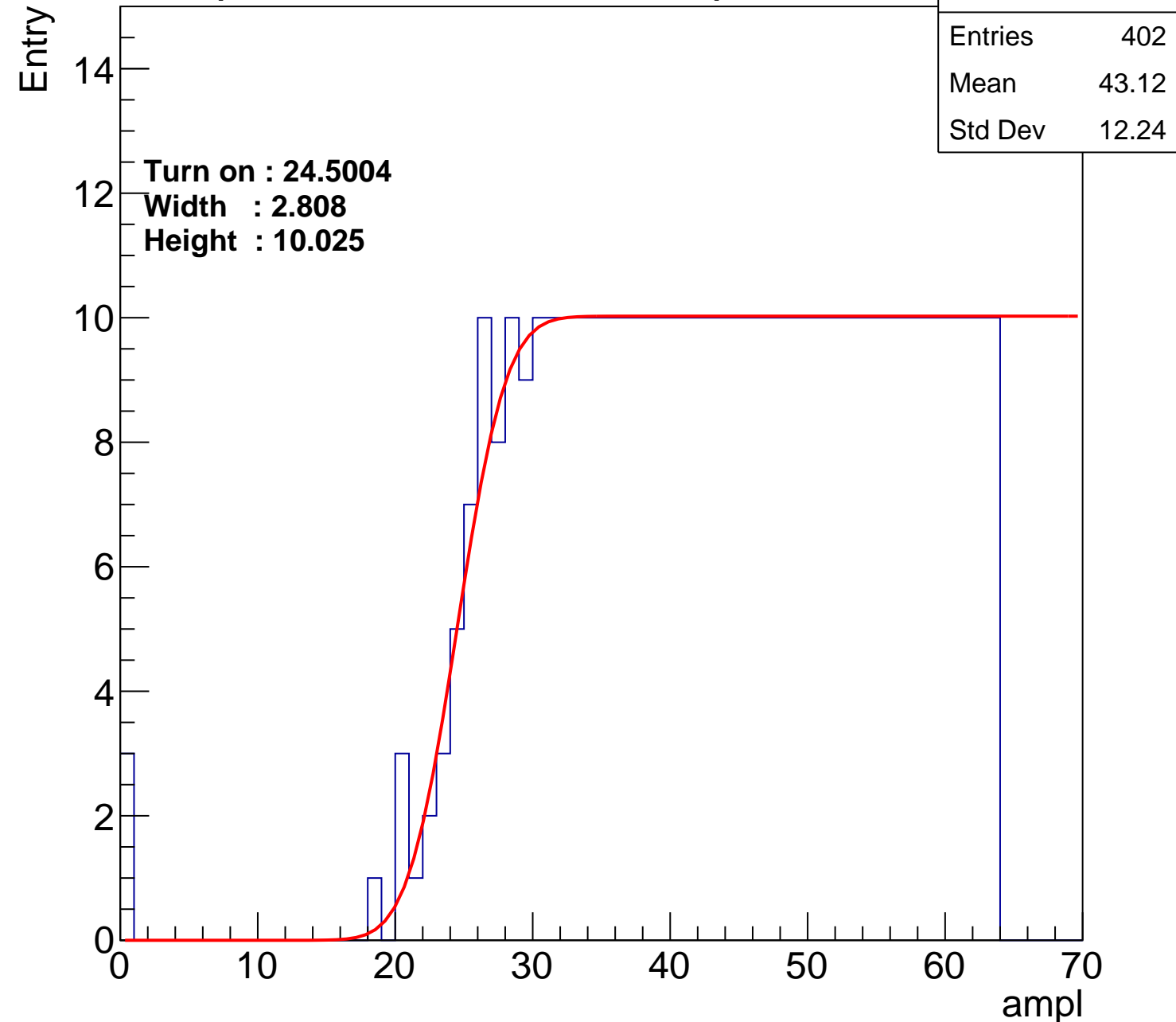
Width : 2.808

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.93
Std Dev	11.69

Turn on : 25.6213

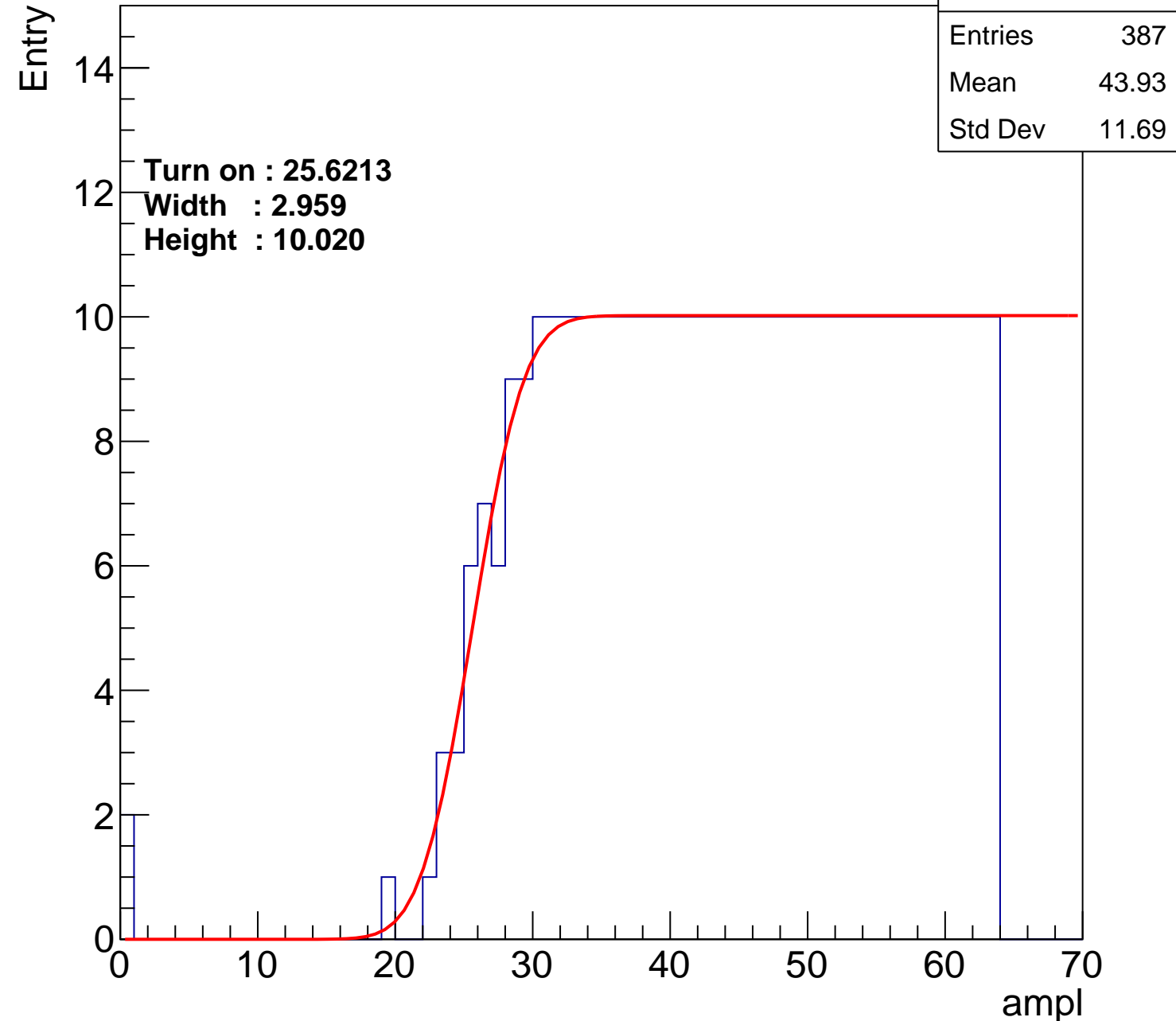
Width : 2.959

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch48

calib_packv5_042523_0143.root, FC#10, port B3

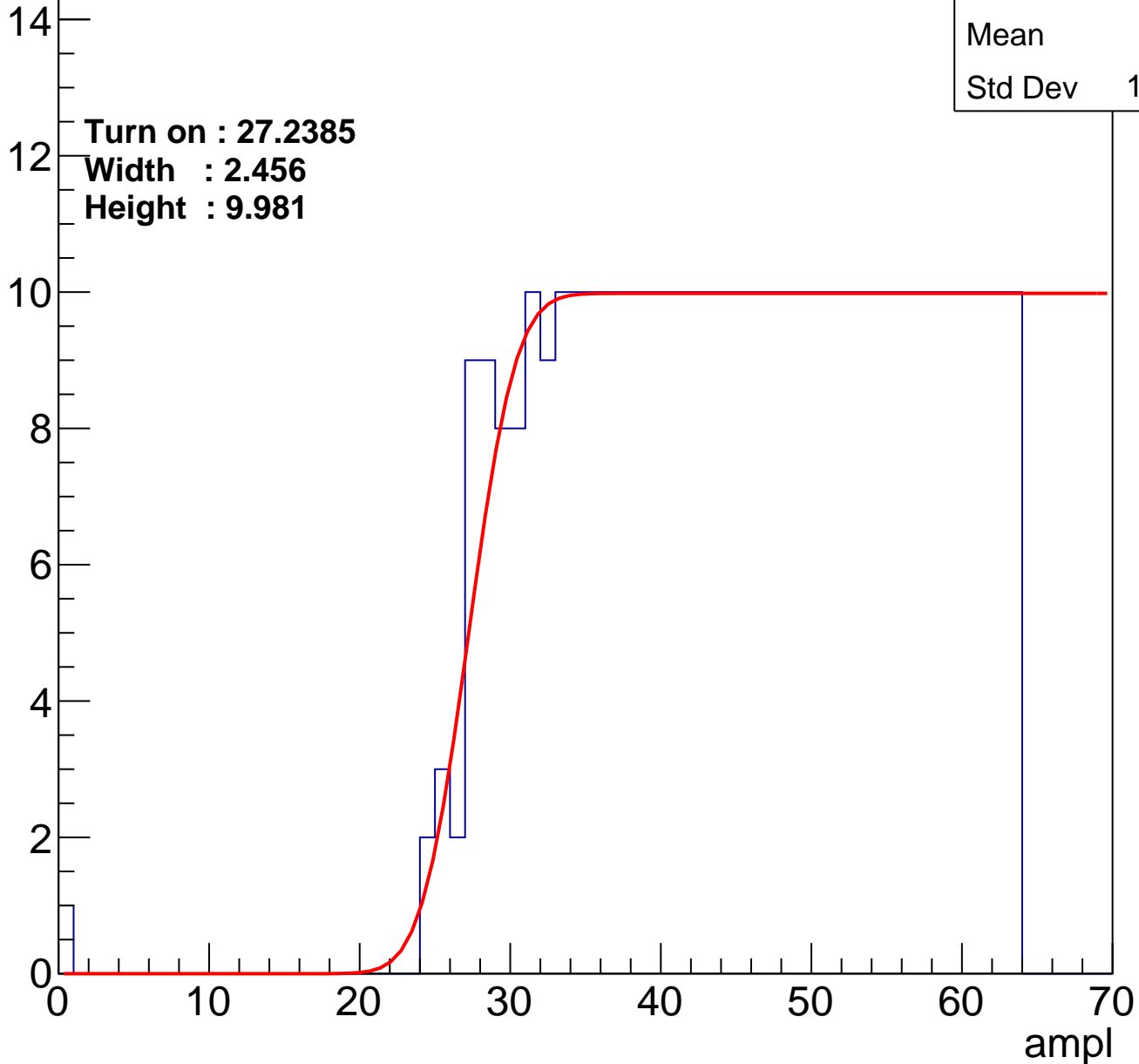
Entries	371
Mean	44.8
Std Dev	11.04

Turn on : 27.2385

Width : 2.456

Height : 9.981

Entry



B1L002S, U9-ch49

calib_packv5_042523_0143.root, FC#10, port B3

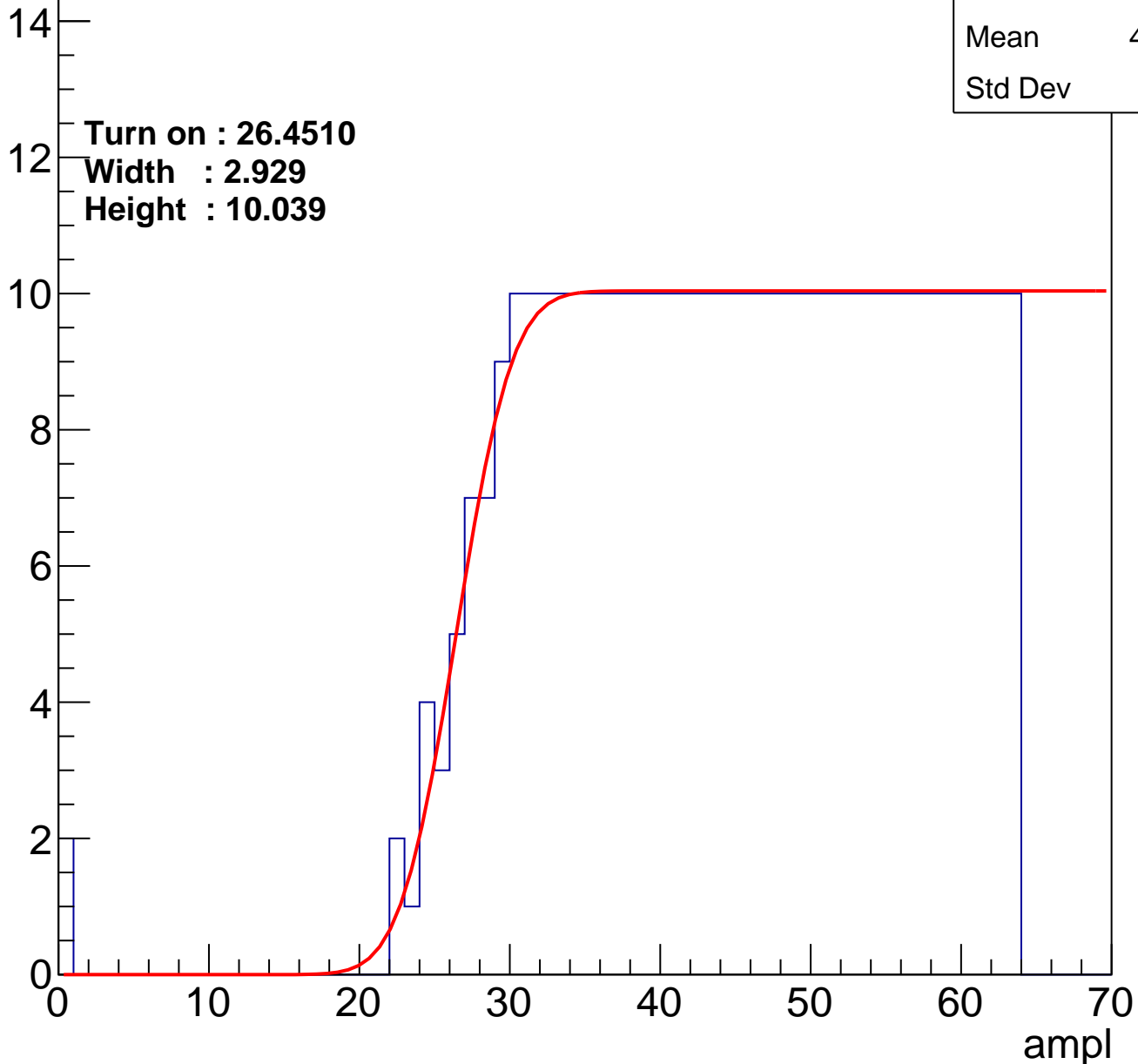
Entry

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.4510

Width : 2.929

Height : 10.039



B1L002S, U9-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.2461

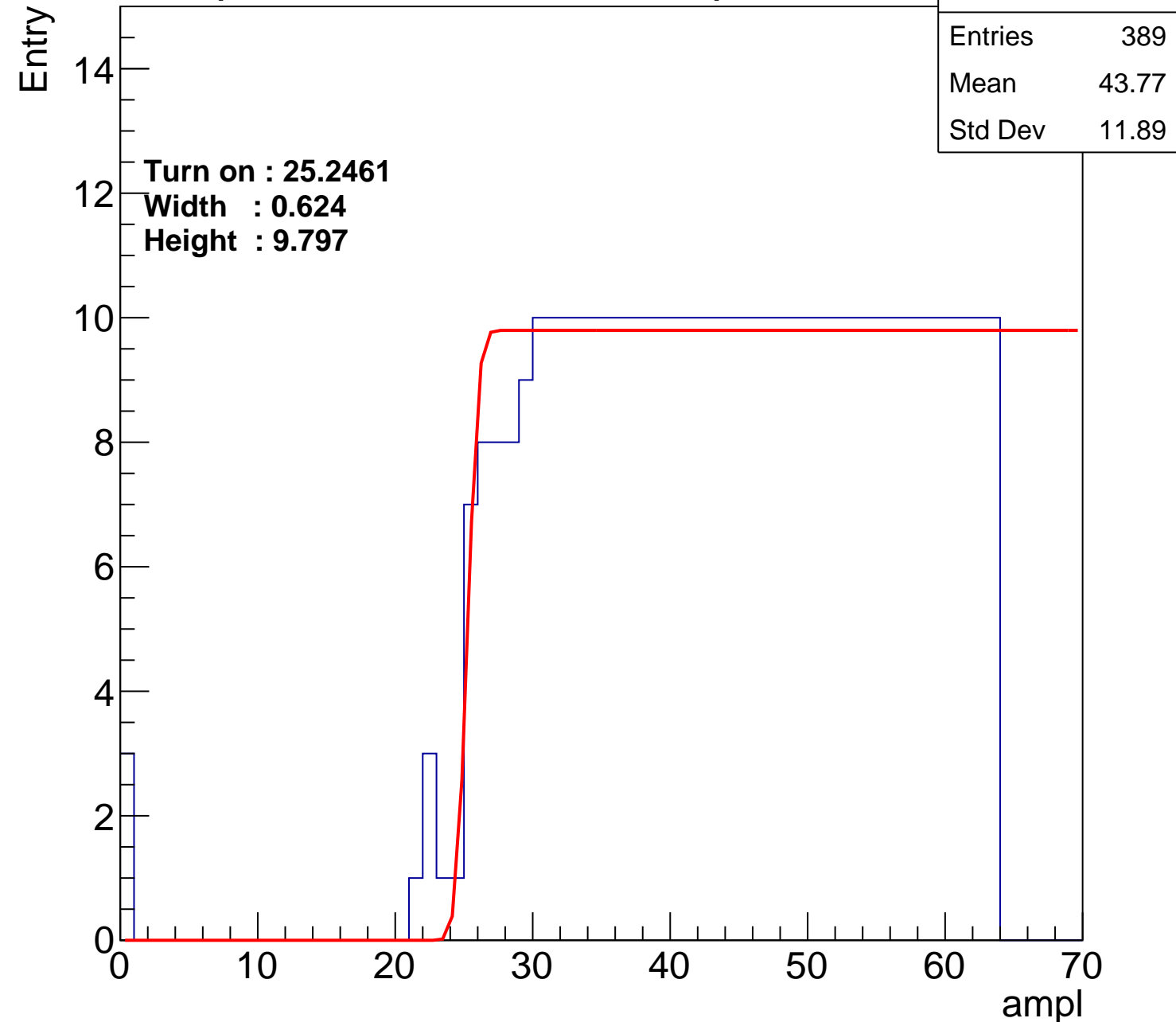
Width : 0.624

Height : 9.797

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.2419

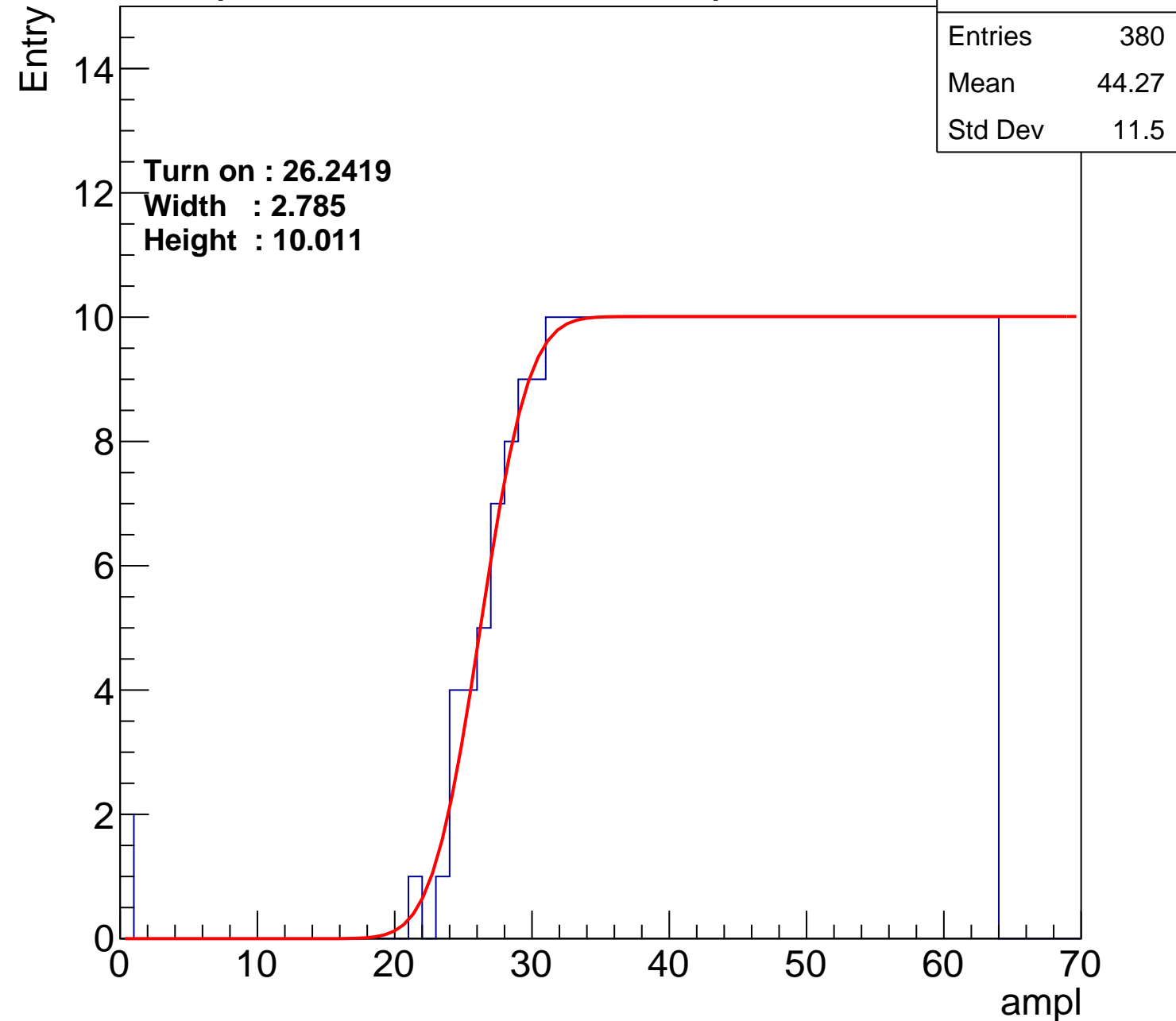
Width : 2.785

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.44
Std Dev	11.93

Turn on : 24.8049

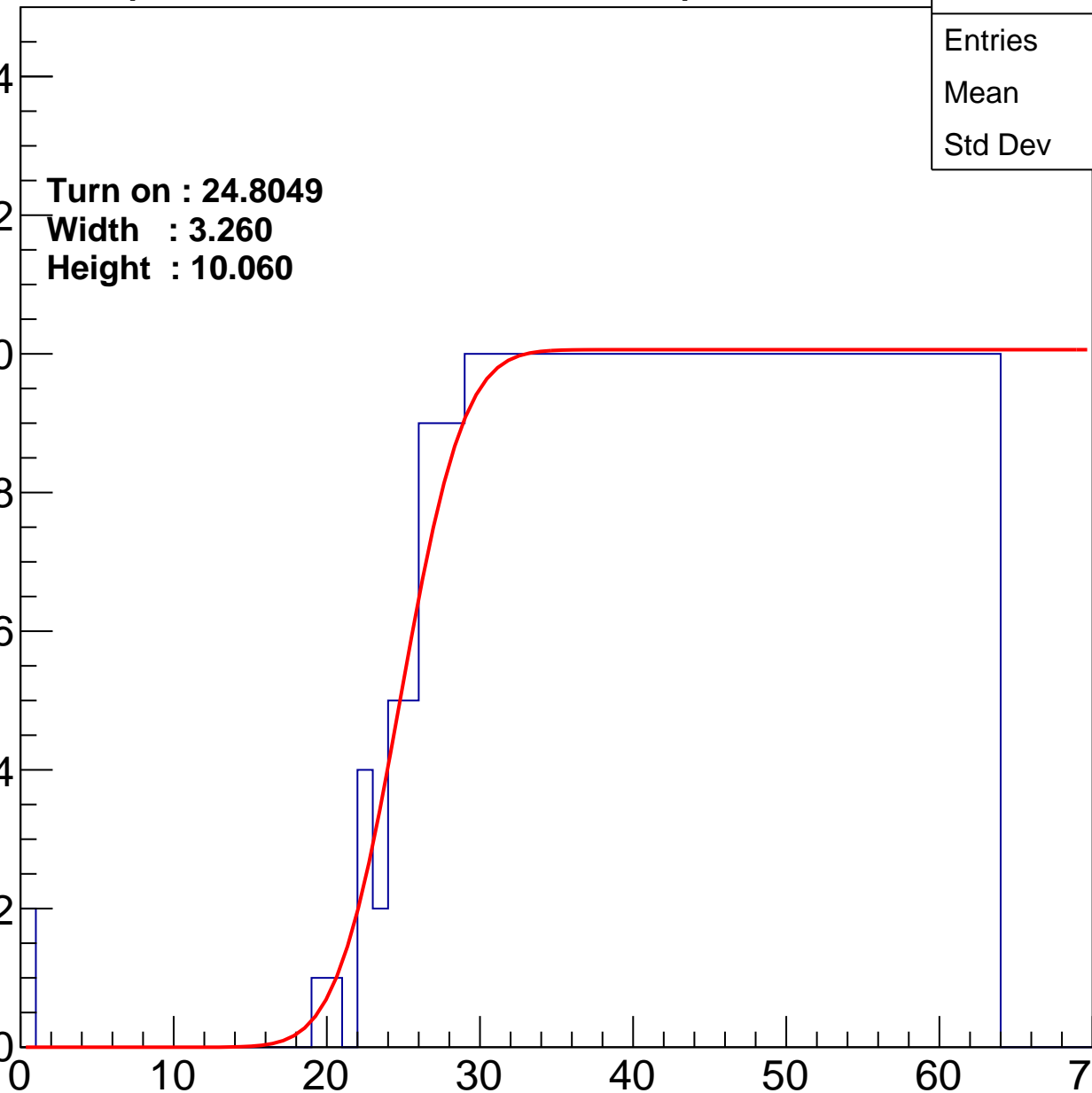
Width : 3.260

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	357
Mean	45.43
Std Dev	10.87

Turn on : 28.5654

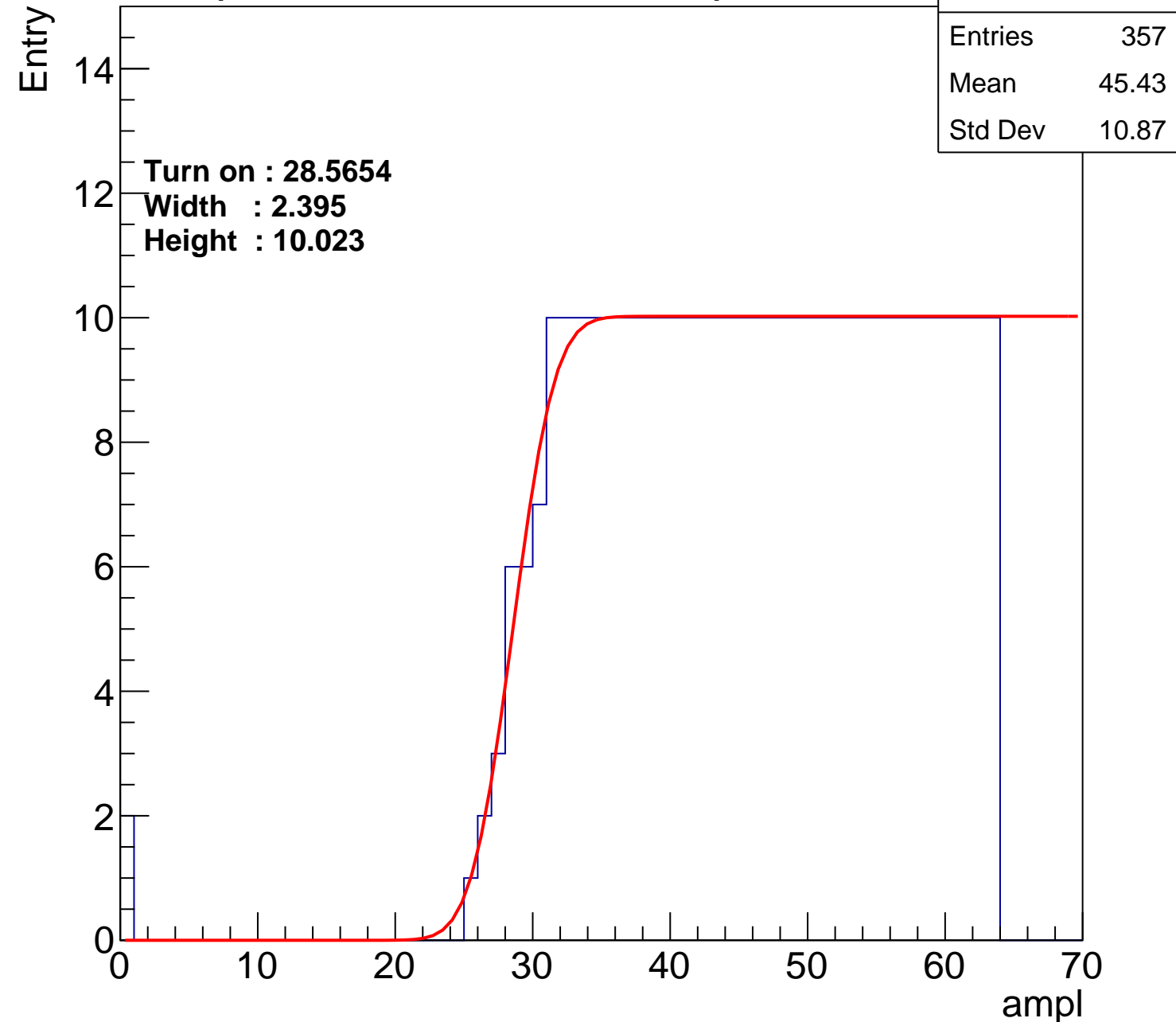
Width : 2.395

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.54
Std Dev	11.71

Turn on : 25.1442

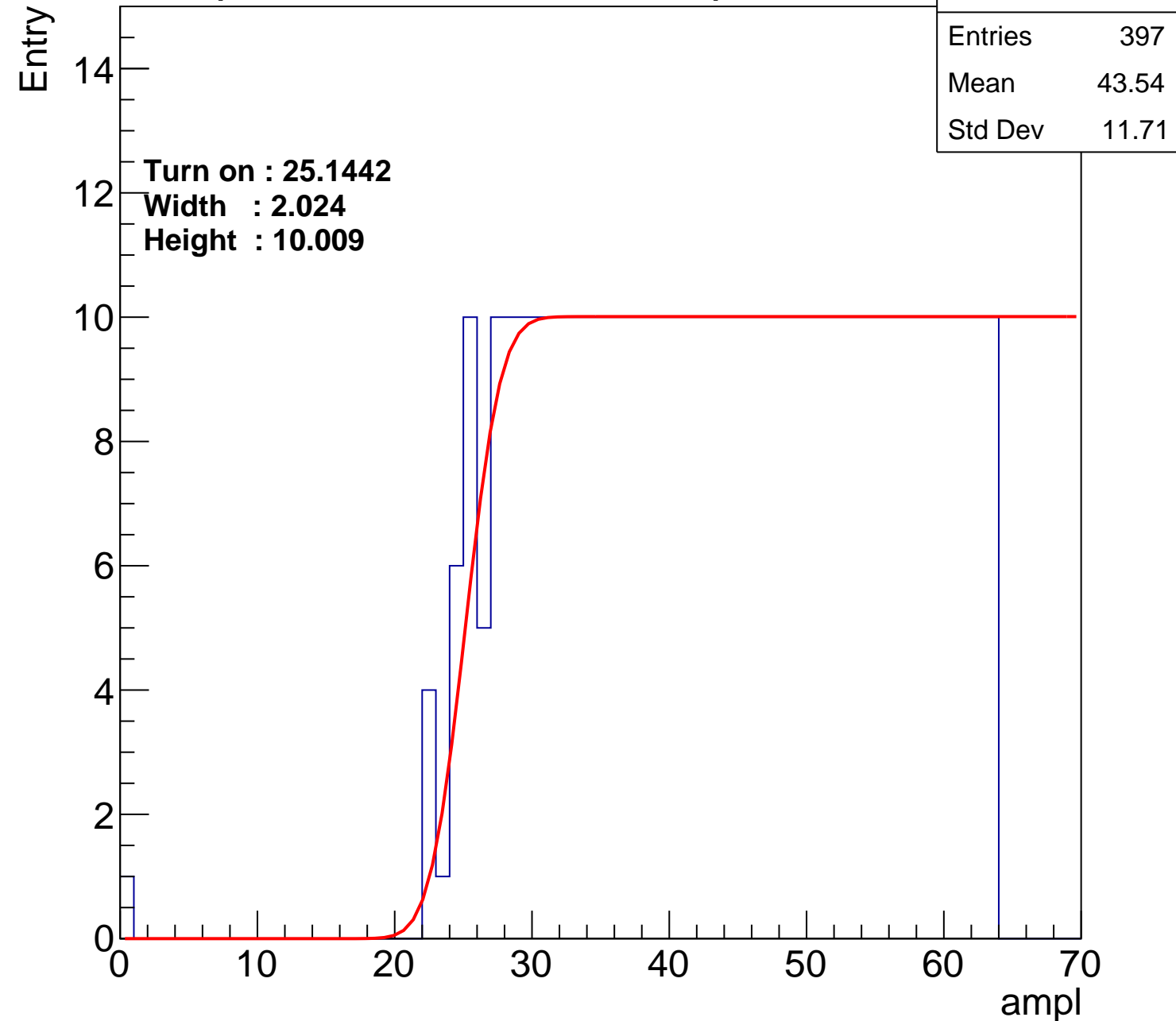
Width : 2.024

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.4466

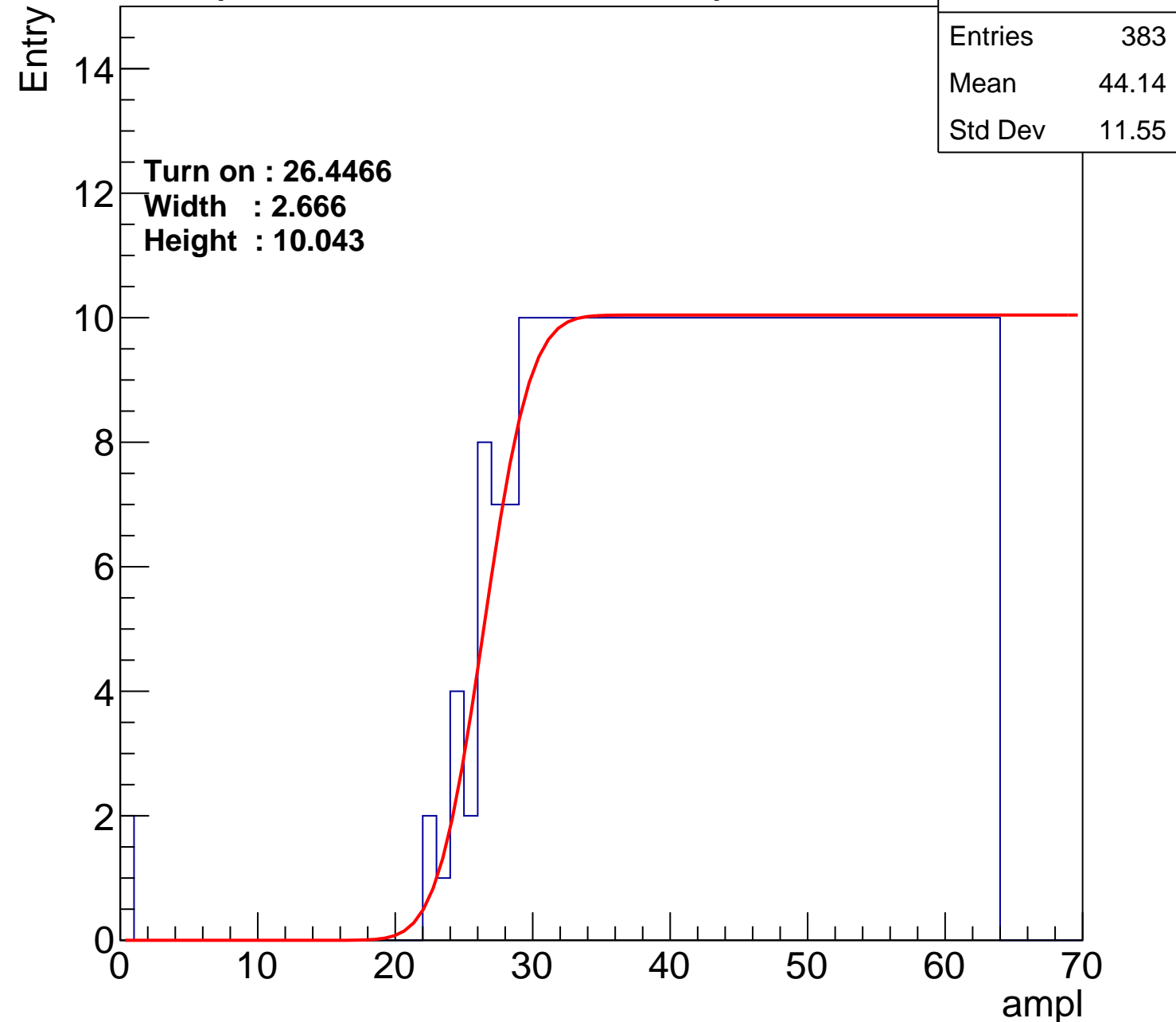
Width : 2.666

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.59
Std Dev	11.97

Turn on : 25.0609

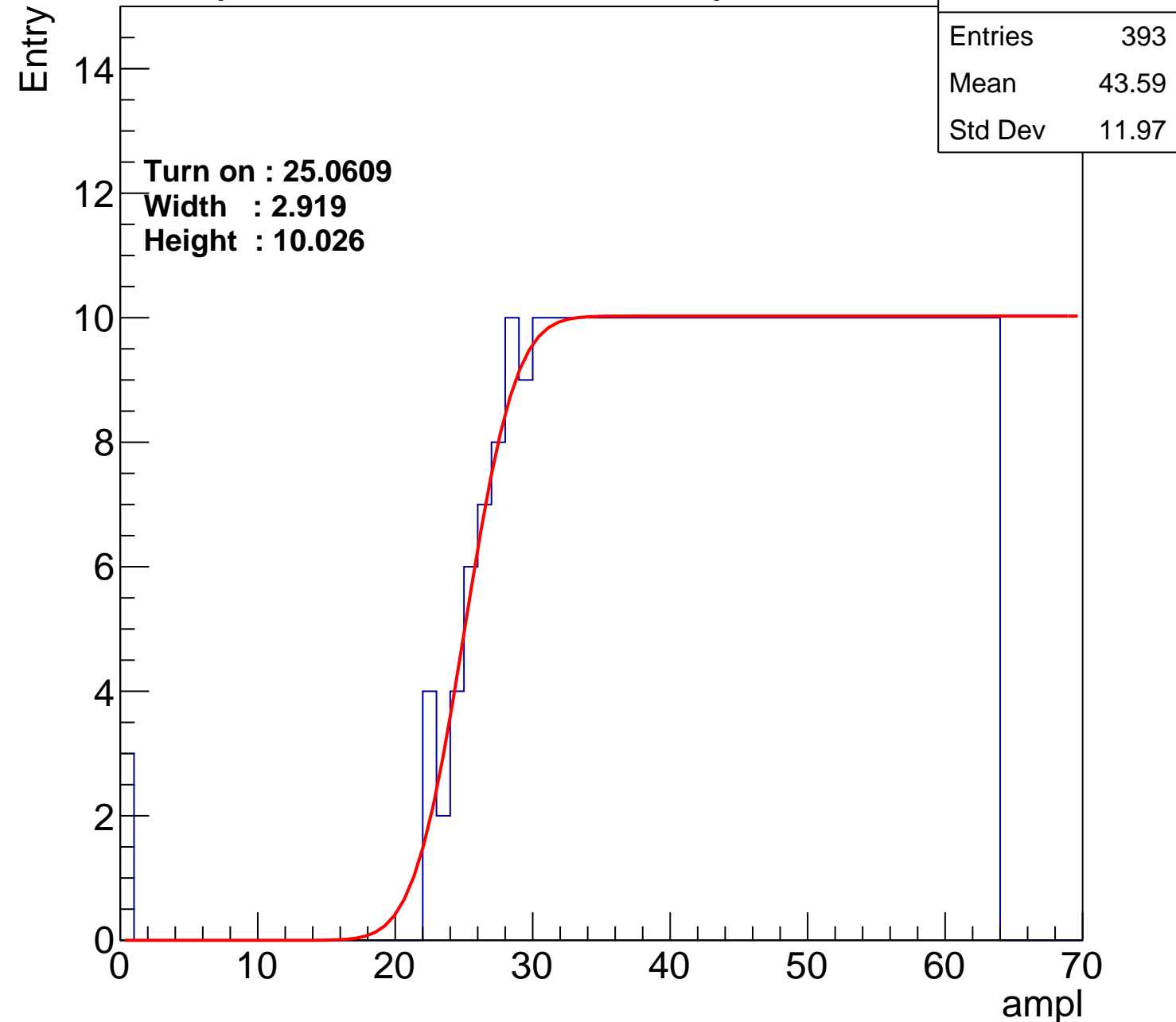
Width : 2.919

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.05
Std Dev	11.58

Turn on : 24.8452

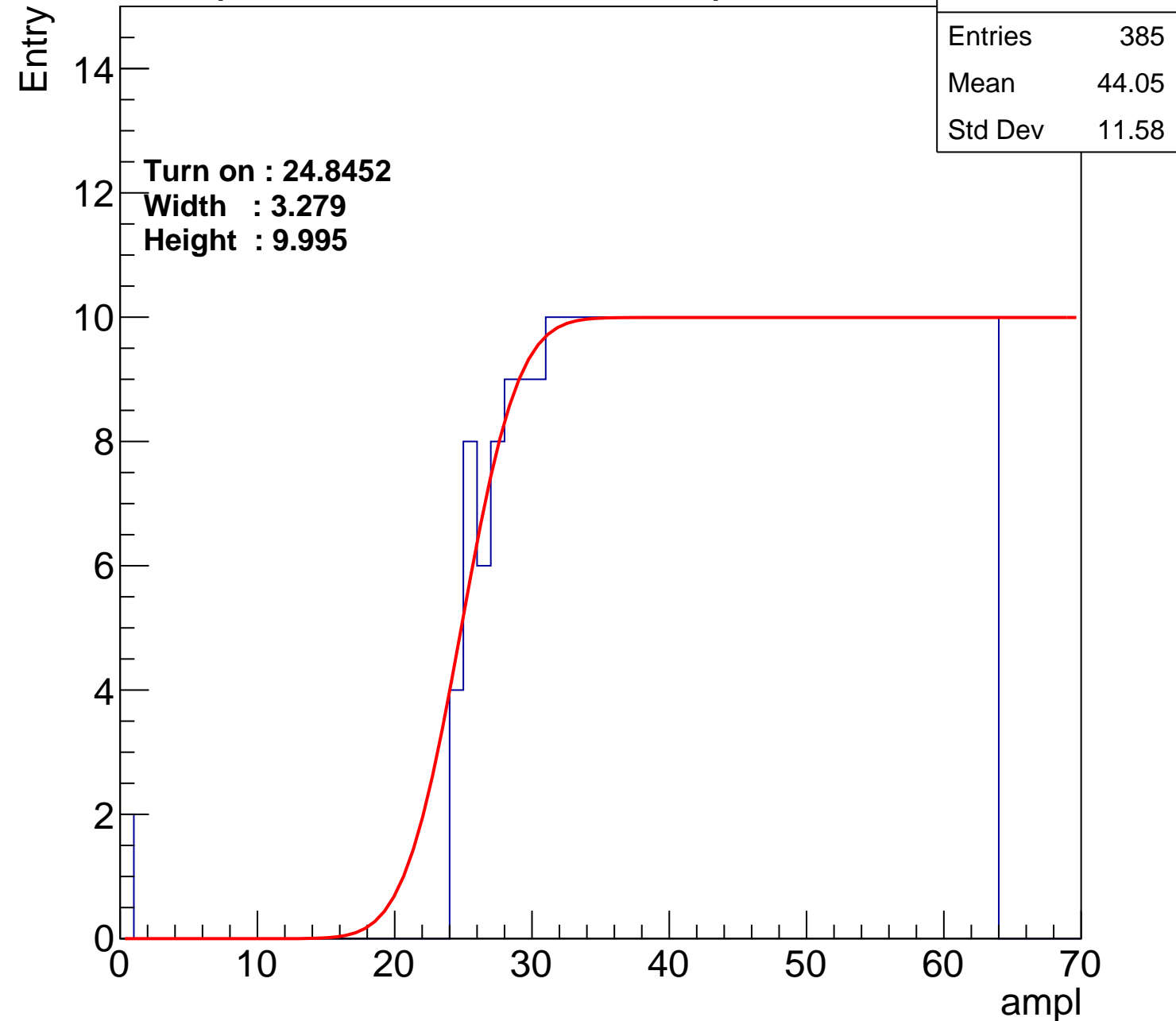
Width : 3.279

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.29

Turn on : 26.3896

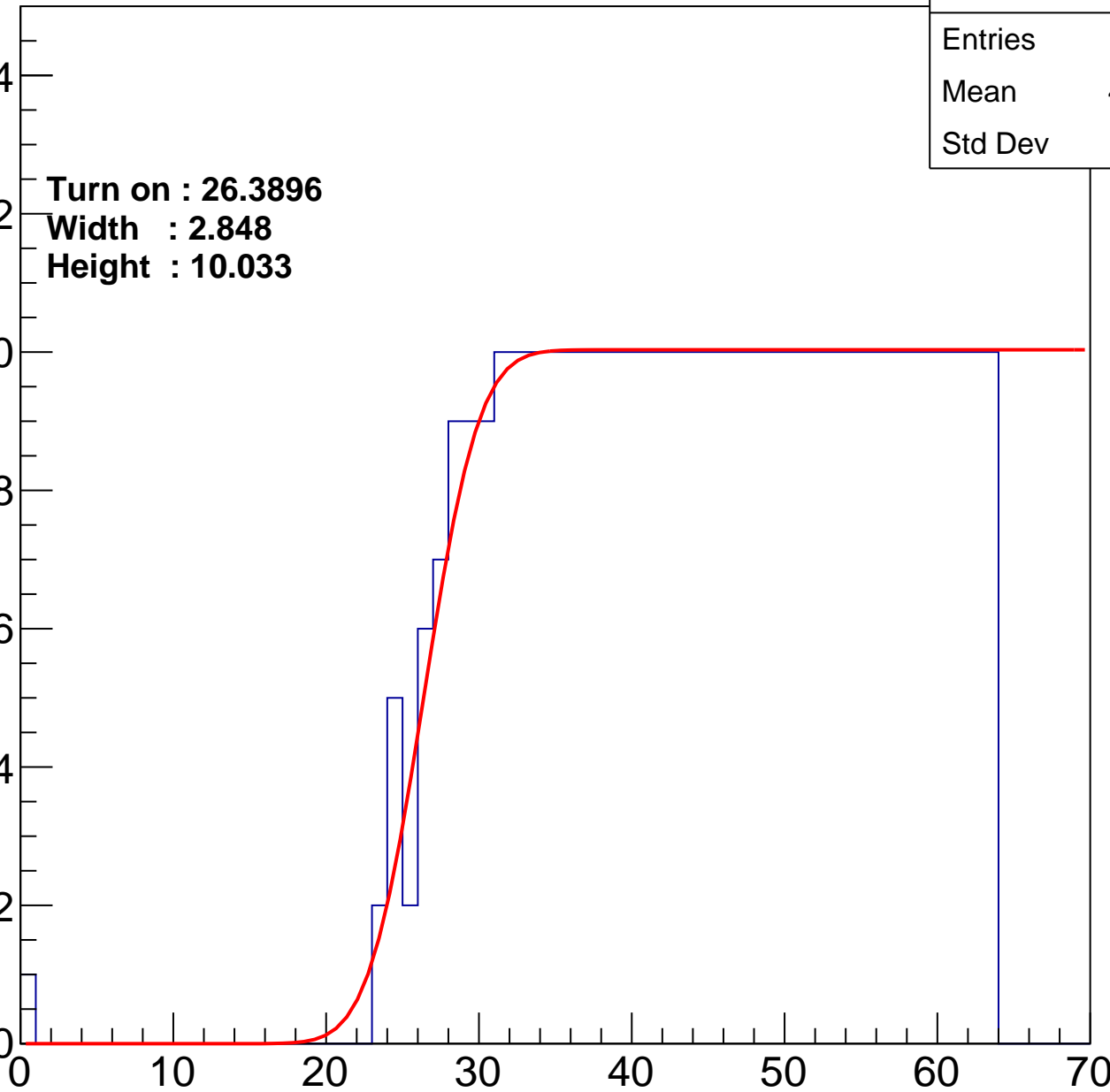
Width : 2.848

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	366
Mean	44.97
Std Dev	11.13

Turn on : 27.8483

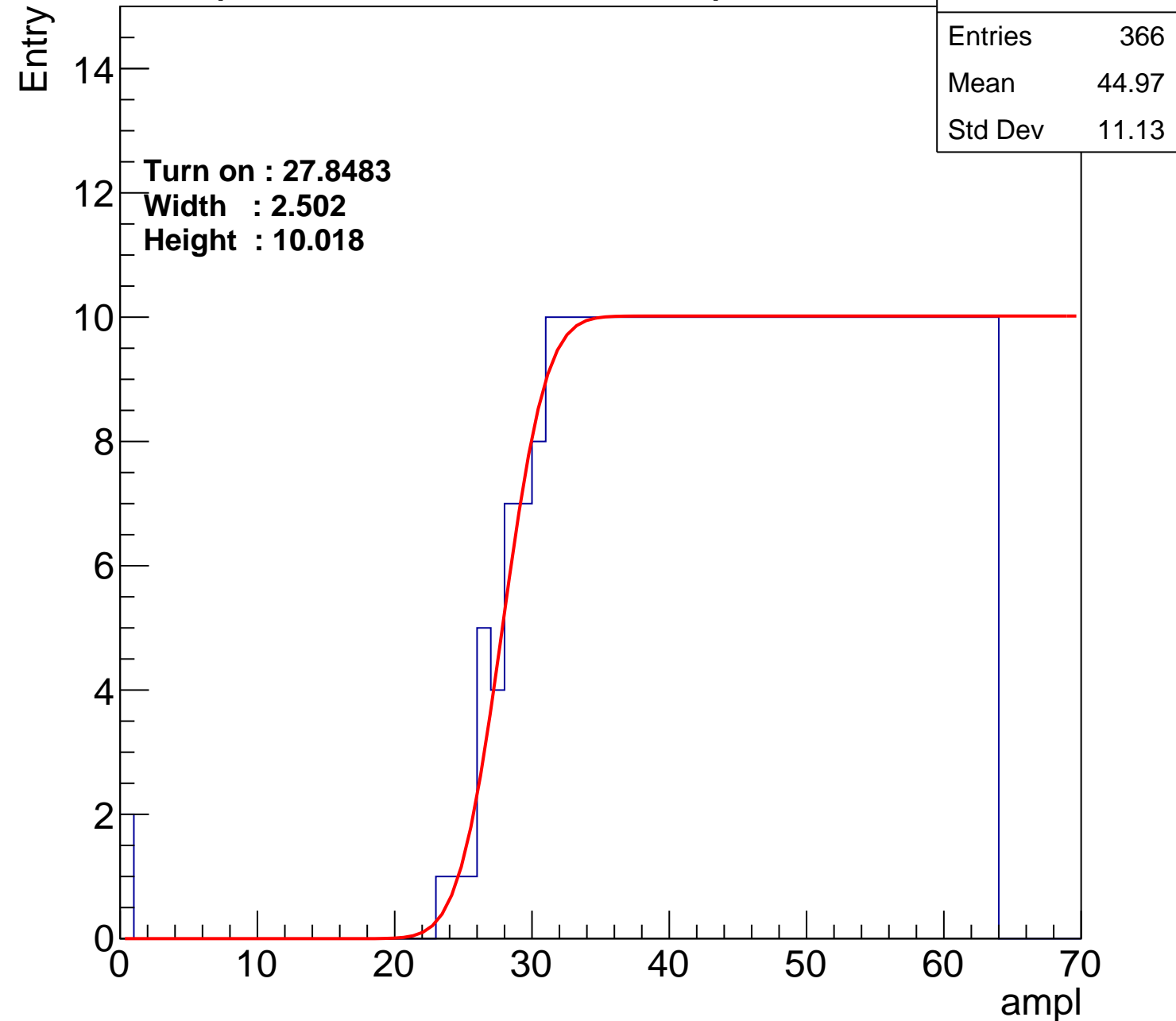
Width : 2.502

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.65
Std Dev	11.92

Turn on : 25.4385

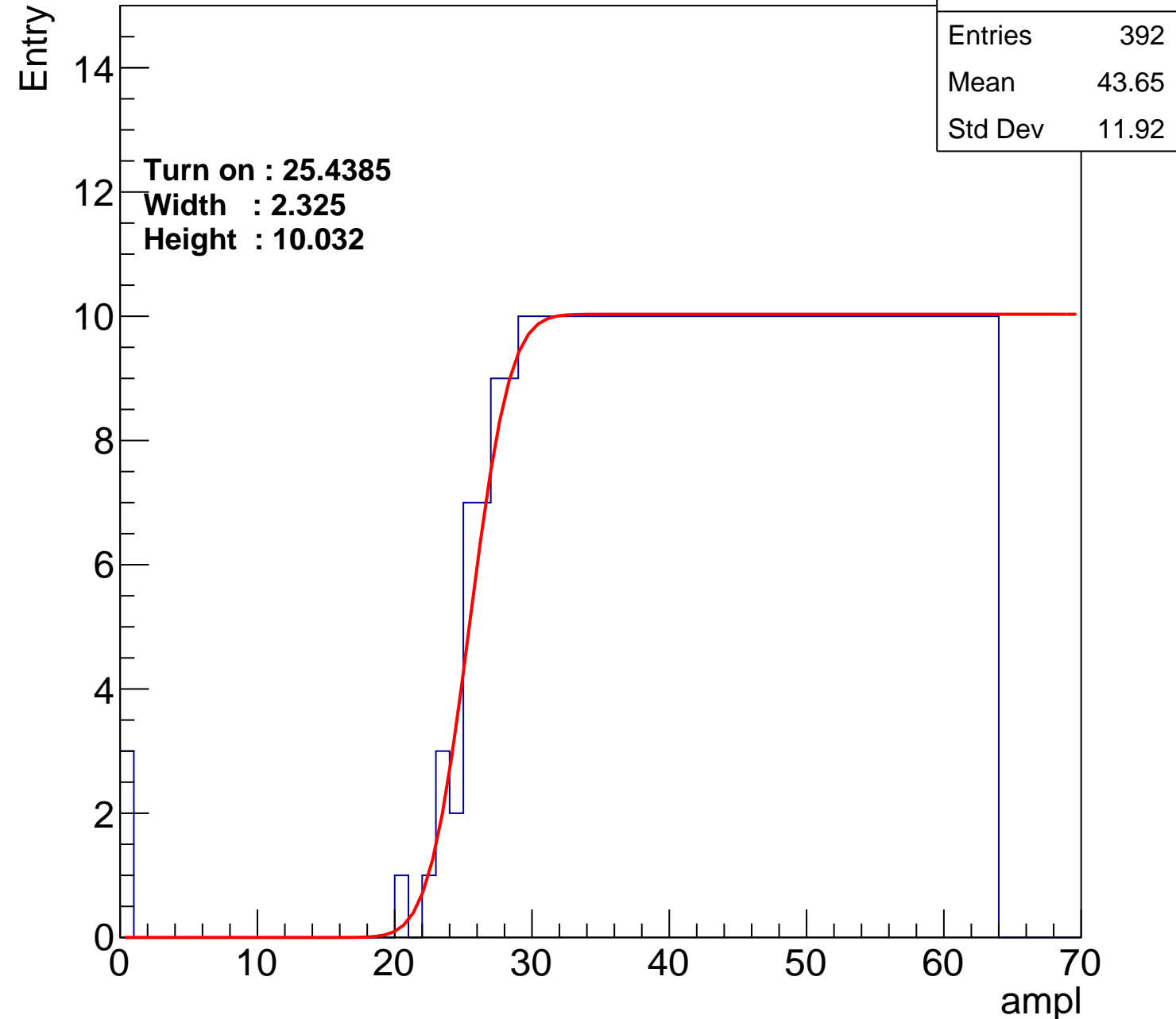
Width : 2.325

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 28.2593

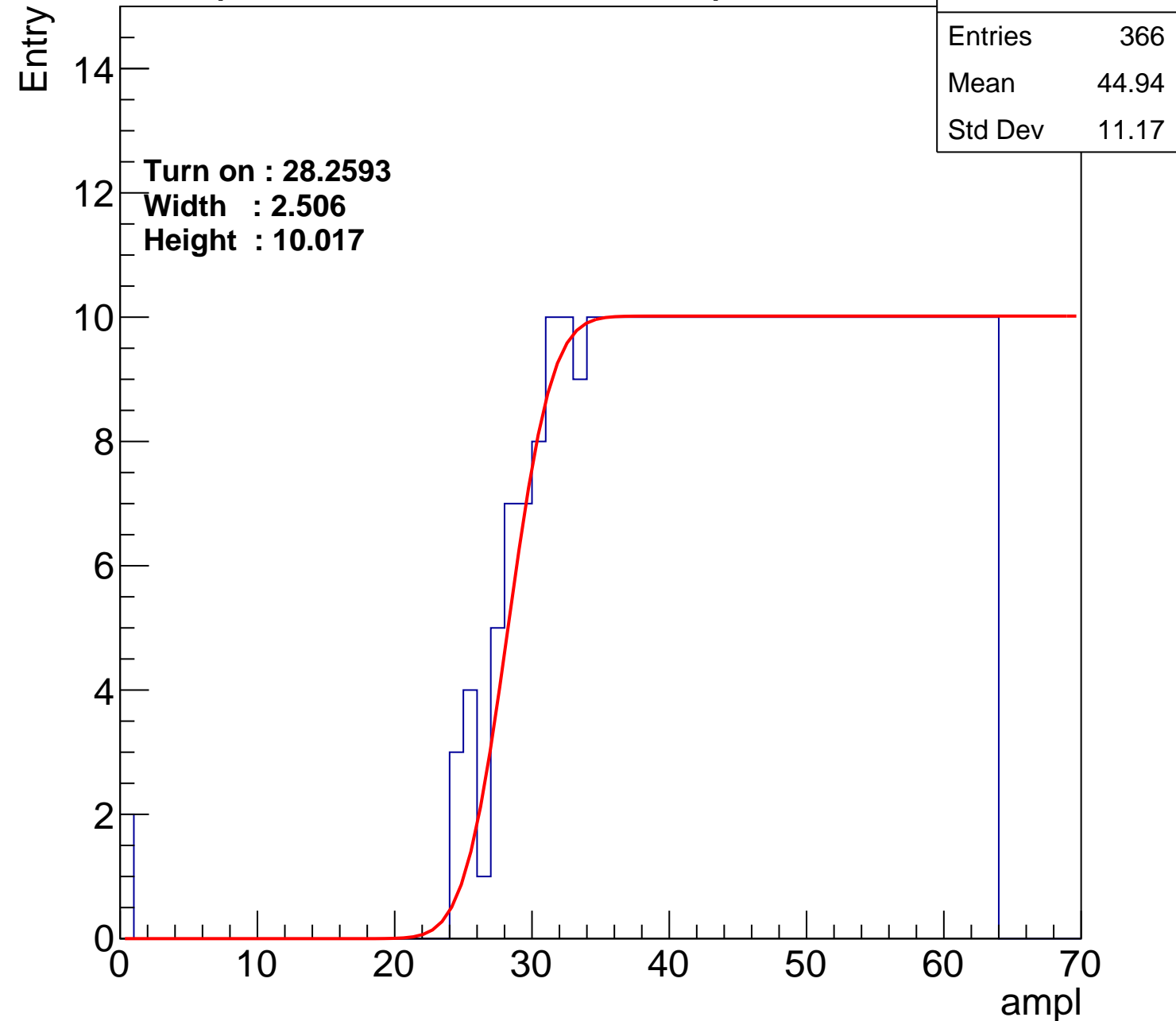
Width : 2.506

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 24.7797

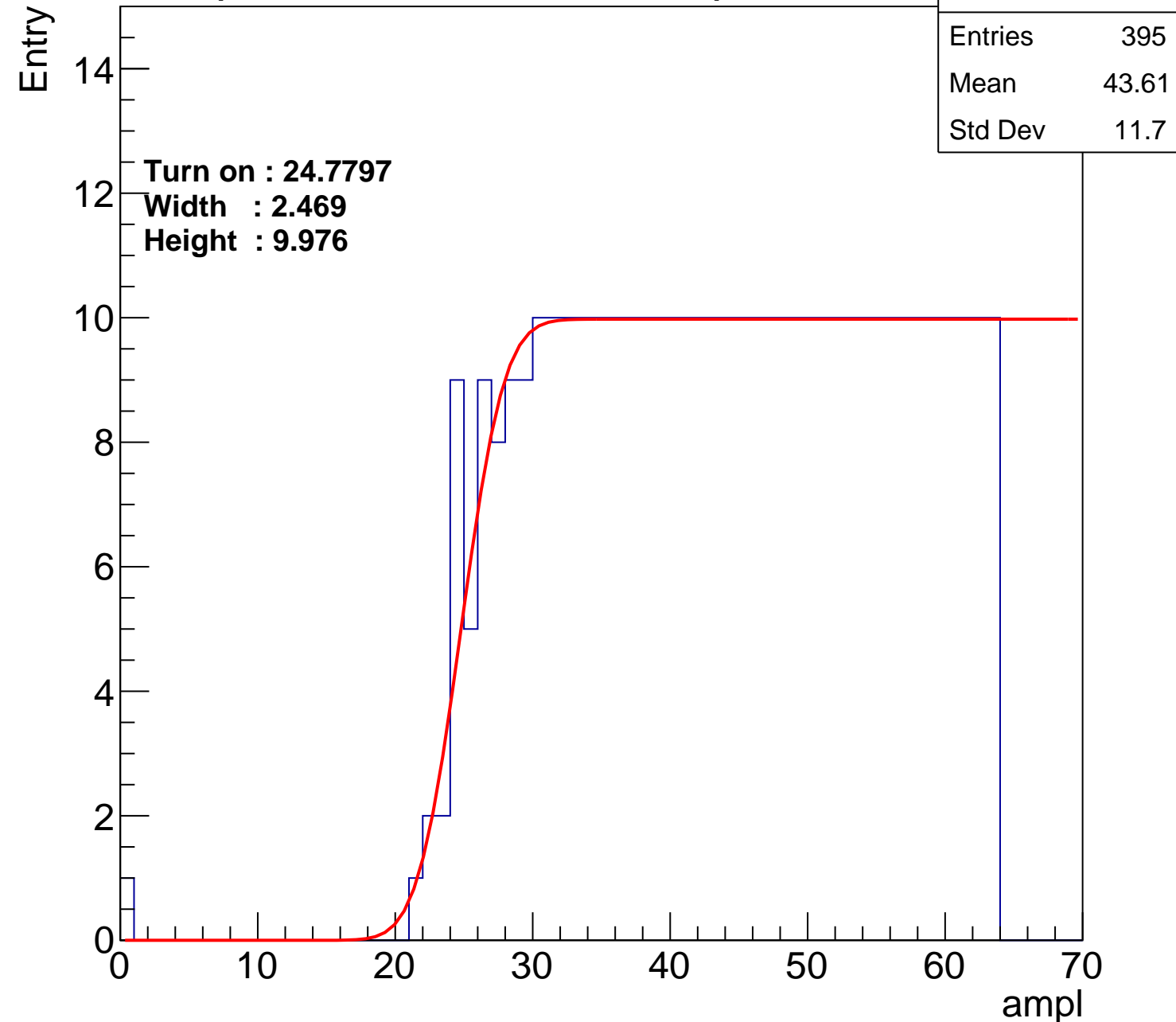
Width : 2.469

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.3
Std Dev	11.99

Turn on : 23.9841

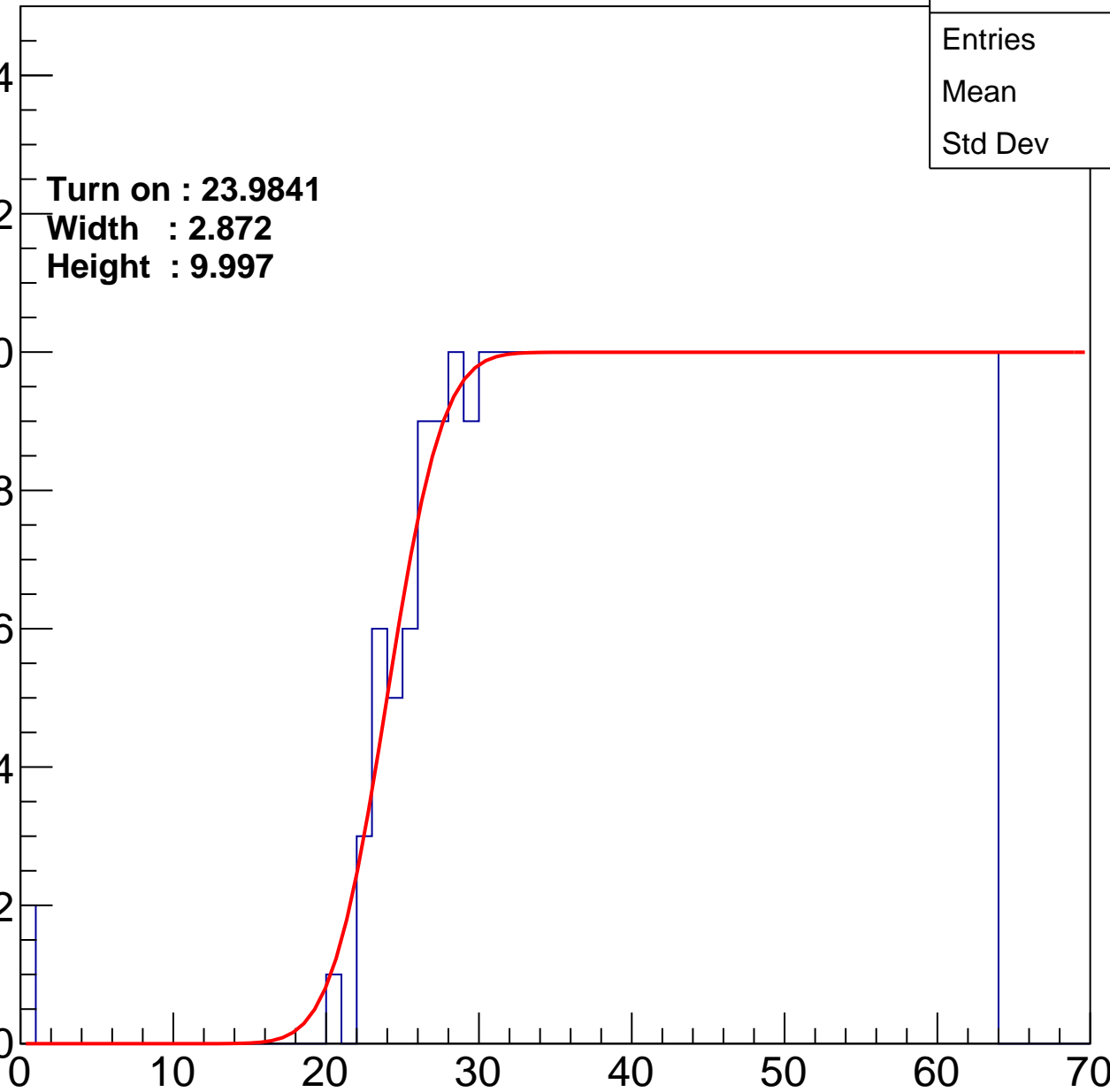
Width : 2.872

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch64

calib_packv5_042523_0143.root, FC#10, port B3

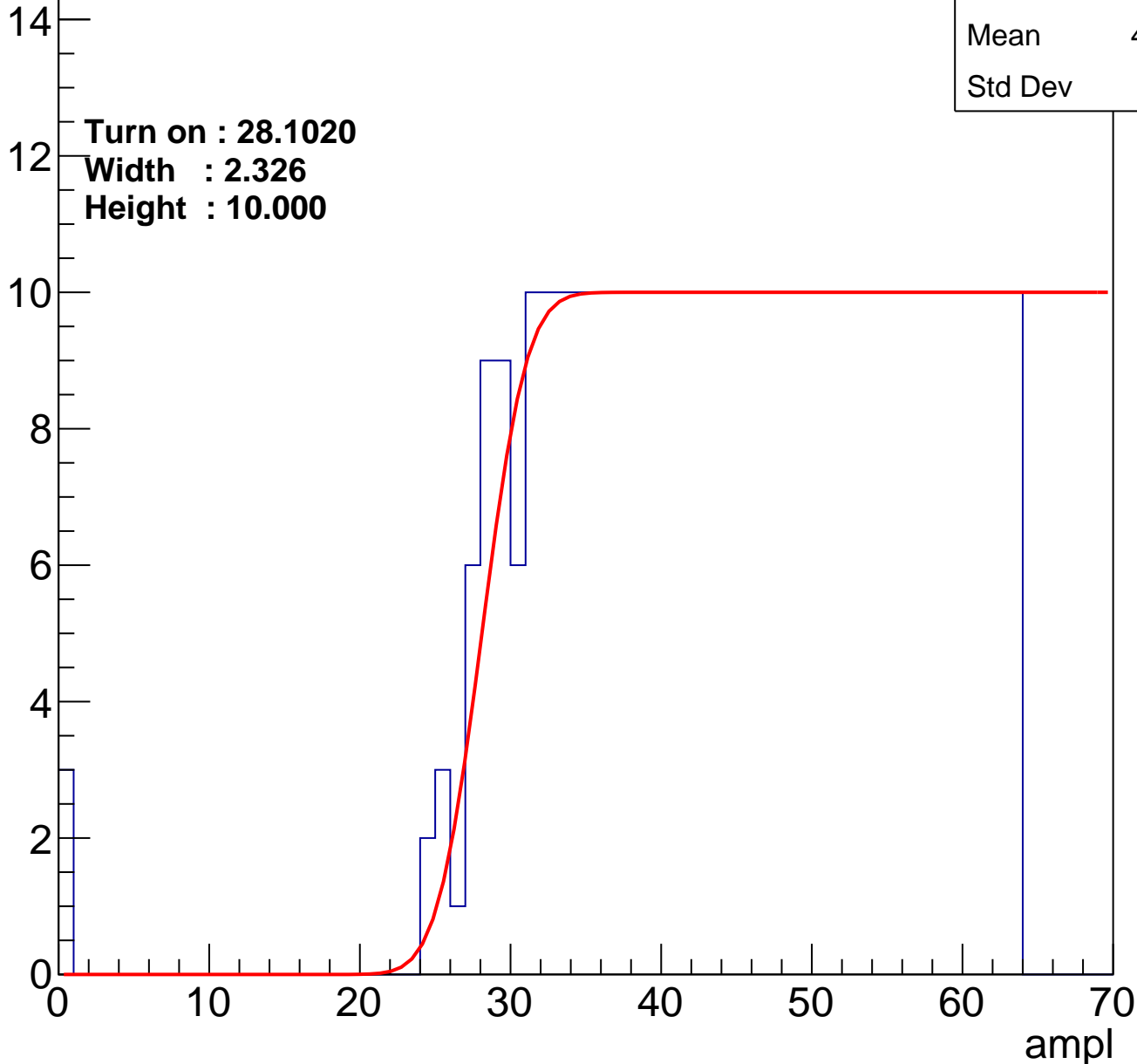
Entry

Entries	369
Mean	44.75
Std Dev	11.4

Turn on : 28.1020

Width : 2.326

Height : 10.000



B1L002S, U9-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 25.9920

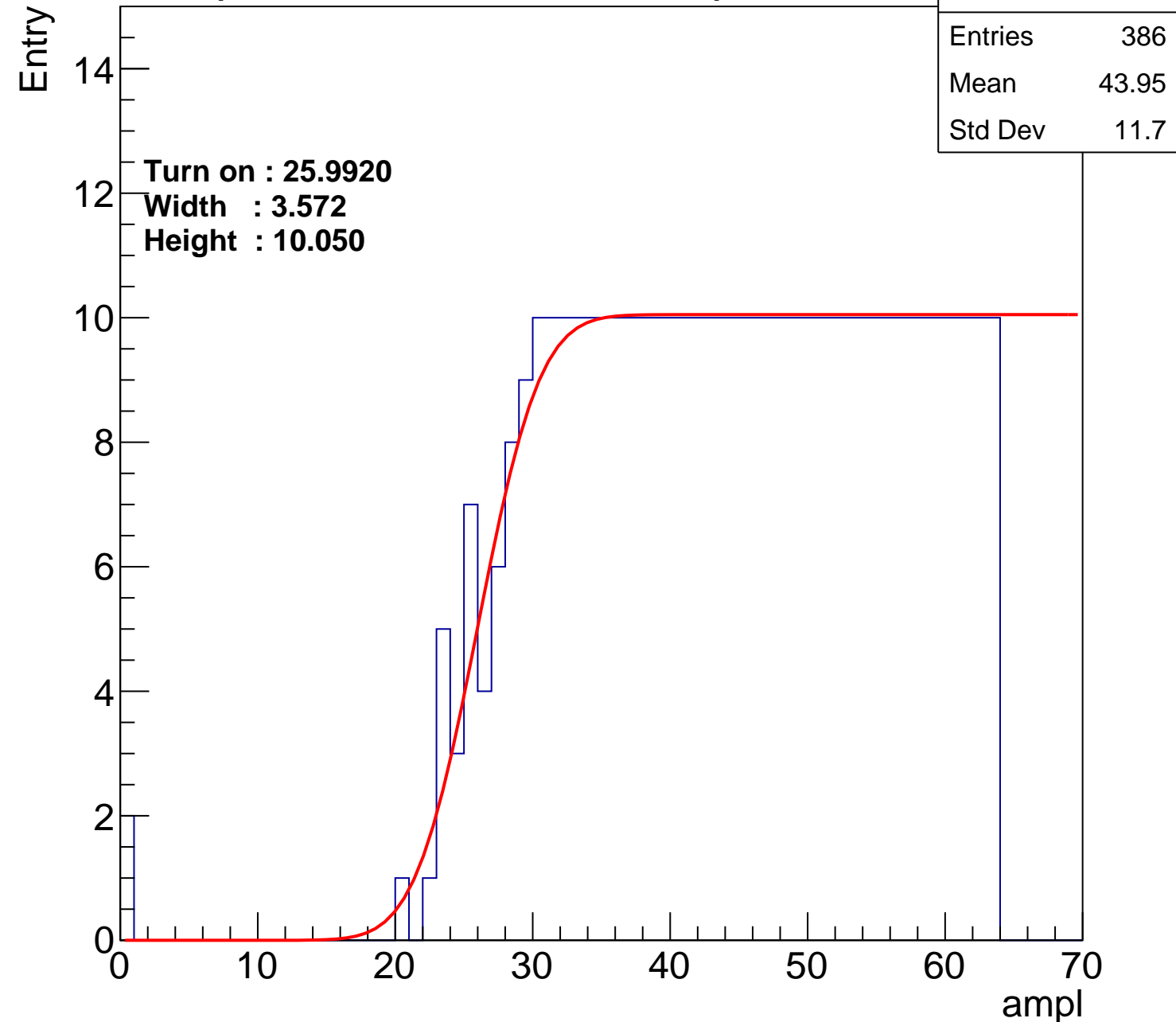
Width : 3.572

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.22
Std Dev	12.29

Turn on : 24.8589

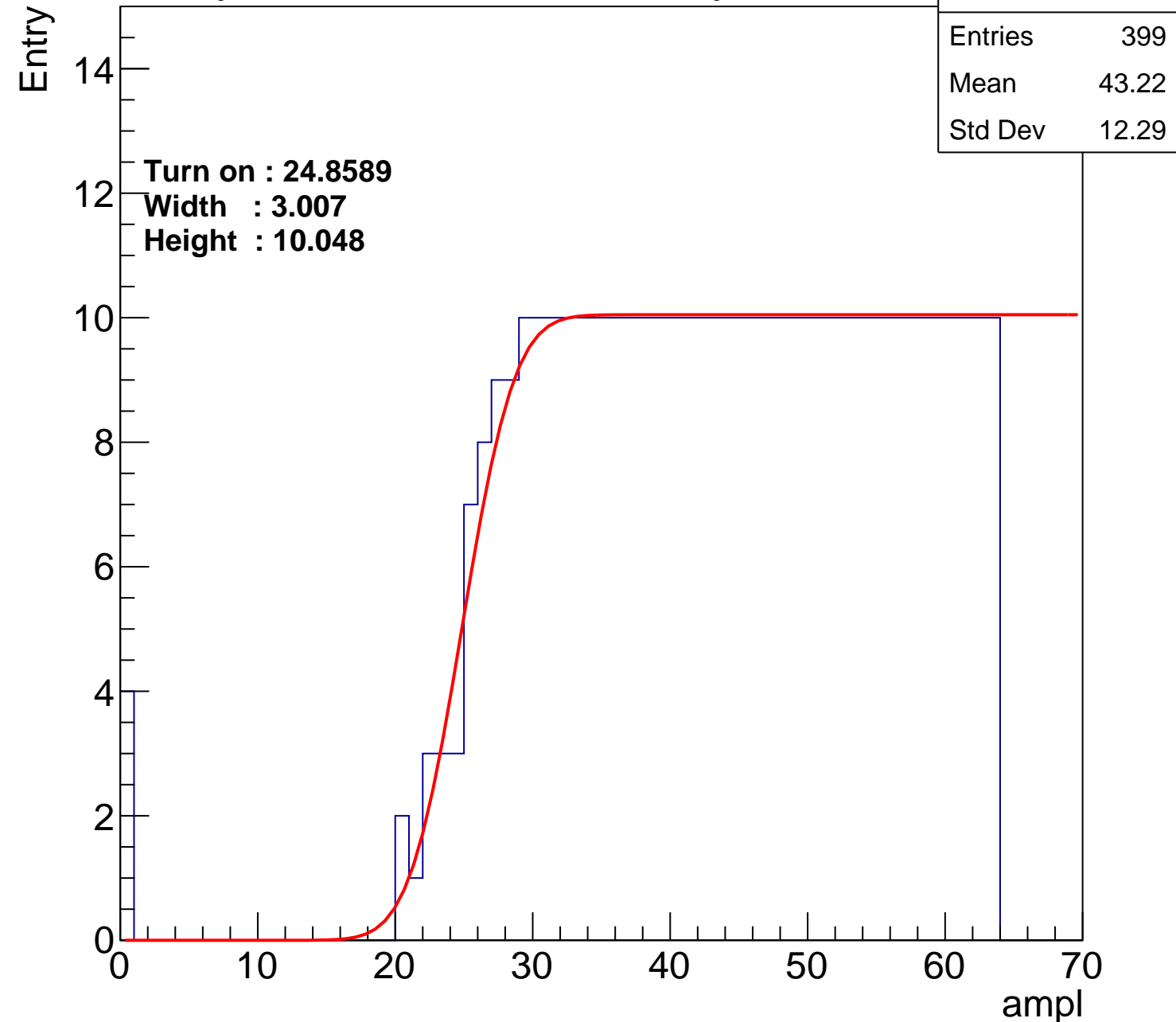
Width : 3.007

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.29
Std Dev	11.96

Turn on : 24.2862

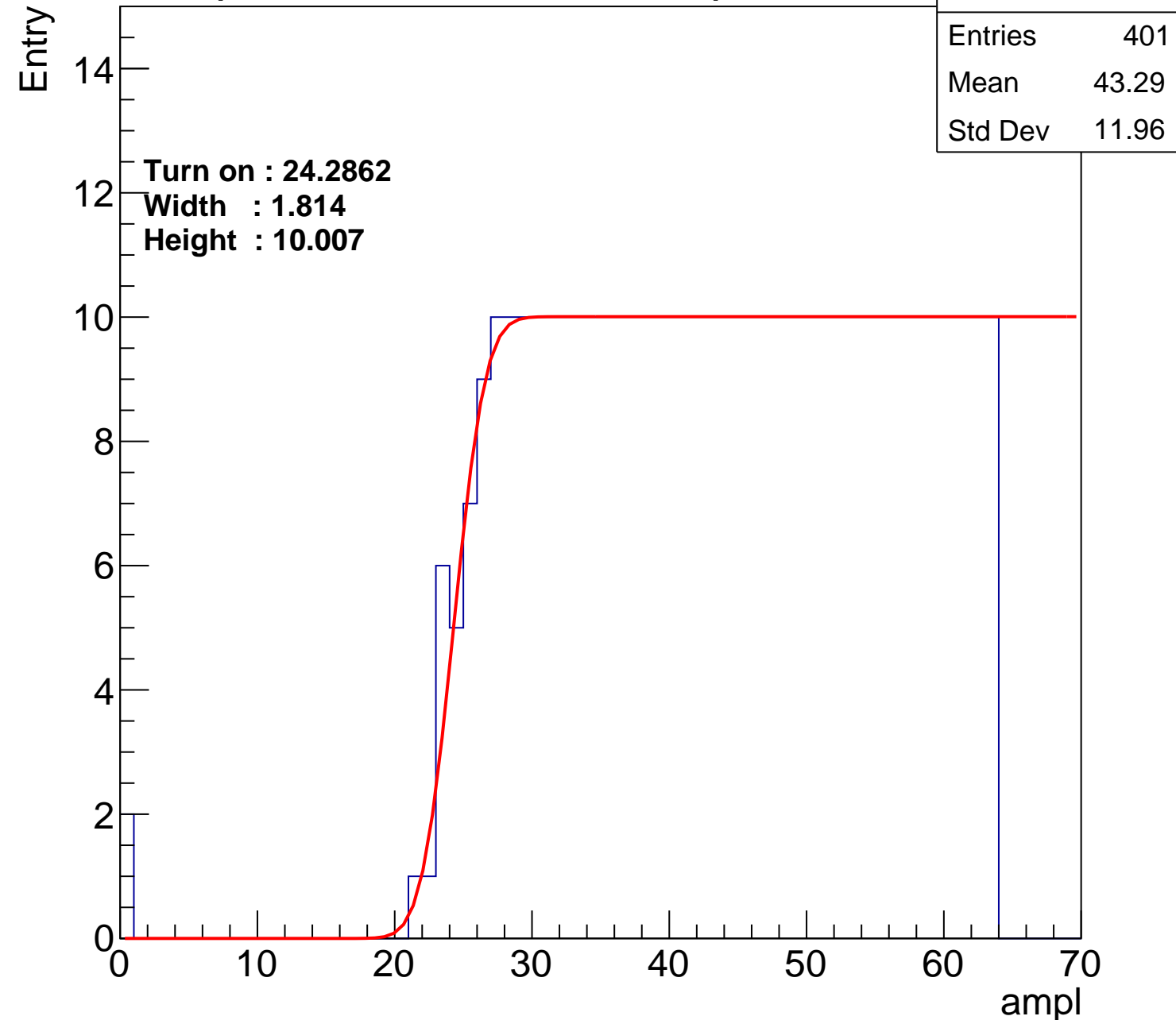
Width : 1.814

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.3649

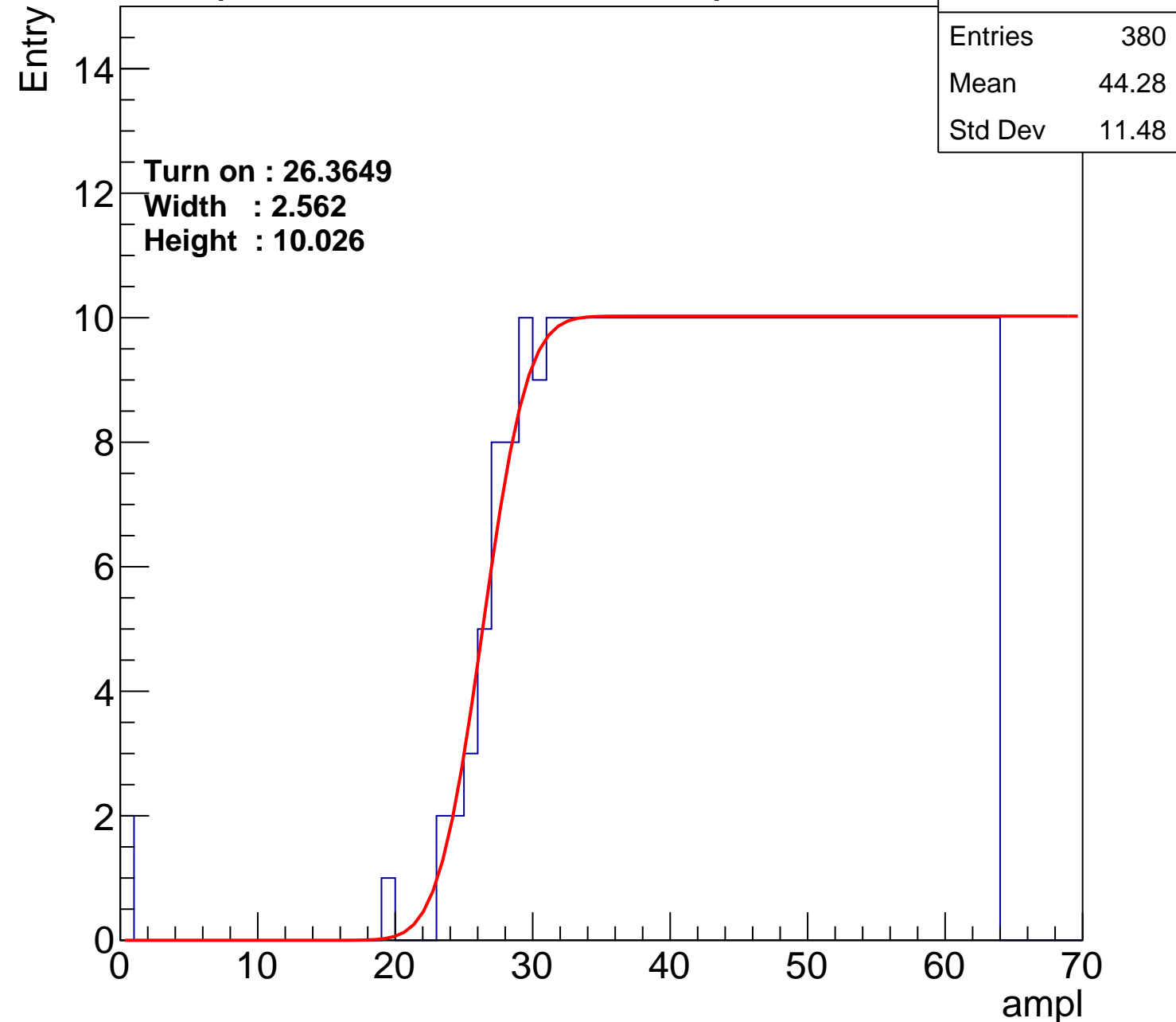
Width : 2.562

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.0222

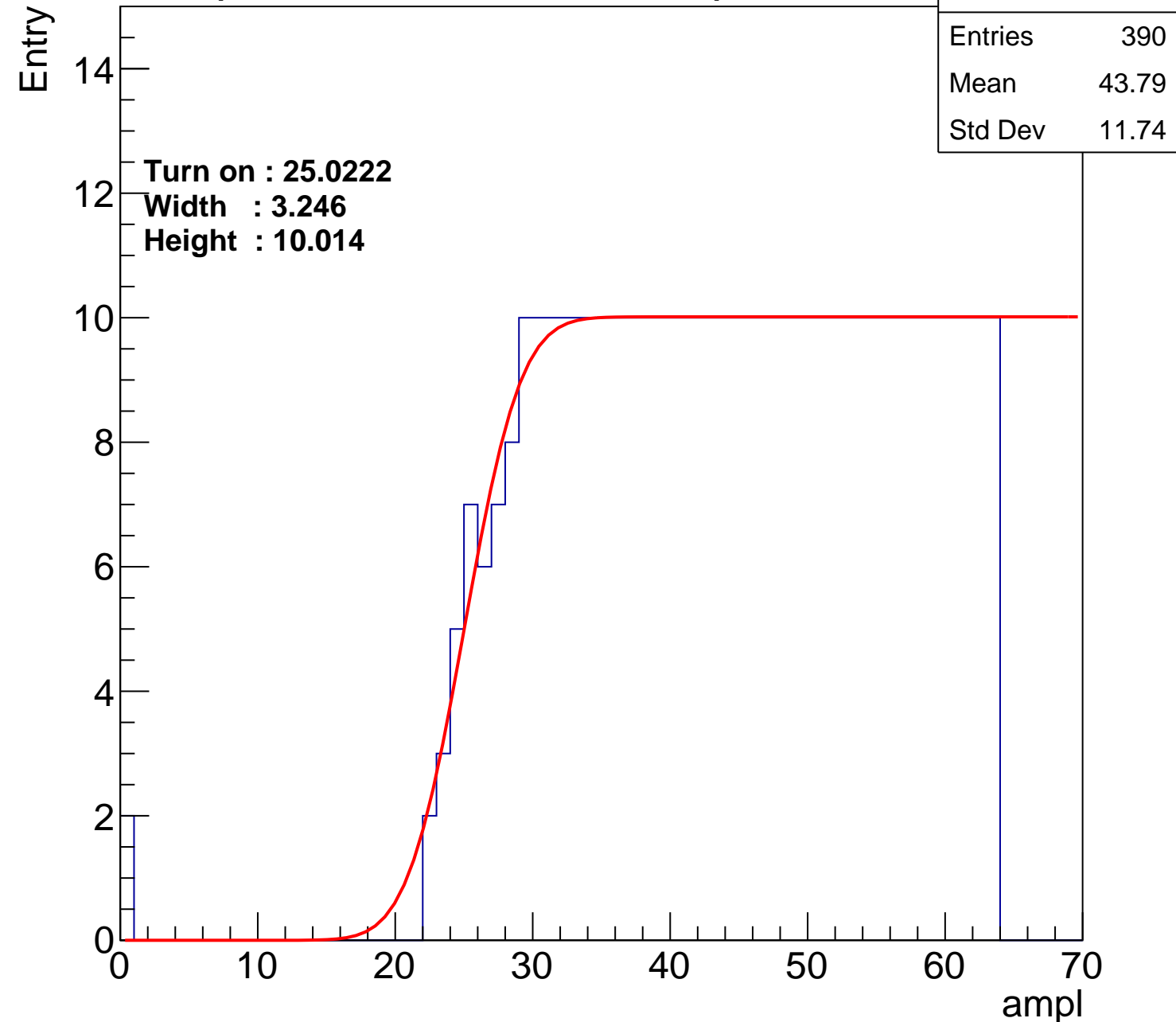
Width : 3.246

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
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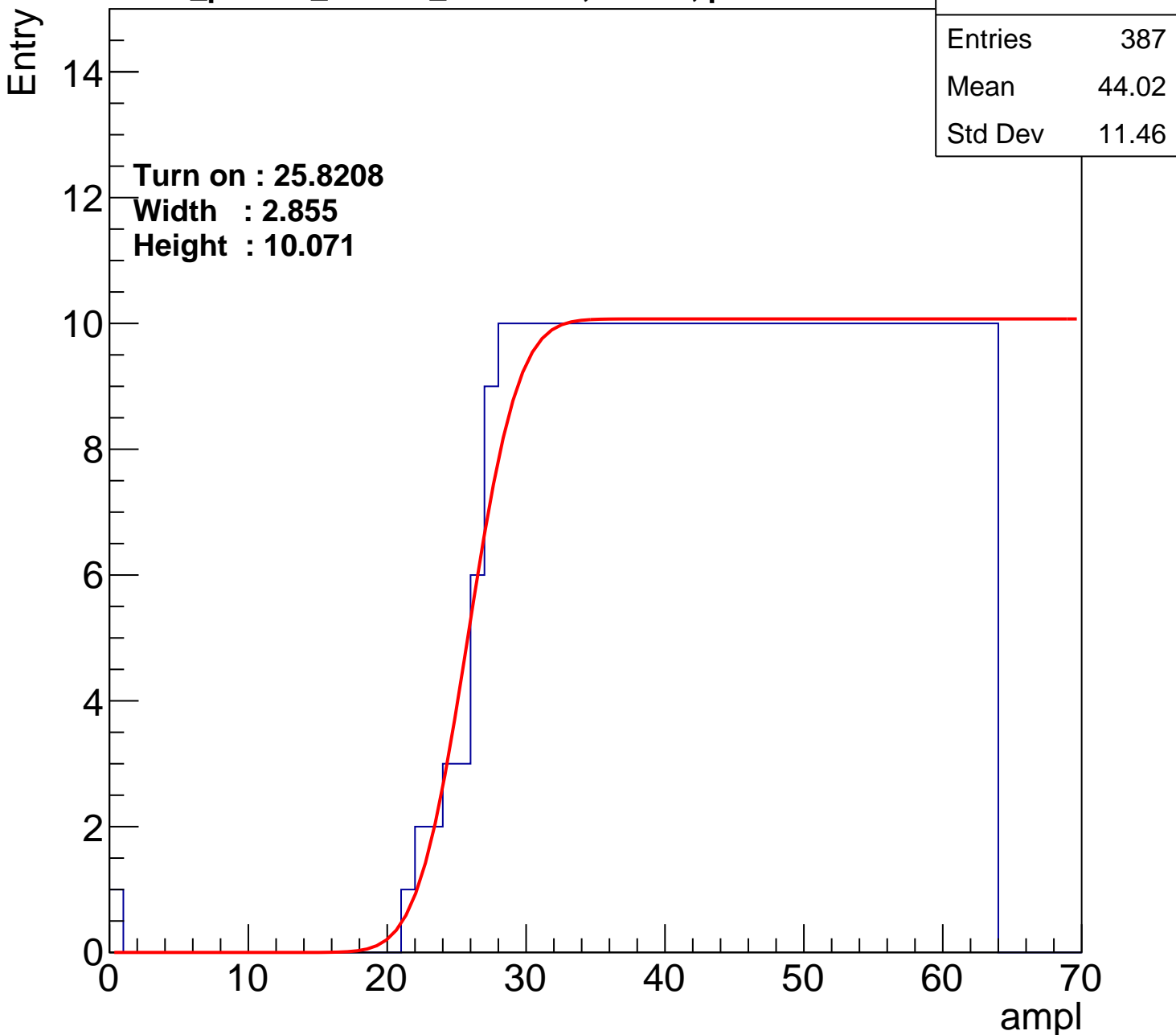
Mean	44.02
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Std Dev	11.46
---------	-------

Turn on : 25.8208

Width : 2.855

Height : 10.071



B1L002S, U9-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 27.1226

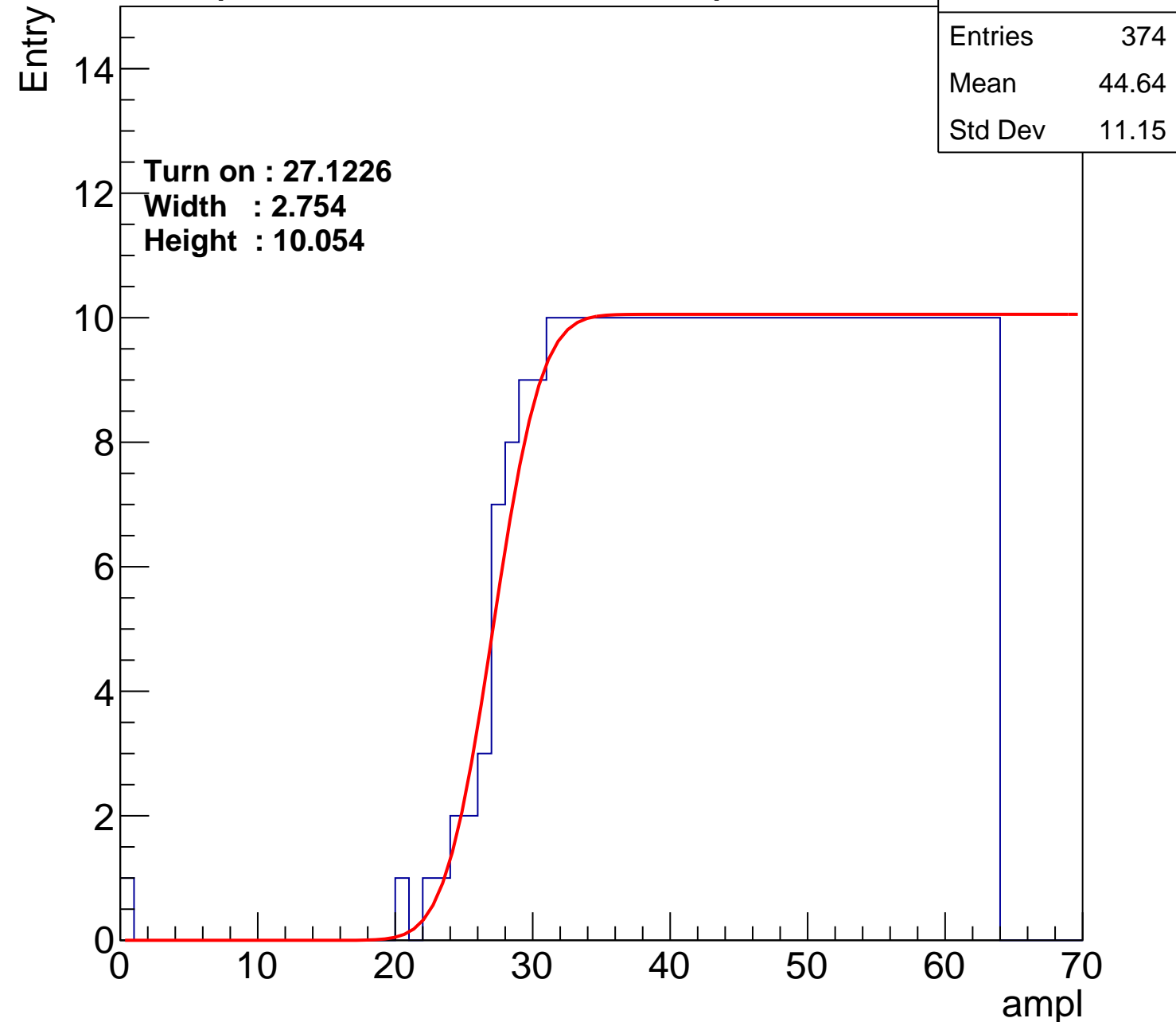
Width : 2.754

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.4715

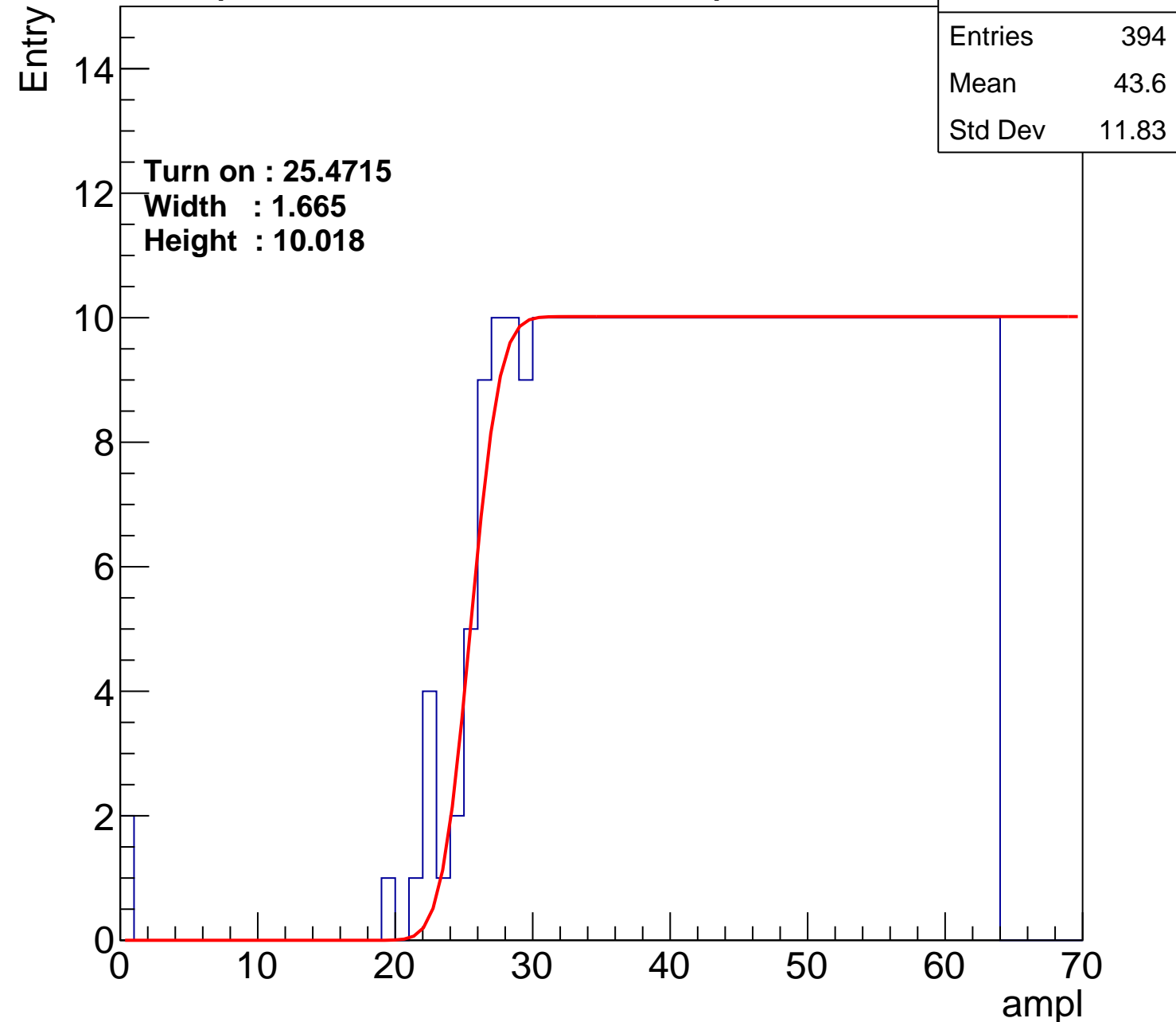
Width : 1.665

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.24
Std Dev	12.15

Turn on : 24.2607

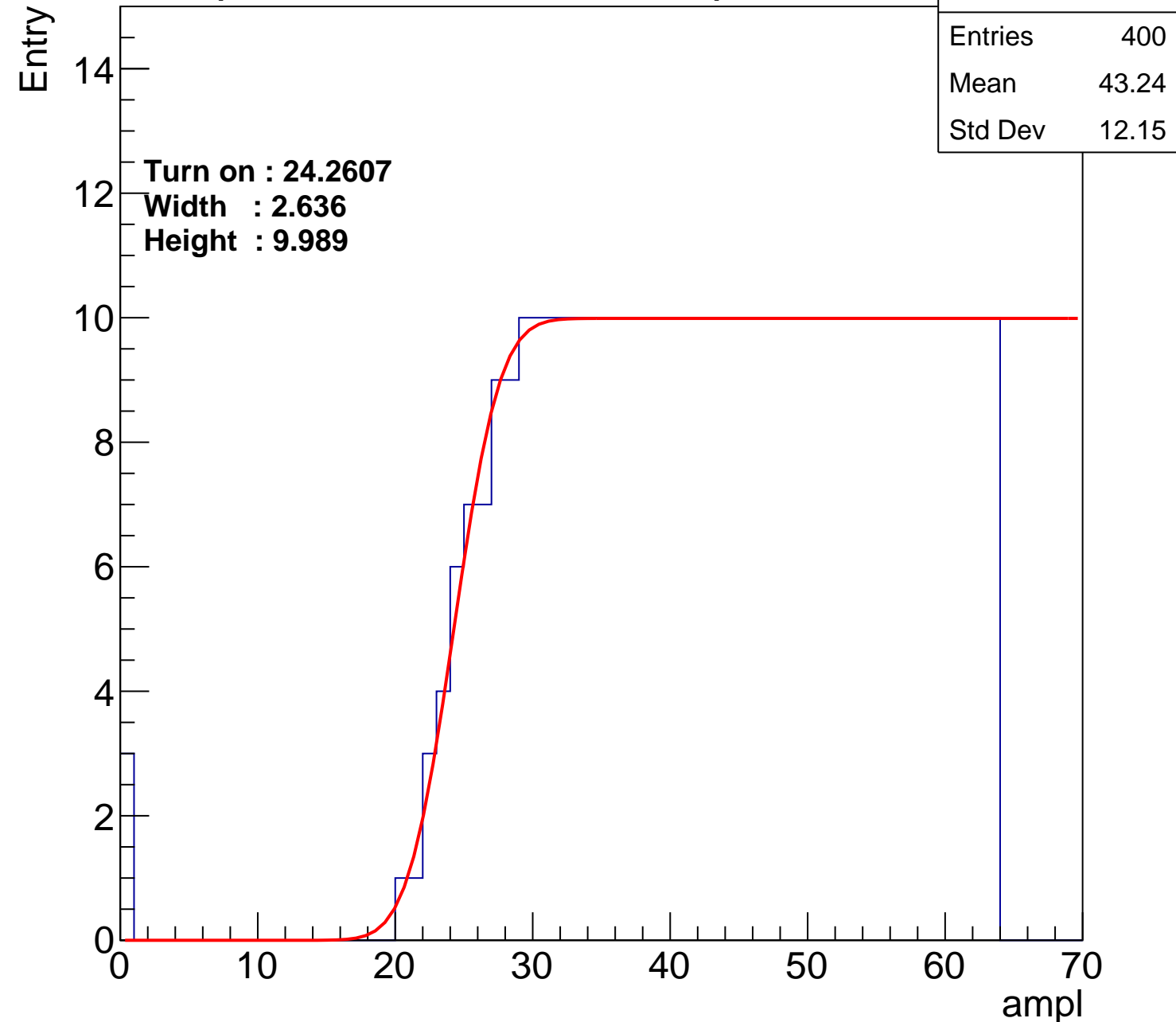
Width : 2.636

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.69
Std Dev	11.26

Turn on : 27.3292

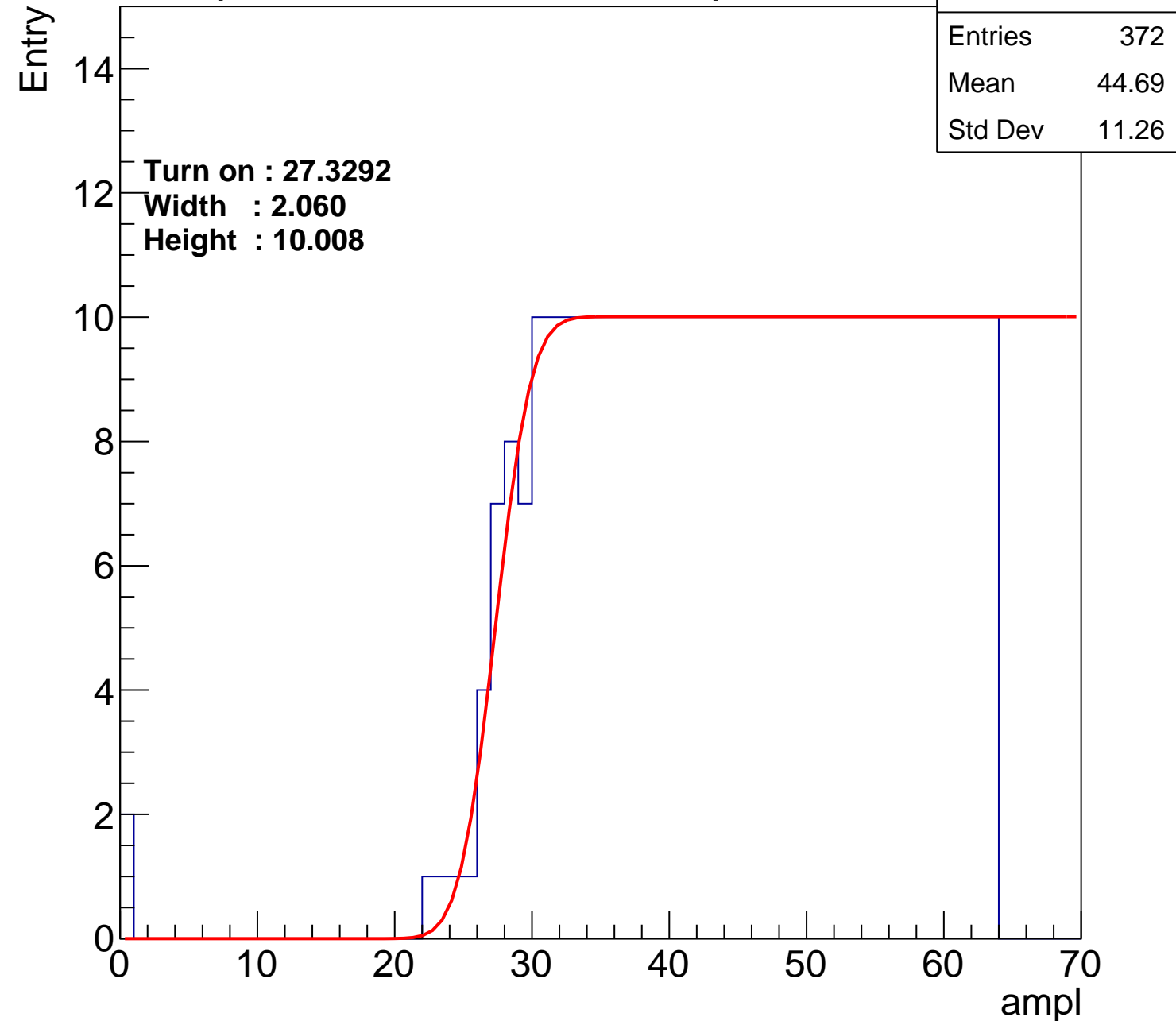
Width : 2.060

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch75

calib_packv5_042523_0143.root, FC#10, port B3

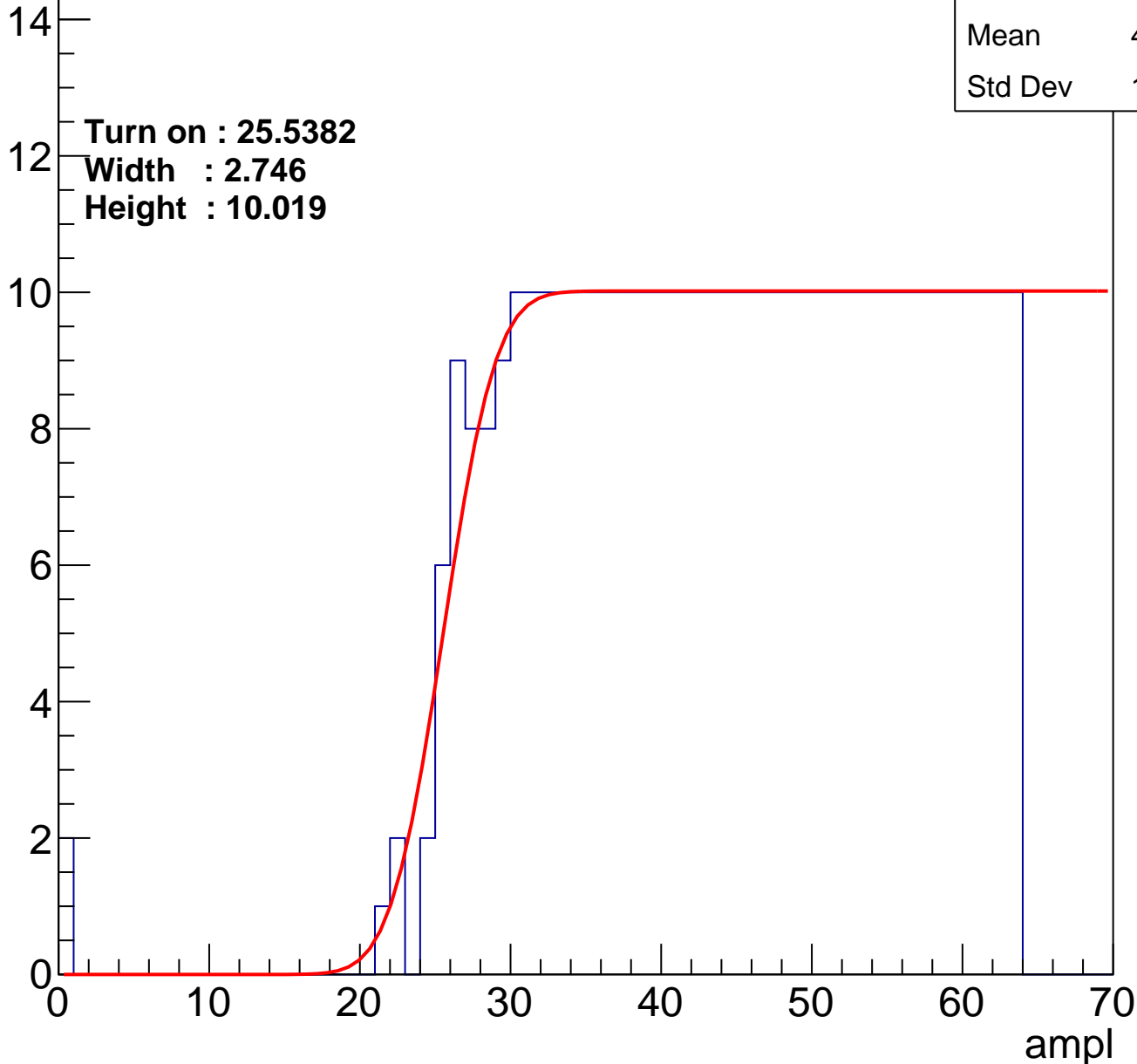
Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.5382

Width : 2.746

Height : 10.019

Entry



B1L002S, U9-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.57
Std Dev	11.71

Turn on : 24.5266

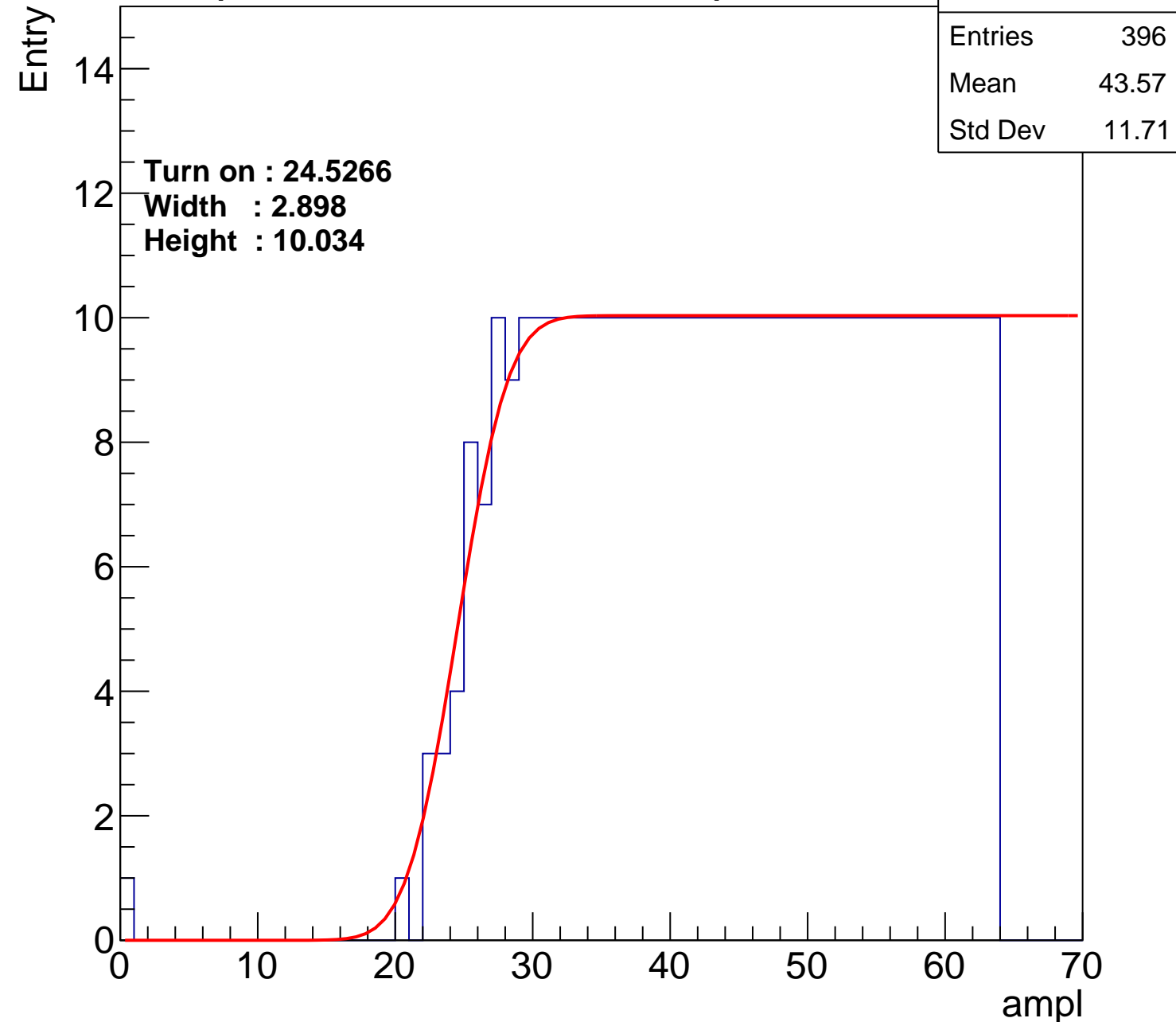
Width : 2.898

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.57
Std Dev	11.16

Turn on : 26.4706

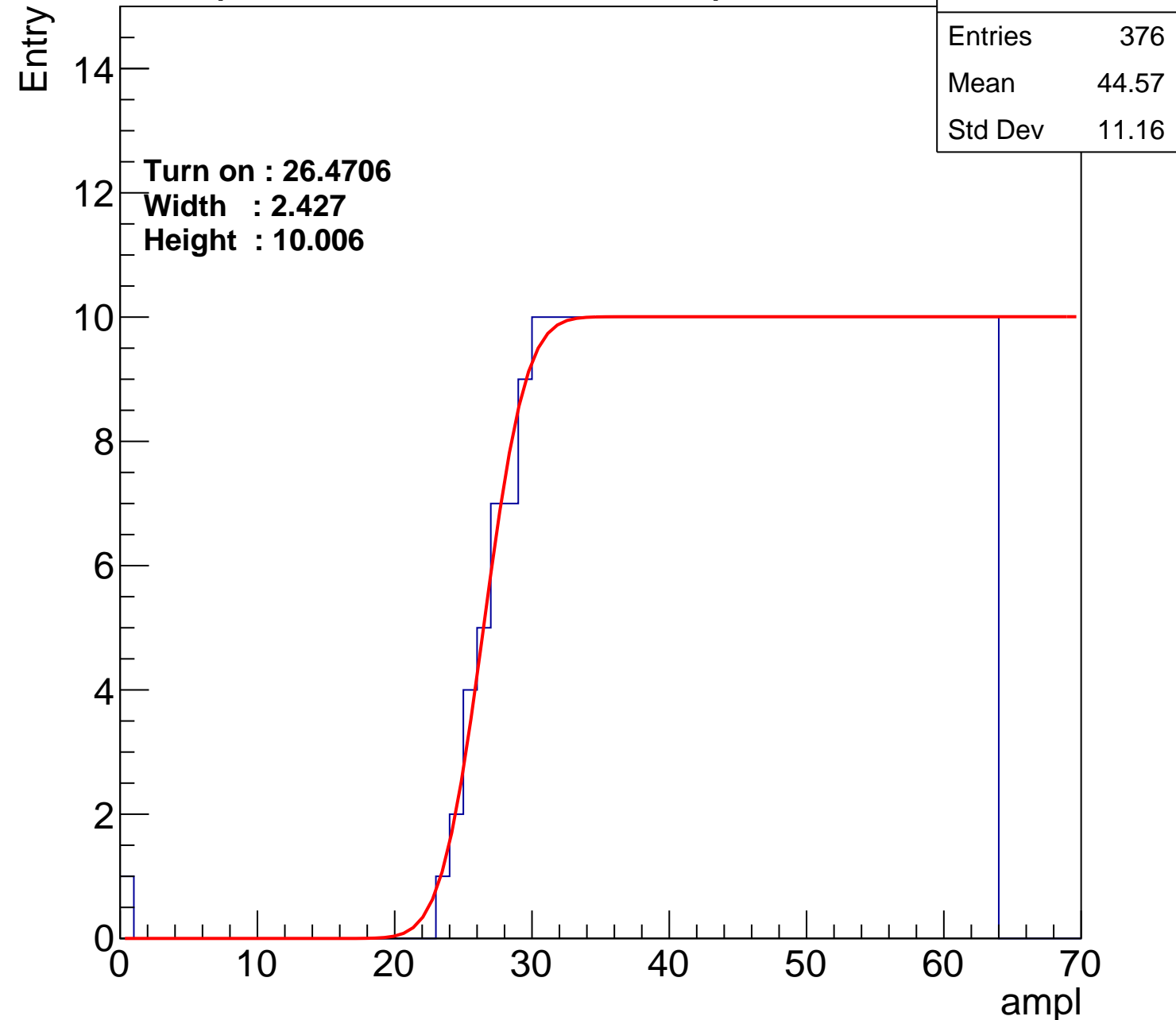
Width : 2.427

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 25.5991

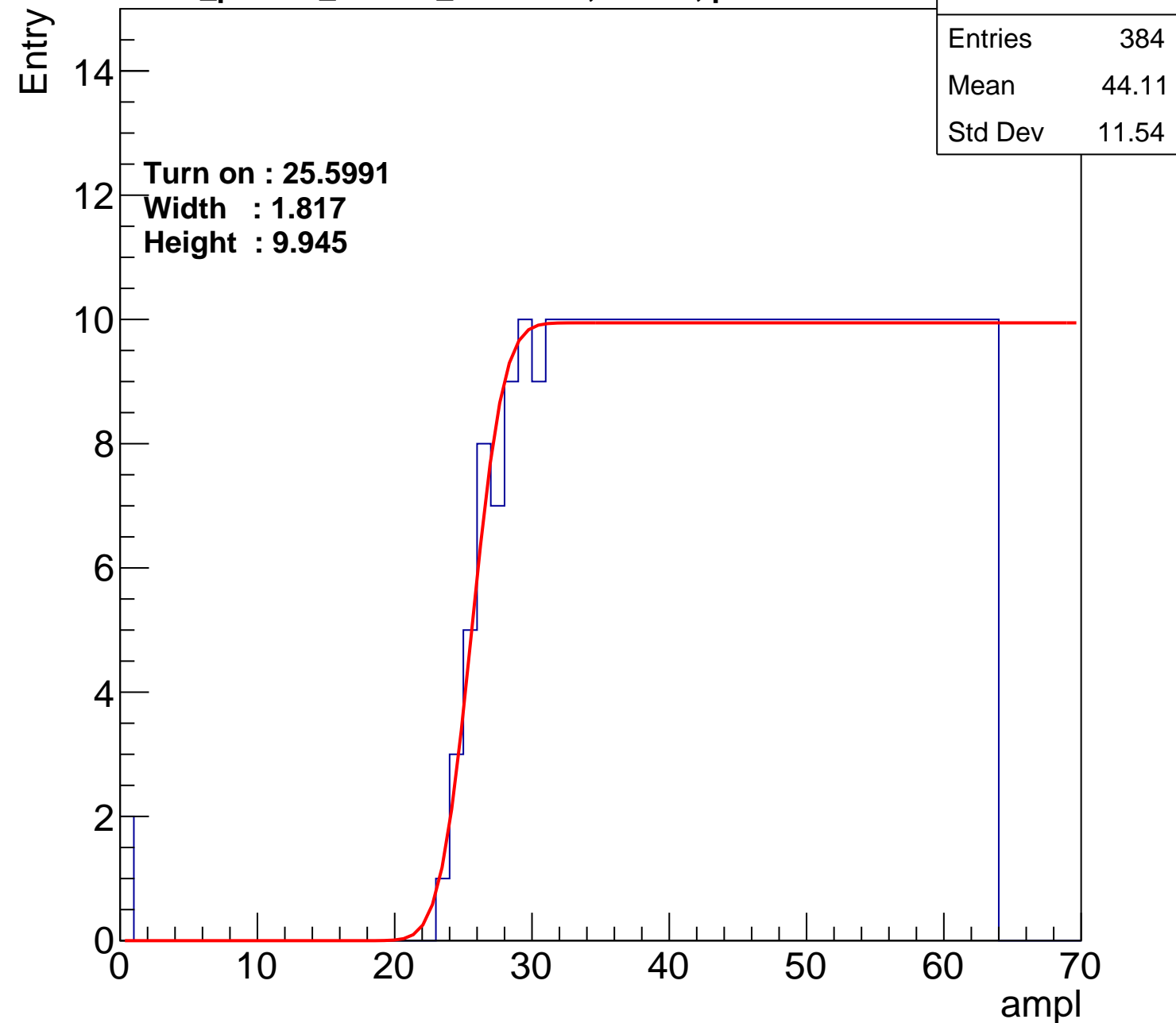
Width : 1.817

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.13
Std Dev	11.94

Turn on : 23.8691

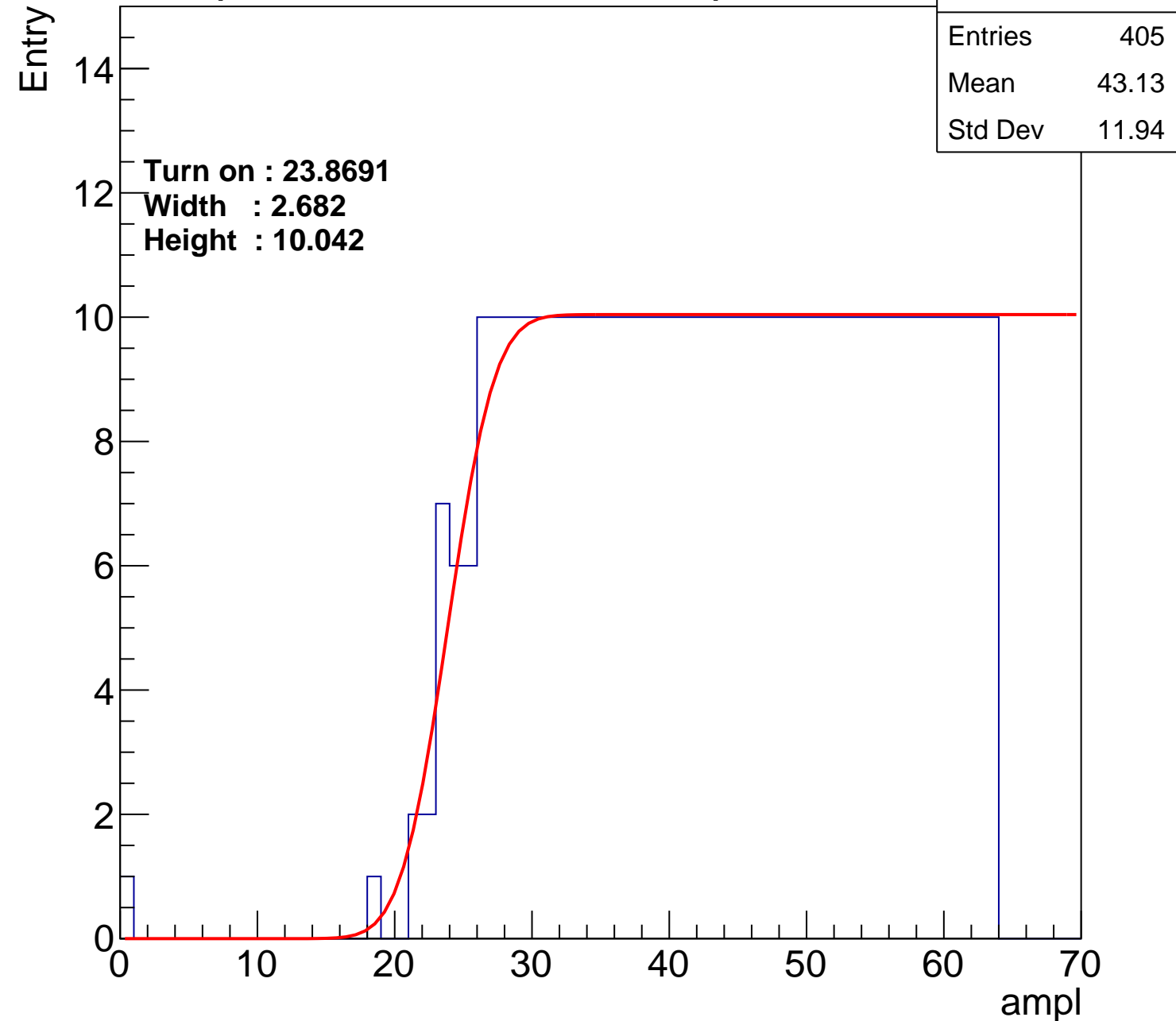
Width : 2.682

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.65
Std Dev	12.1

Turn on : 25.4172

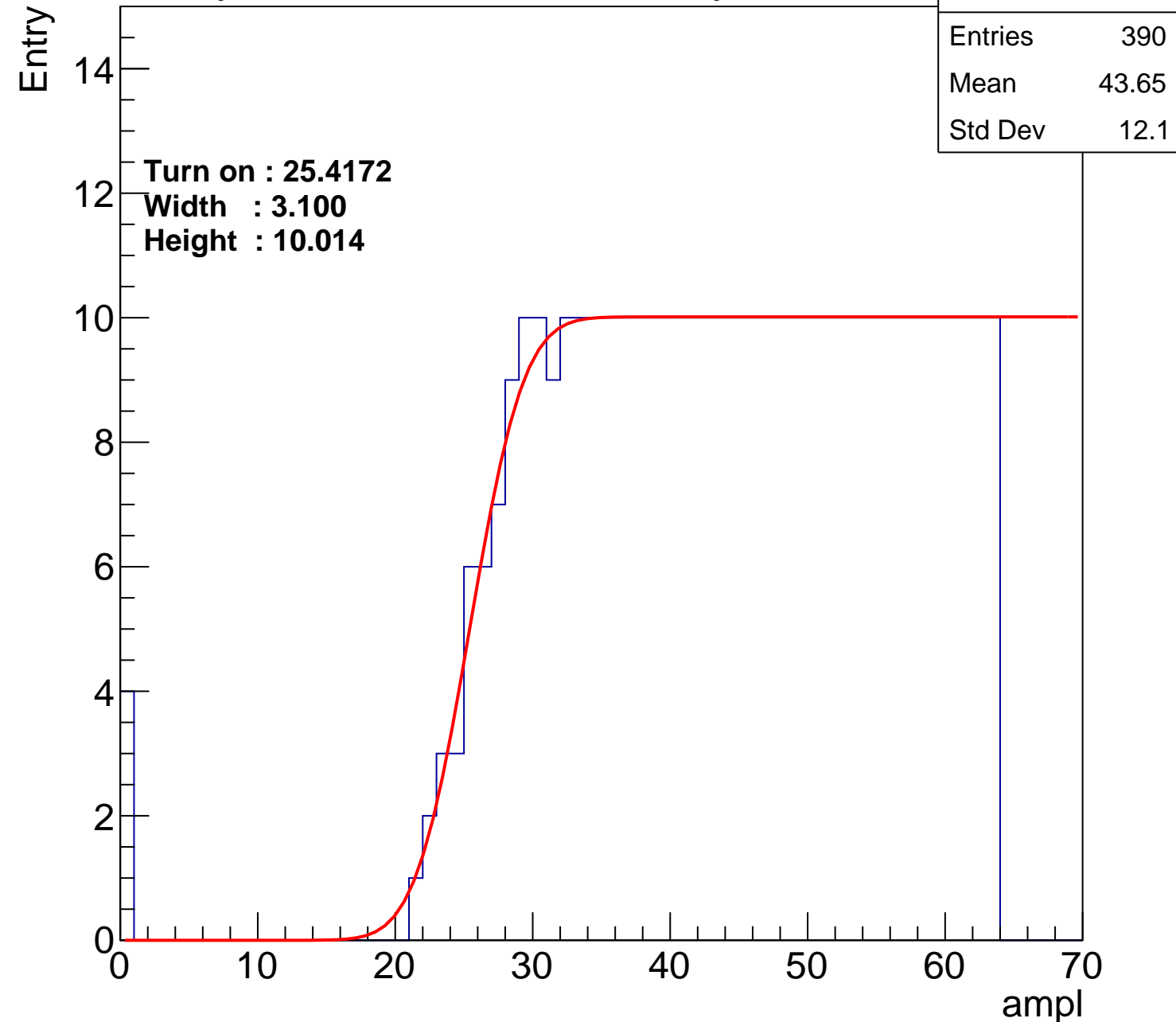
Width : 3.100

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	43
Std Dev	12.15

Turn on : 22.7813

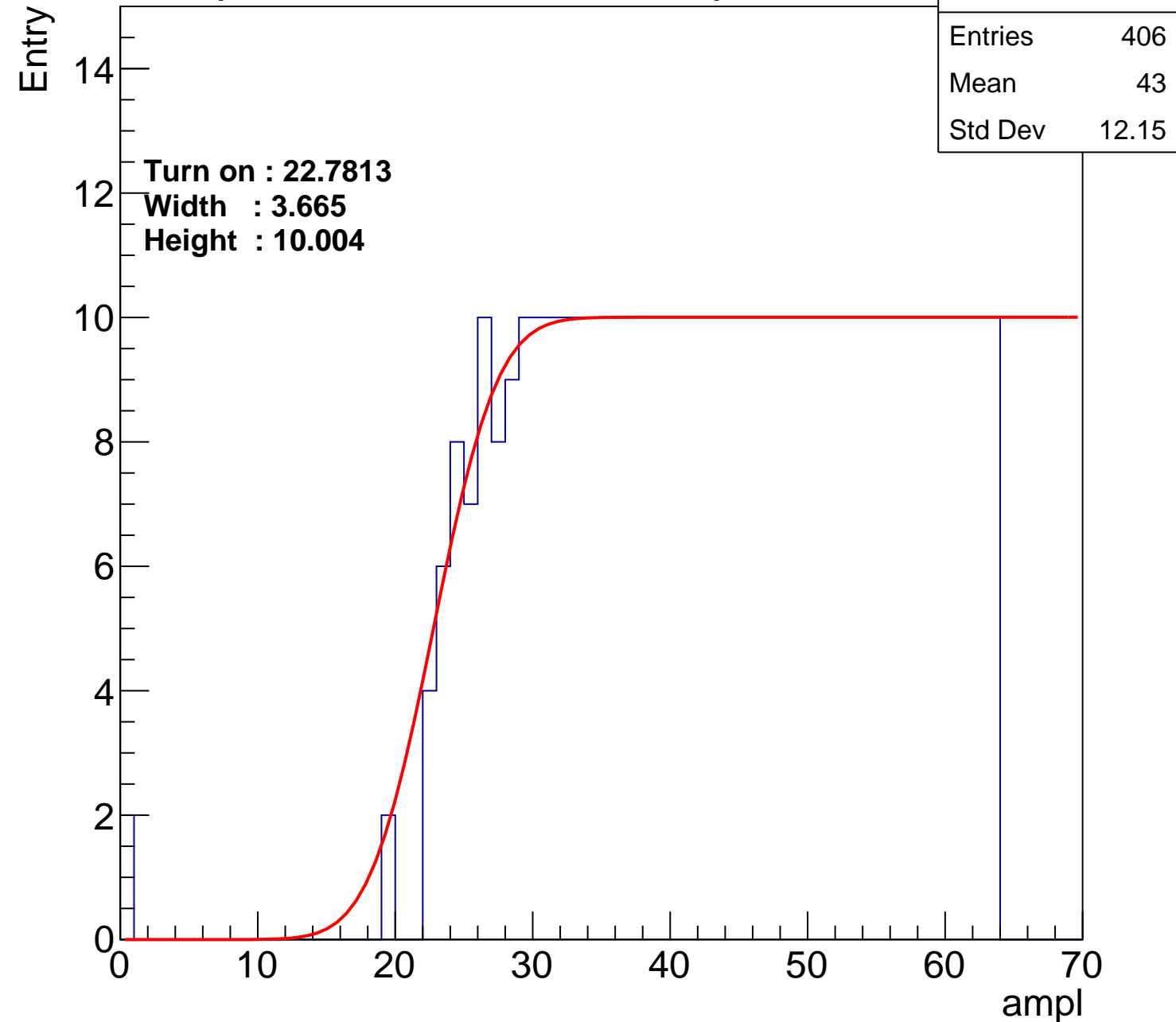
Width : 3.665

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.41
Std Dev	11.92

Turn on : 23.9055

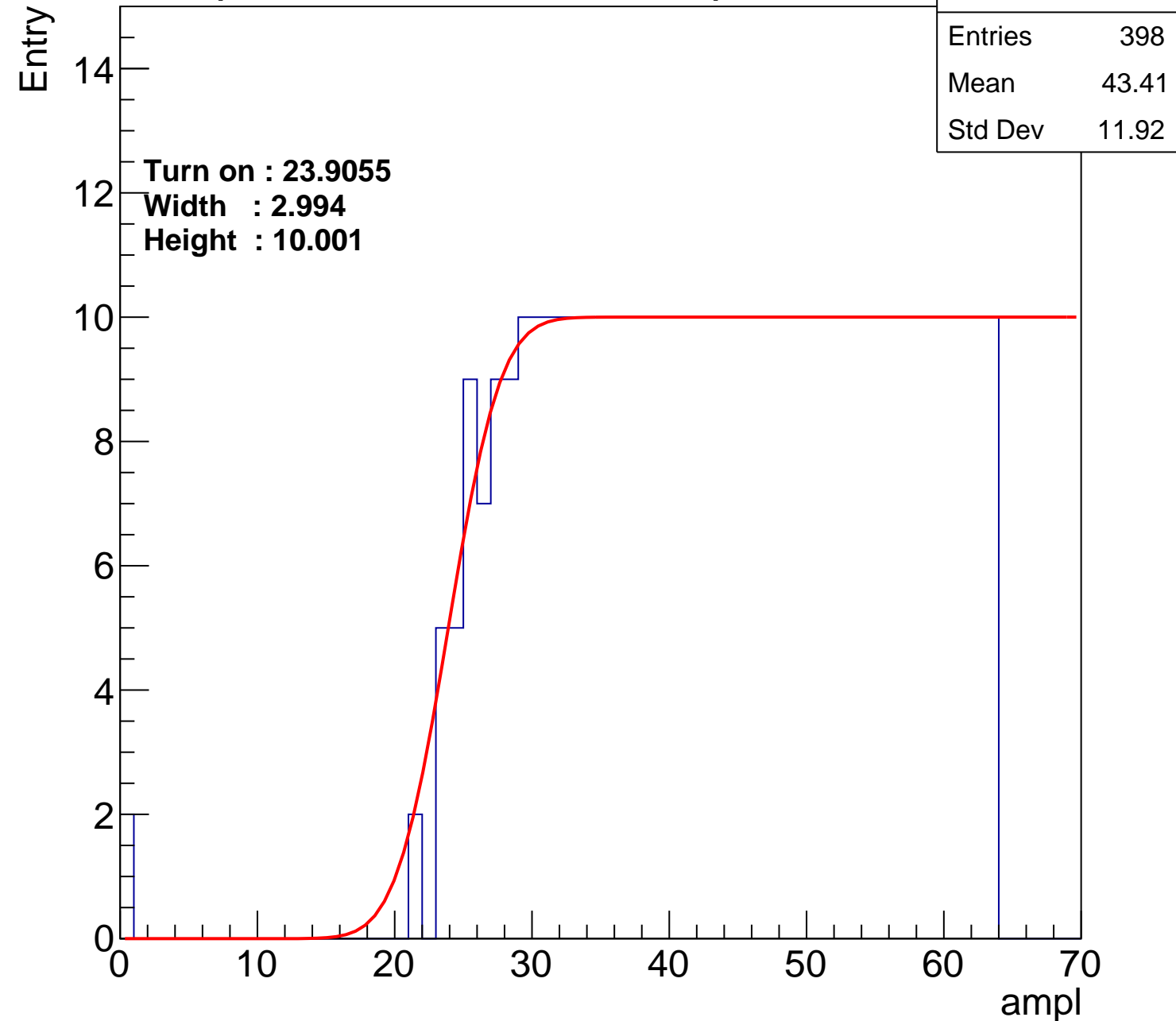
Width : 2.994

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.54
Std Dev	12.4

Turn on : 25.6007

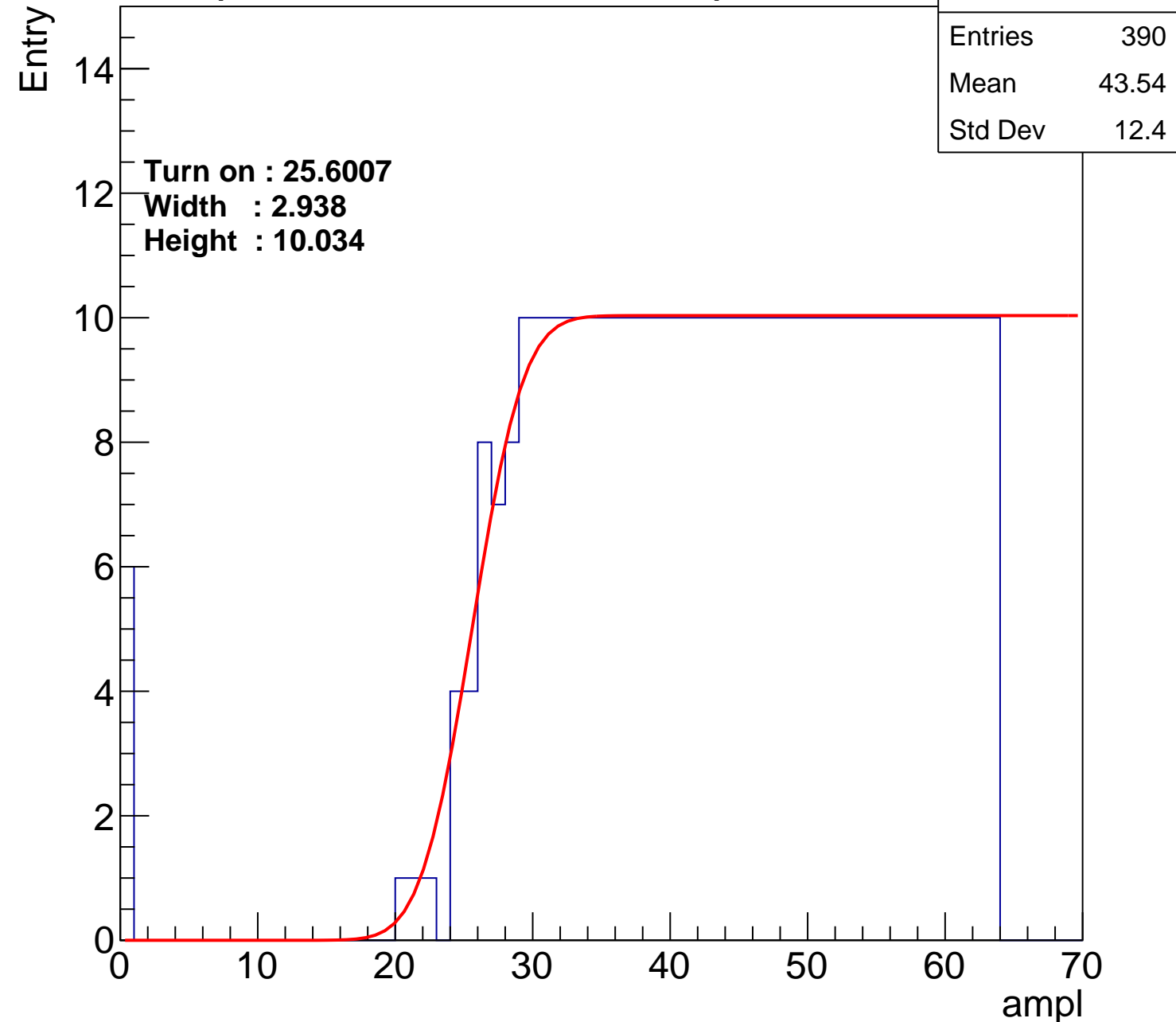
Width : 2.938

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 24.9514

Width : 3.292

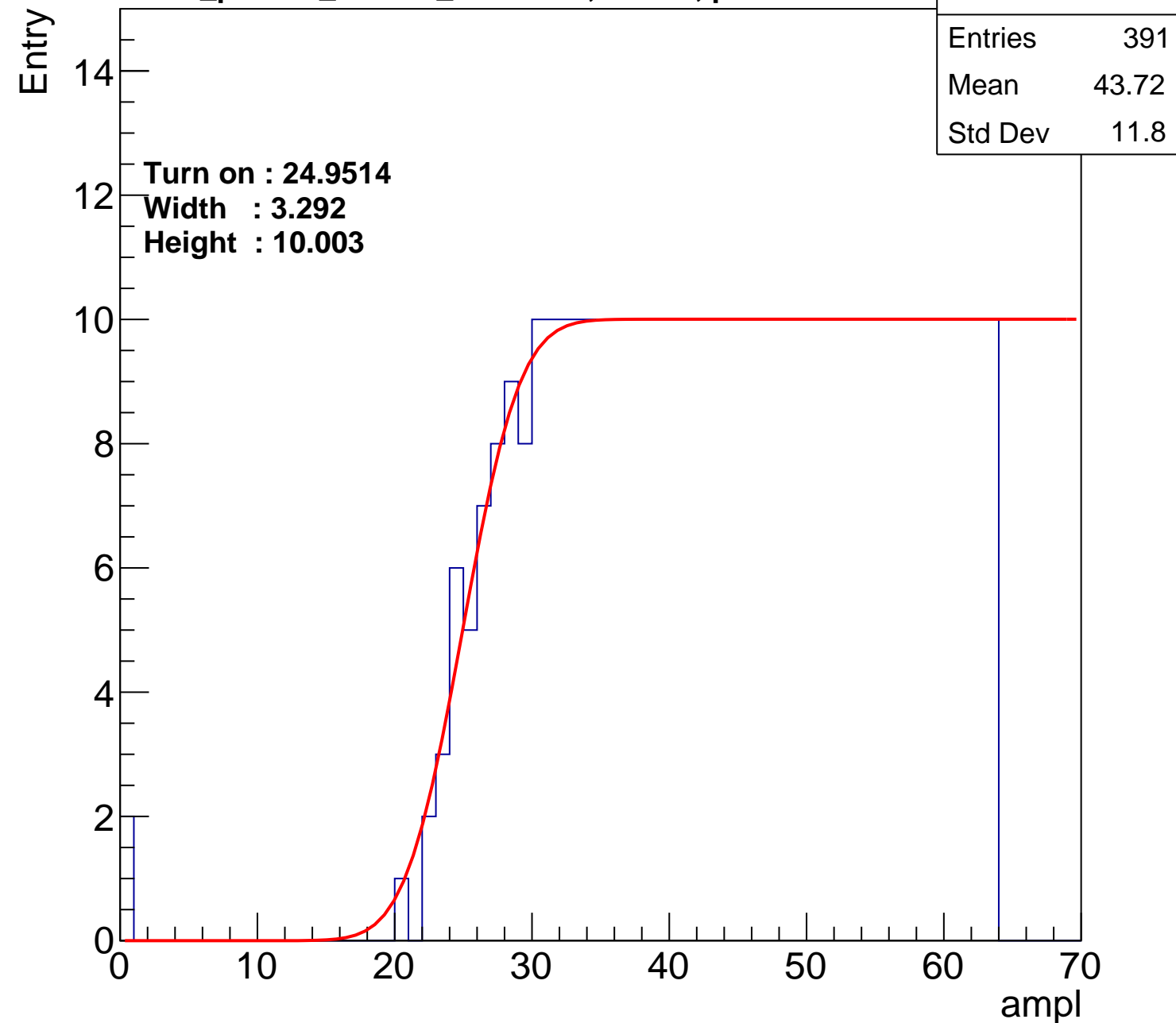
Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U9-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.03
Std Dev	11.76

Turn on : 26.2492

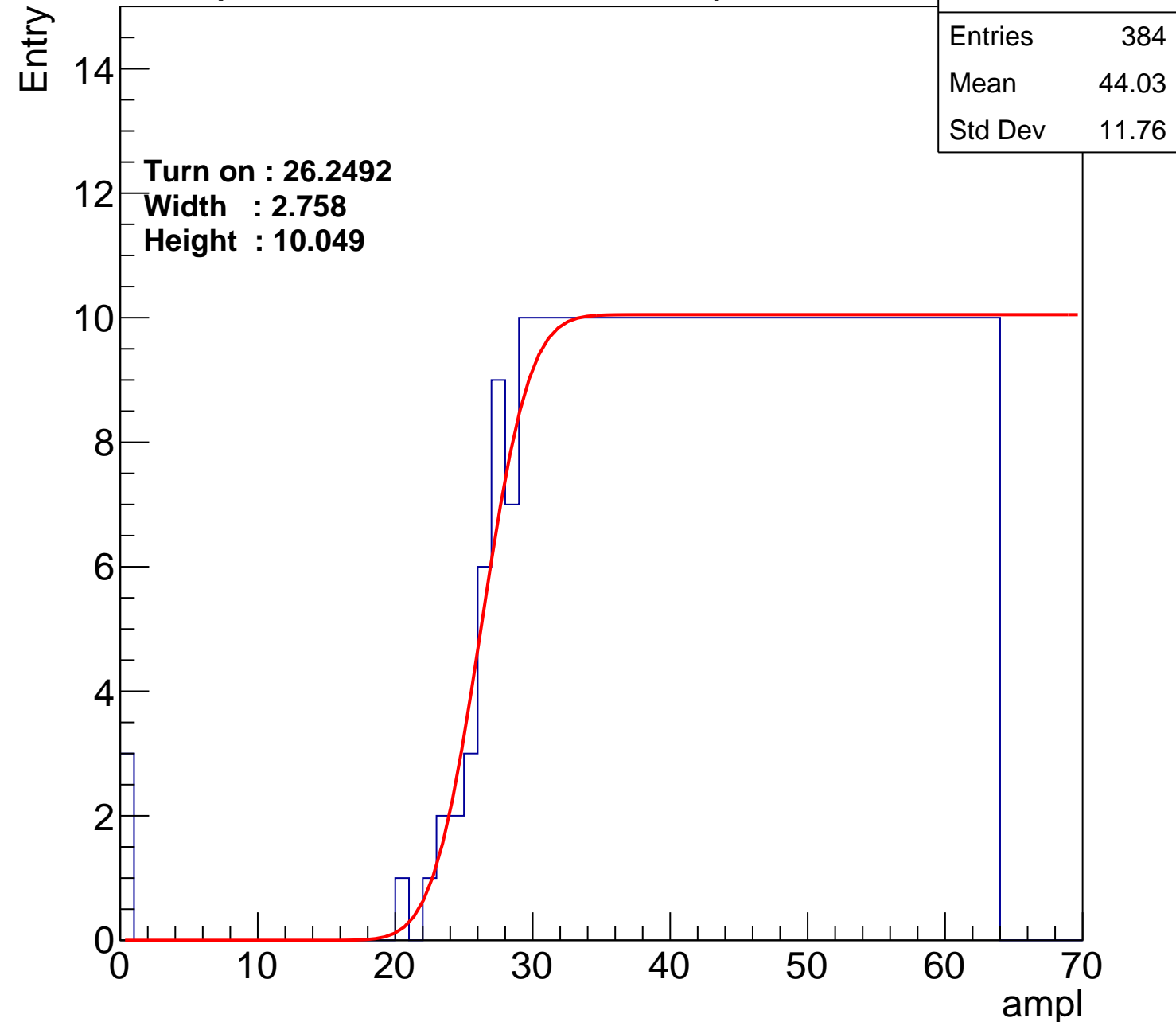
Width : 2.758

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.35
Std Dev	11.97

Turn on : 24.5261

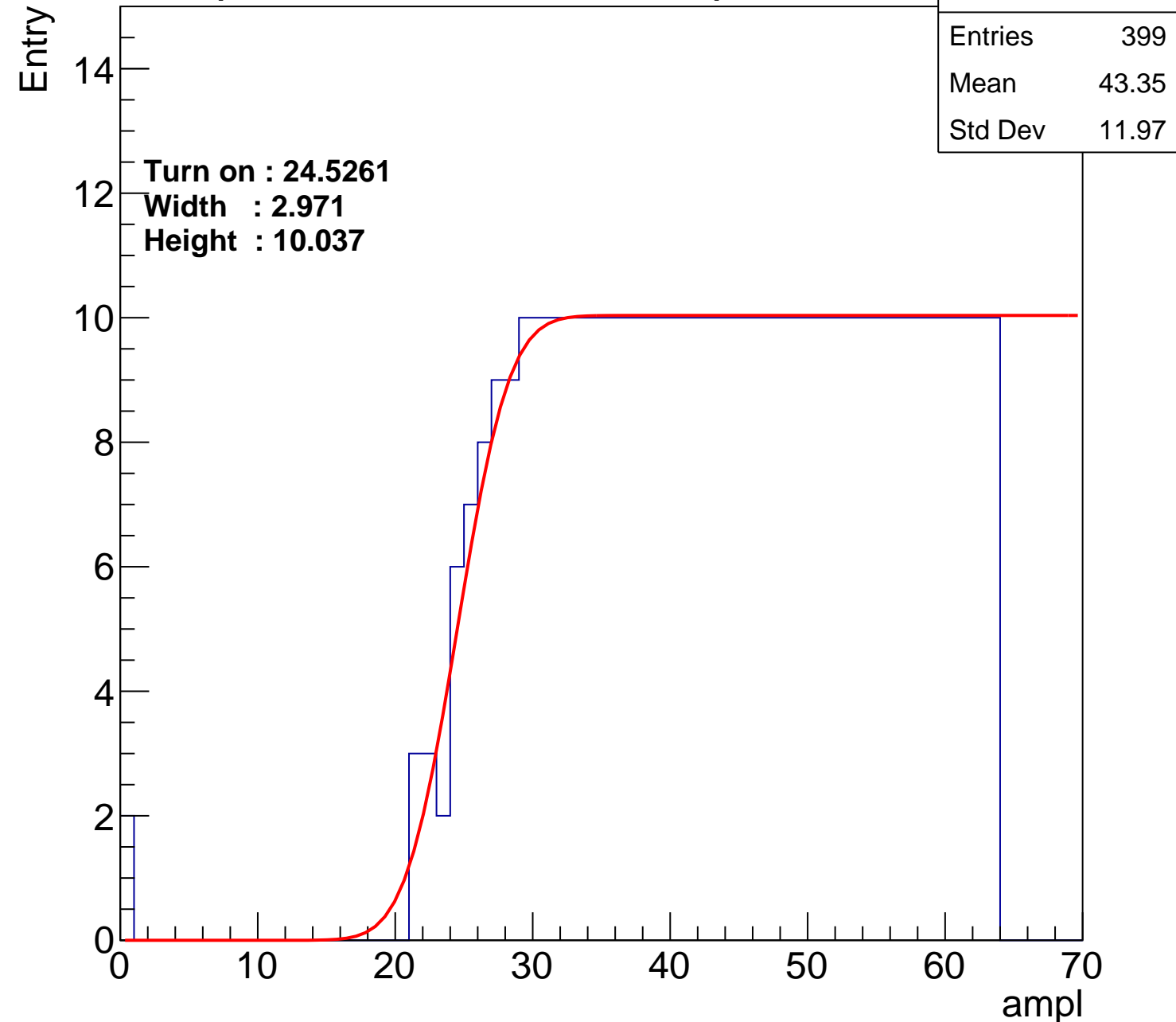
Width : 2.971

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.86

Turn on : 25.1489

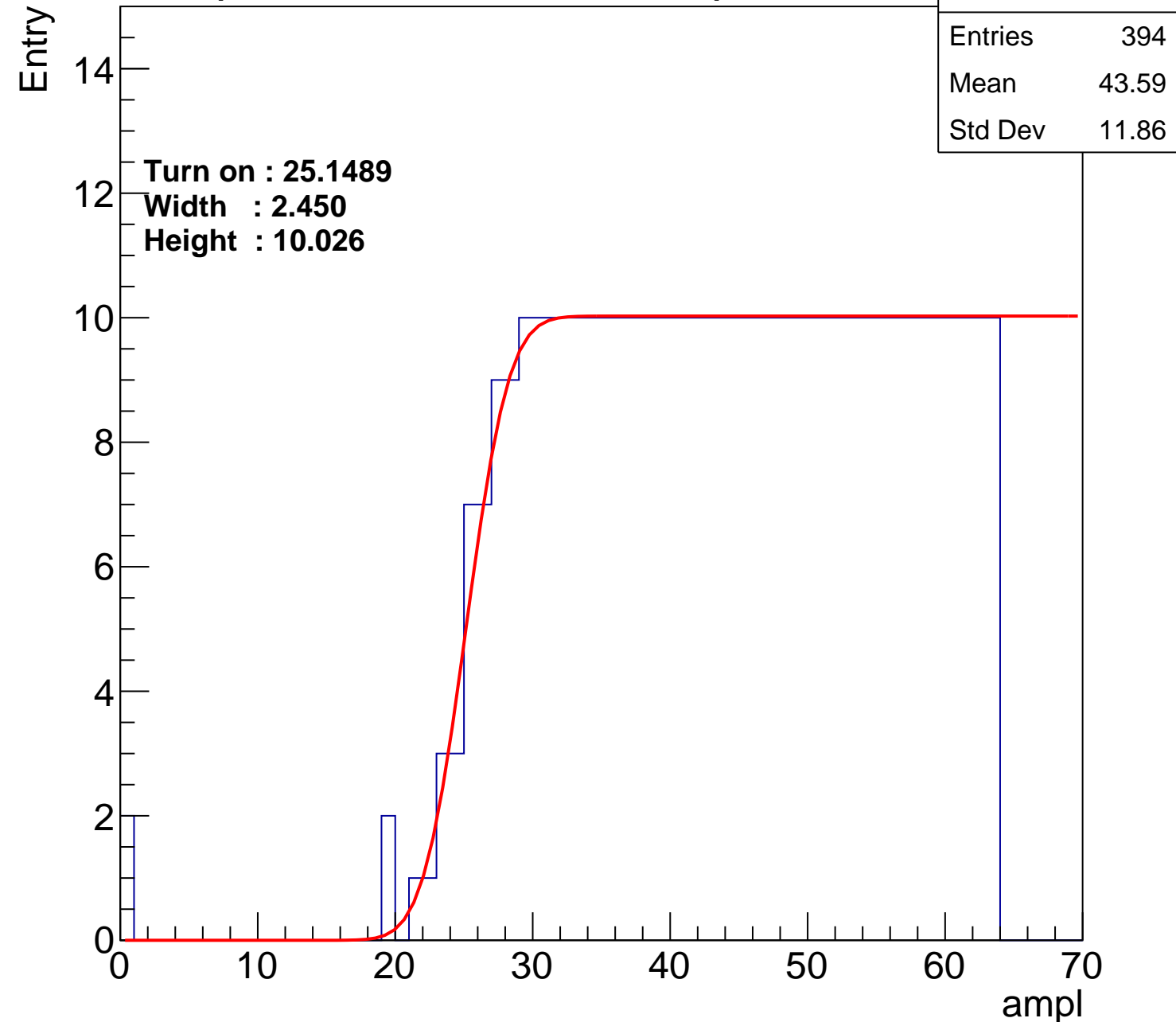
Width : 2.450

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.46
Std Dev	12.18

Turn on : 25.0115

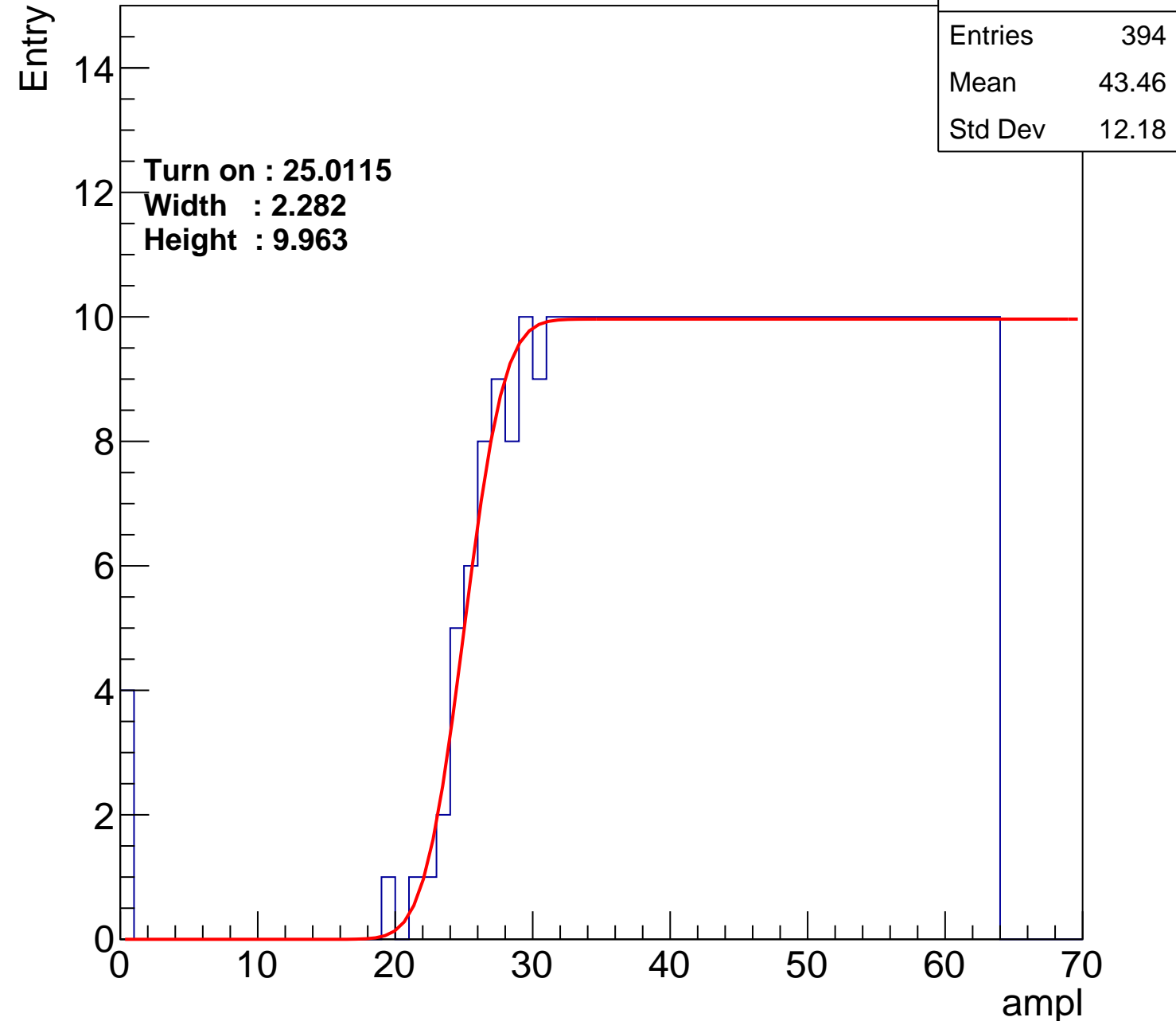
Width : 2.282

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch89

calib_packv5_042523_0143.root, FC#10, port B3

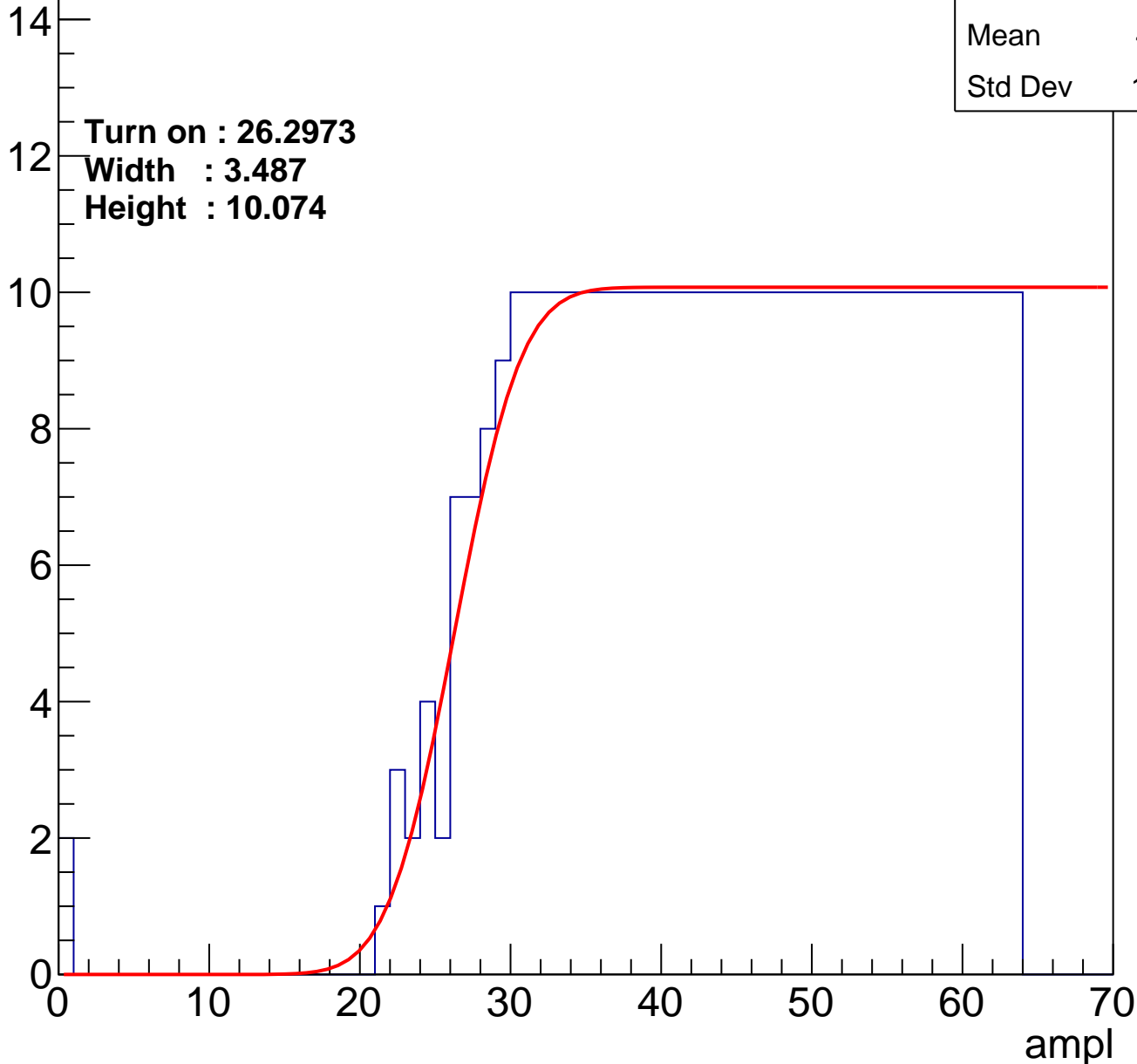
Entry

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 26.2973

Width : 3.487

Height : 10.074



B1L002S, U9-ch90

calib_packv5_042523_0143.root, FC#10, port B3

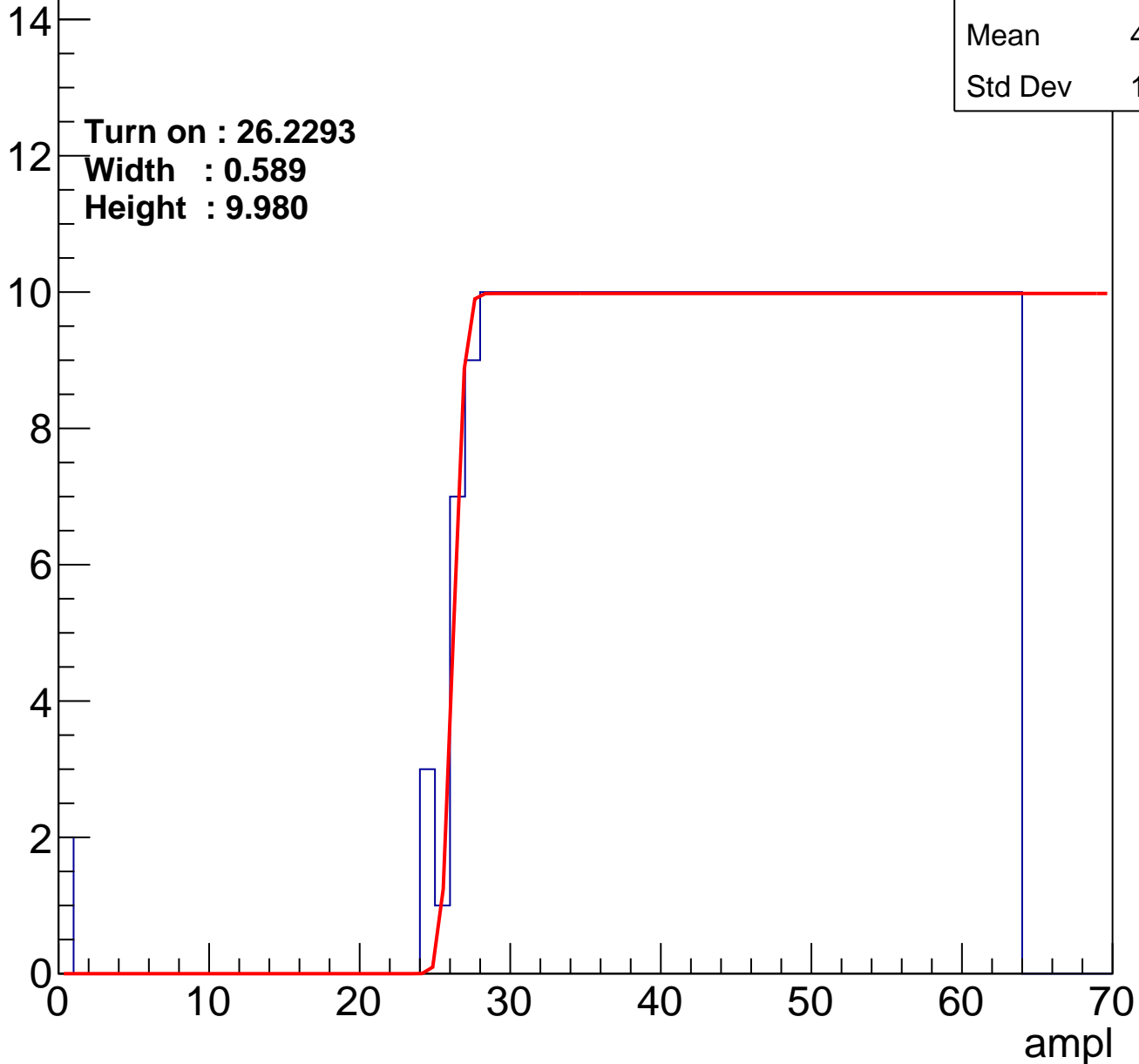
Entries	382
Mean	44.25
Std Dev	11.43

Turn on : 26.2293

Width : 0.589

Height : 9.980

Entry



B1L002S, U9-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.3296

Width : 2.179

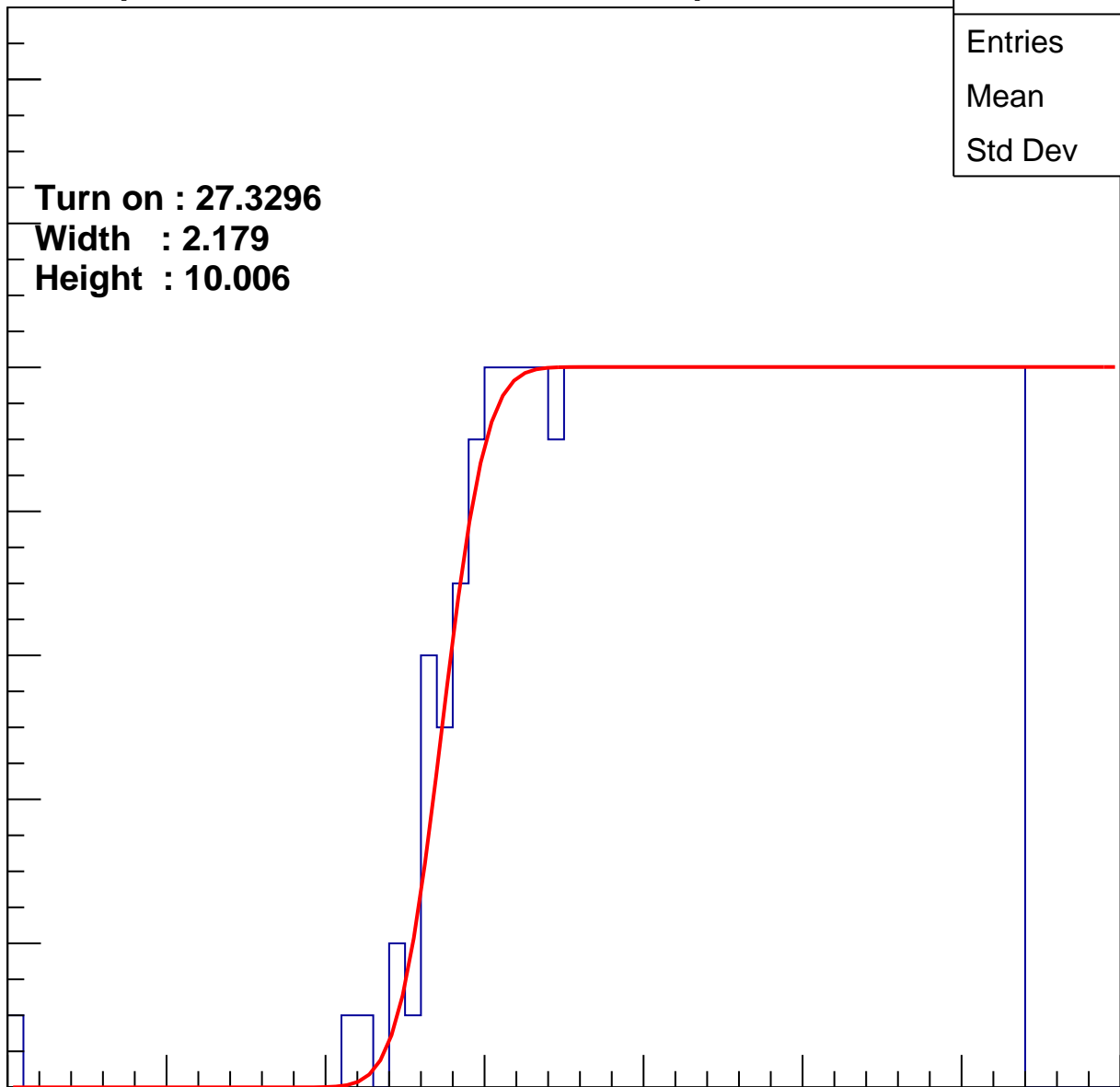
Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U9-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.98
Std Dev	11.69

Turn on : 25.9070

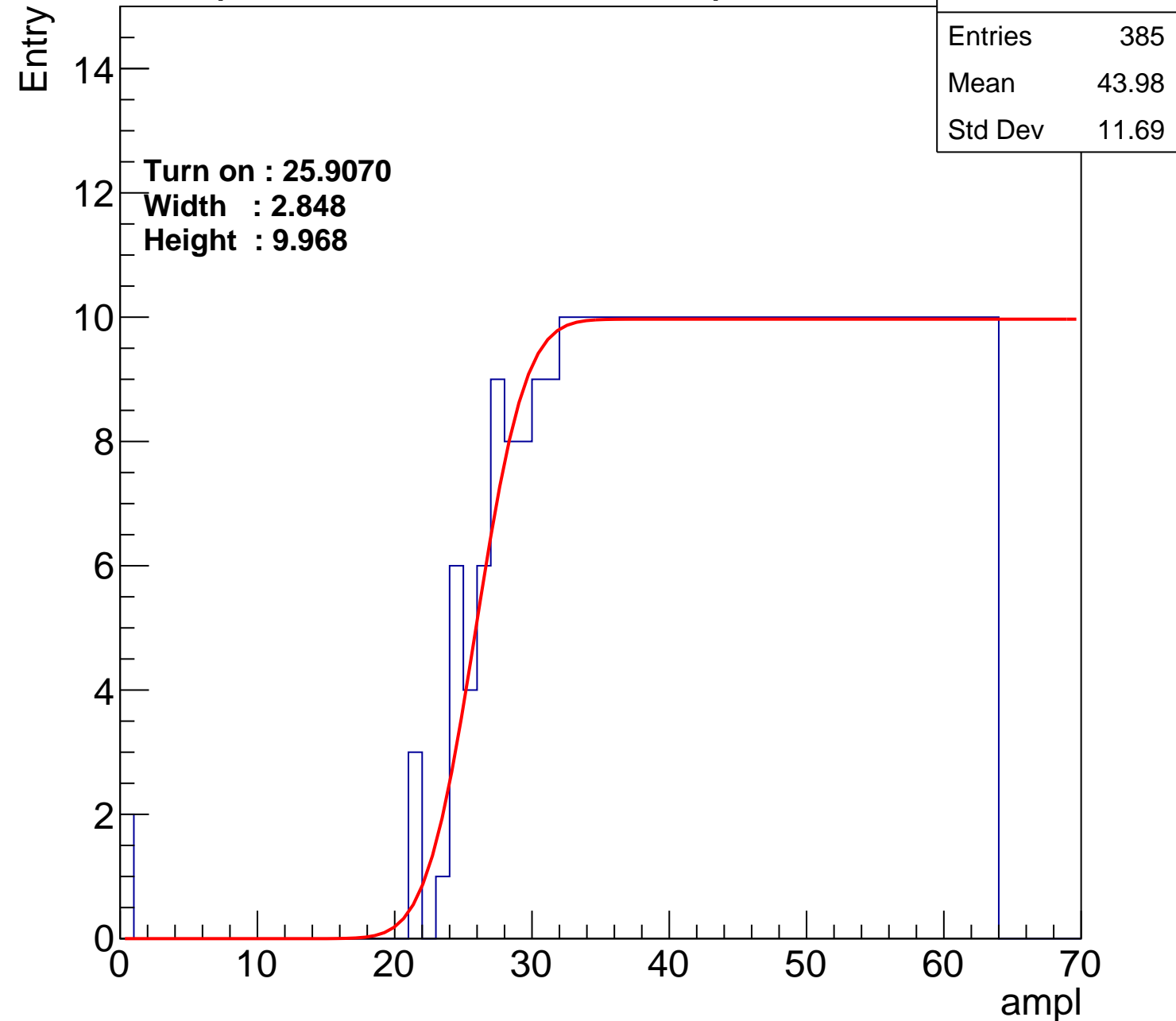
Width : 2.848

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 25.8875

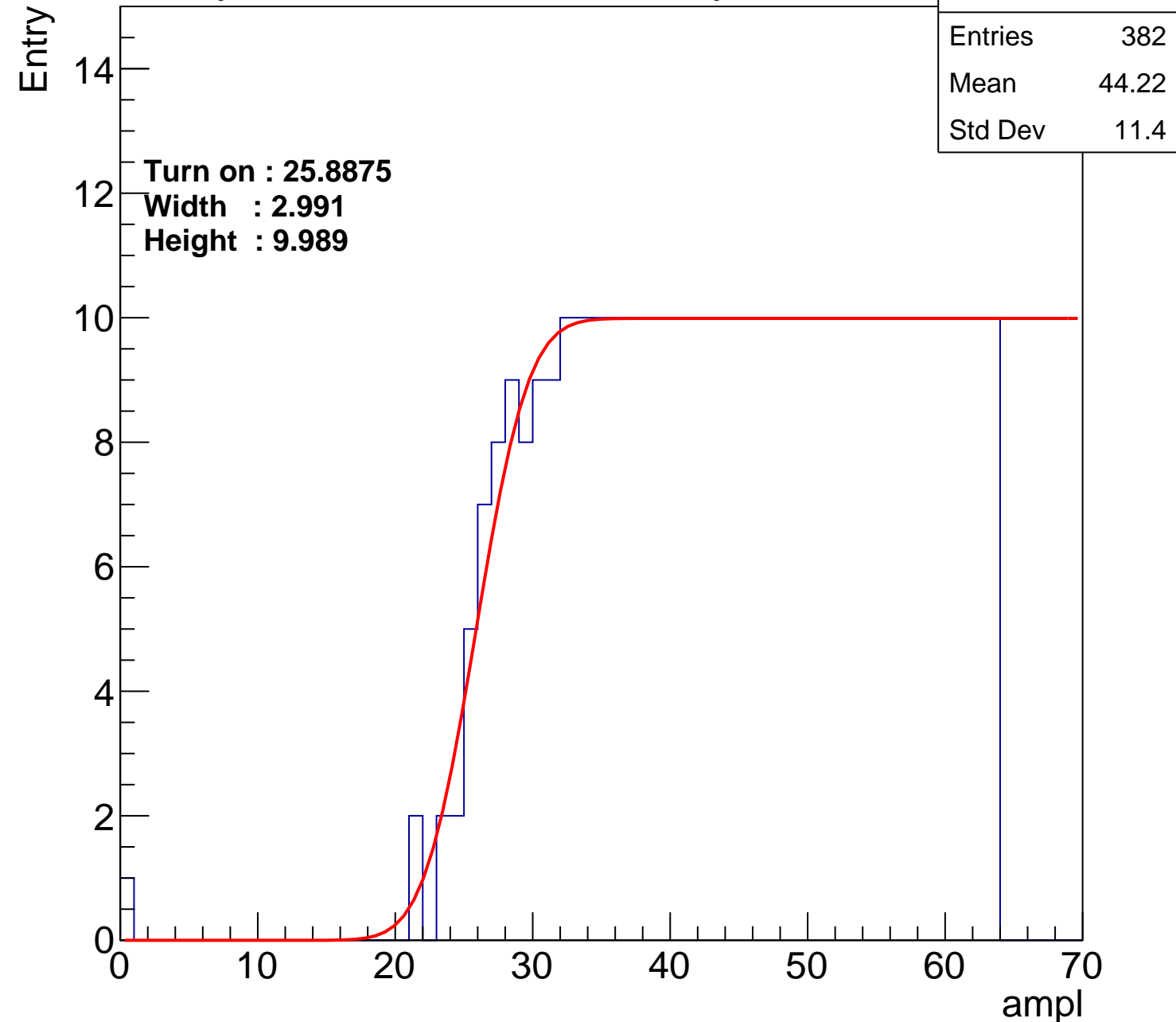
Width : 2.991

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.4947

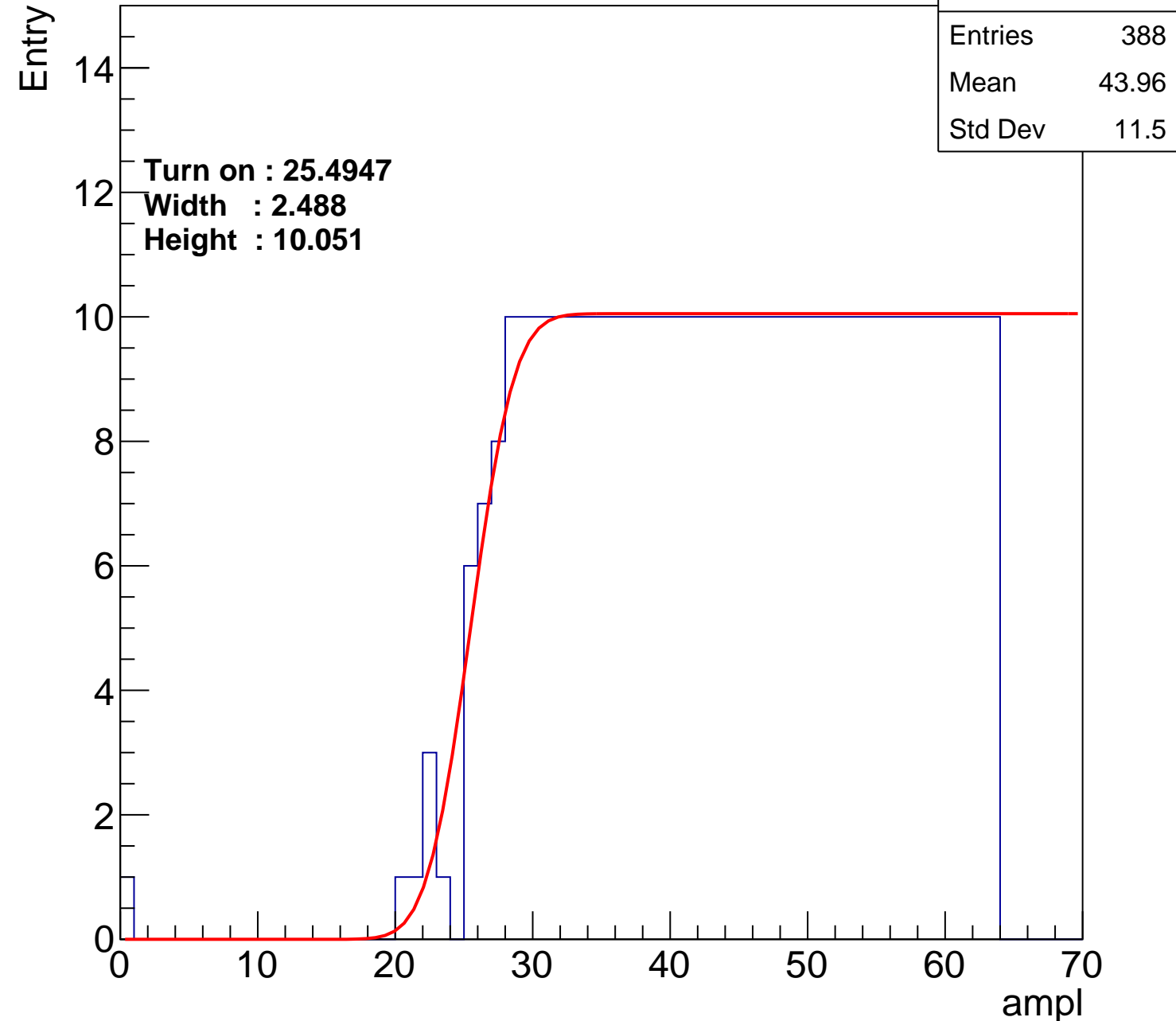
Width : 2.488

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.17
Std Dev	12.54

Turn on : 24.8597

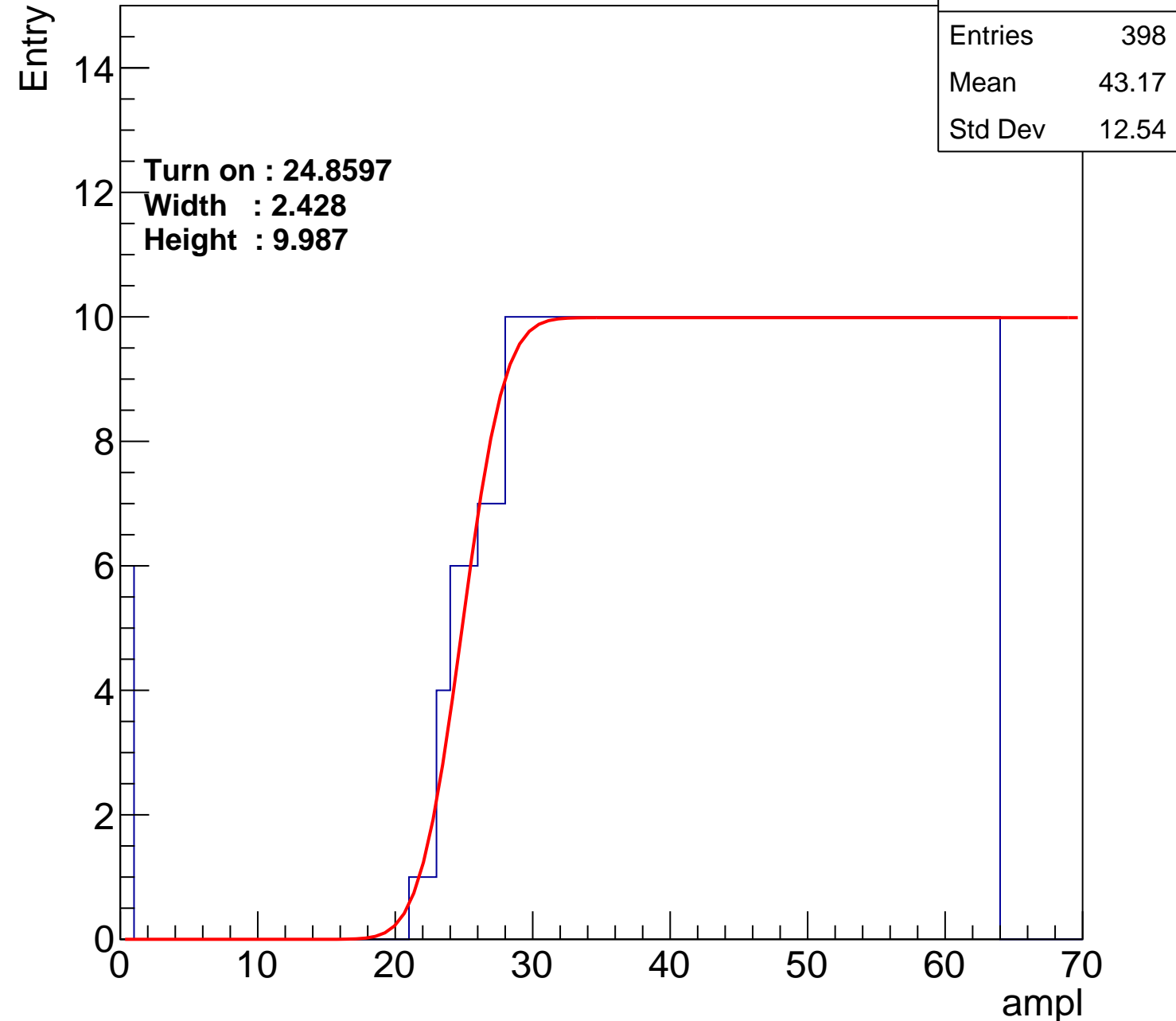
Width : 2.428

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.36
Std Dev	11.59

Turn on : 26.8333

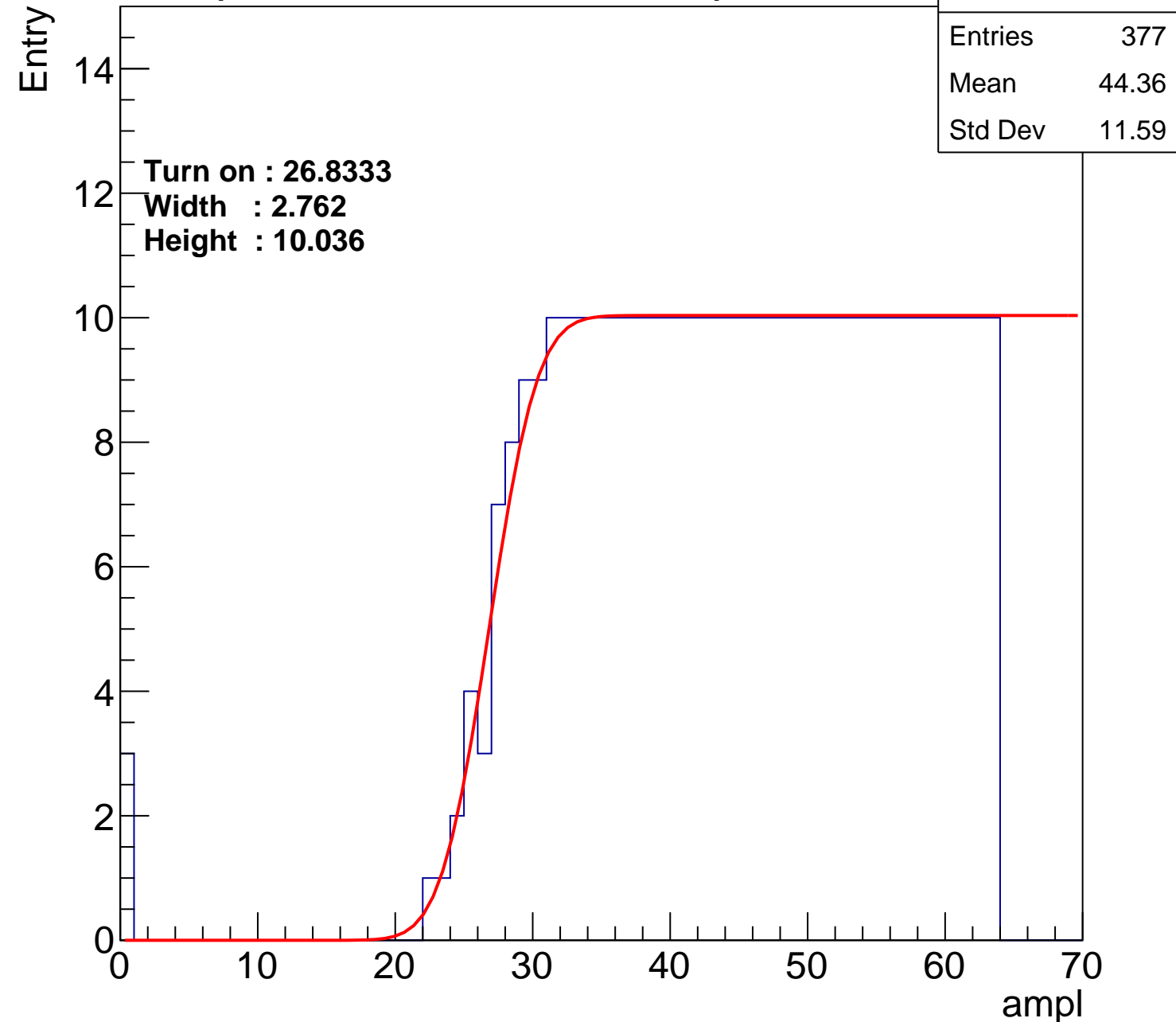
Width : 2.762

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	43.97
Std Dev	11.95

Turn on : 26.5149

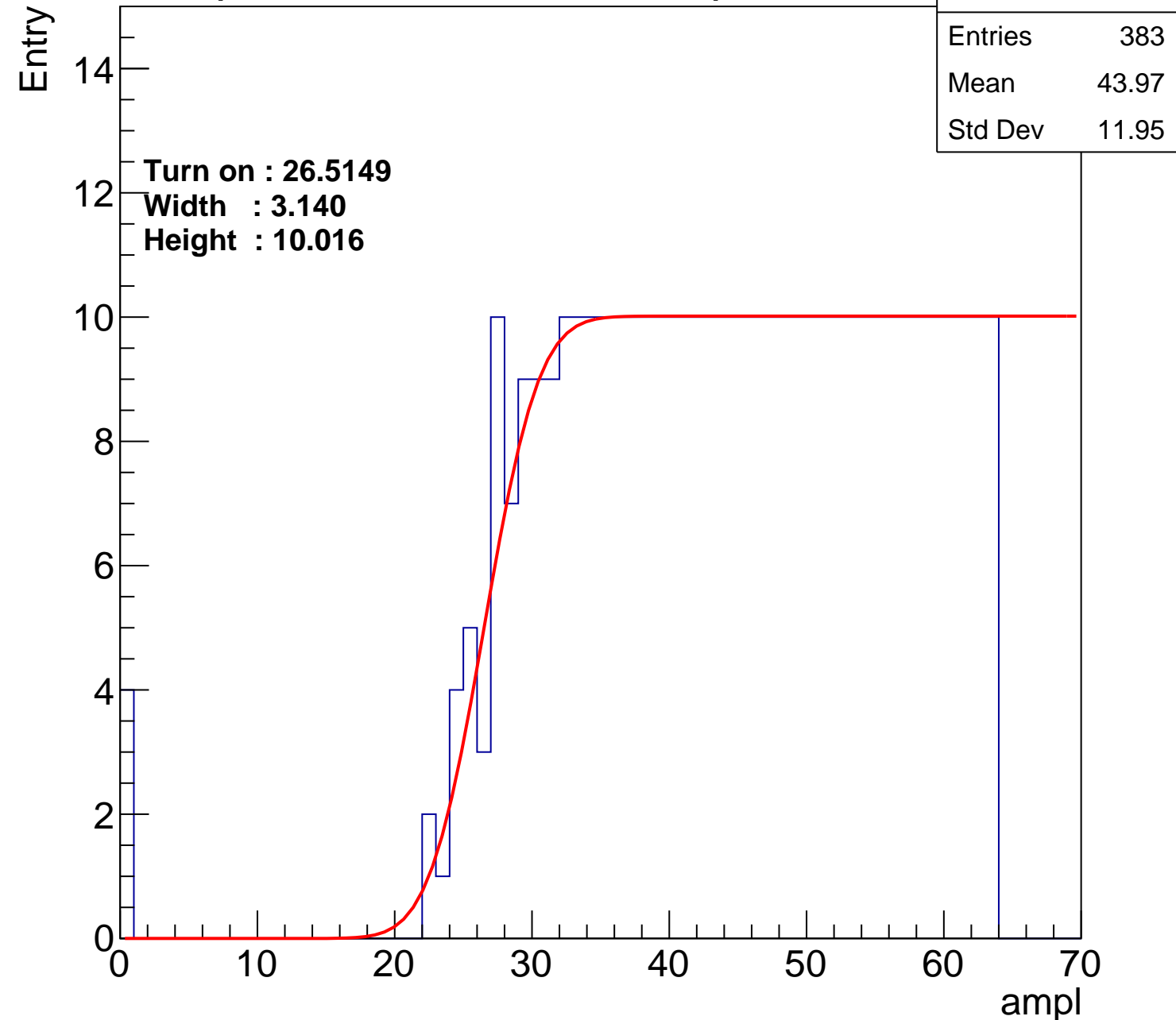
Width : 3.140

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 24.4810

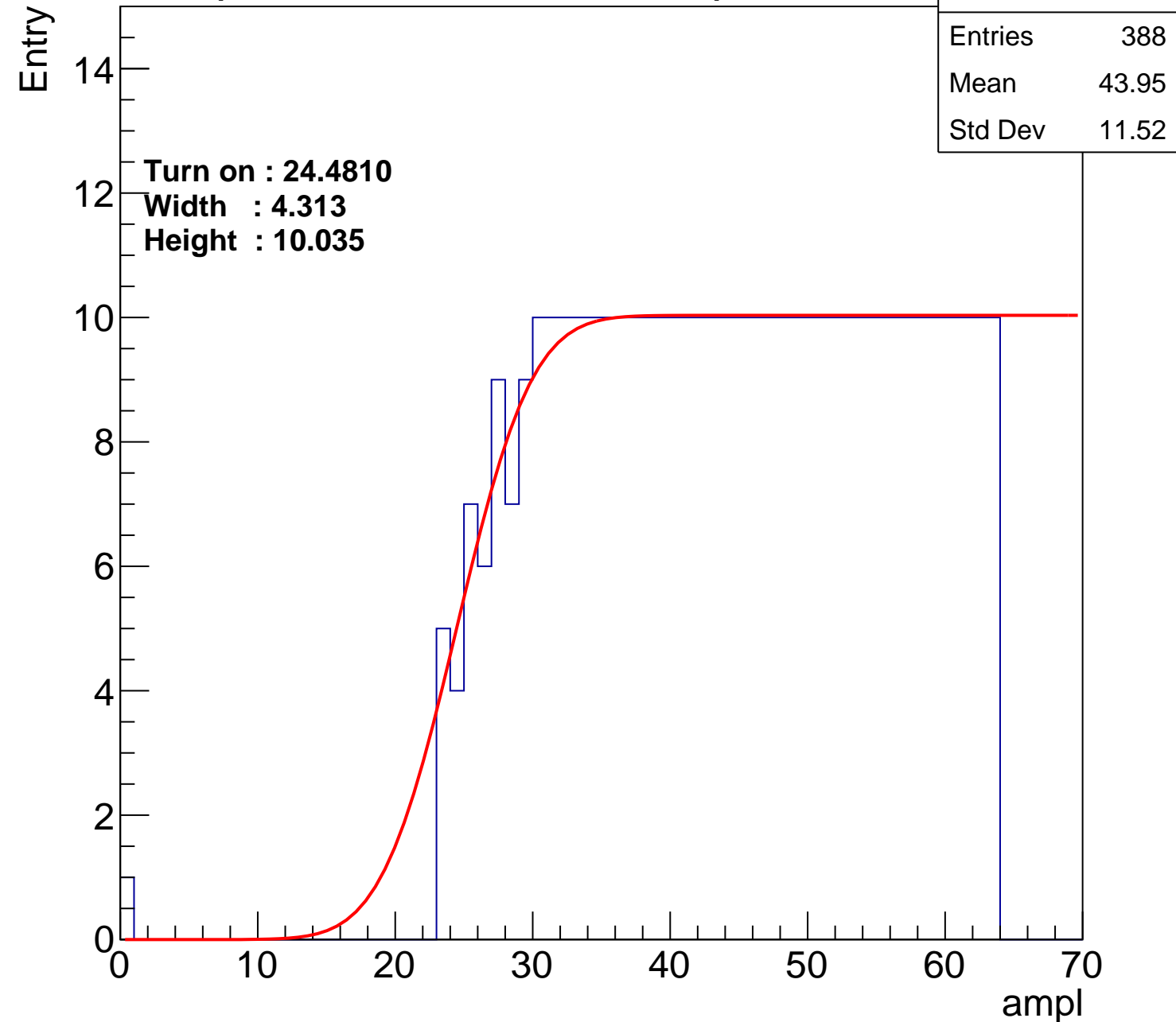
Width : 4.313

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.45
Std Dev	11.91

Turn on : 24.2583

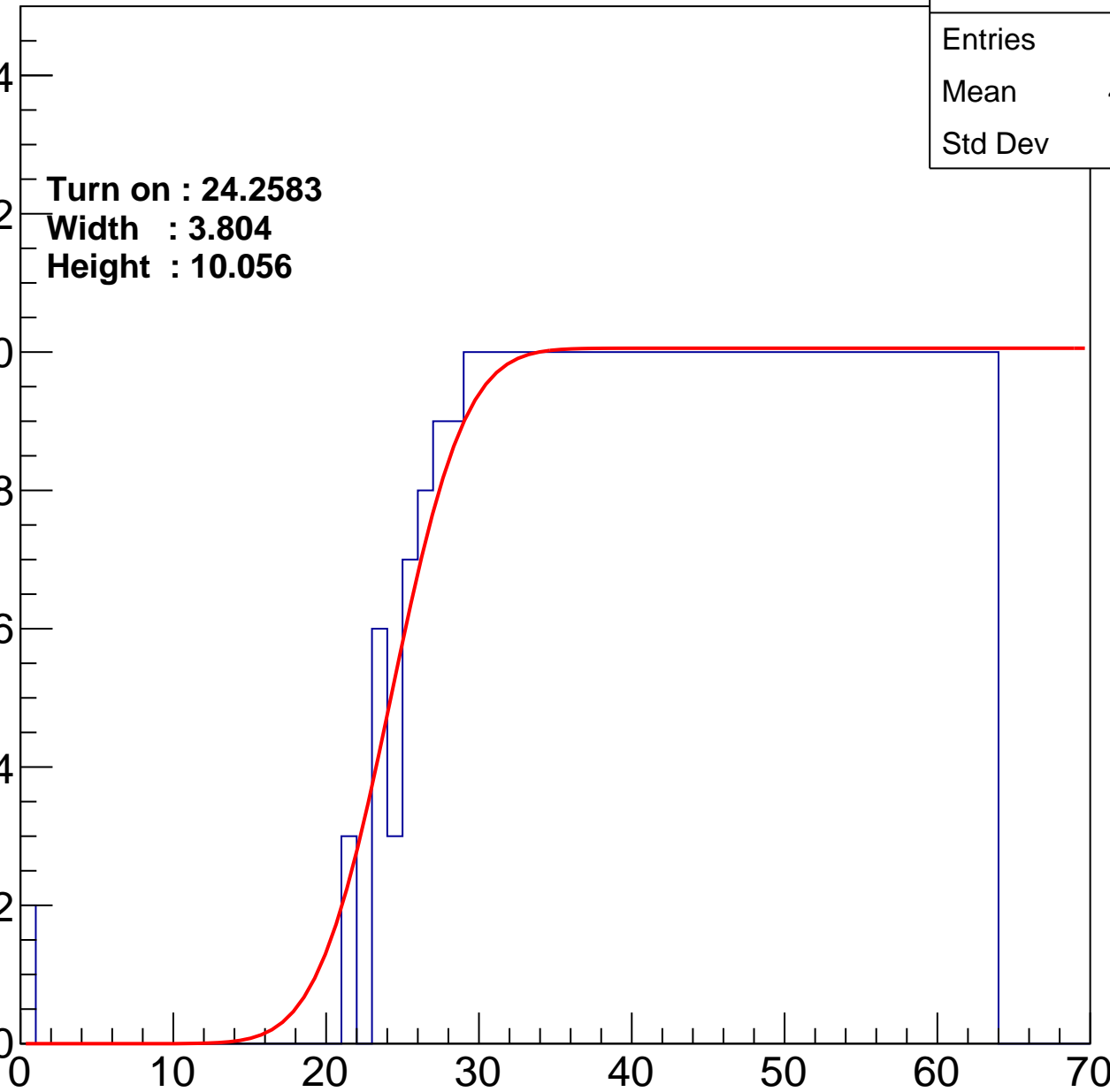
Width : 3.804

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch100

calib_packv5_042523_0143.root, FC#10, port B3

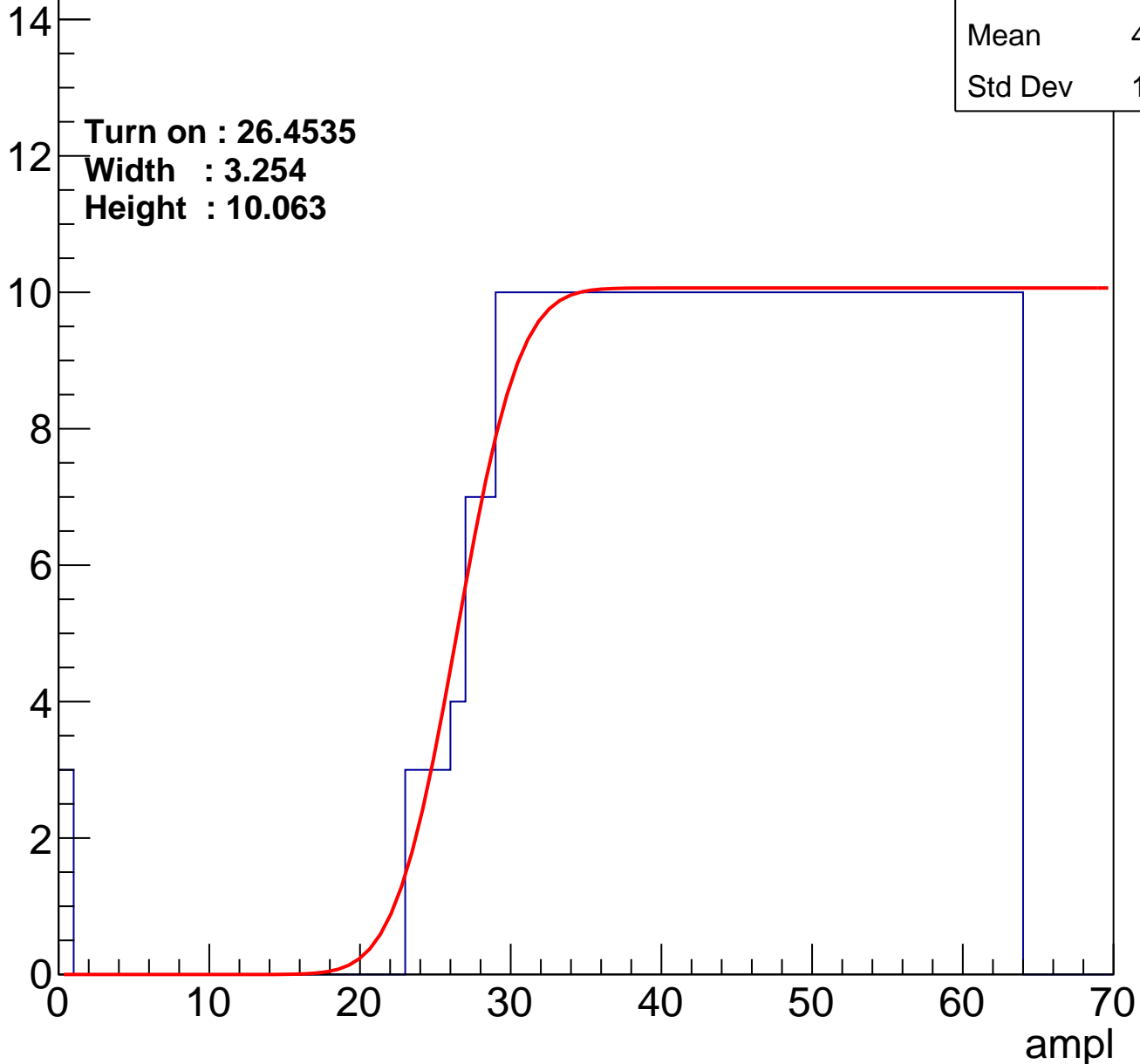
Entries	380
Mean	44.22
Std Dev	11.65

Turn on : 26.4535

Width : 3.254

Height : 10.063

Entry



B1L002S, U9-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.67
Std Dev	12.06

Turn on : 25.5074

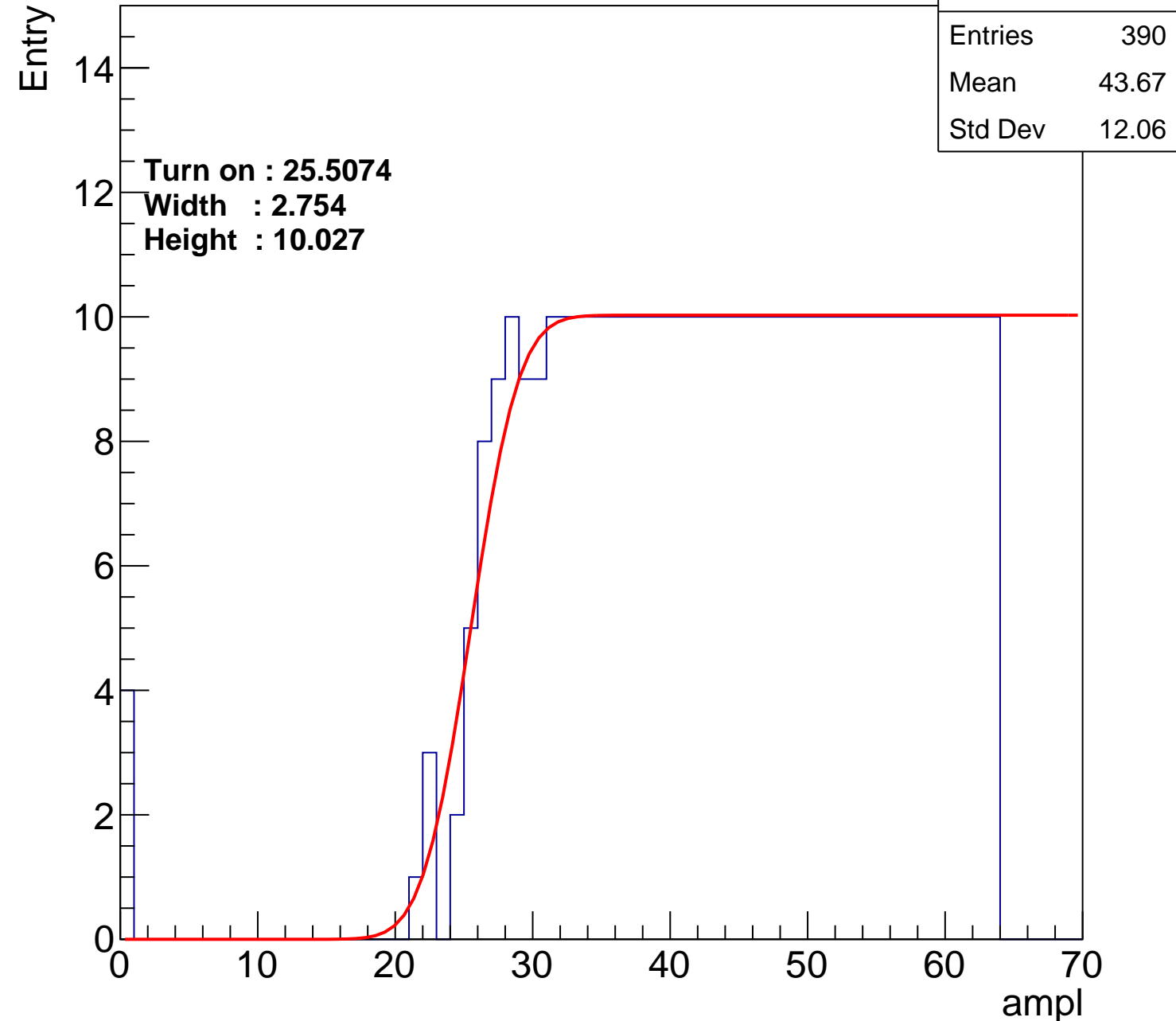
Width : 2.754

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 25.1726

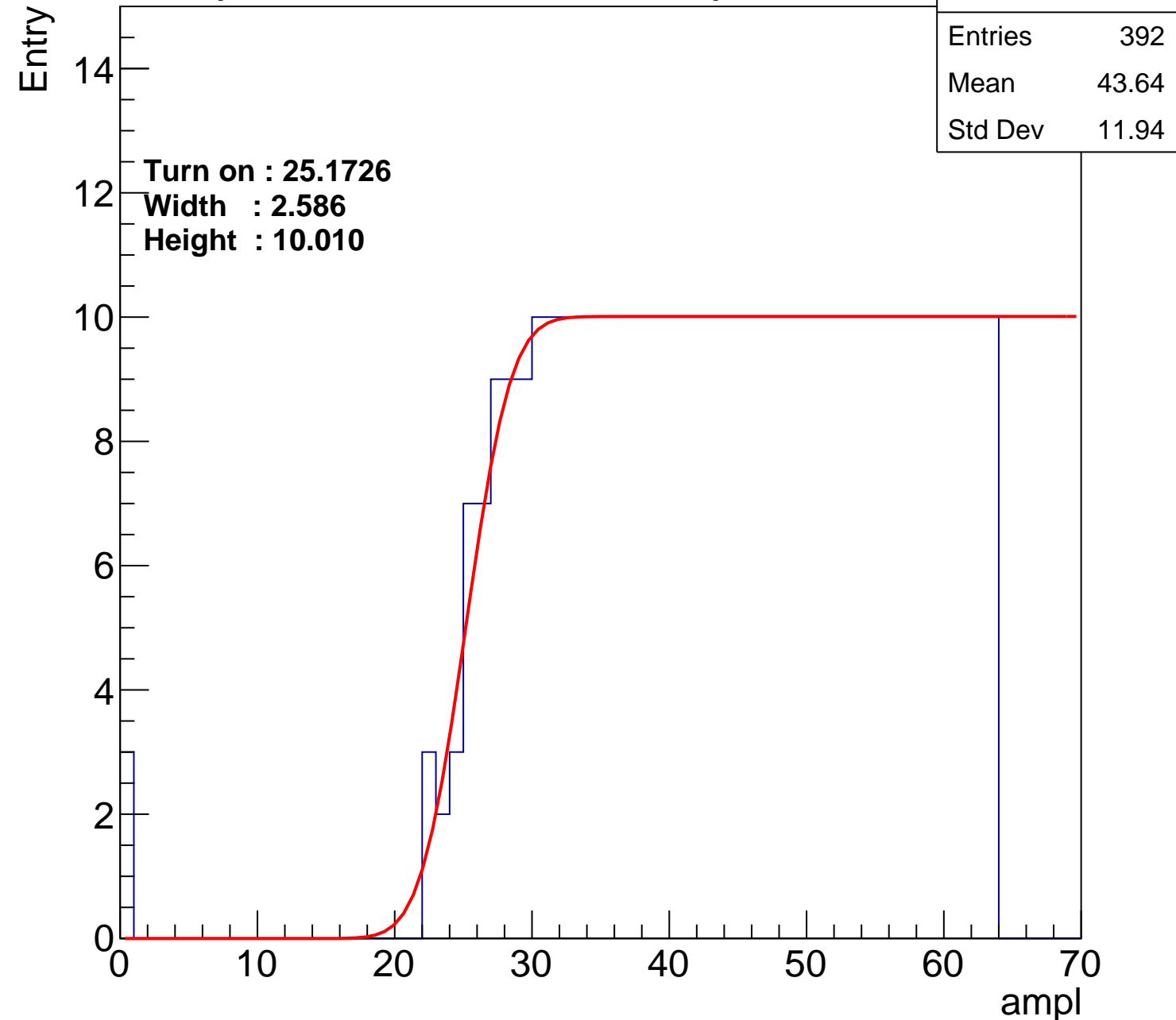
Width : 2.586

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.31
Std Dev	11.76

Turn on : 27.0499

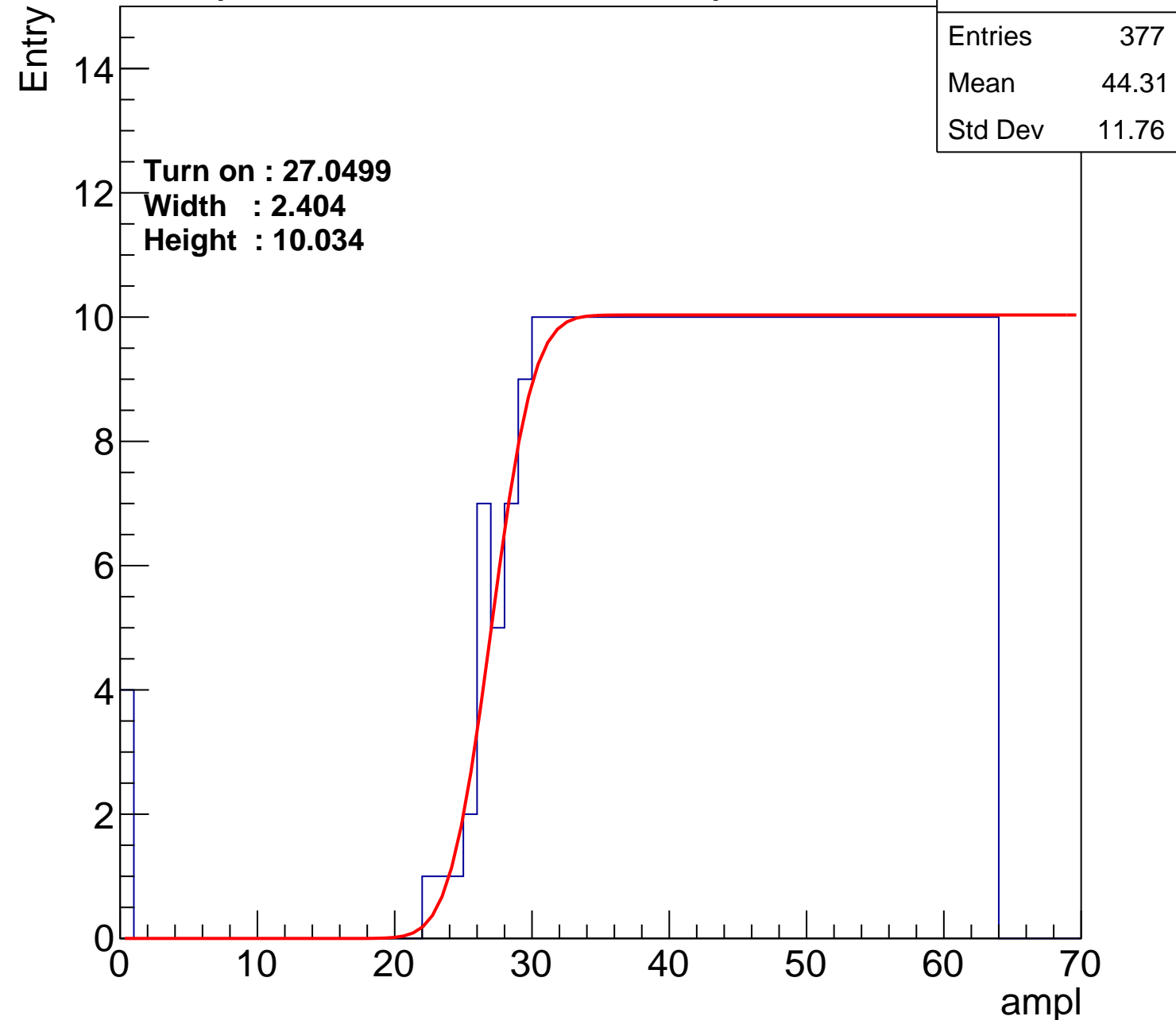
Width : 2.404

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.86
Std Dev	12.66

Turn on : 24.4347

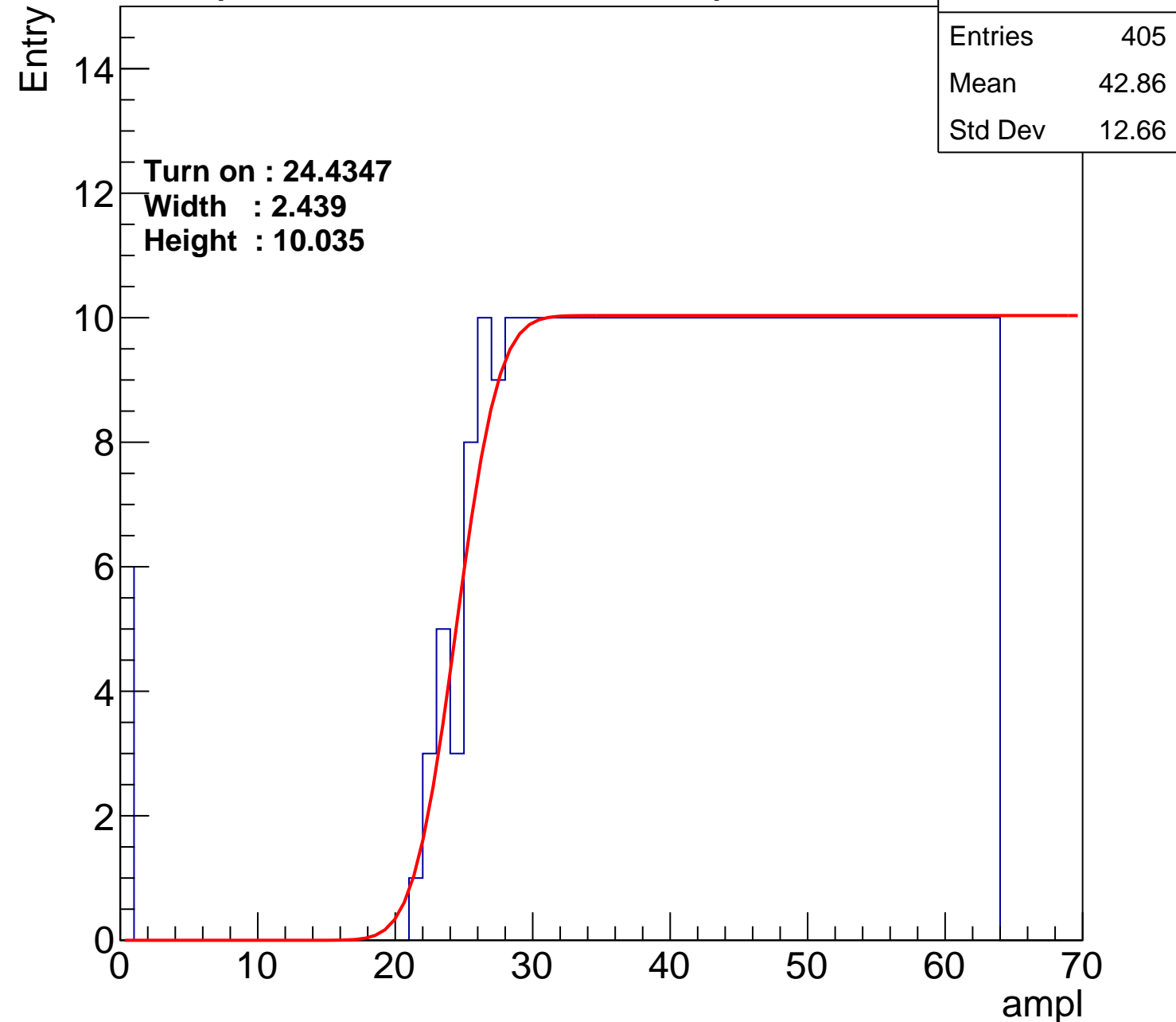
Width : 2.439

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.43
Std Dev	11.95

Turn on : 24.7445

Width : 2.217

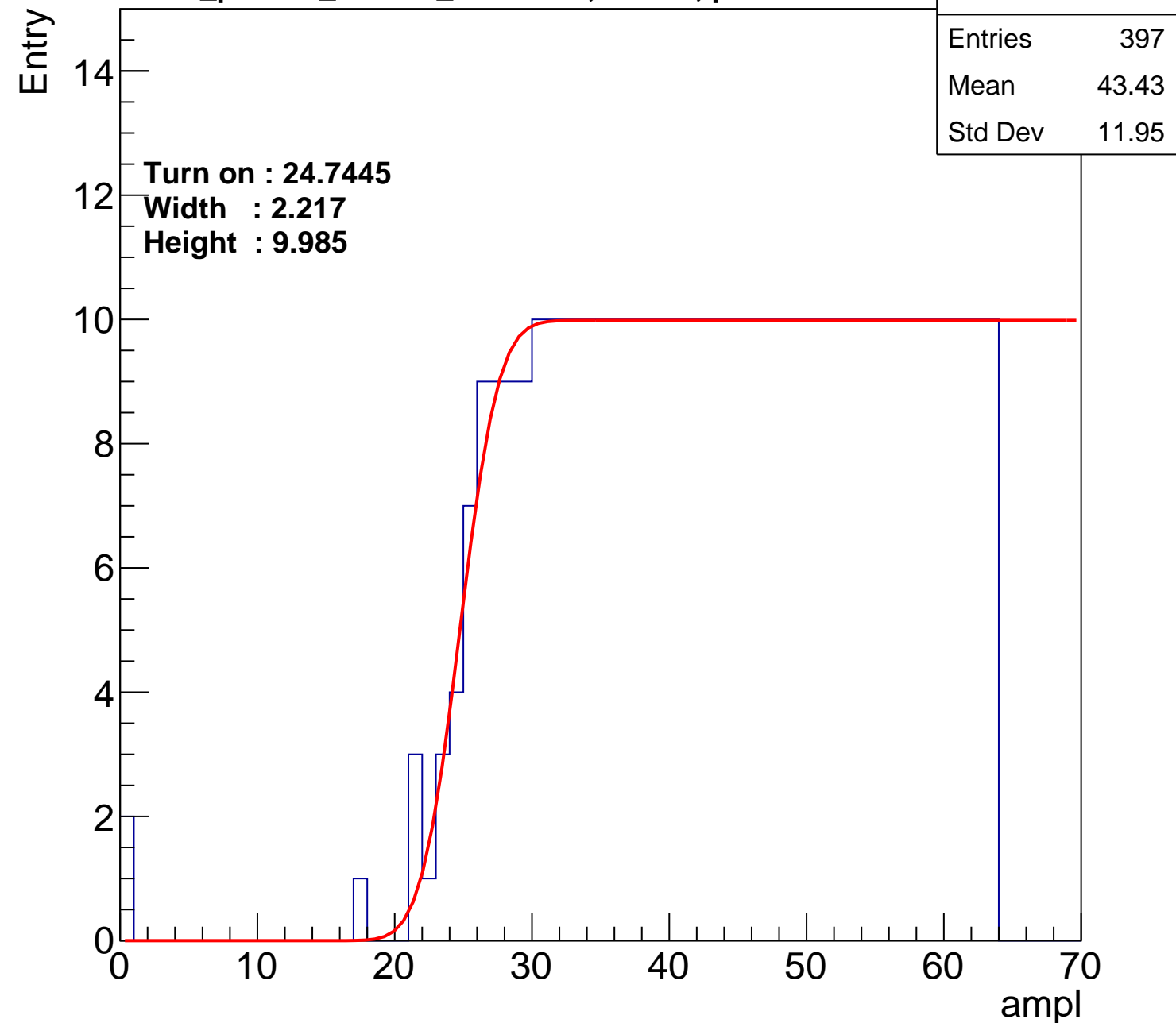
Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U9-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.05
Std Dev	11.72

Turn on : 26.0691

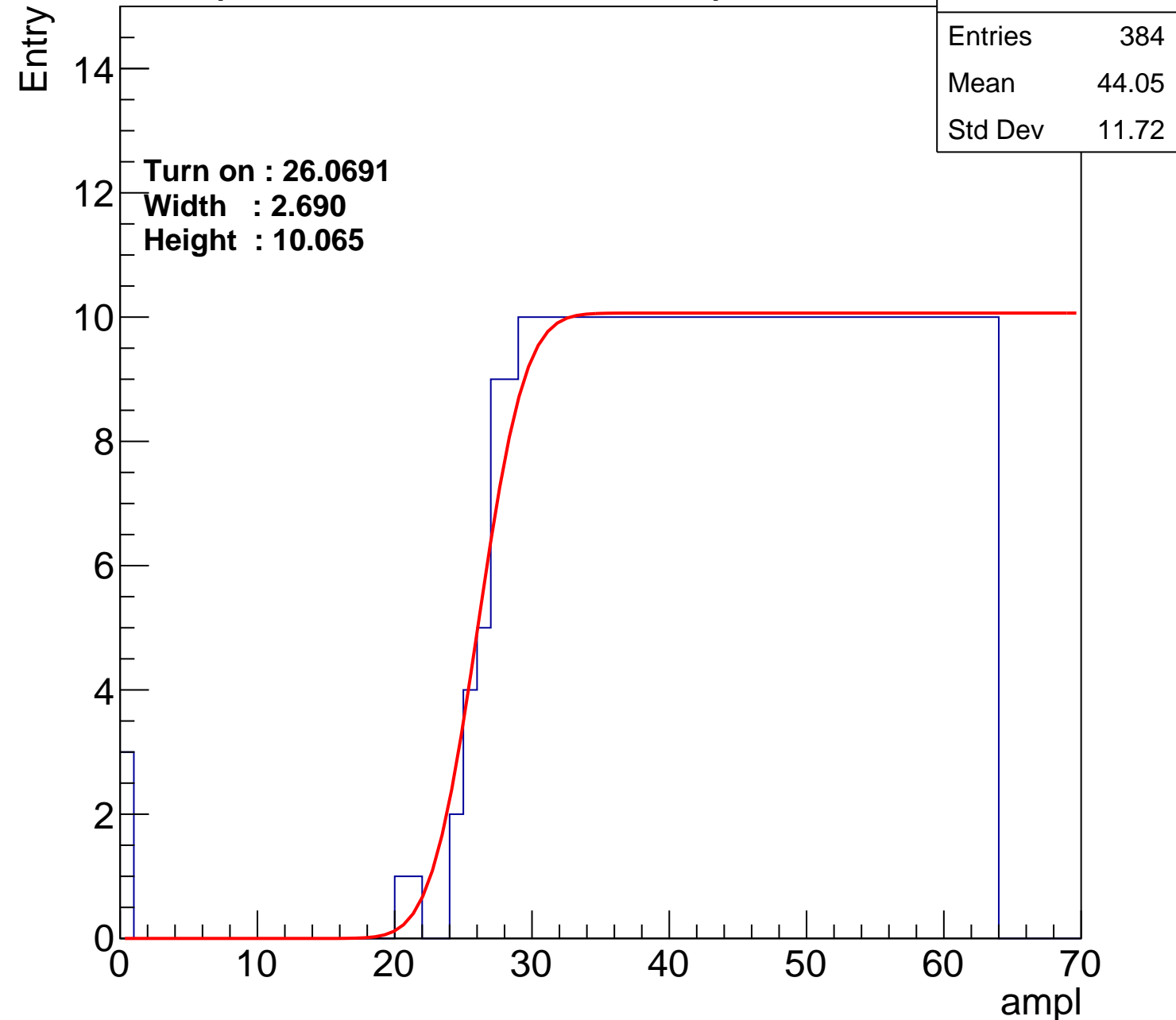
Width : 2.690

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 26.8045

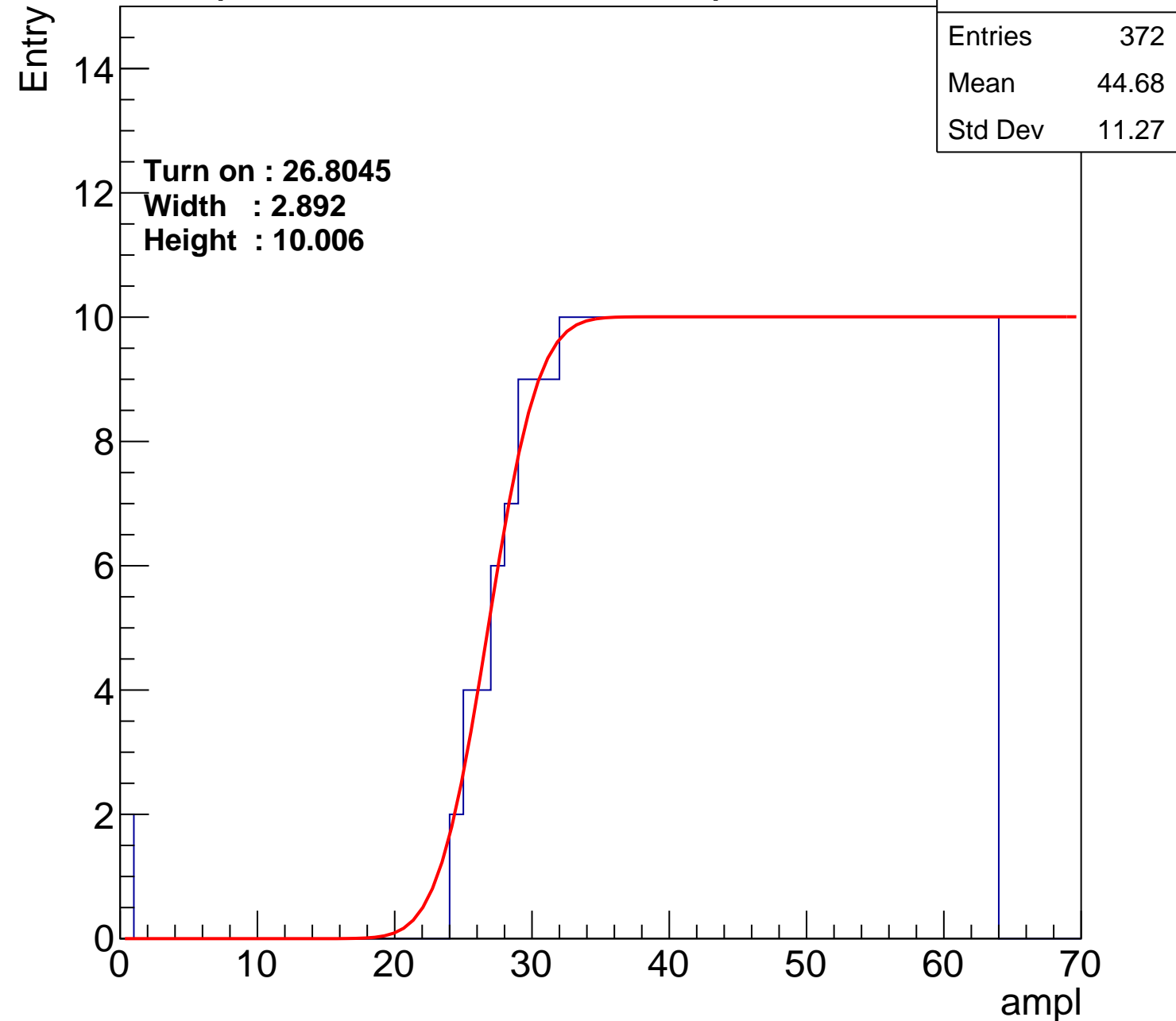
Width : 2.892

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch108

calib_packv5_042523_0143.root, FC#10, port B3

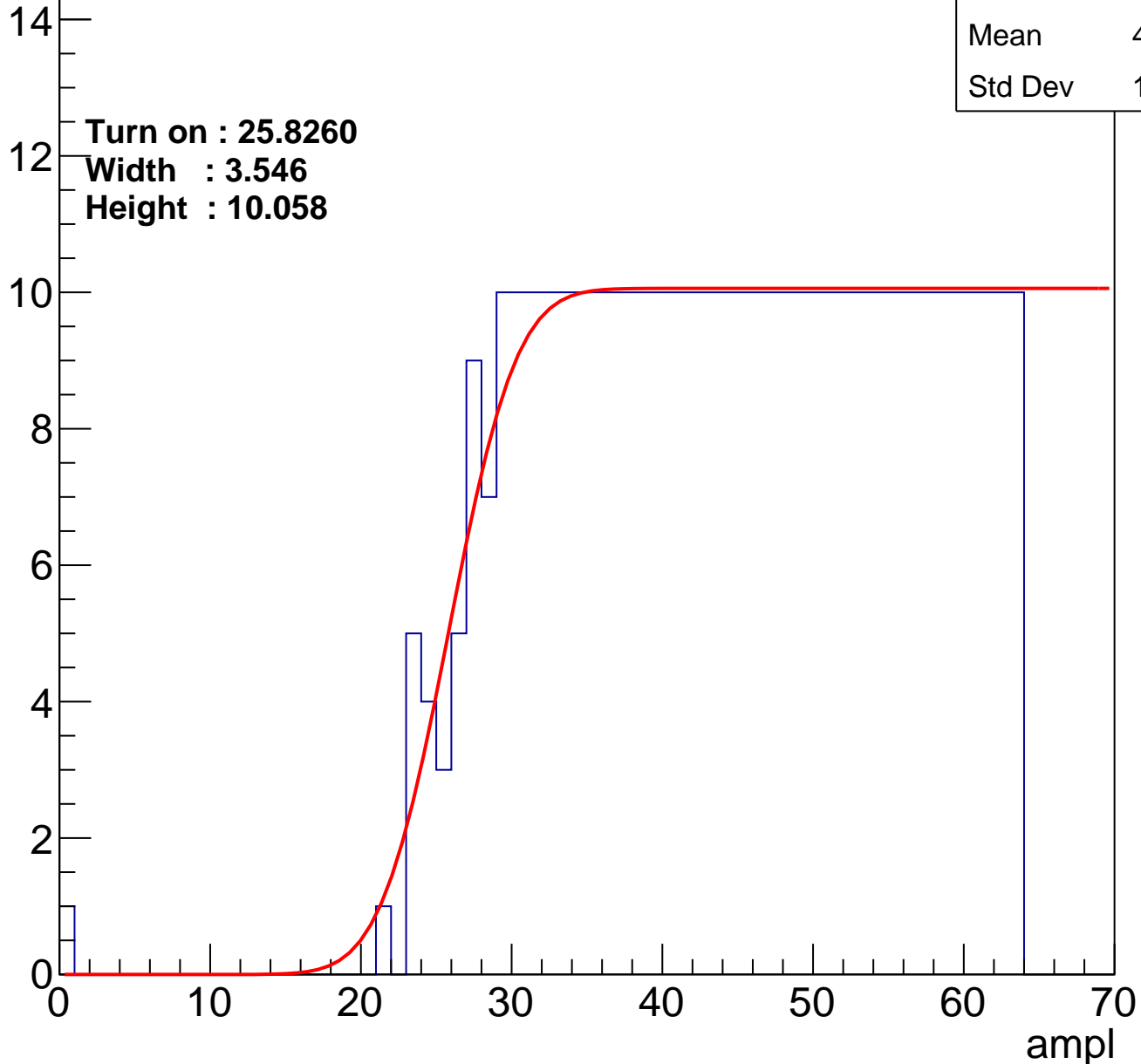
Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.8260

Width : 3.546

Height : 10.058

Entry



B1L002S, U9-ch109

calib_packv5_042523_0143.root, FC#10, port B3

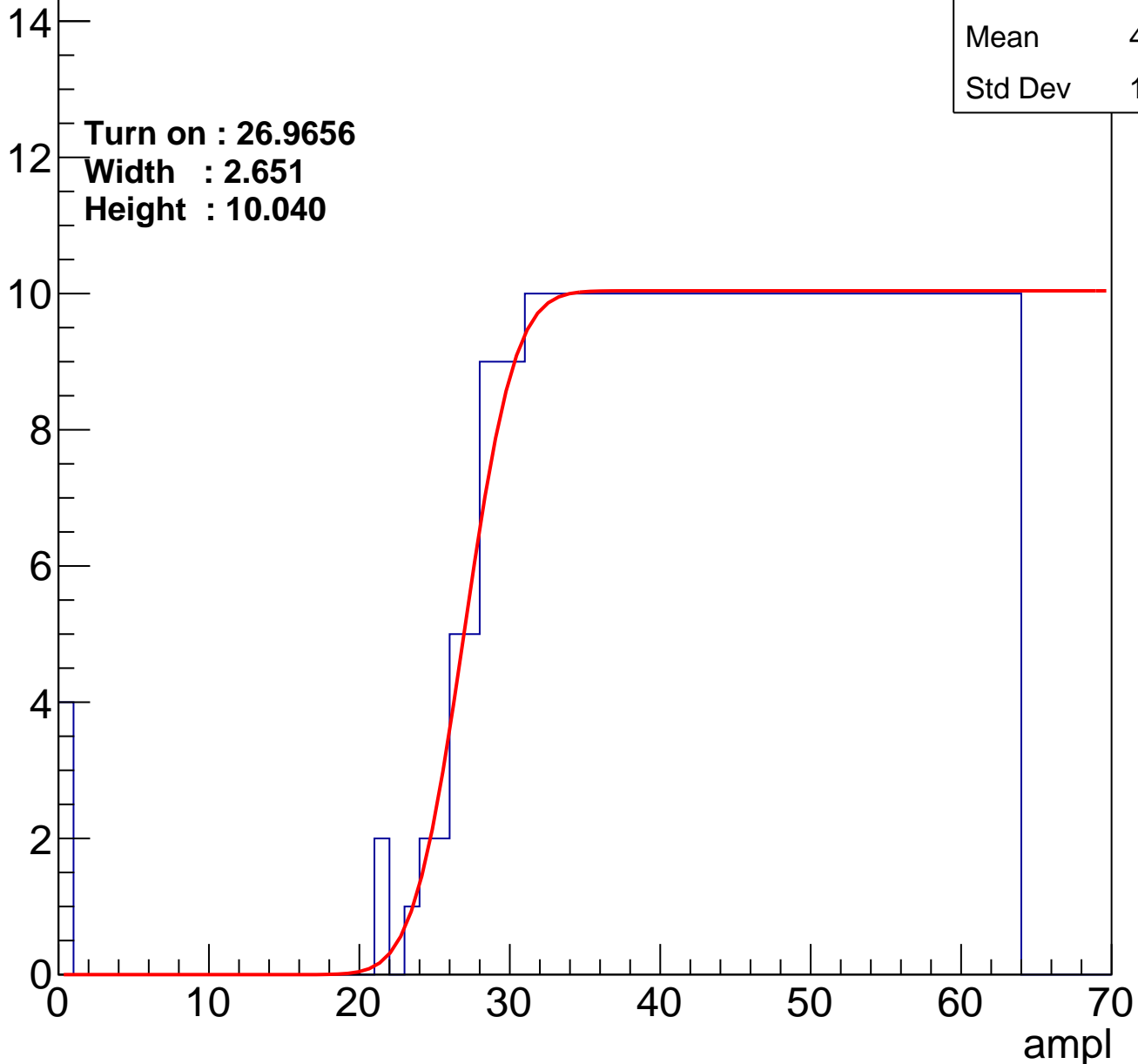
Entries	378
Mean	44.24
Std Dev	11.82

Turn on : 26.9656

Width : 2.651

Height : 10.040

Entry



B1L002S, U9-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.97
Std Dev	12.26

Turn on : 24.0292

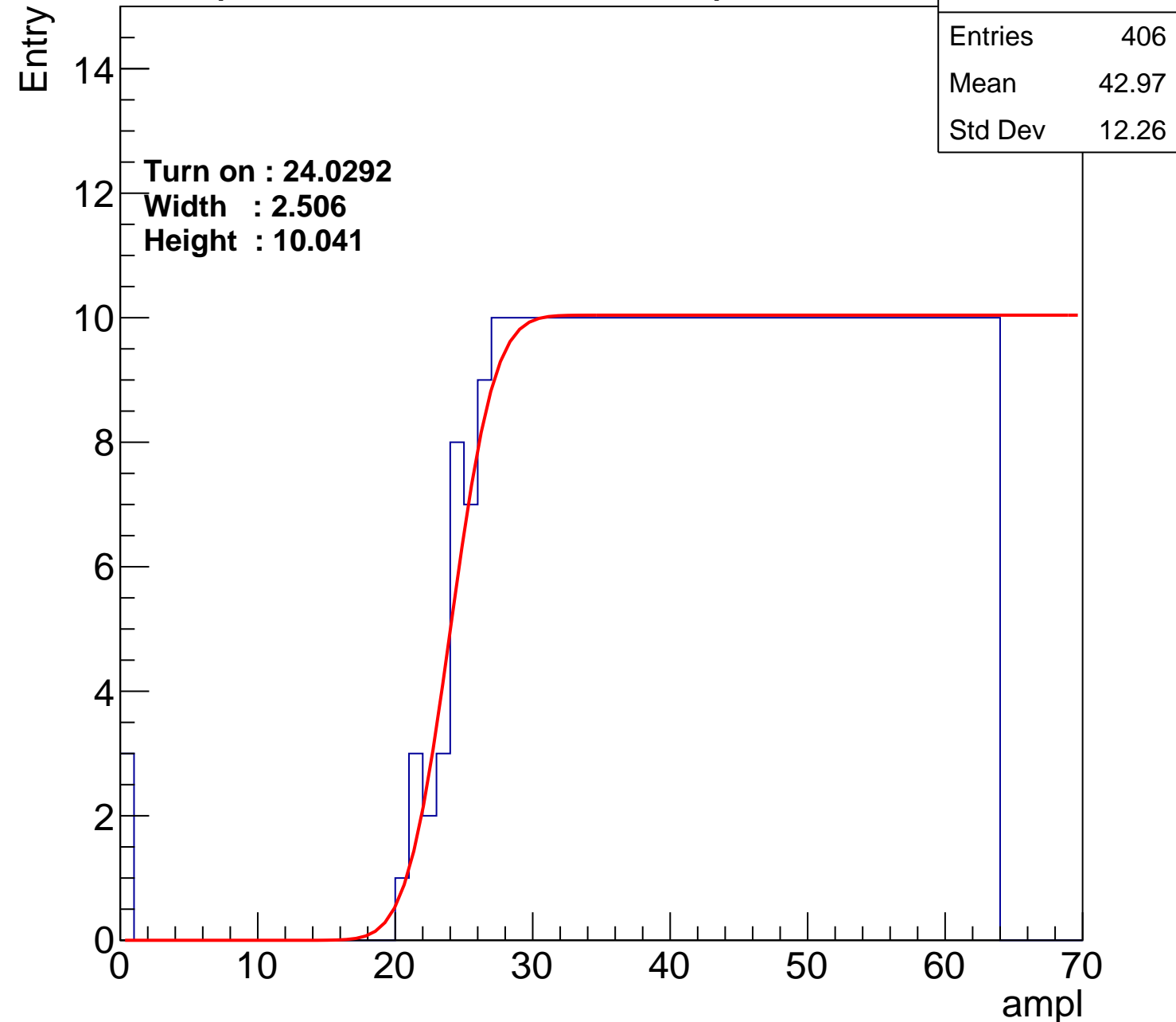
Width : 2.506

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch111

calib_packv5_042523_0143.root, FC#10, port B3

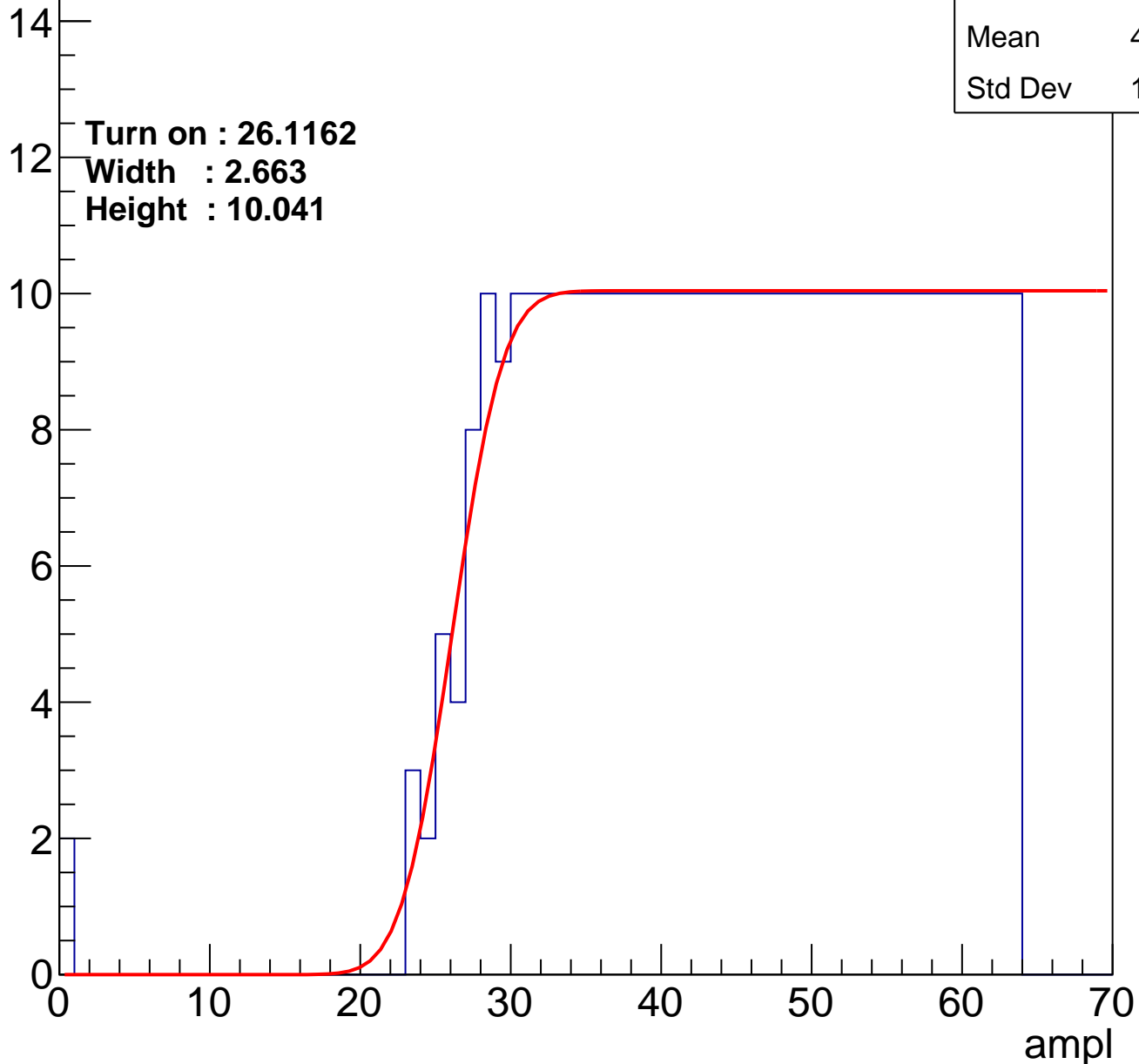
Entries	383
Mean	44.16
Std Dev	11.52

Turn on : 26.1162

Width : 2.663

Height : 10.041

Entry



B1L002S, U9-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.37
Std Dev	11.98

Turn on : 24.2228

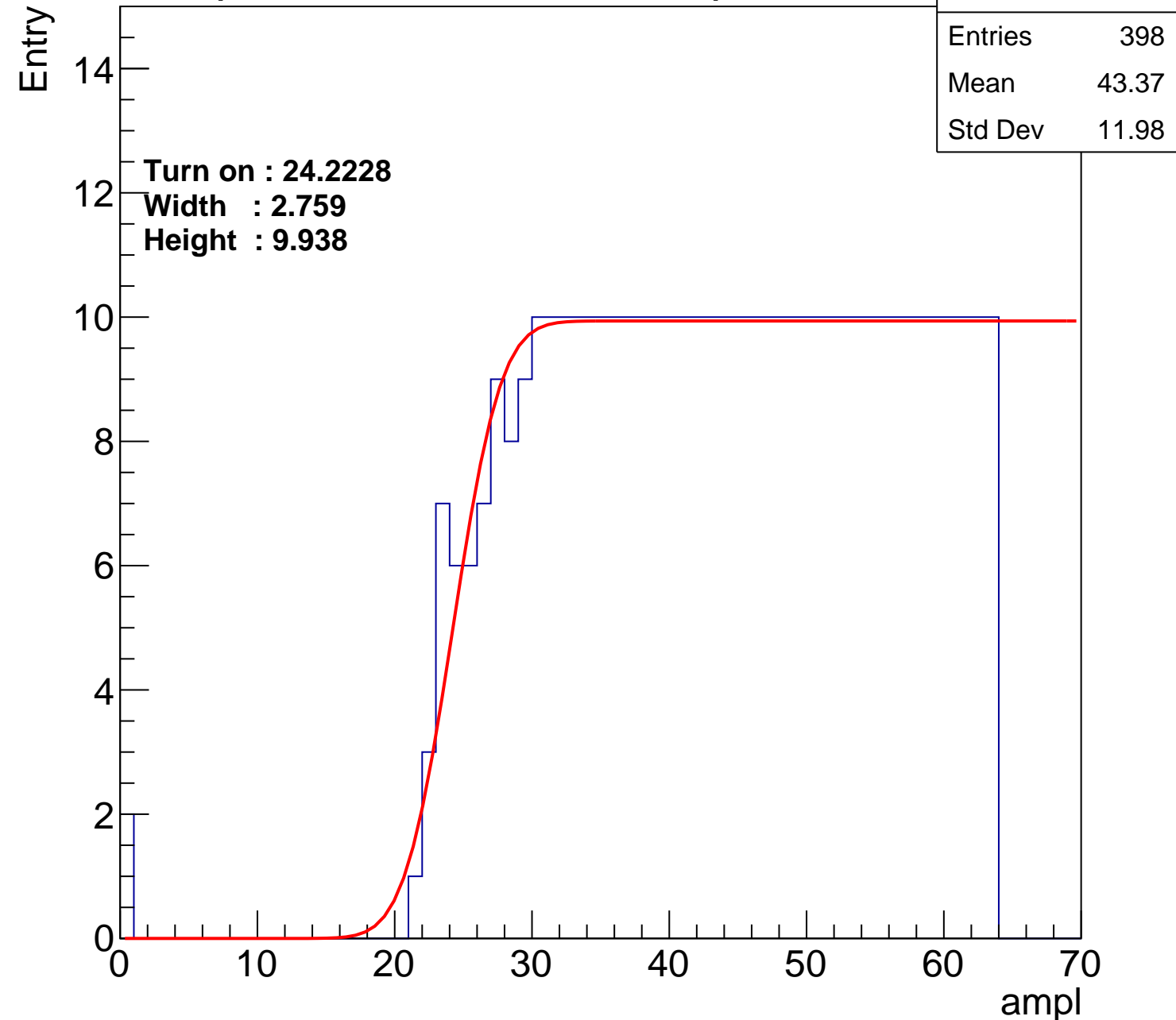
Width : 2.759

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.6792

Width : 2.394

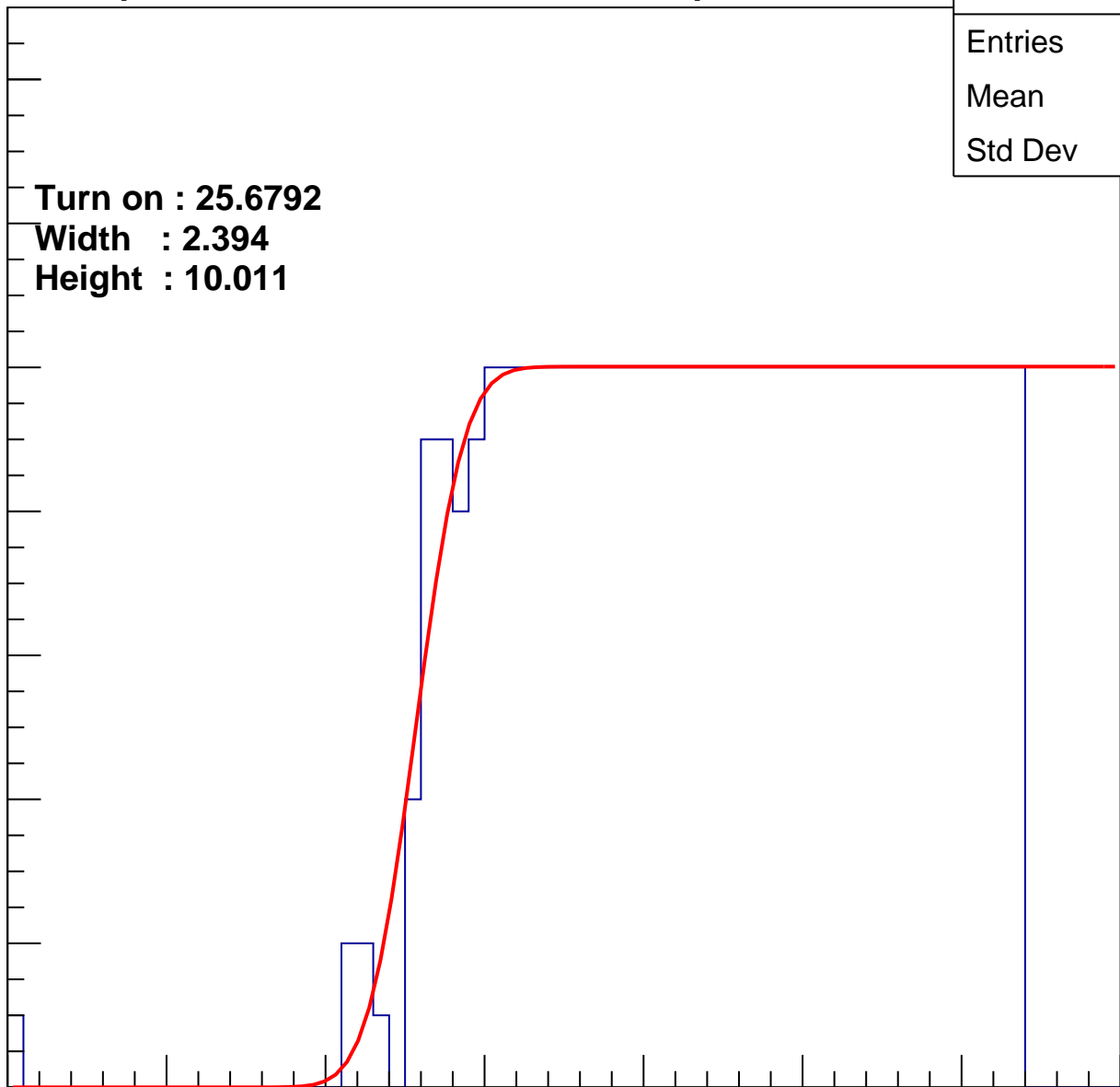
Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U9-ch114

calib_packv5_042523_0143.root, FC#10, port B3

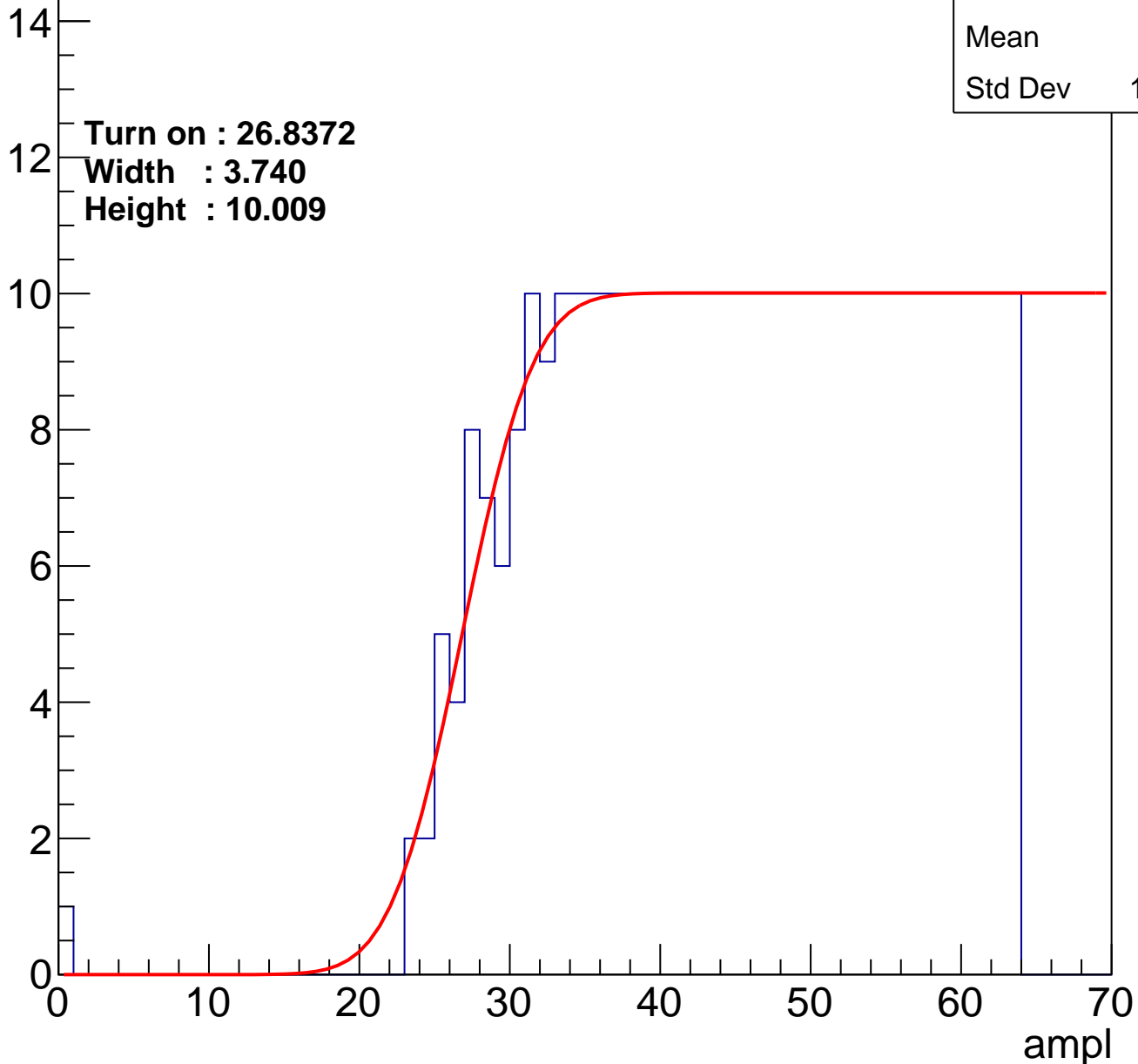
Entries	372
Mean	44.7
Std Dev	11.16

Turn on : 26.8372

Width : 3.740

Height : 10.009

Entry



B1L002S, U9-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.79
Std Dev	12.03

Turn on : 25.8005

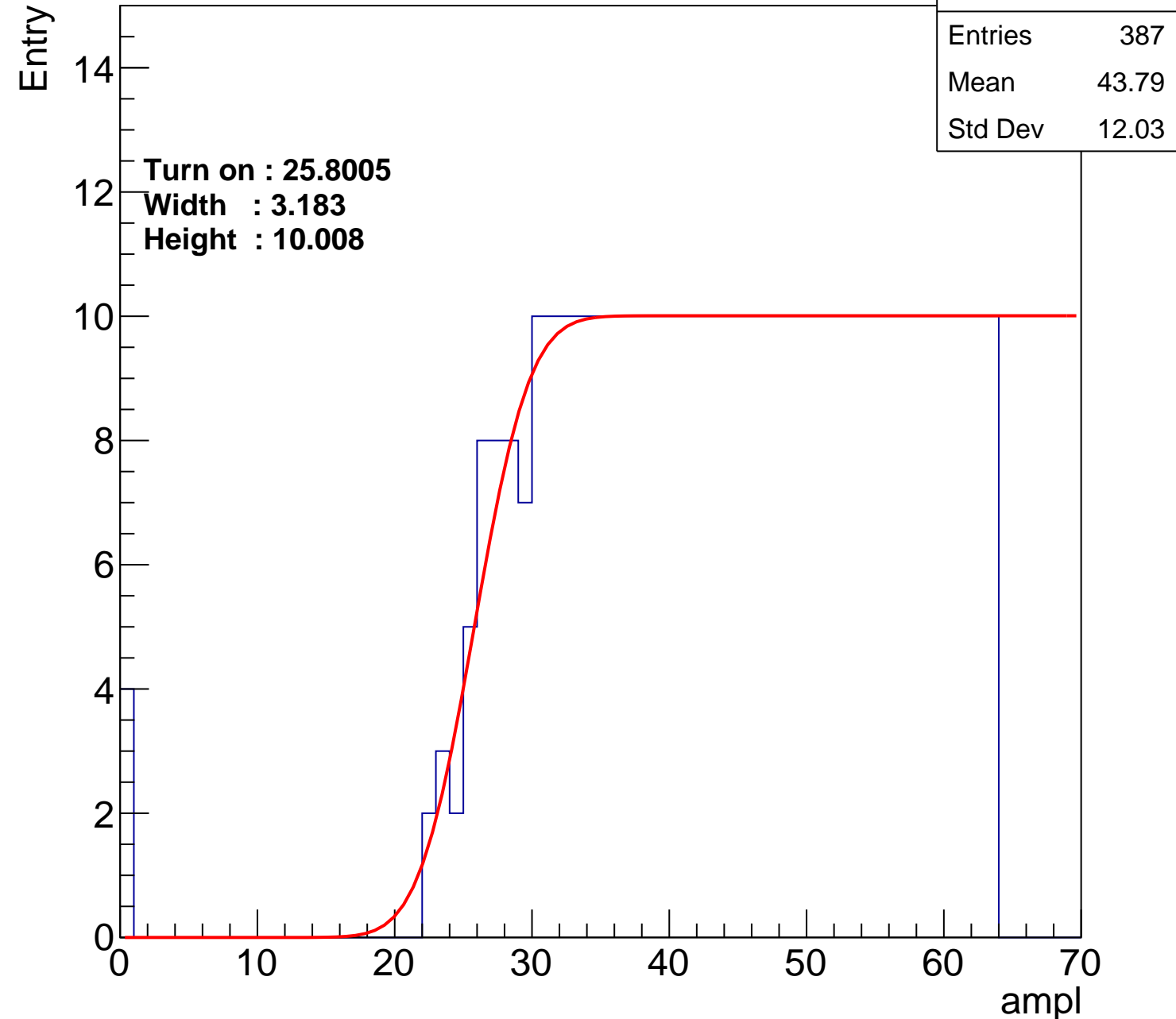
Width : 3.183

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch116

calib_packv5_042523_0143.root, FC#10, port B3

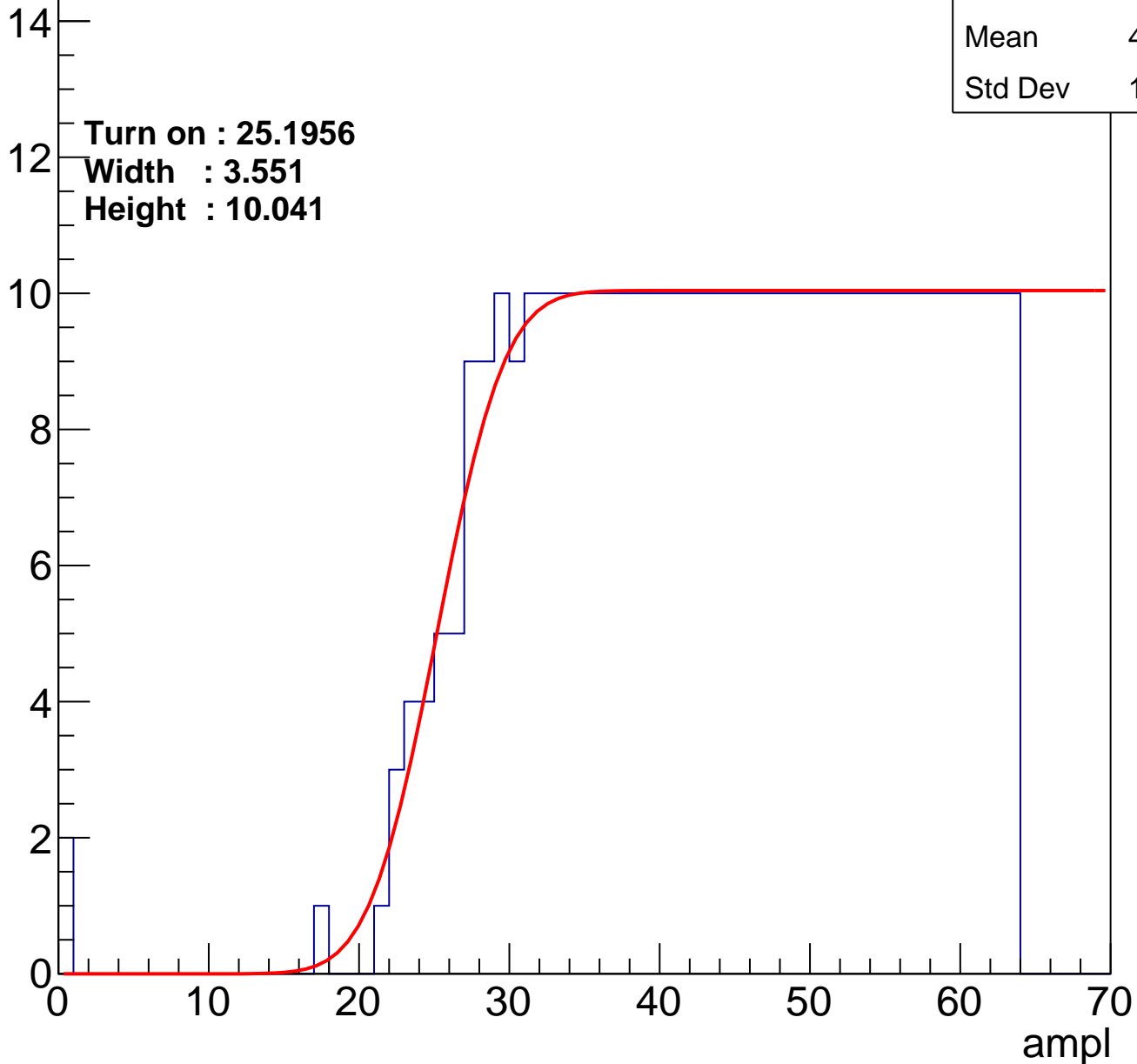
Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.1956

Width : 3.551

Height : 10.041

Entry



B1L002S, U9-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.13

Turn on : 27.2683

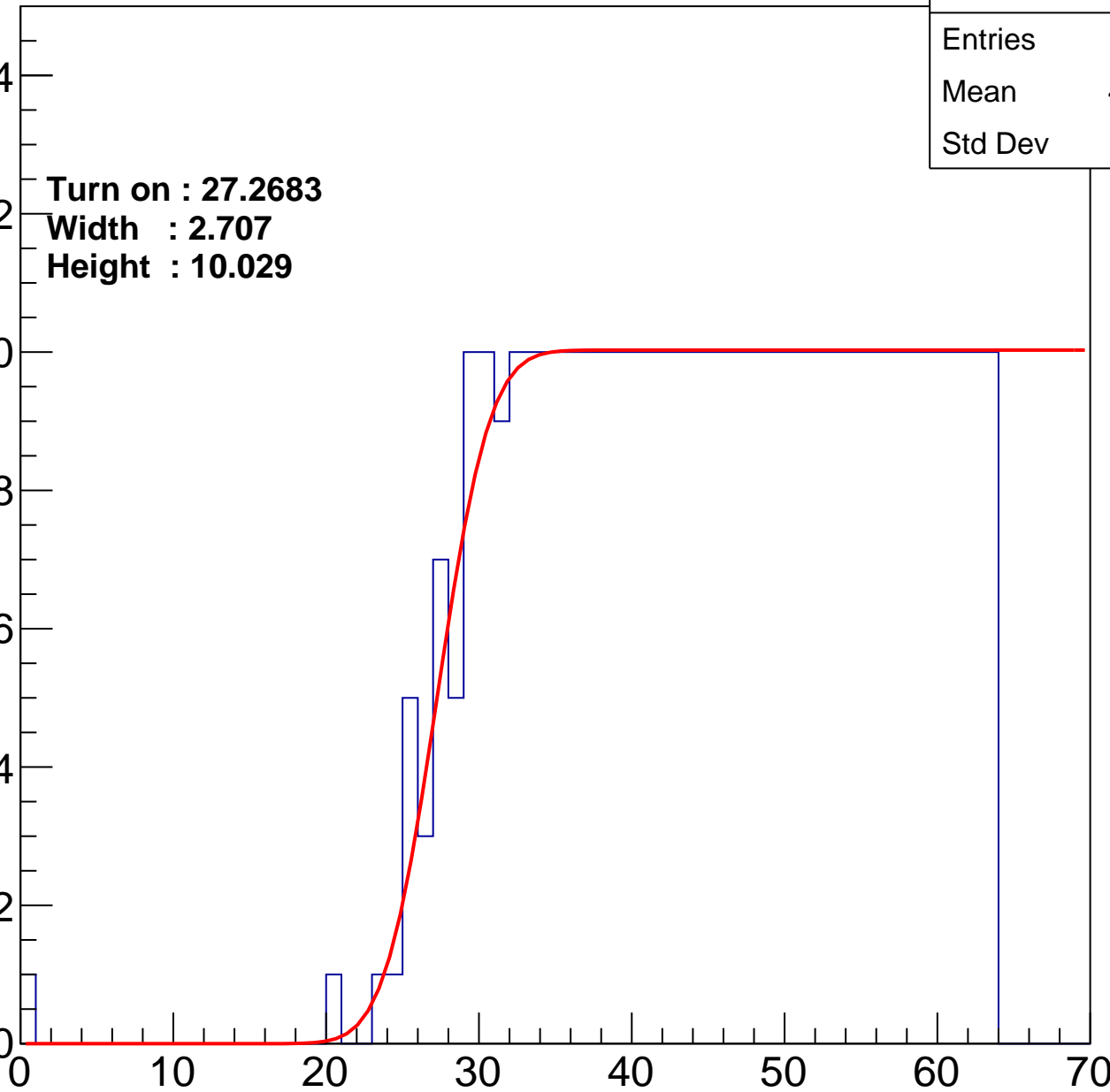
Width : 2.707

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.89
Std Dev	11.91

Turn on : 26.5366

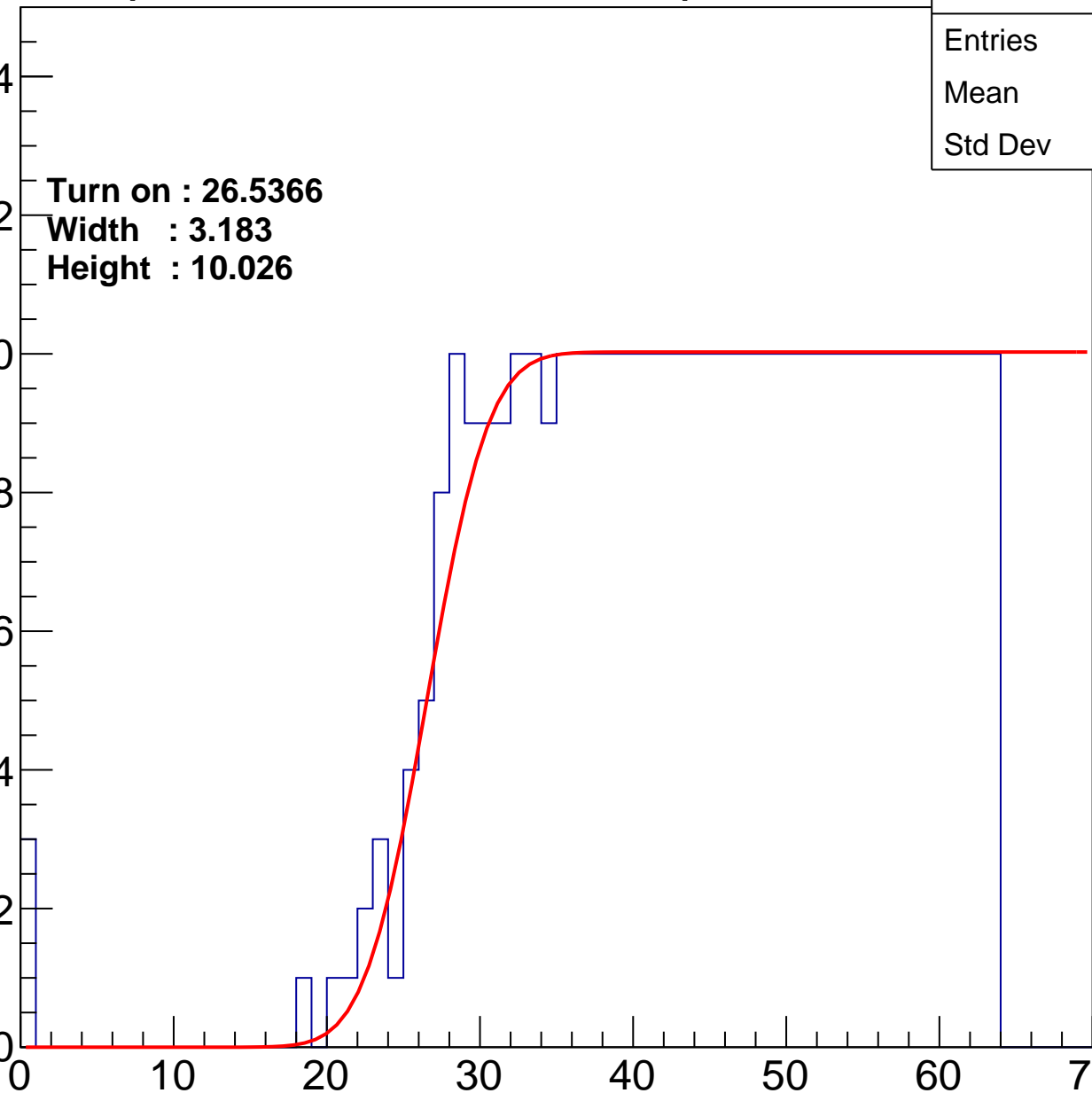
Width : 3.183

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.4017

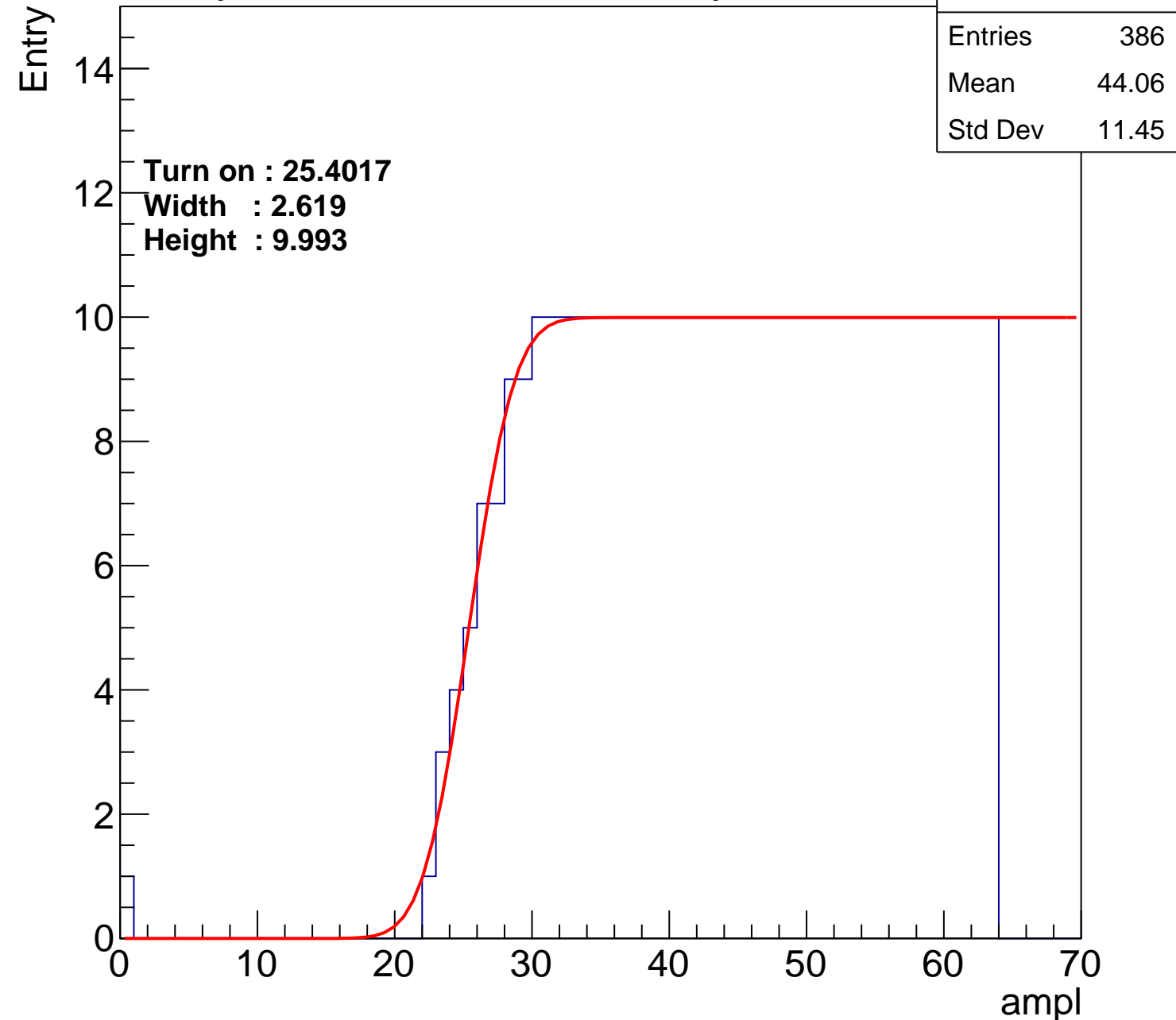
Width : 2.619

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.97
Std Dev	12.33

Turn on : 24.2341

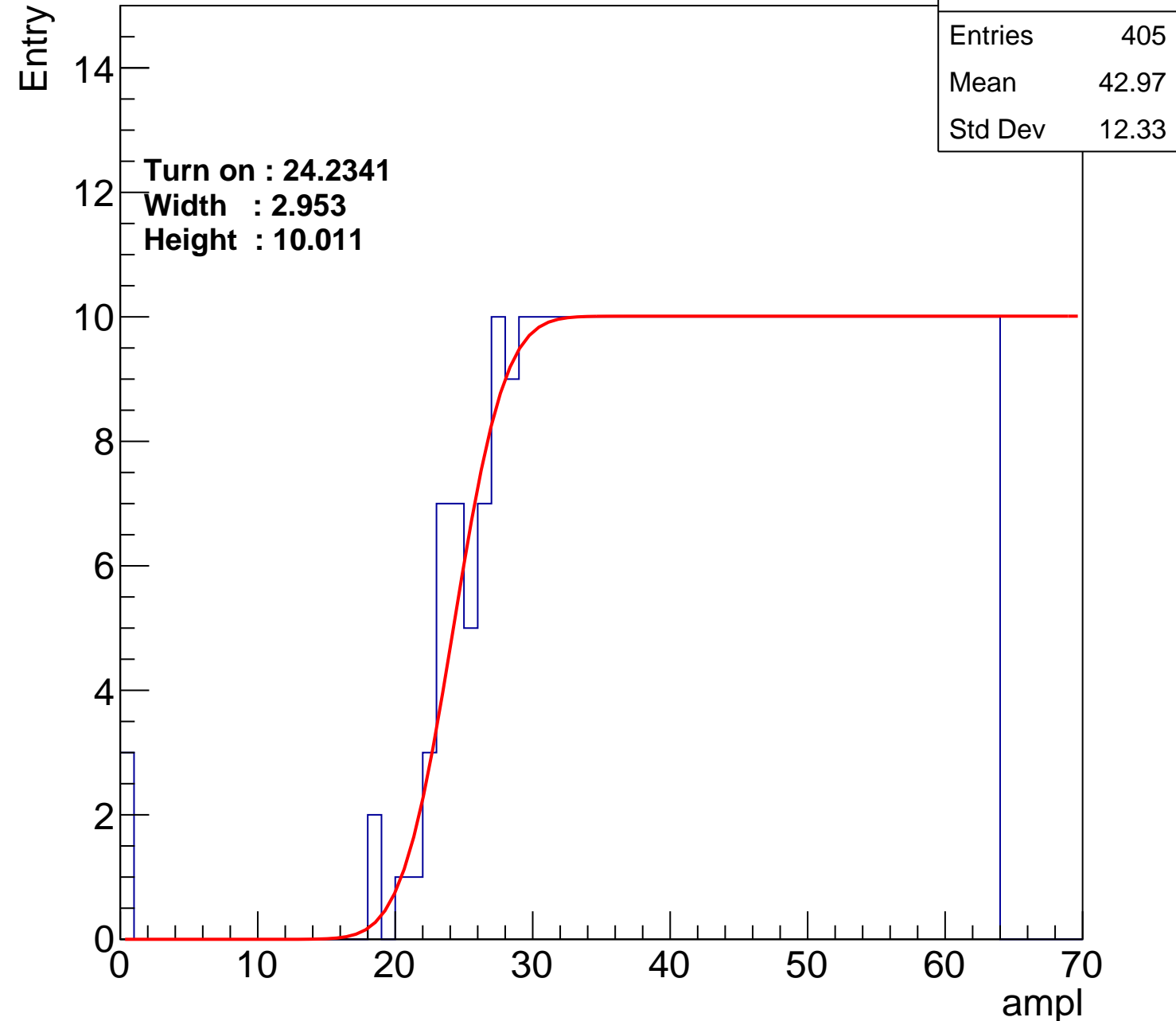
Width : 2.953

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.6579

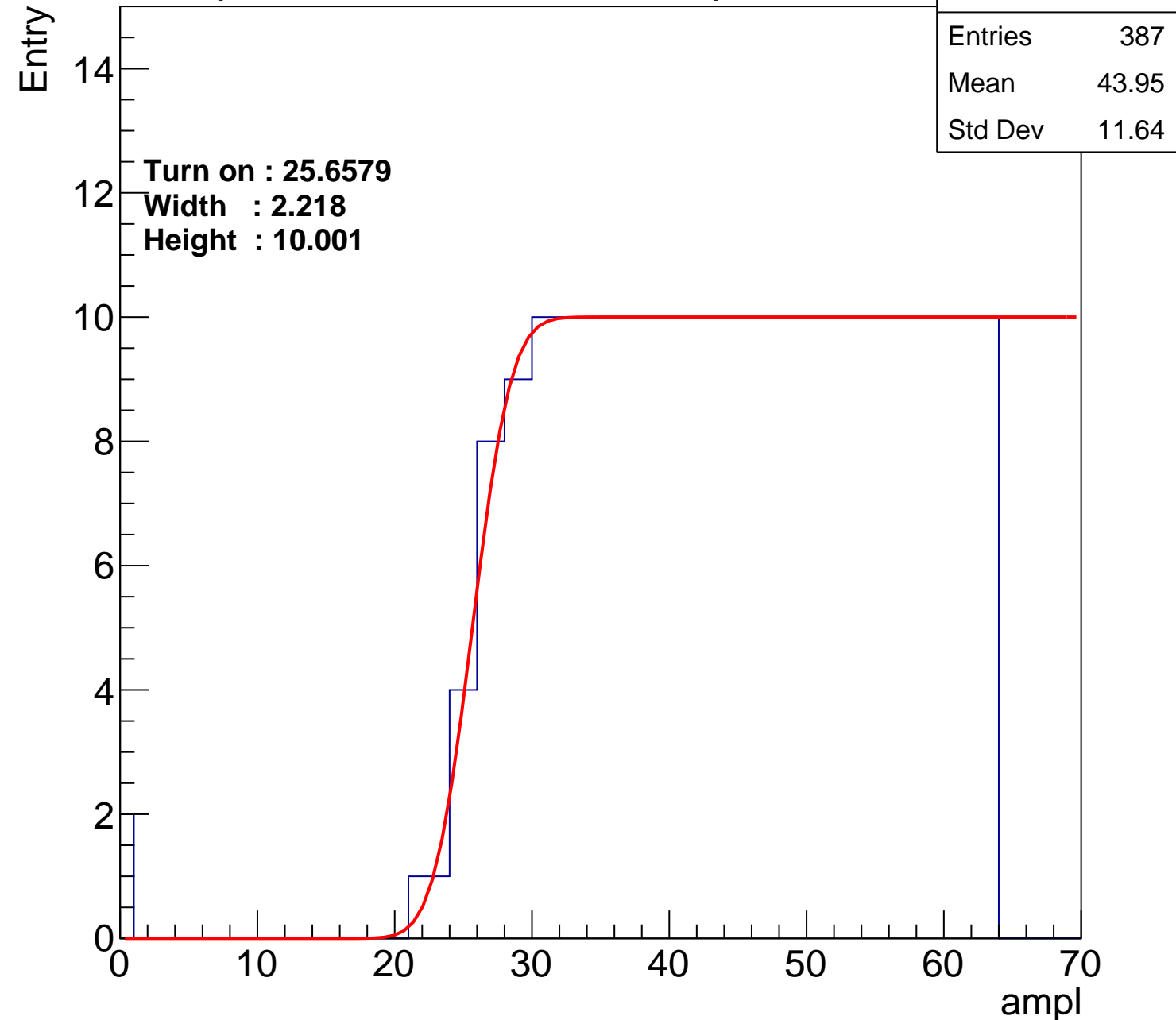
Width : 2.218

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.12
Std Dev	11.53

Turn on : 25.4393

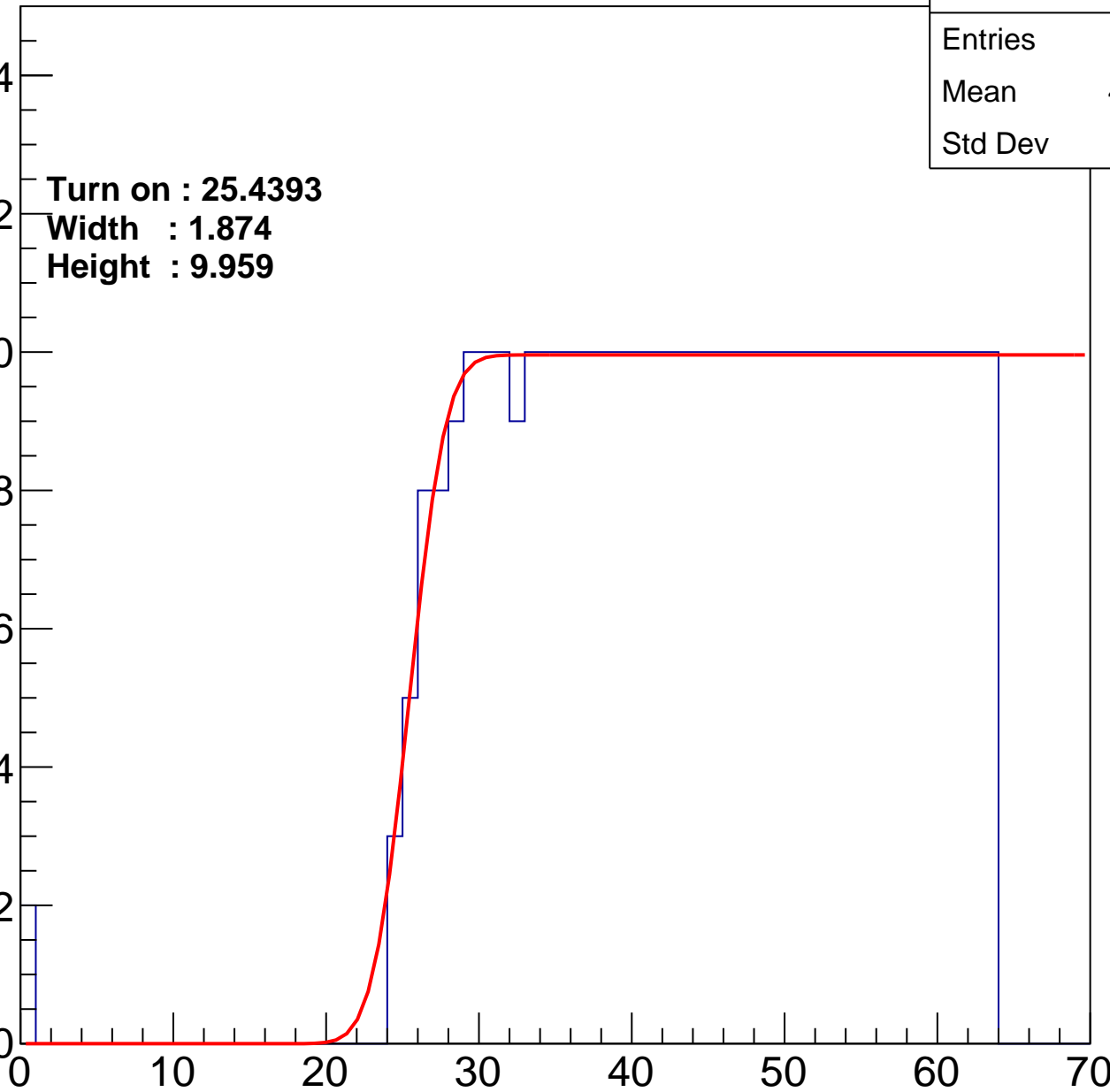
Width : 1.874

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch123

calib_packv5_042523_0143.root, FC#10, port B3

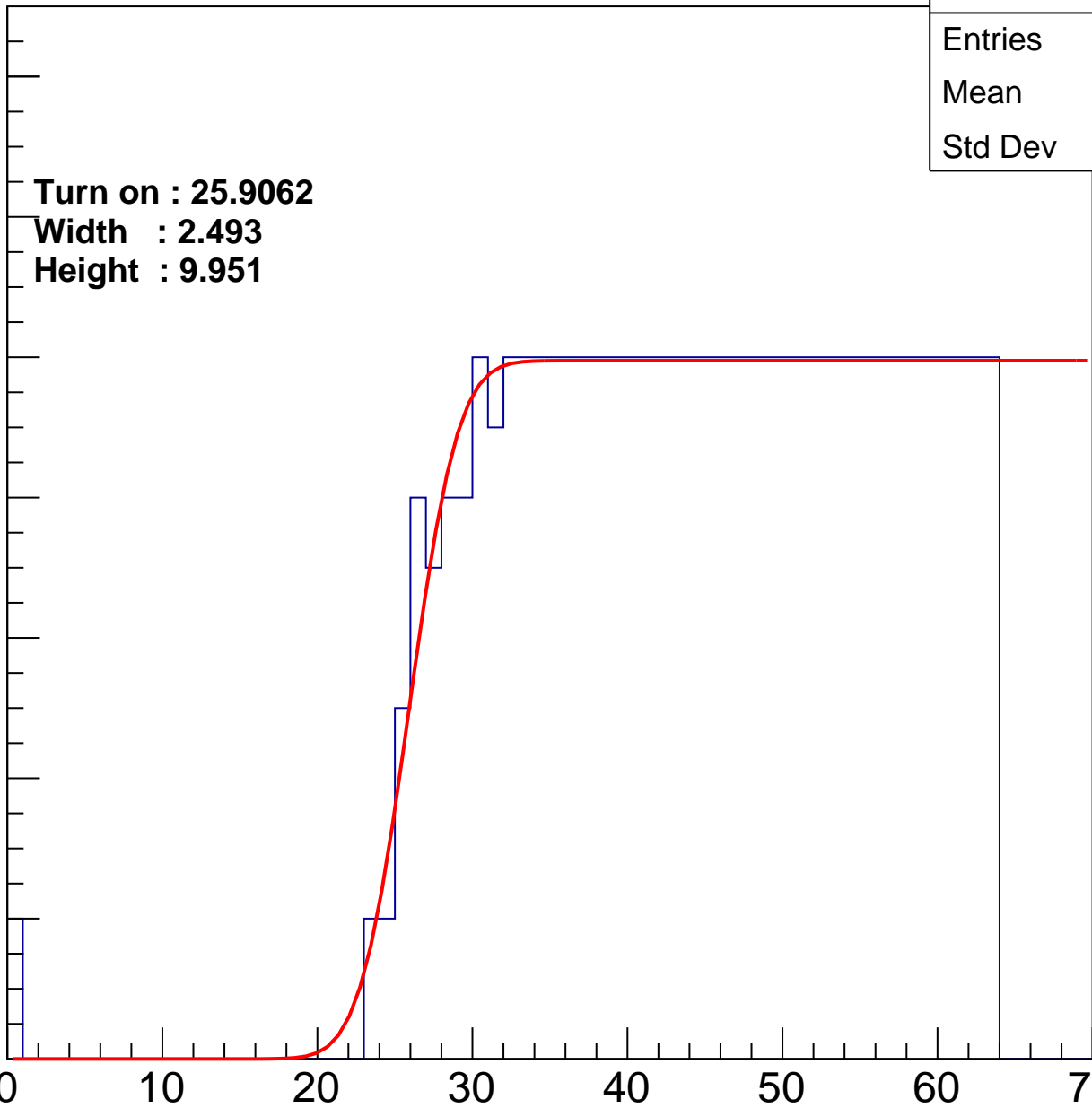
Entry

14
12
10
8
6
4
2
0

Turn on : 25.9062
Width : 2.493
Height : 9.951

Entries	381
Mean	44.23
Std Dev	11.51

ampl



B1L002S, U9-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	362
Mean	45.16
Std Dev	11.03

Turn on : 28.4546

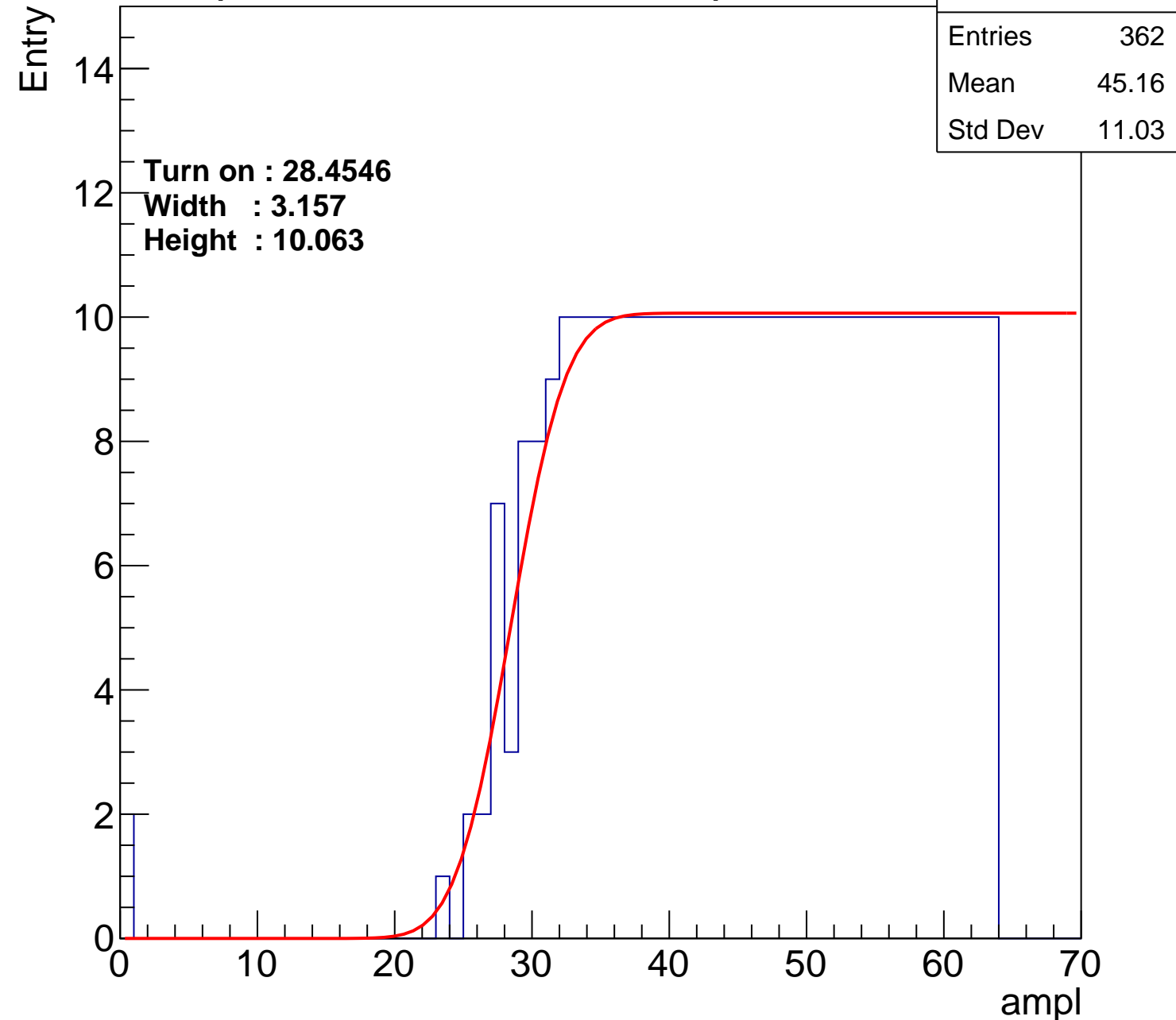
Width : 3.157

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch125

calib_packv5_042523_0143.root, FC#10, port B3

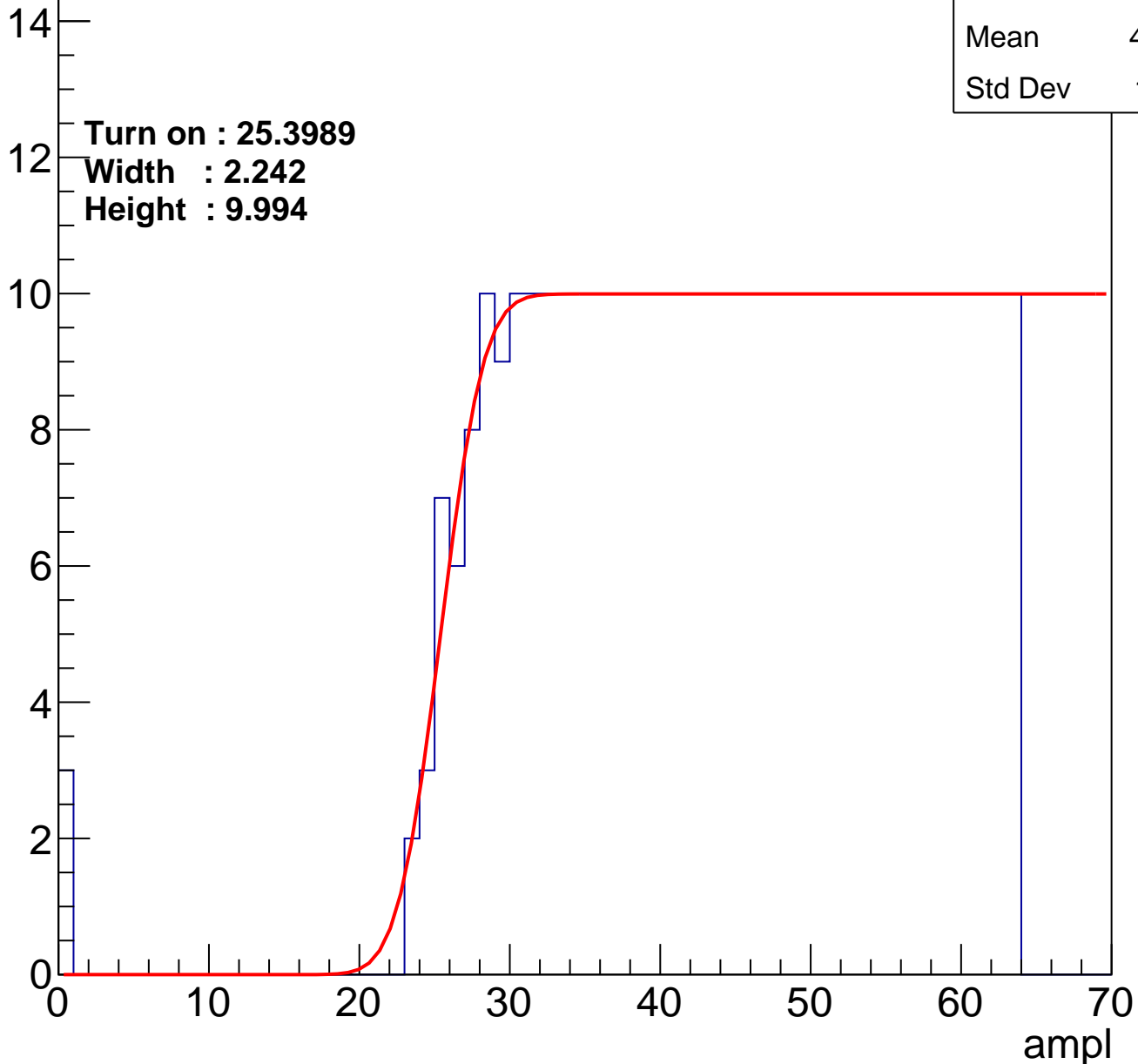
Entries	388
Mean	43.86
Std Dev	11.81

Turn on : 25.3989

Width : 2.242

Height : 9.994

Entry



B1L002S, U9-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.77
Std Dev	11.65

Turn on : 25.2196

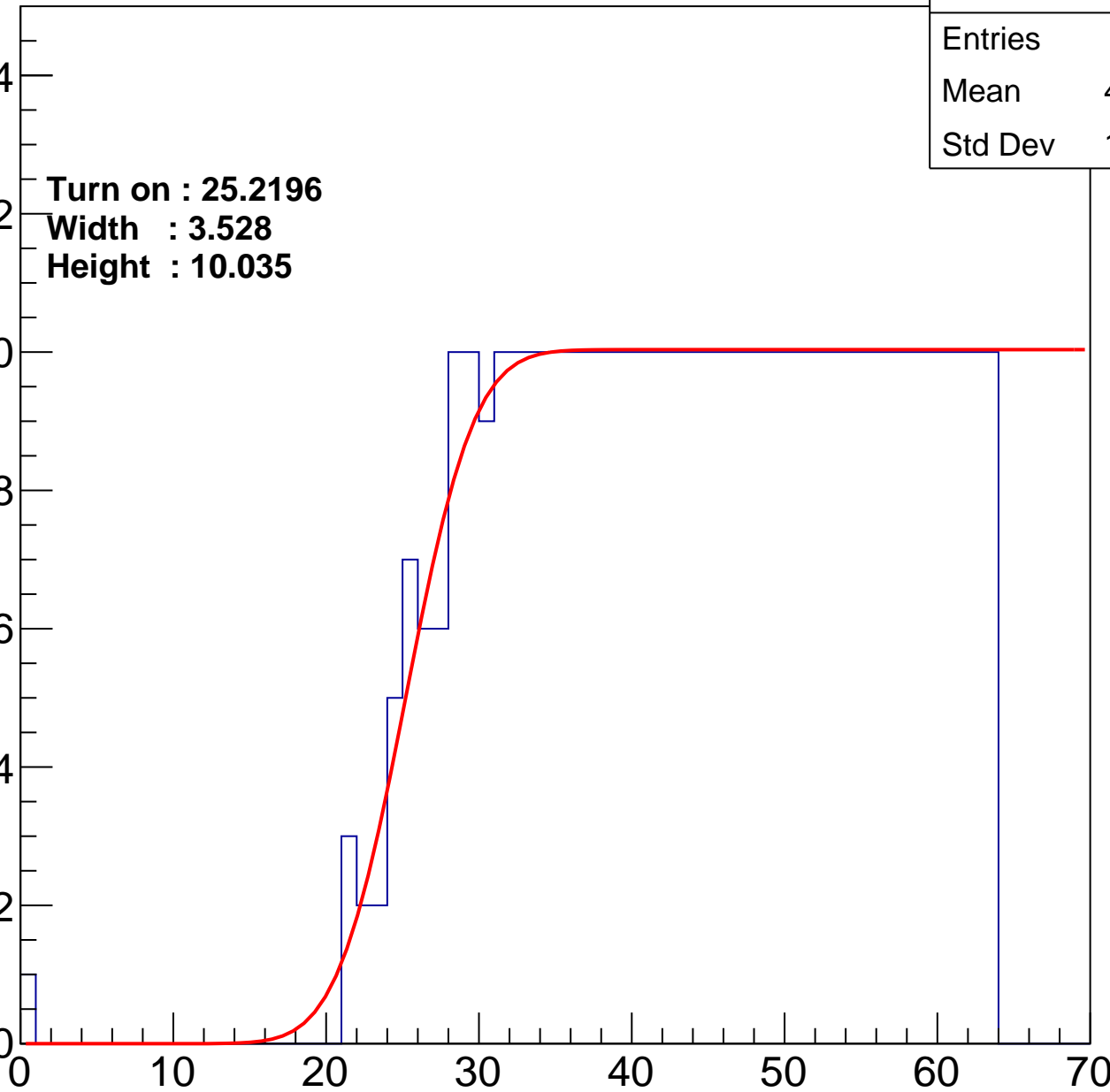
Width : 3.528

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U9-ch127

calib_packv5_042523_0143.root, FC#10, port B3

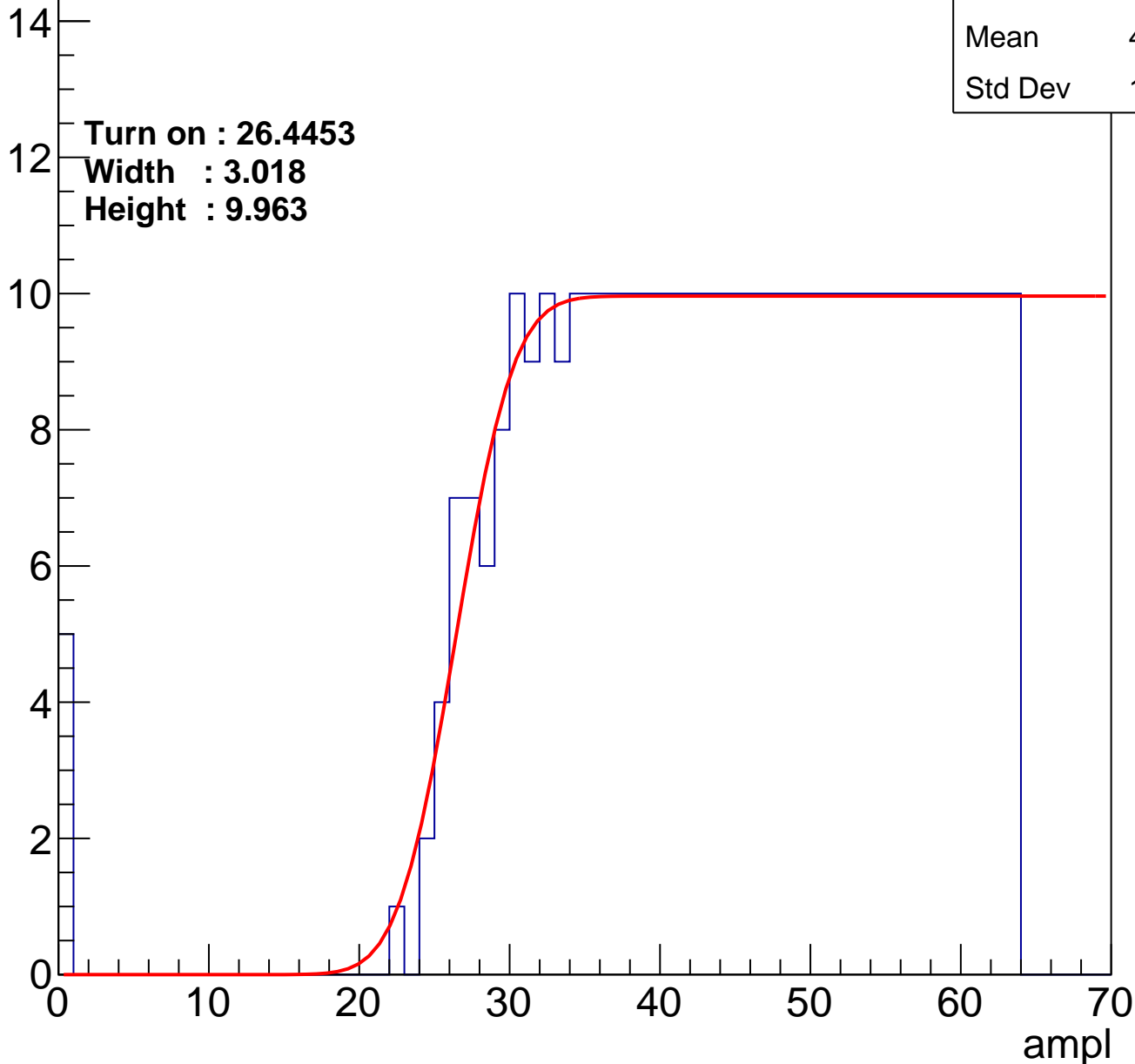
Entries	378
Mean	44.15
Std Dev	12.02

Turn on : 26.4453

Width : 3.018

Height : 9.963

Entry



B1L002S, U9-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.15
Std Dev	12.02

Turn on : 26.4453

Width : 3.018

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl

