

B1L102S, U14-ch0

calib_packv5_042523_0143.root, FC#11, port A2

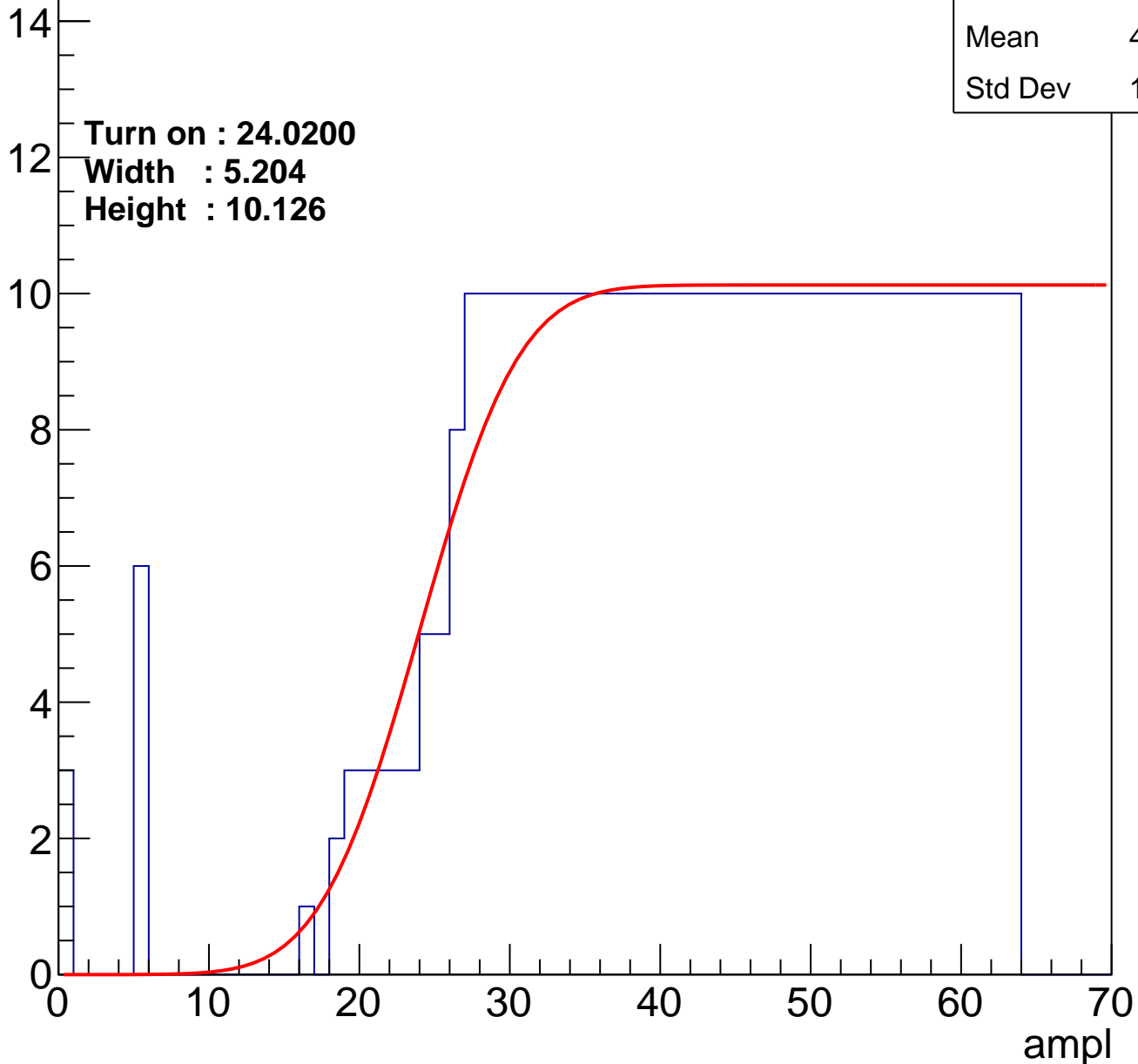
Entries	415
Mean	42.17
Std Dev	13.23

Turn on : 24.0200

Width : 5.204

Height : 10.126

Entry



B1L102S, U14-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.87
Std Dev	11.73

Turn on : 26.3028

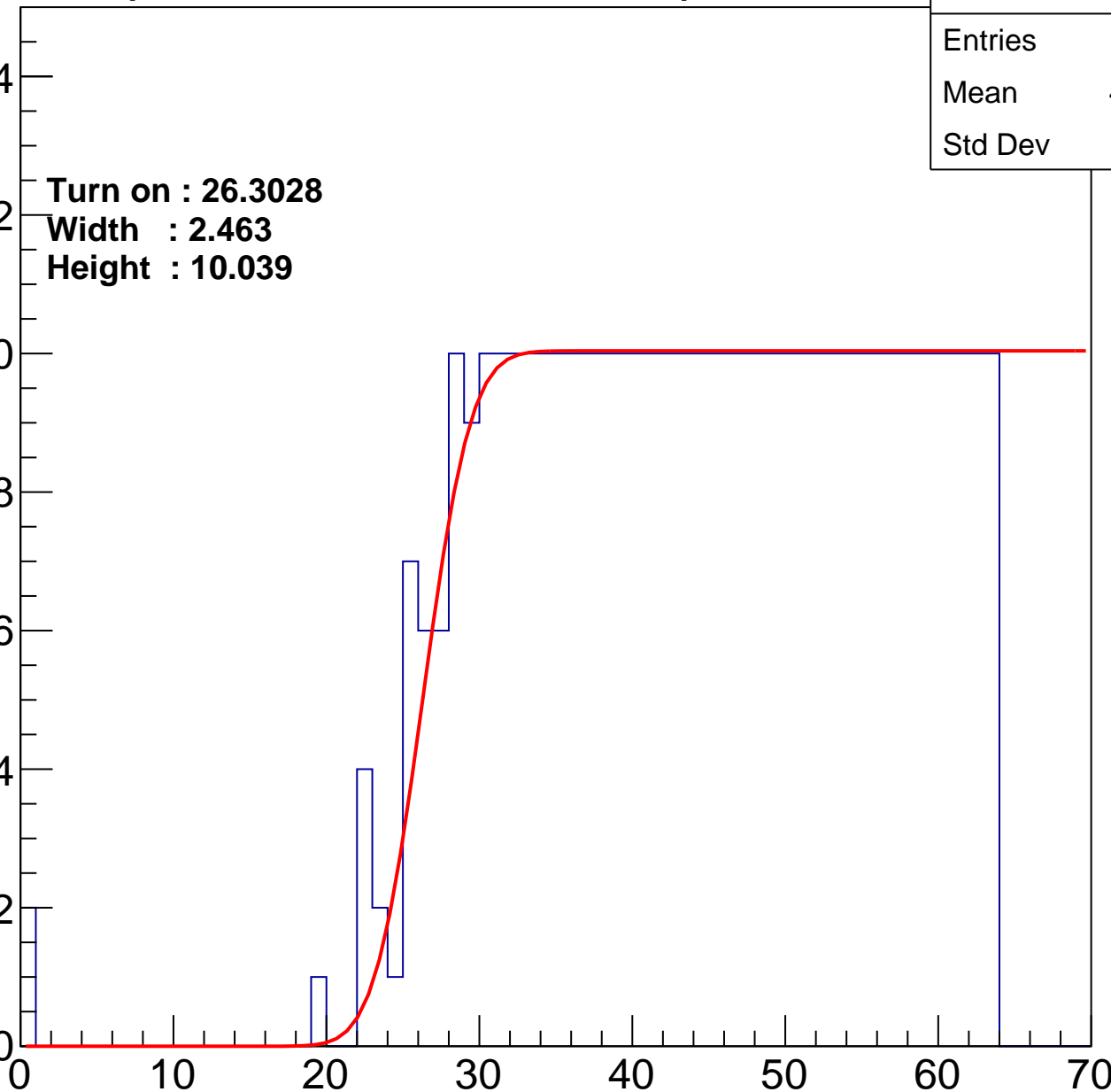
Width : 2.463

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 25.3105

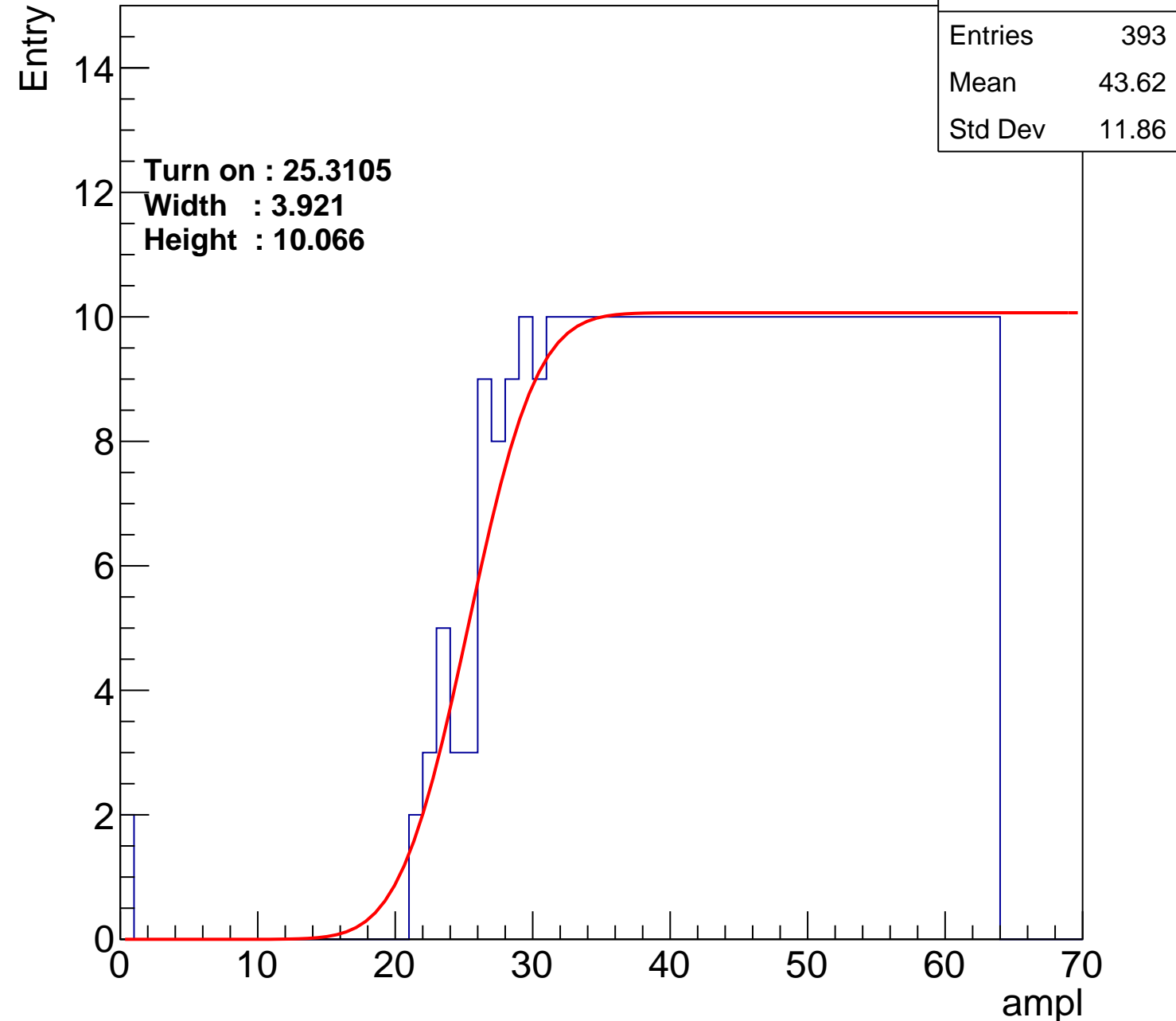
Width : 3.921

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.64
Std Dev	12.1

Turn on : 25.1304

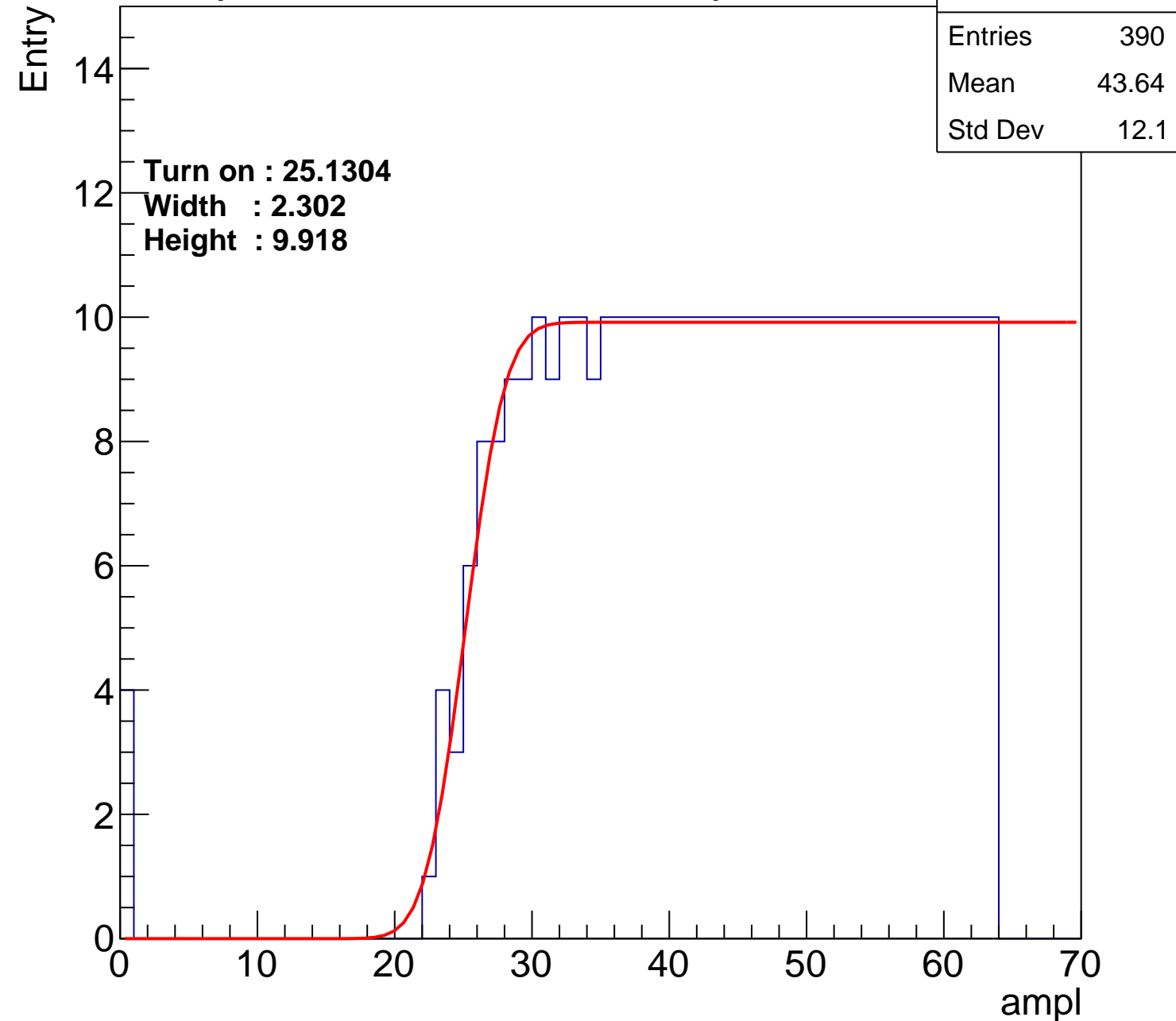
Width : 2.302

Height : 9.918

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.37
Std Dev	11.81

Turn on : 27.1140

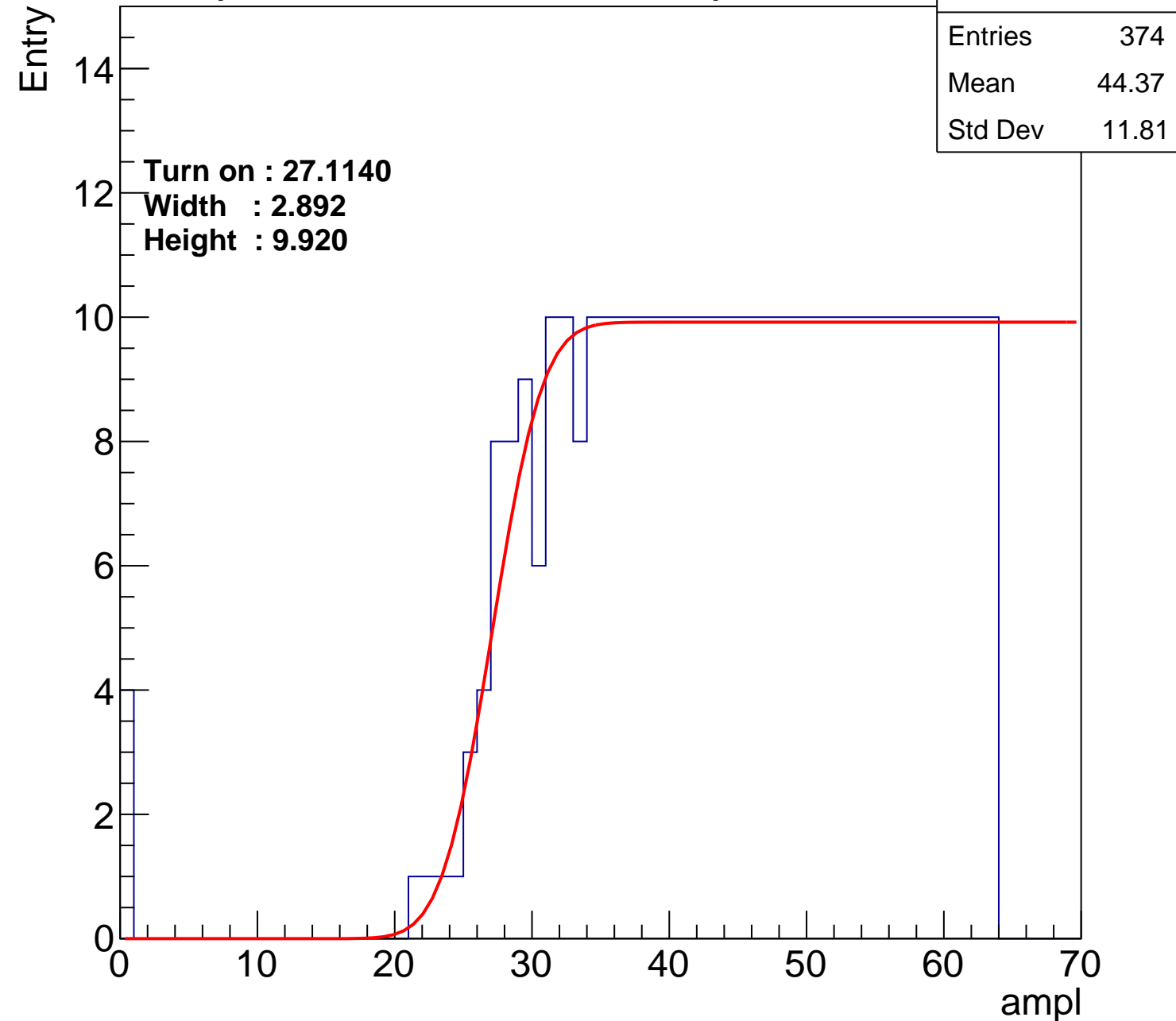
Width : 2.892

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.7
Std Dev	11.95

Turn on : 25.5823

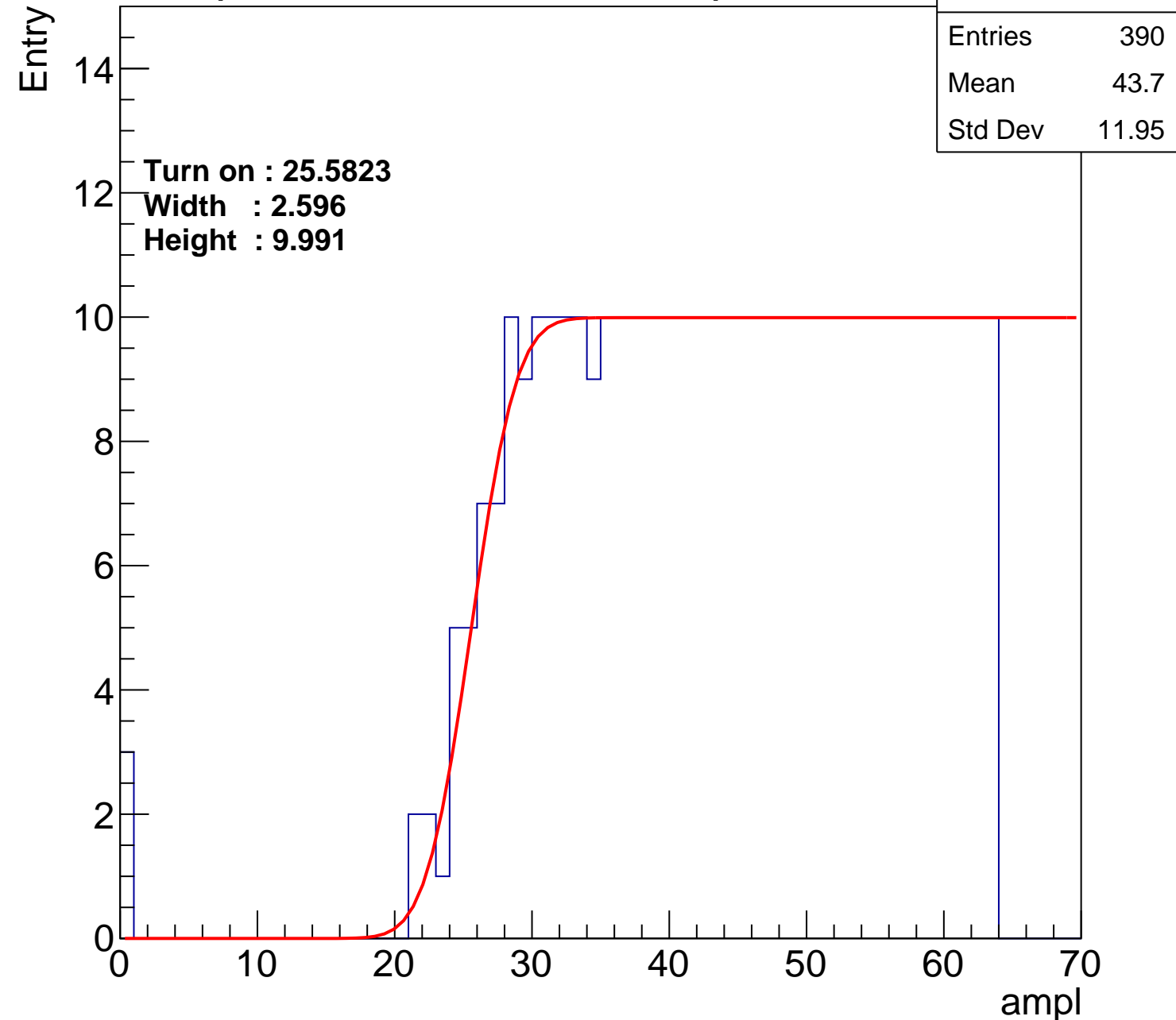
Width : 2.596

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.35
Std Dev	12.24

Turn on : 25.1555

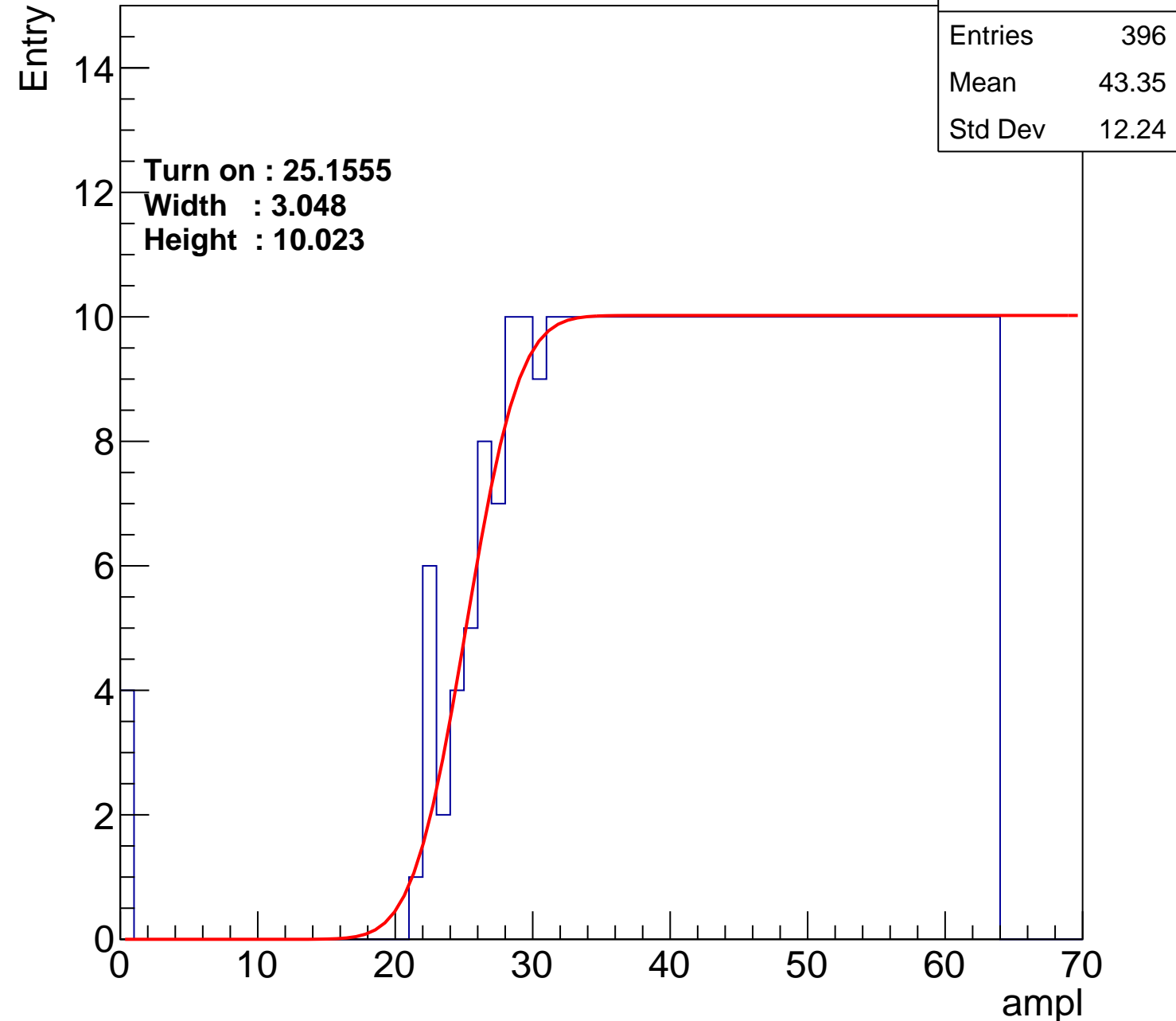
Width : 3.048

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.36
Std Dev	12.24

Turn on : 24.8136

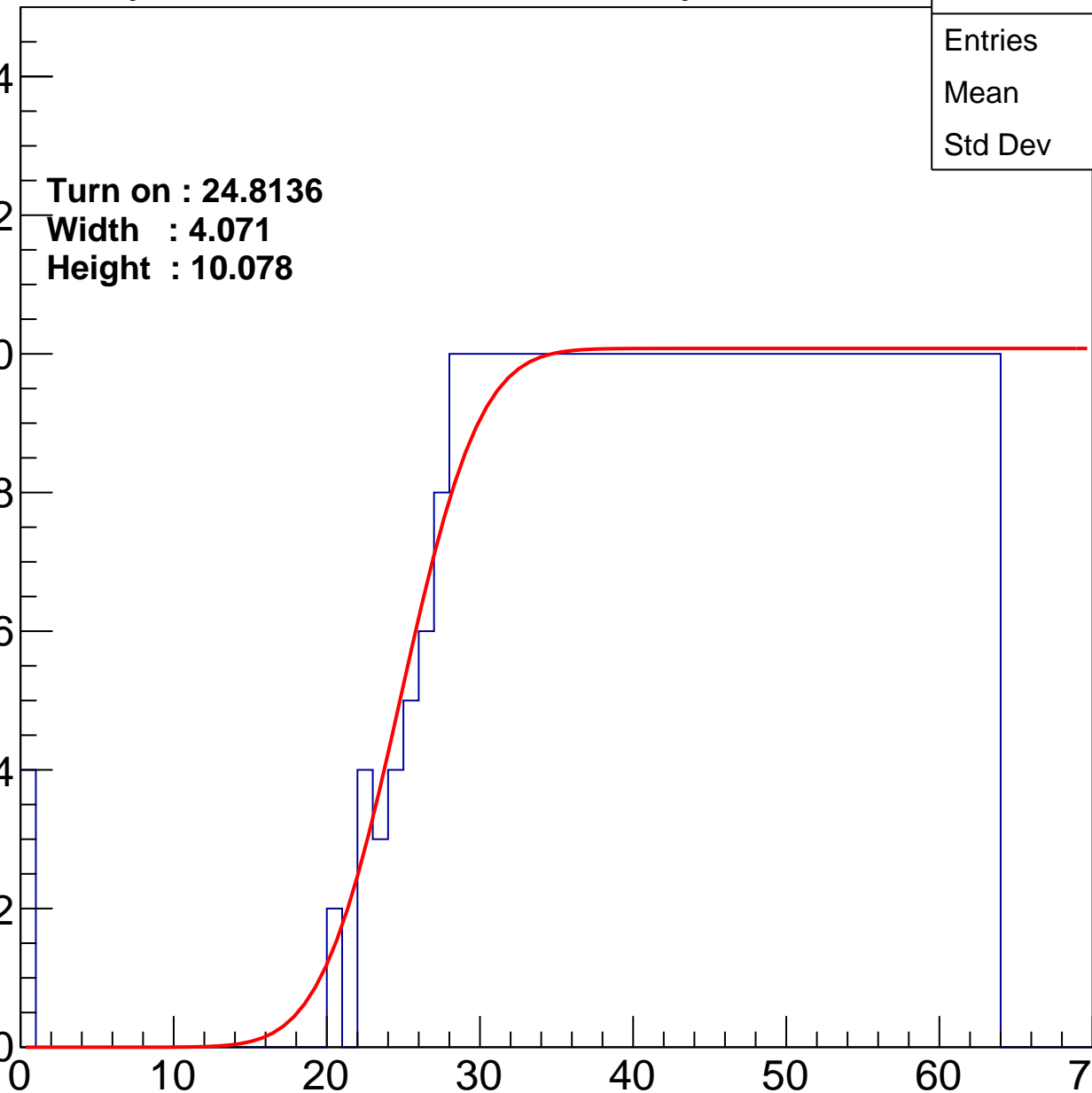
Width : 4.071

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 25.7149

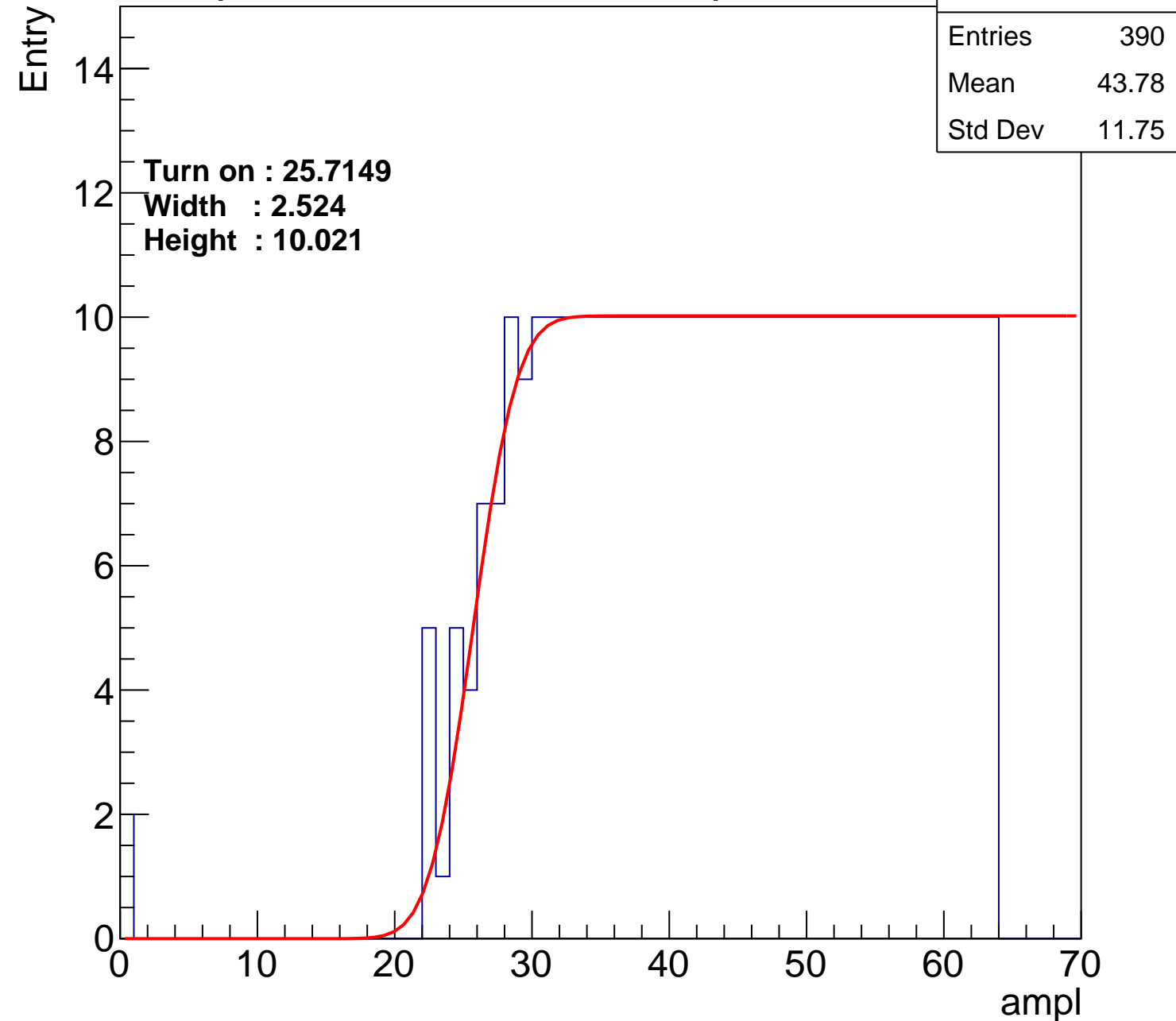
Width : 2.524

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	408
Mean	42.81
Std Dev	12.46

Turn on : 23.2700

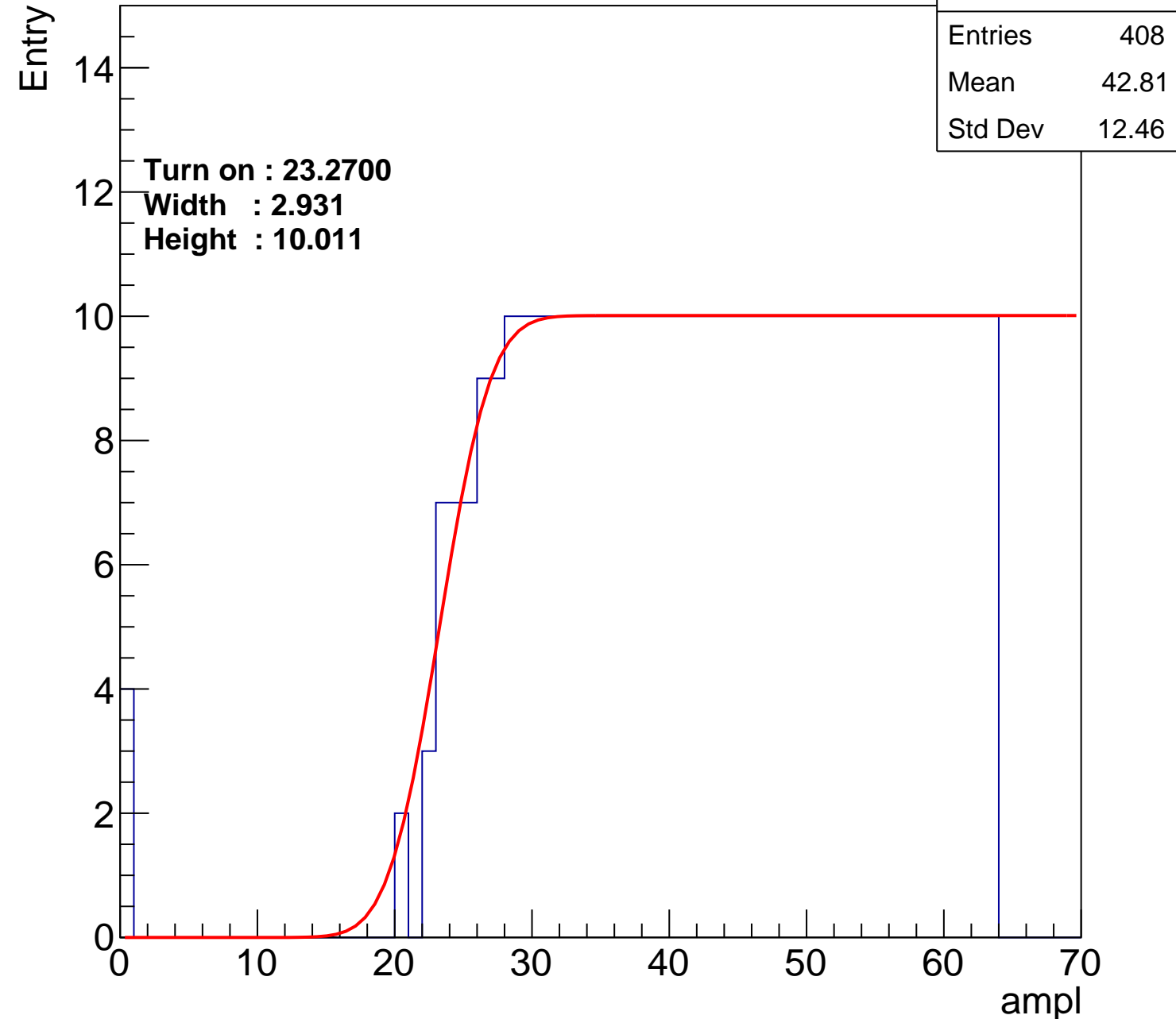
Width : 2.931

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch10

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.26
Std Dev	12.07

Turn on : 24.6121

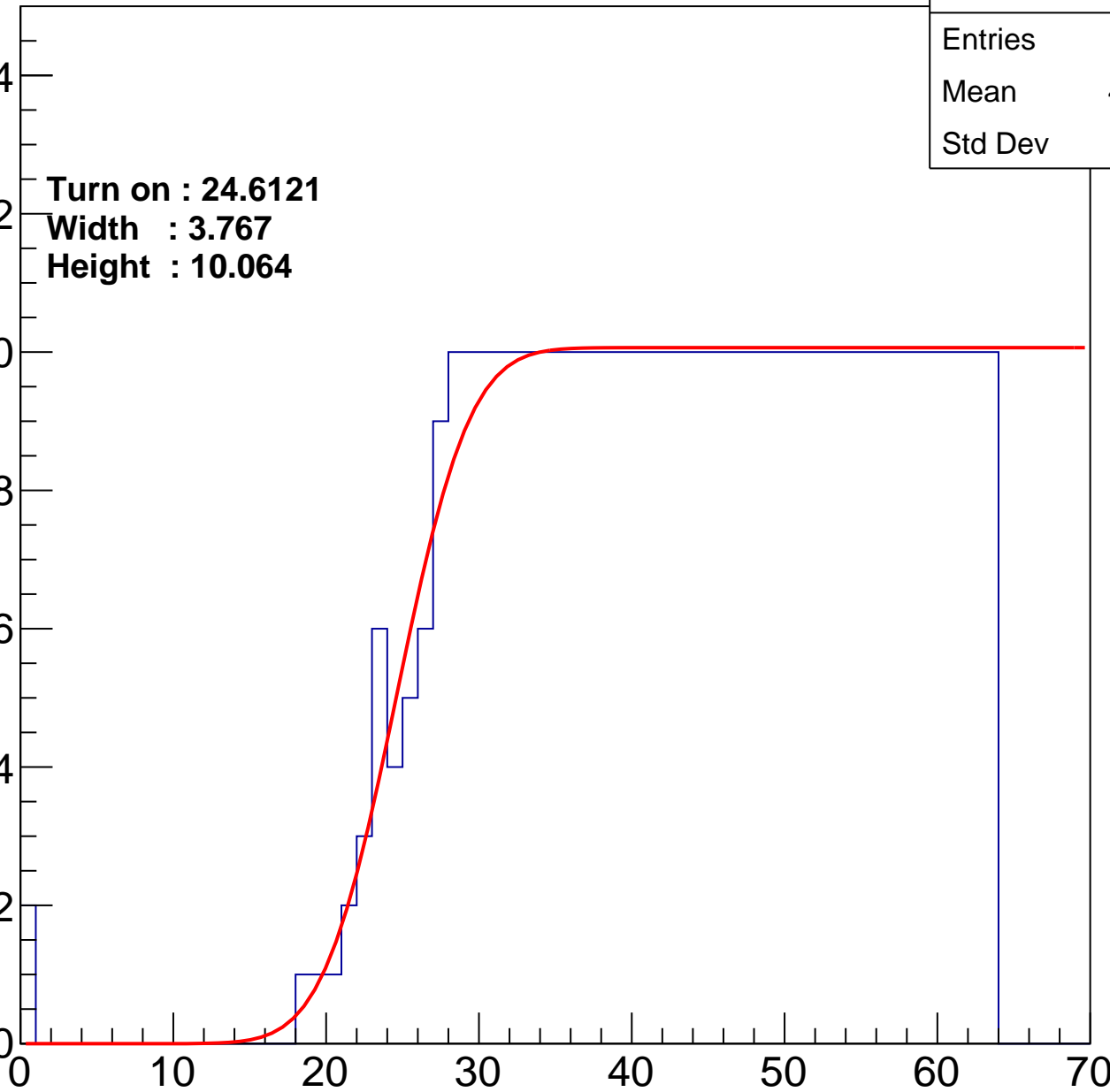
Width : 3.767

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch11

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.71
Std Dev	11.9

Turn on : 26.0515

Width : 2.031

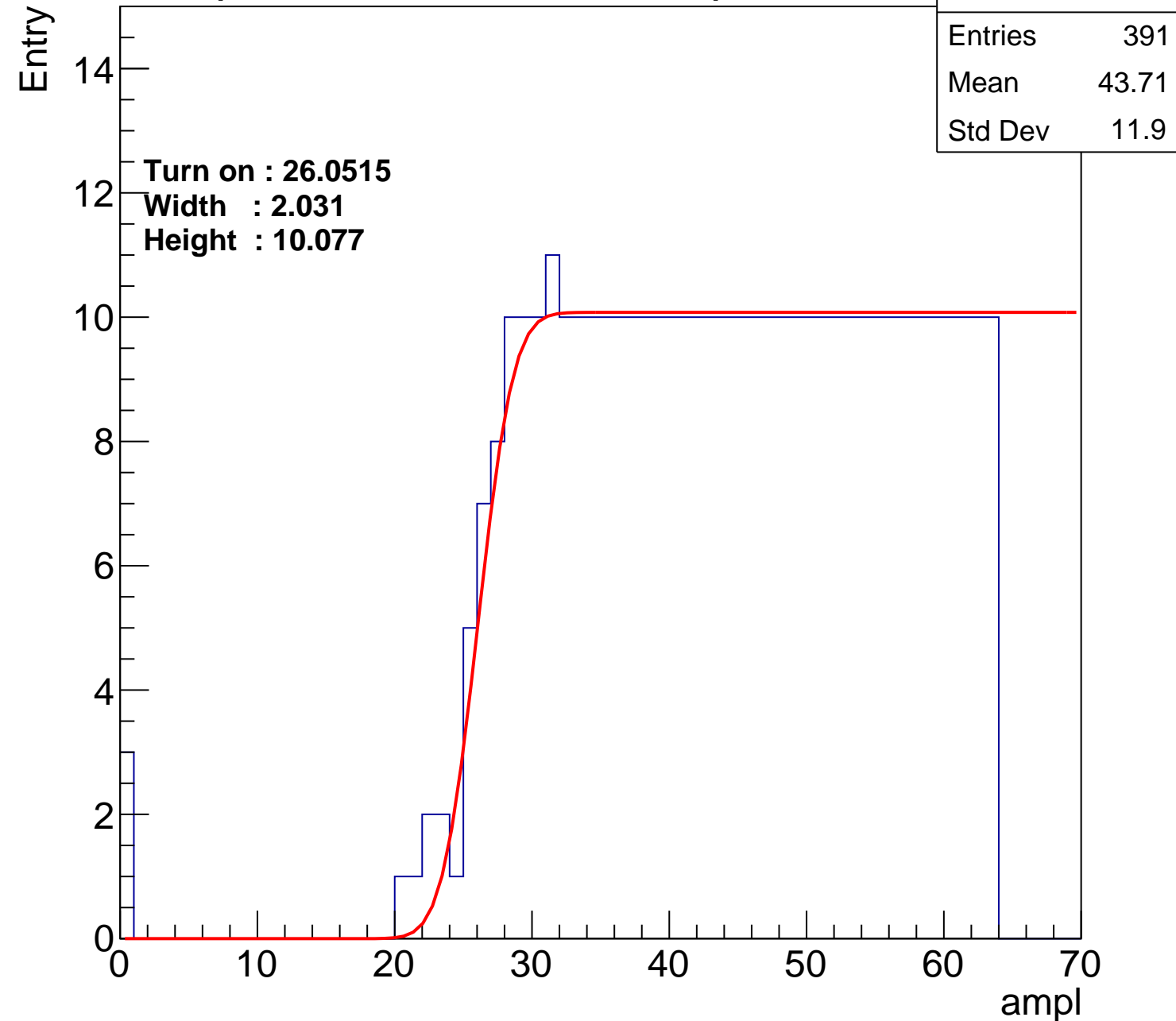
Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U14-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.37
Std Dev	11.94

Turn on : 24.2589

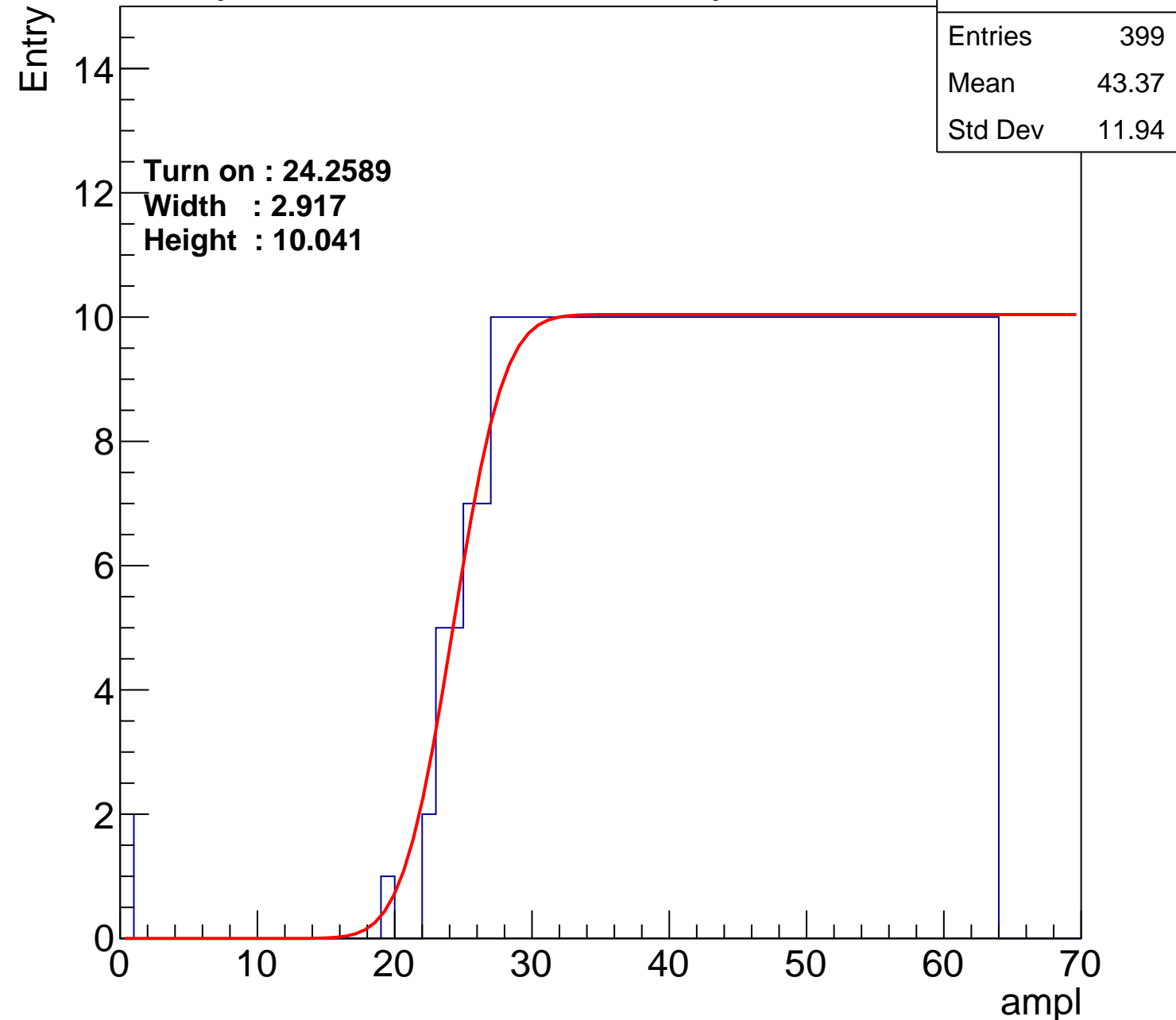
Width : 2.917

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch13

calib_packv5_042523_0143.root, FC#11, port A2

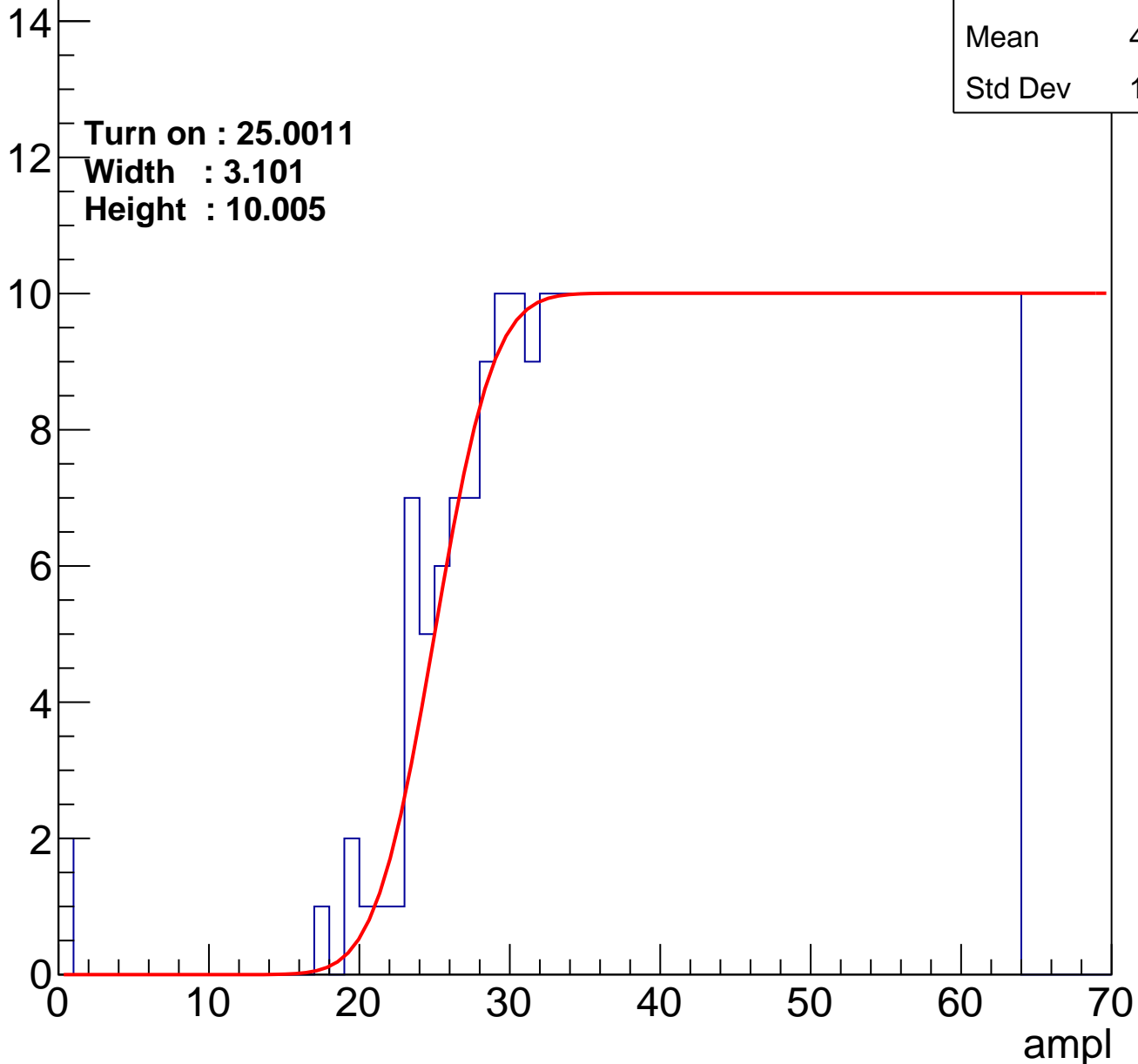
Entries	398
Mean	43.32
Std Dev	12.08

Turn on : 25.0011

Width : 3.101

Height : 10.005

Entry



B1L102S, U14-ch14

calib_packv5_042523_0143.root, FC#11, port A2

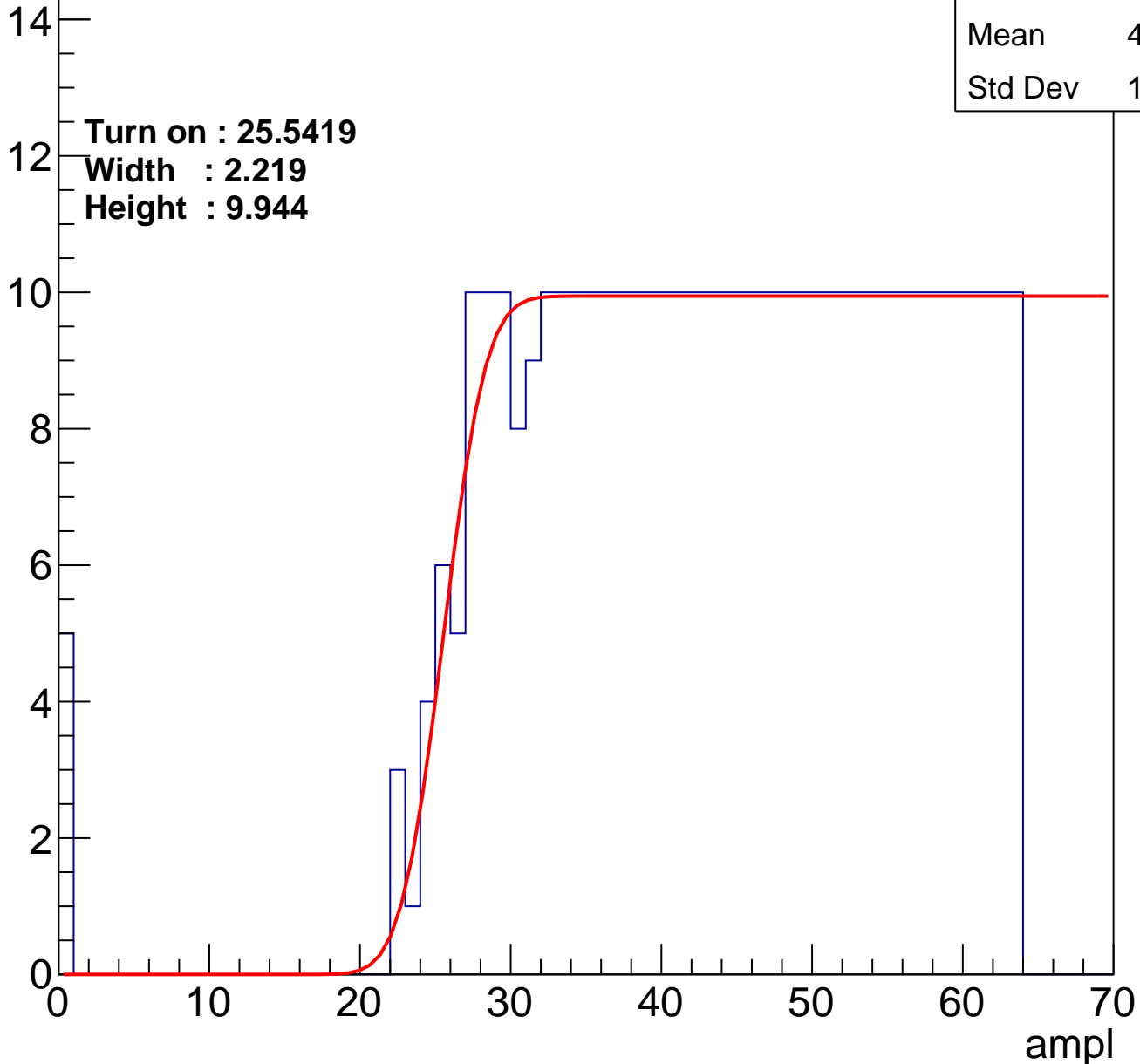
Entries	391
Mean	43.54
Std Dev	12.27

Turn on : 25.5419

Width : 2.219

Height : 9.944

Entry



B1L102S, U14-ch15

calib_packv5_042523_0143.root, FC#11, port A2

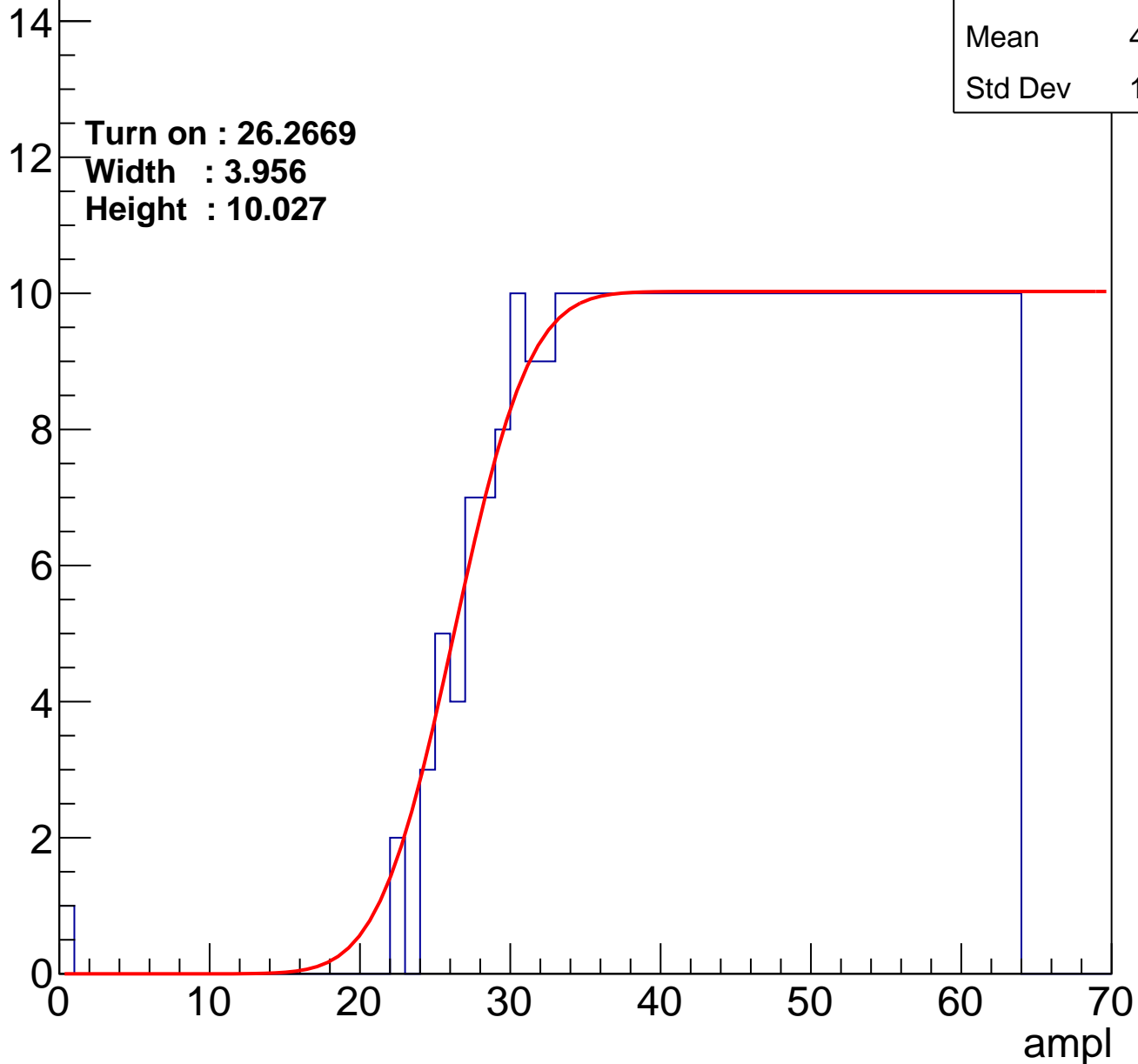
Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.2669

Width : 3.956

Height : 10.027

Entry



B1L102S, U14-ch16

calib_packv5_042523_0143.root, FC#11, port A2

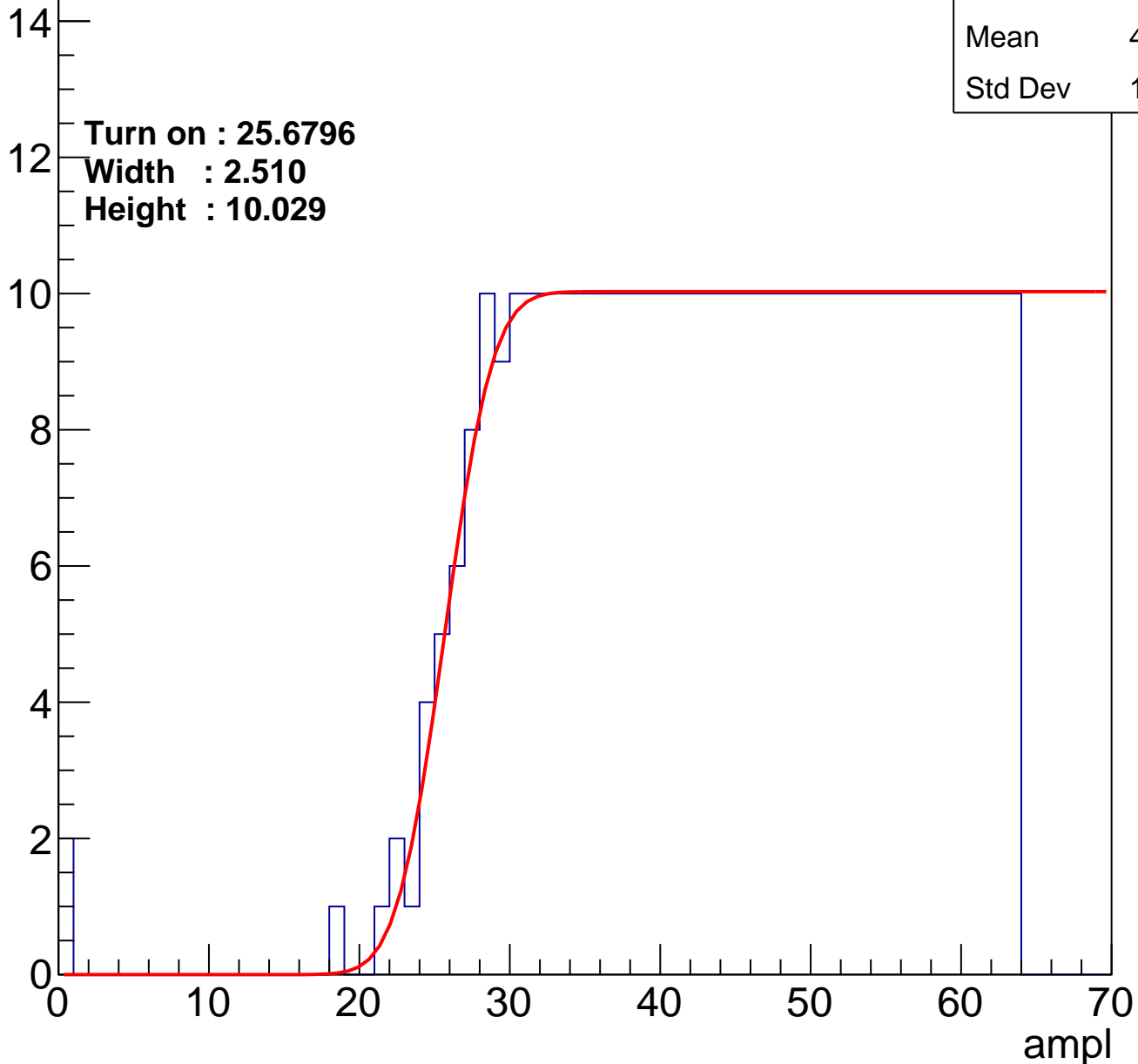
Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.6796

Width : 2.510

Height : 10.029

Entry



B1L102S, U14-ch17

calib_packv5_042523_0143.root, FC#11, port A2

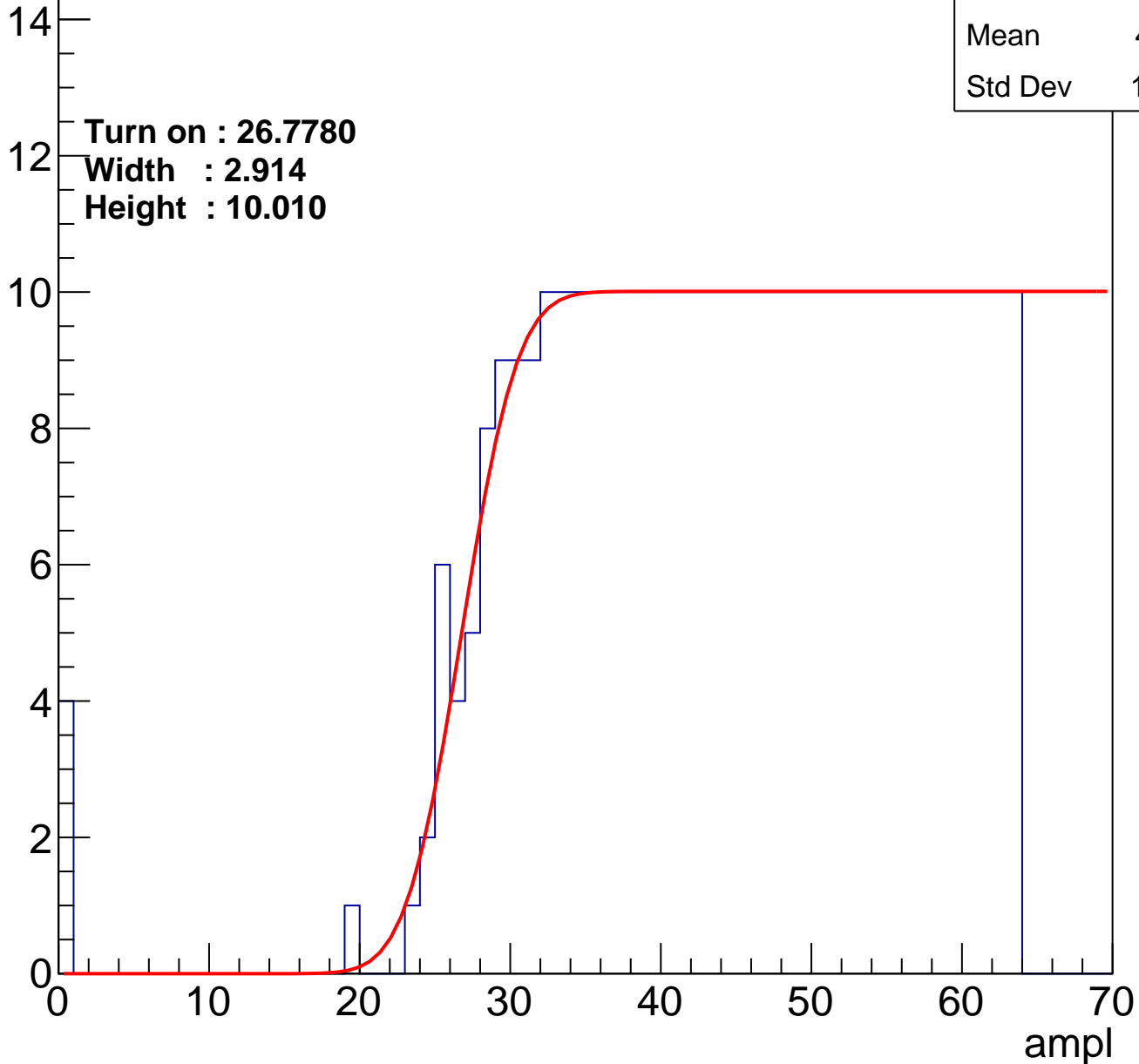
Entries	378
Mean	44.21
Std Dev	11.85

Turn on : 26.7780

Width : 2.914

Height : 10.010

Entry



B1L102S, U14-ch18

calib_packv5_042523_0143.root, FC#11, port A2

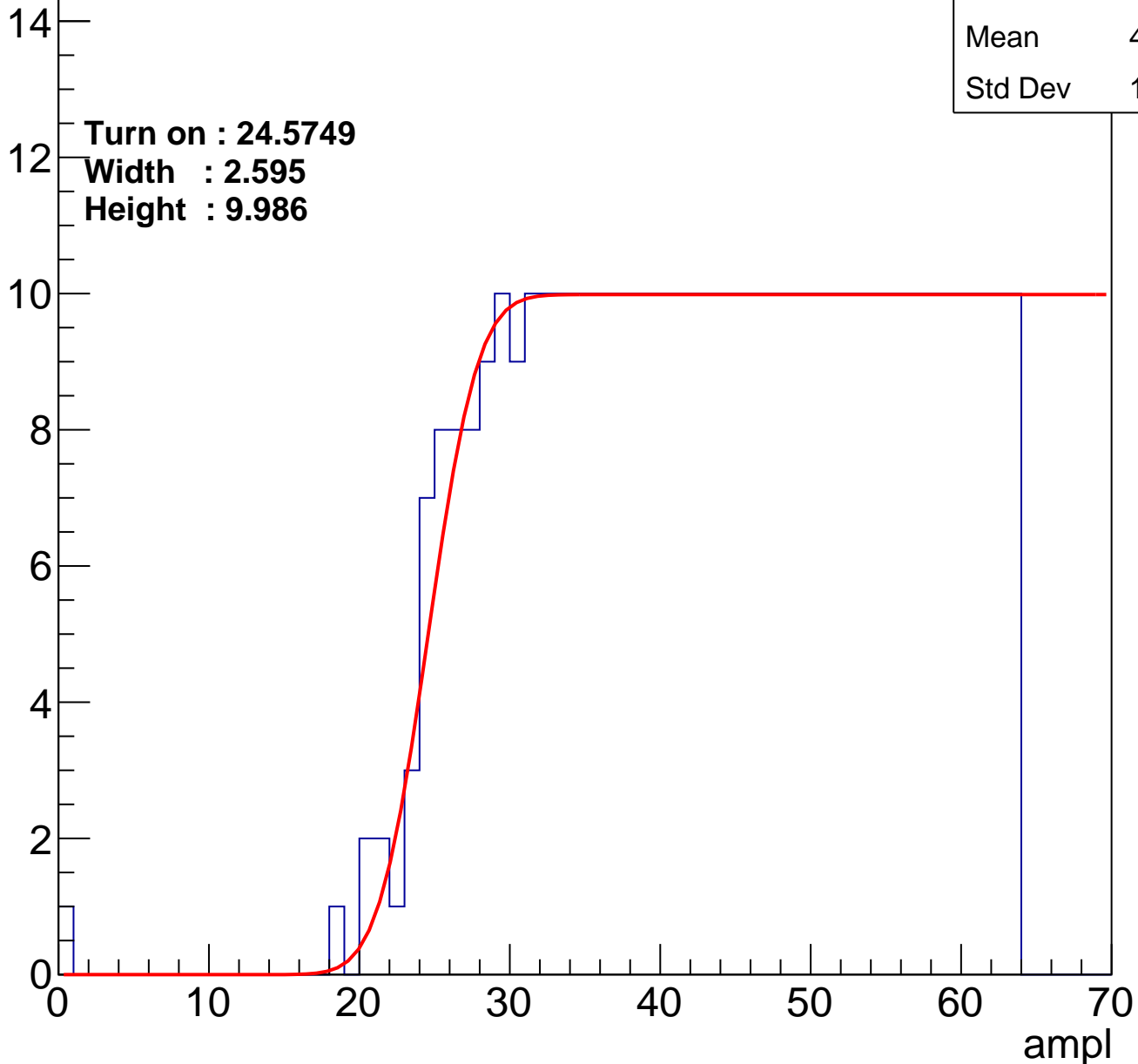
Entries	399
Mean	43.37
Std Dev	11.88

Turn on : 24.5749

Width : 2.595

Height : 9.986

Entry



B1L102S, U14-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	42.97
Std Dev	12.63

Turn on : 24.3918

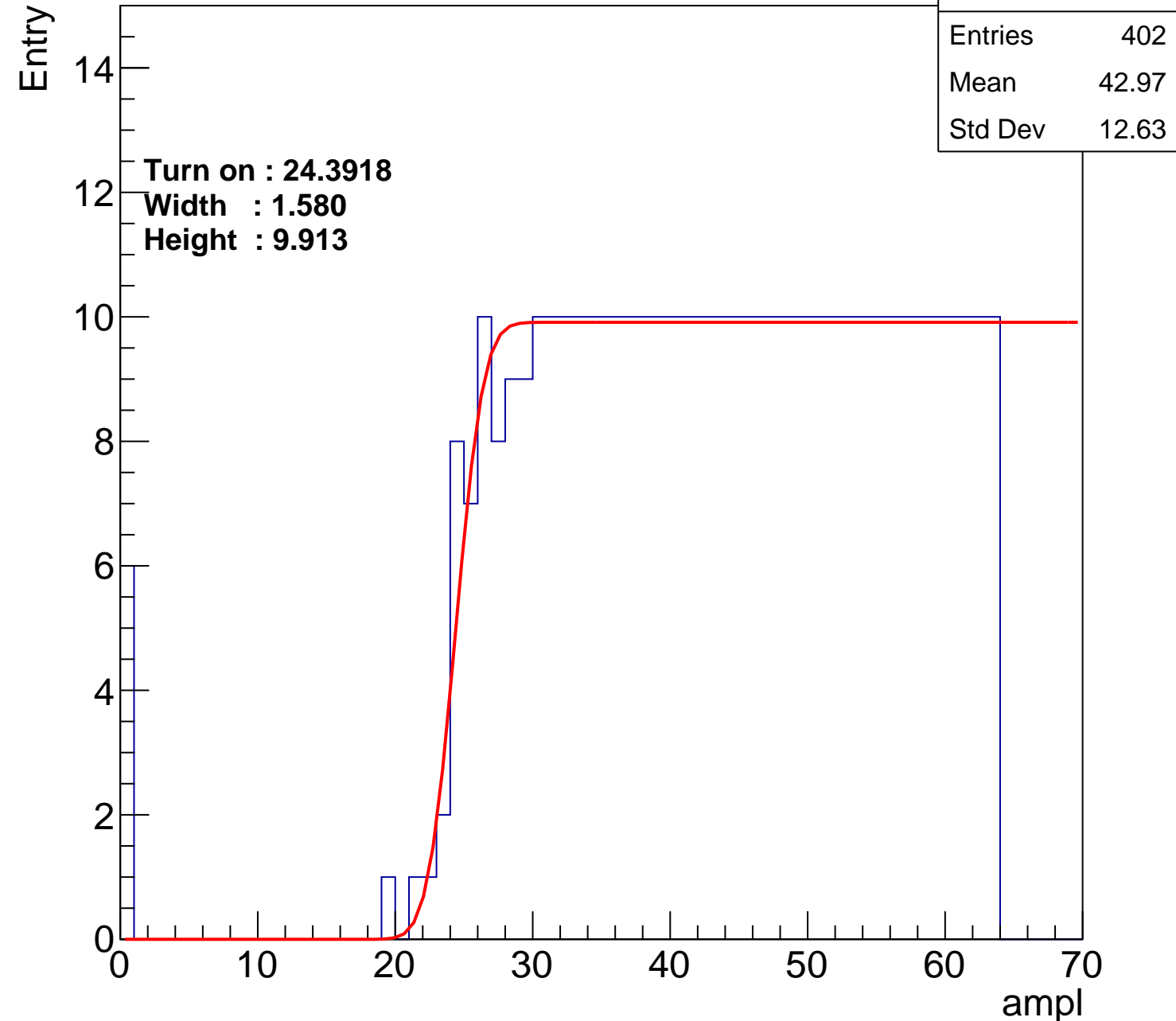
Width : 1.580

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.73
Std Dev	11.84

Turn on : 25.3190

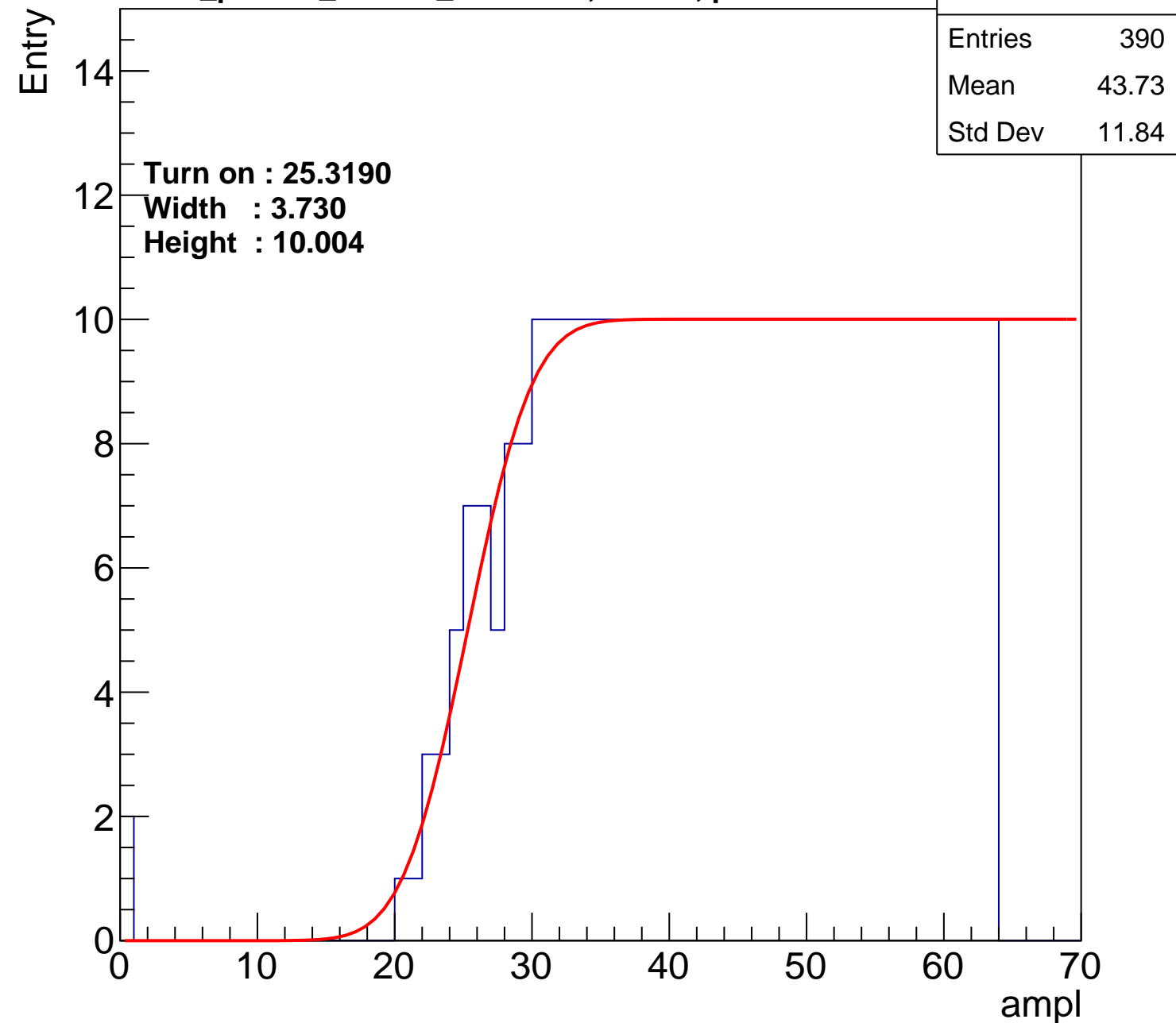
Width : 3.730

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch21

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43
Std Dev	12.38

Turn on : 23.9473

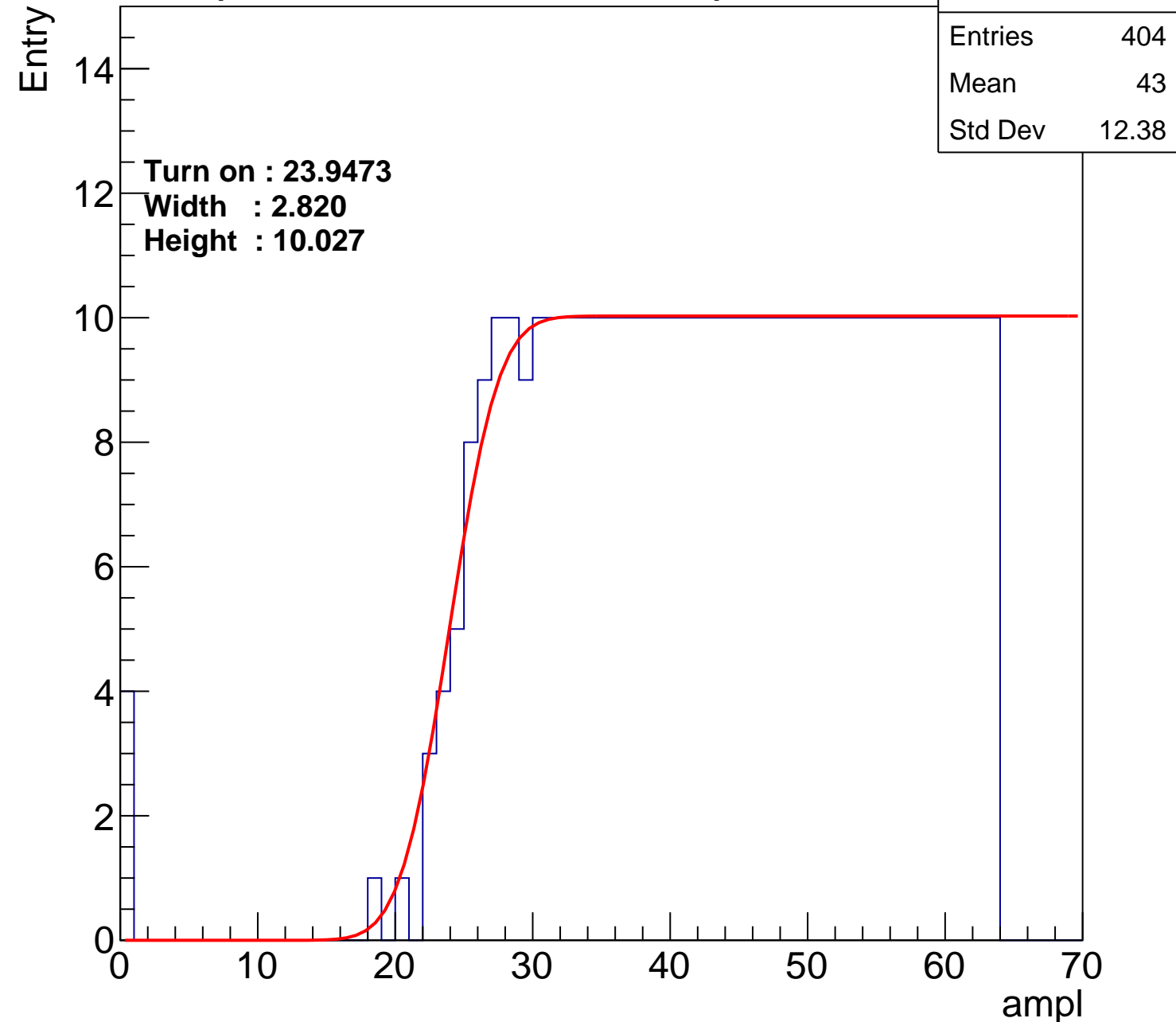
Width : 2.820

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch22

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.25
Std Dev	12.56

Turn on : 25.6770

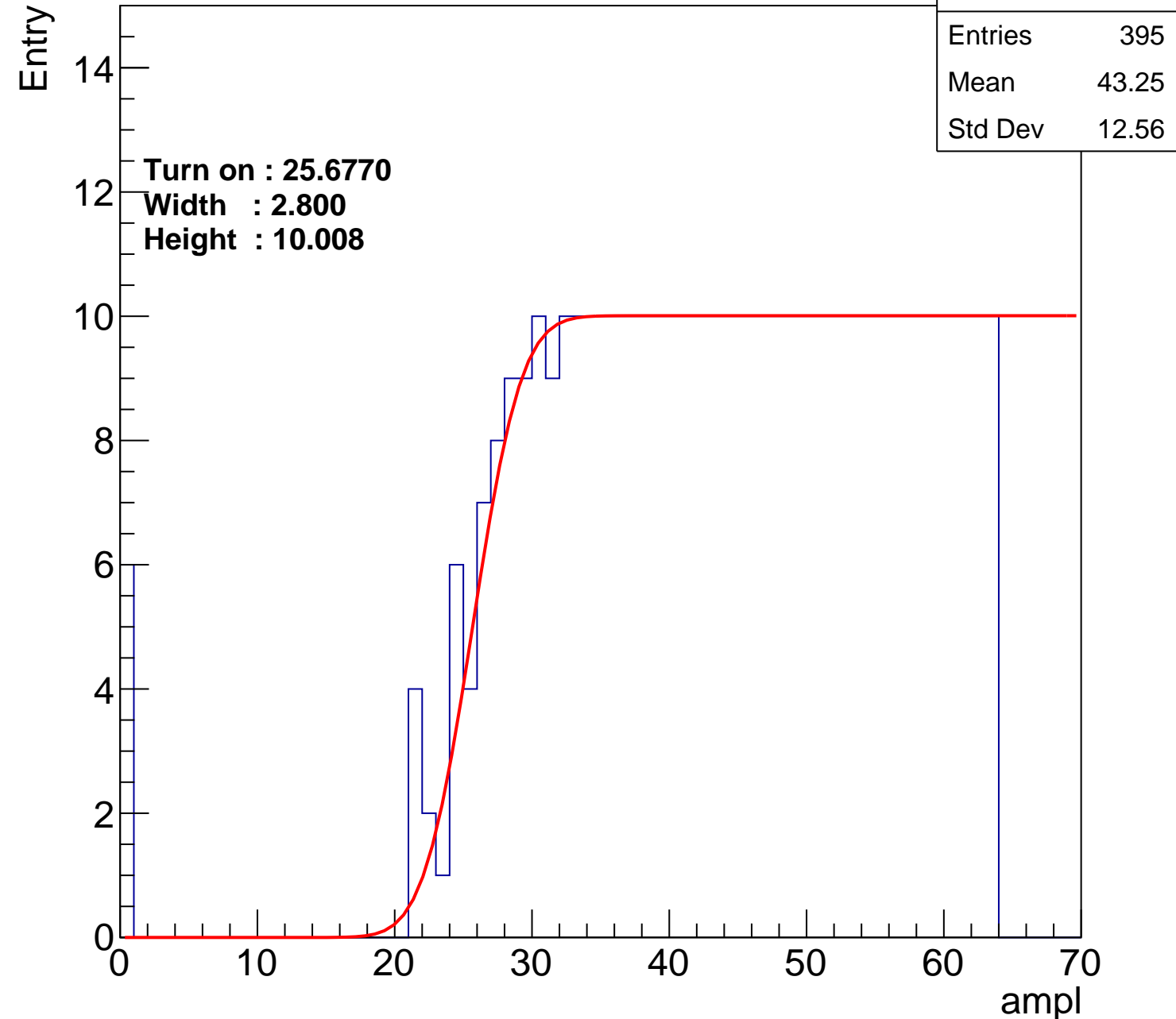
Width : 2.800

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 27.4951

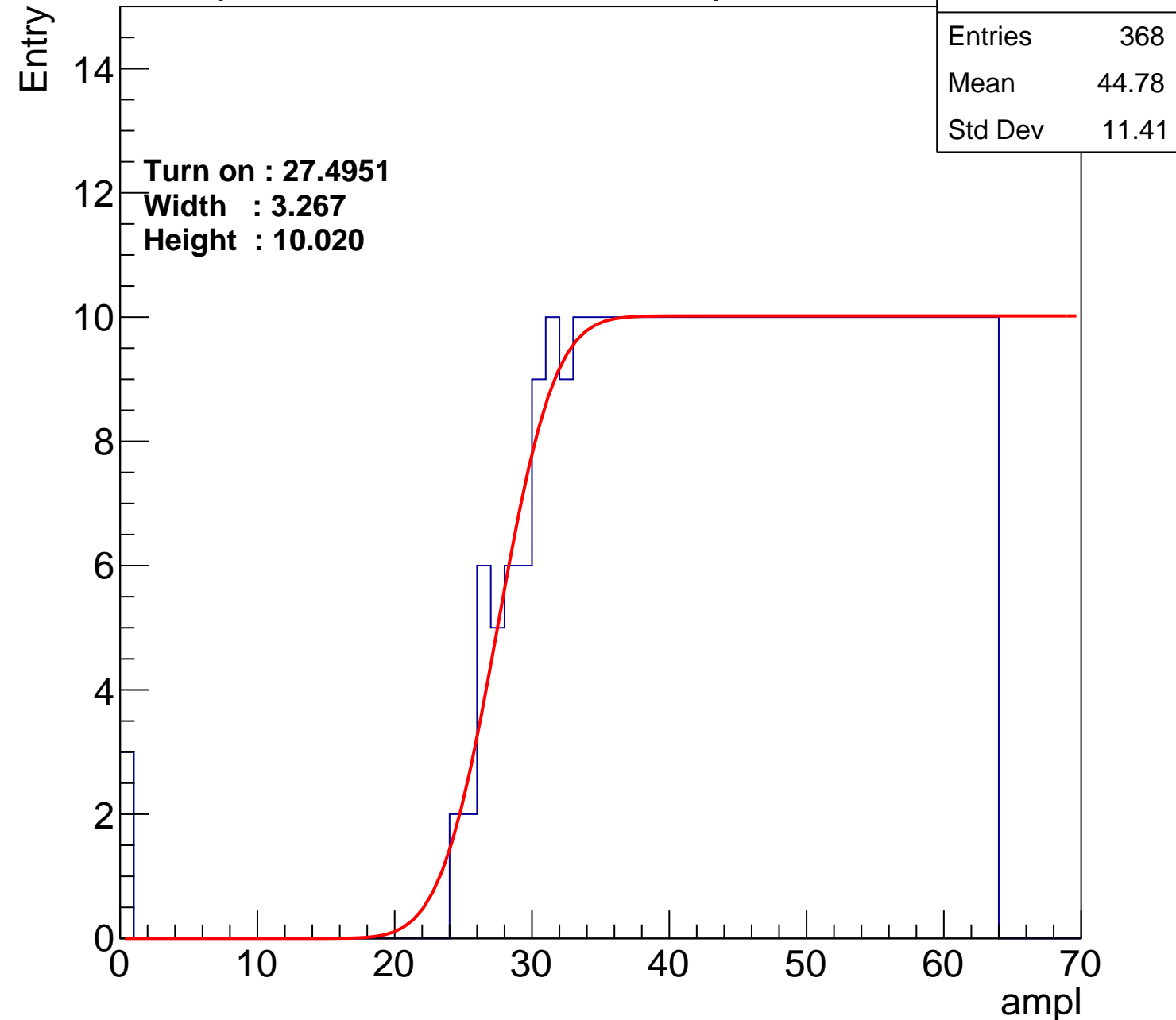
Width : 3.267

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch24

calib_packv5_042523_0143.root, FC#11, port A2

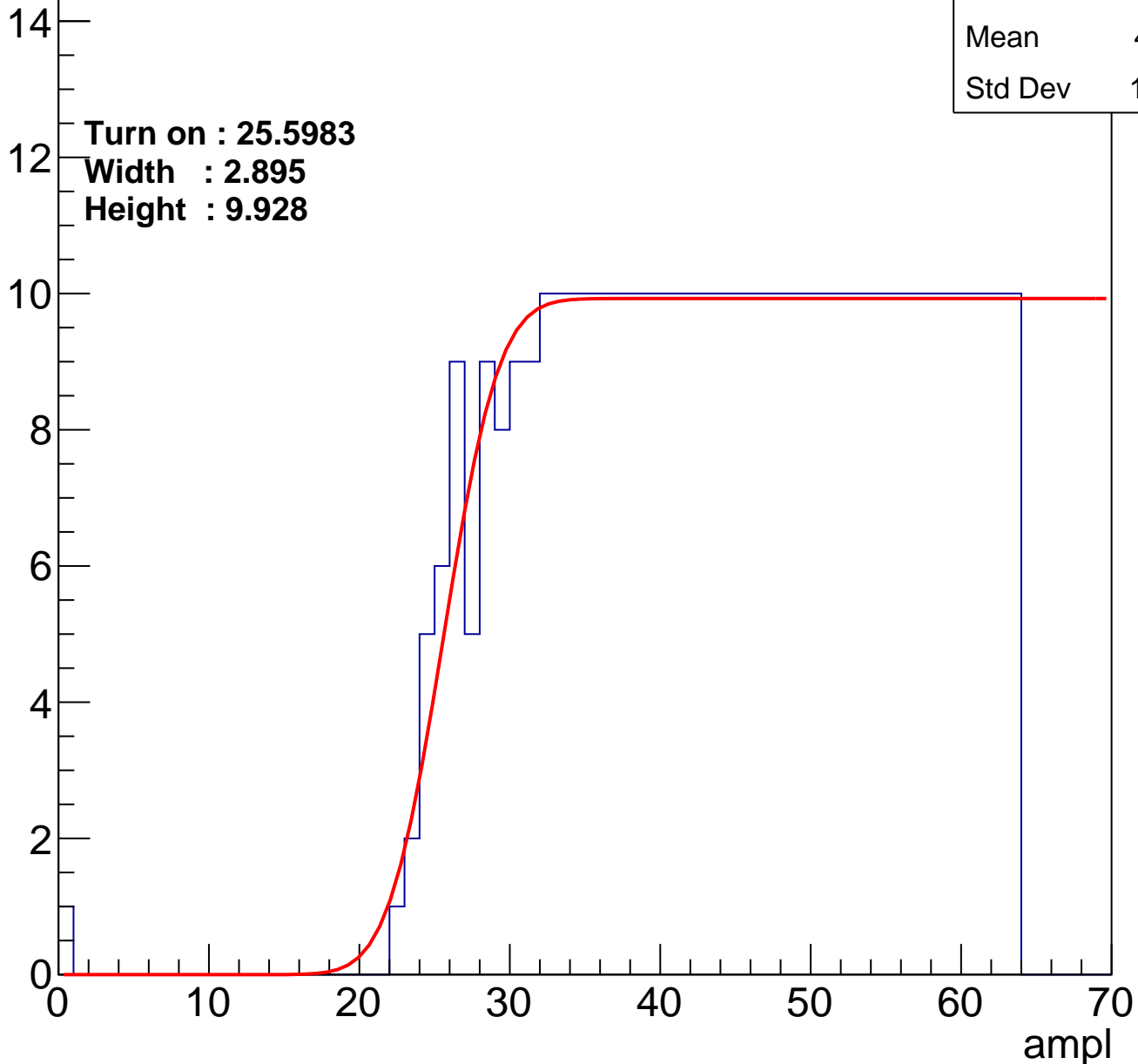
Entries	384
Mean	44.11
Std Dev	11.46

Turn on : 25.5983

Width : 2.895

Height : 9.928

Entry



B1L102S, U14-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.32
Std Dev	12.27

Turn on : 25.0056

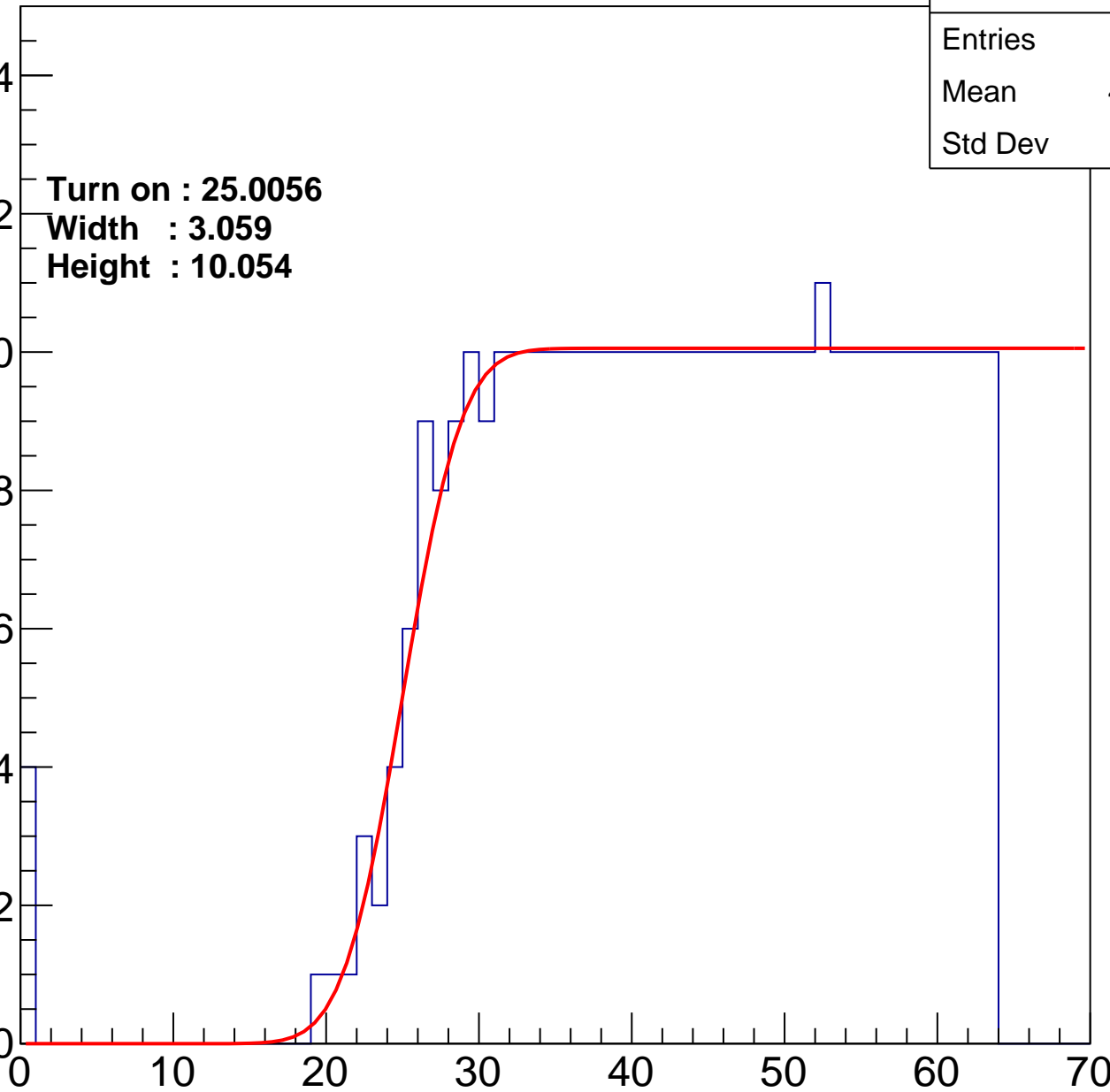
Width : 3.059

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.96
Std Dev	11.93

Turn on : 26.0064

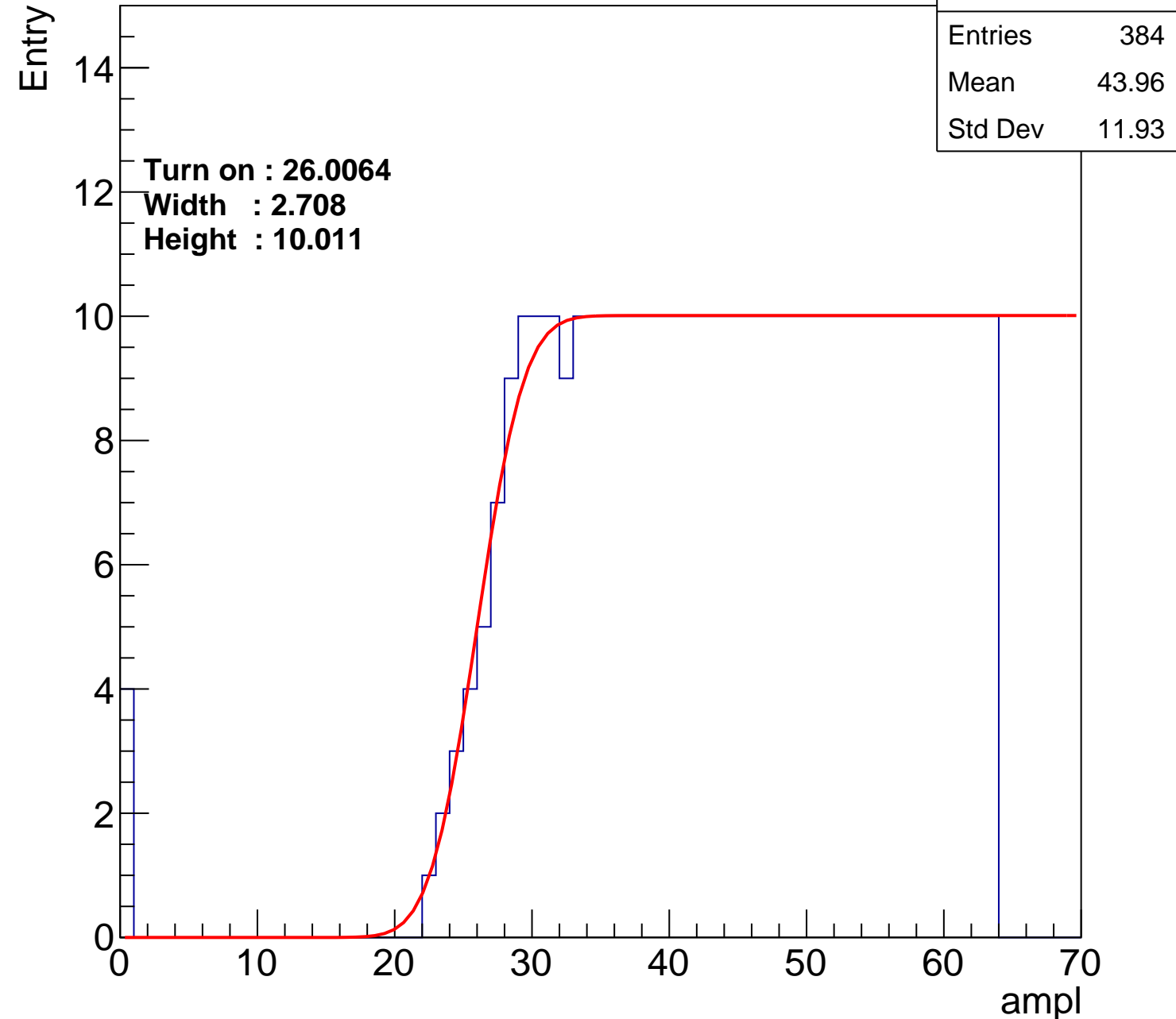
Width : 2.708

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch27

calib_packv5_042523_0143.root, FC#11, port A2

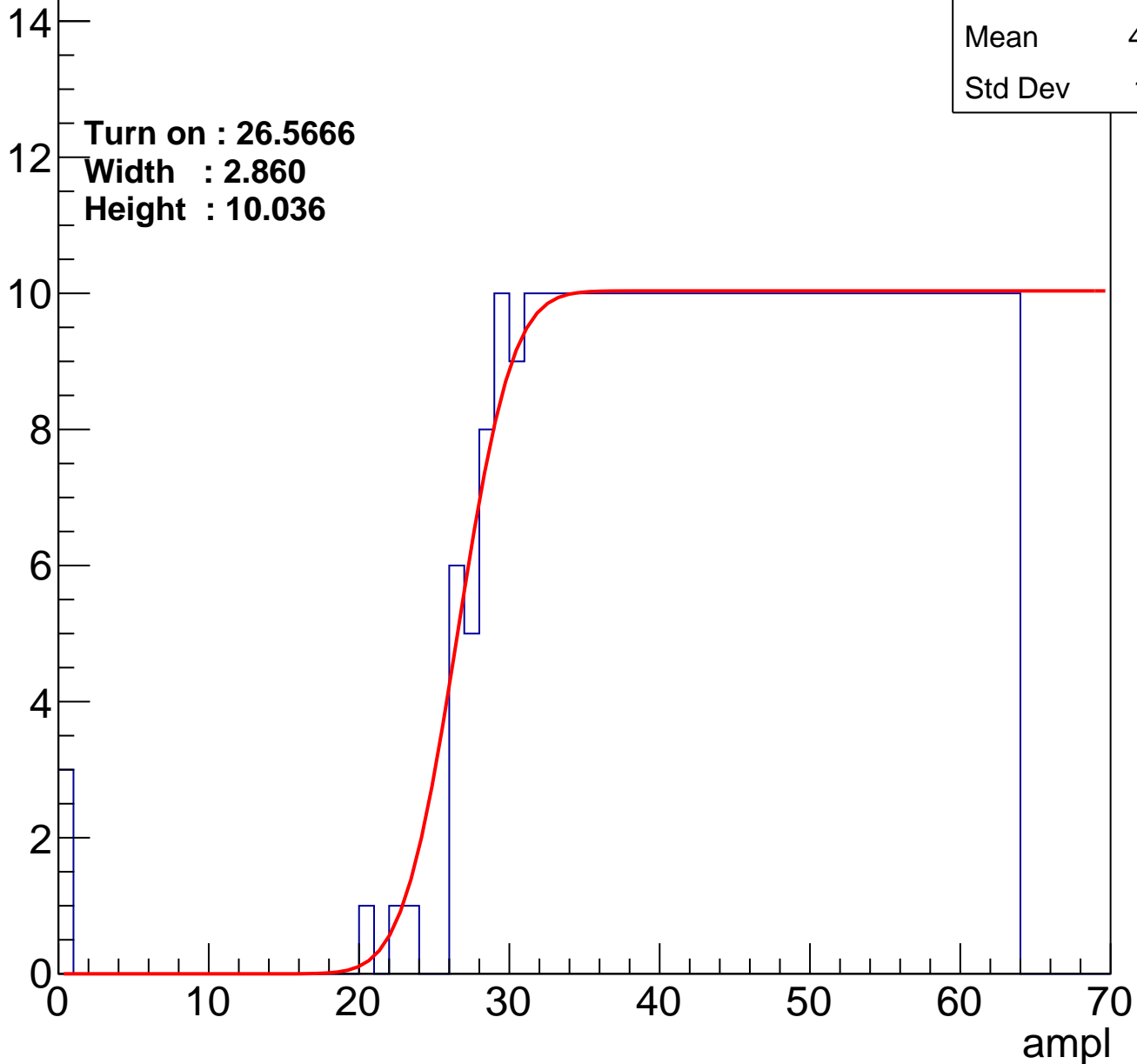
Entry

Entries	374
Mean	44.52
Std Dev	11.51

Turn on : 26.5666

Width : 2.860

Height : 10.036



B1L102S, U14-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.48
Std Dev	12.08

Turn on : 25.1782

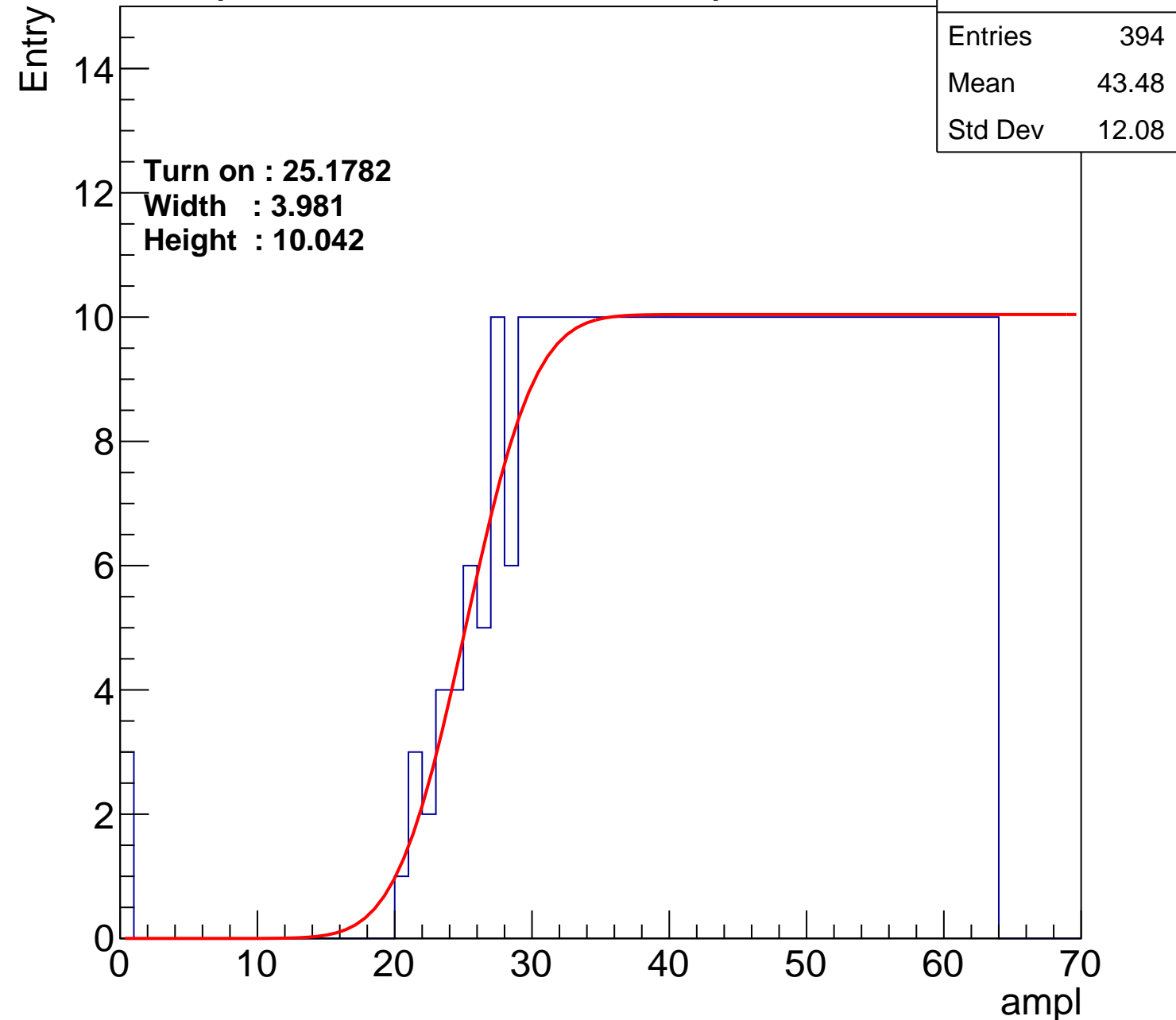
Width : 3.981

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch29

calib_packv5_042523_0143.root, FC#11, port A2

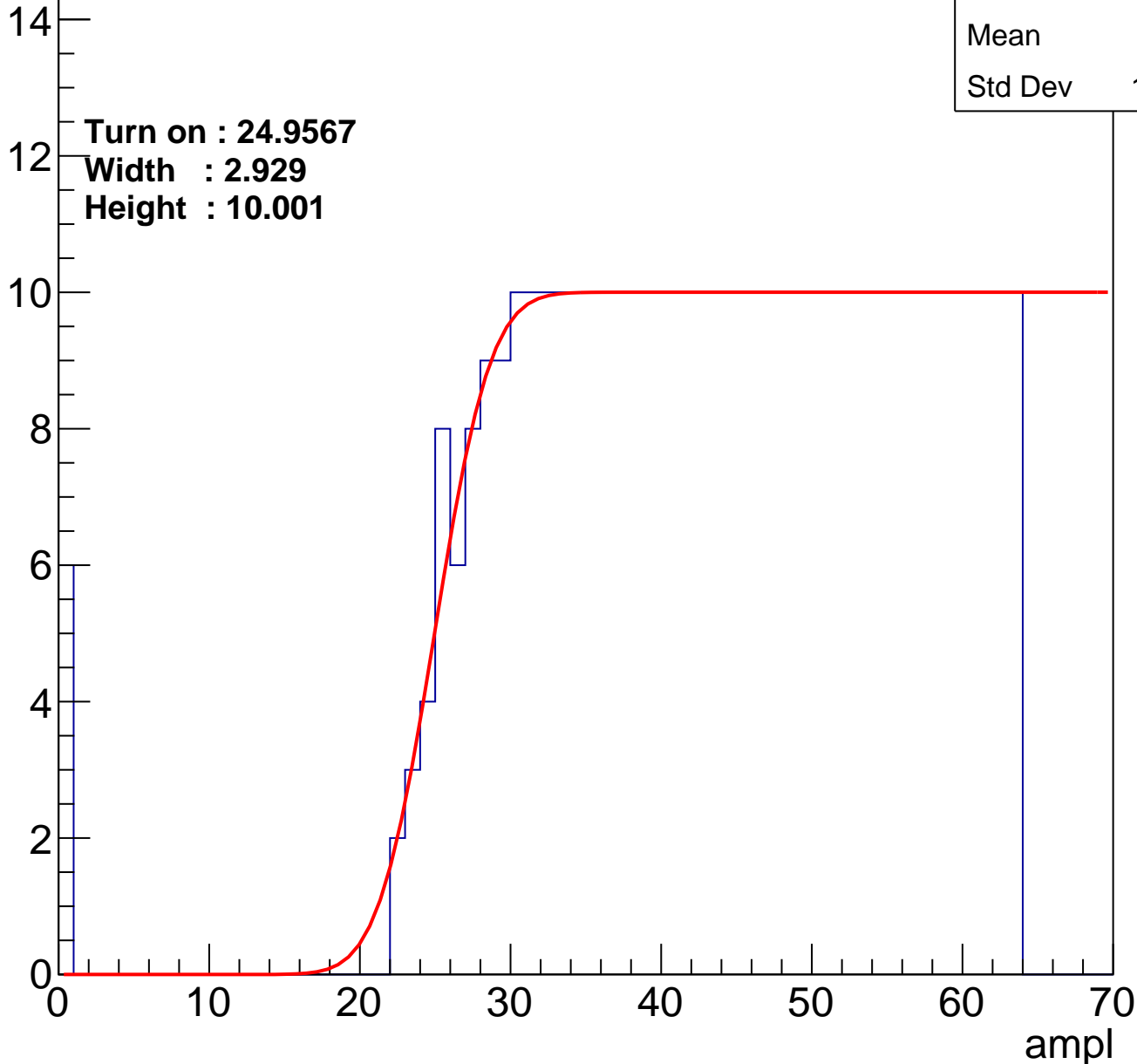
Entries	395
Mean	43.3
Std Dev	12.49

Turn on : 24.9567

Width : 2.929

Height : 10.001

Entry



B1L102S, U14-ch30

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.8
Std Dev	11.78

Turn on : 25.3794

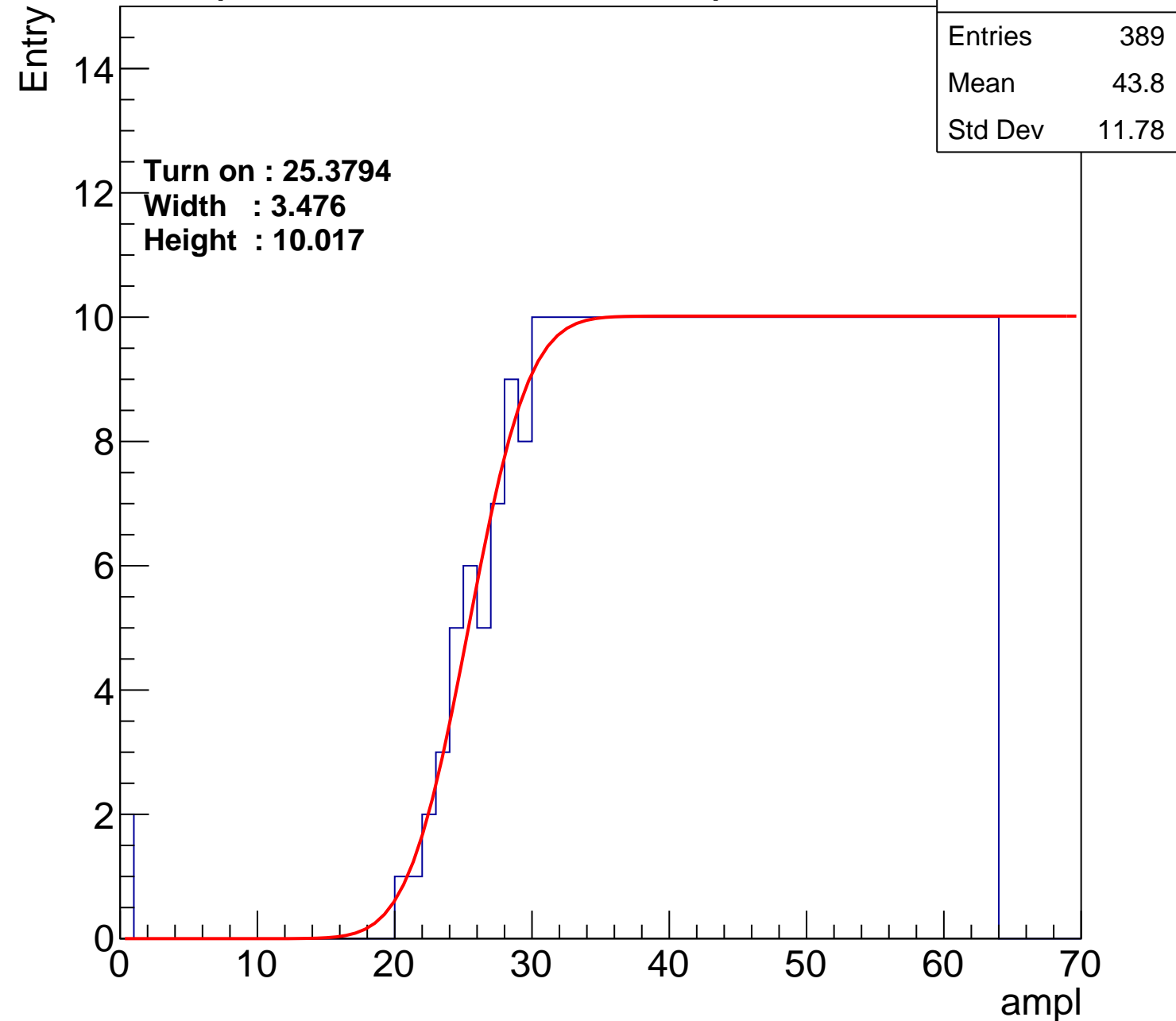
Width : 3.476

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch31

calib_packv5_042523_0143.root, FC#11, port A2

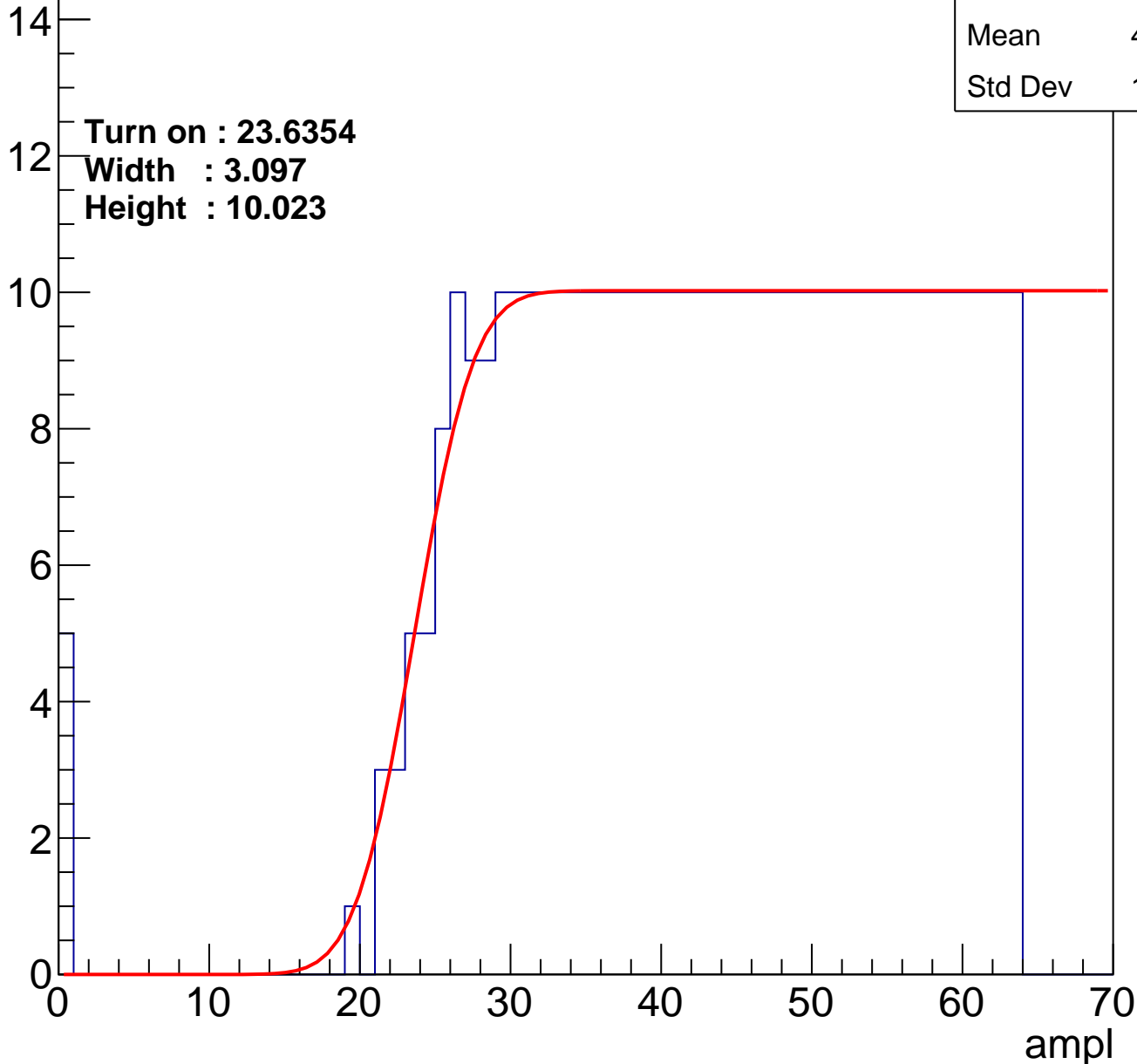
Entries	408
Mean	42.74
Std Dev	12.63

Turn on : 23.6354

Width : 3.097

Height : 10.023

Entry



B1L102S, U14-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	403
Mean	42.95
Std Dev	12.56

Turn on : 24.6134

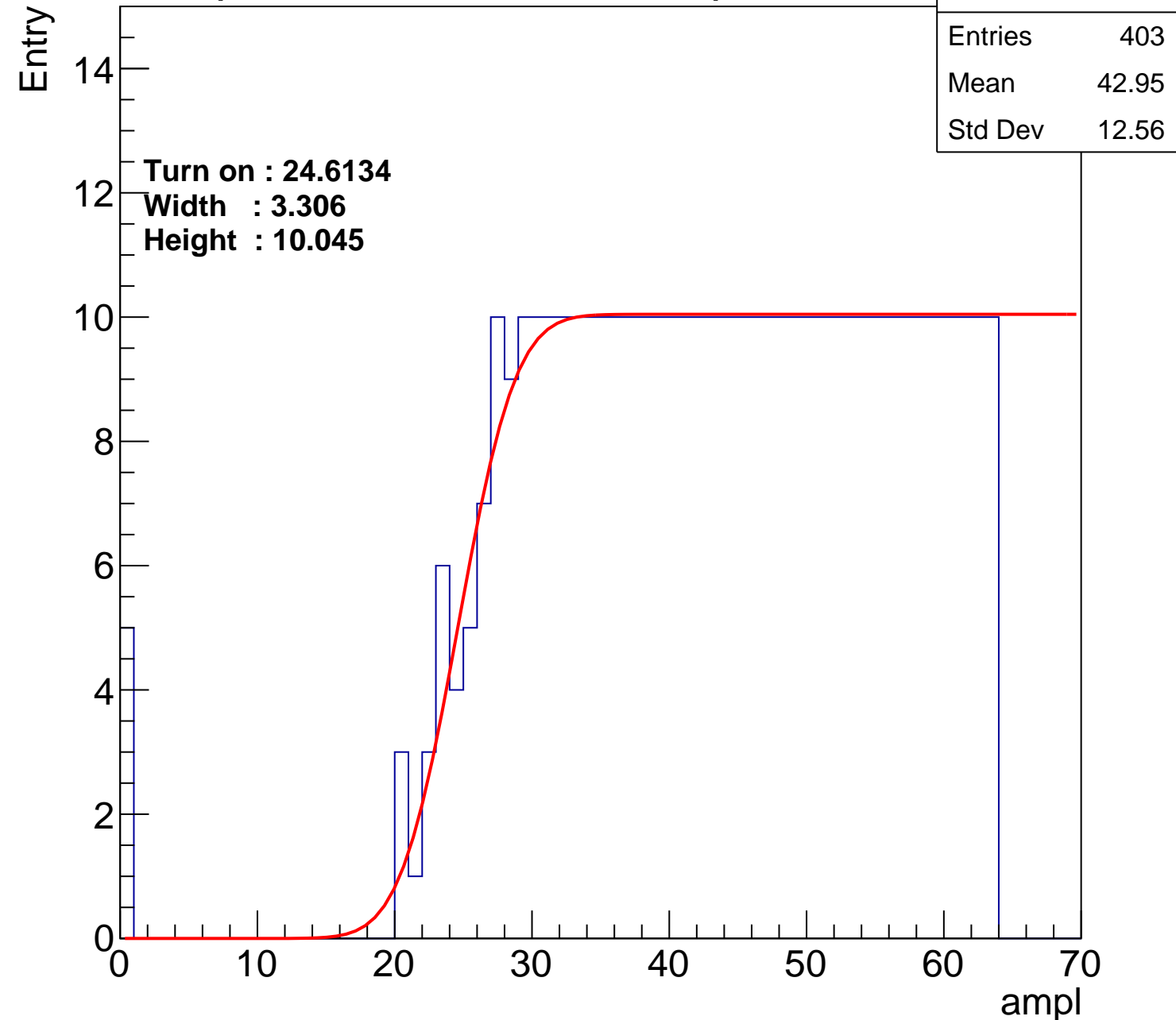
Width : 3.306

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.04
Std Dev	11.95

Turn on : 26.4687

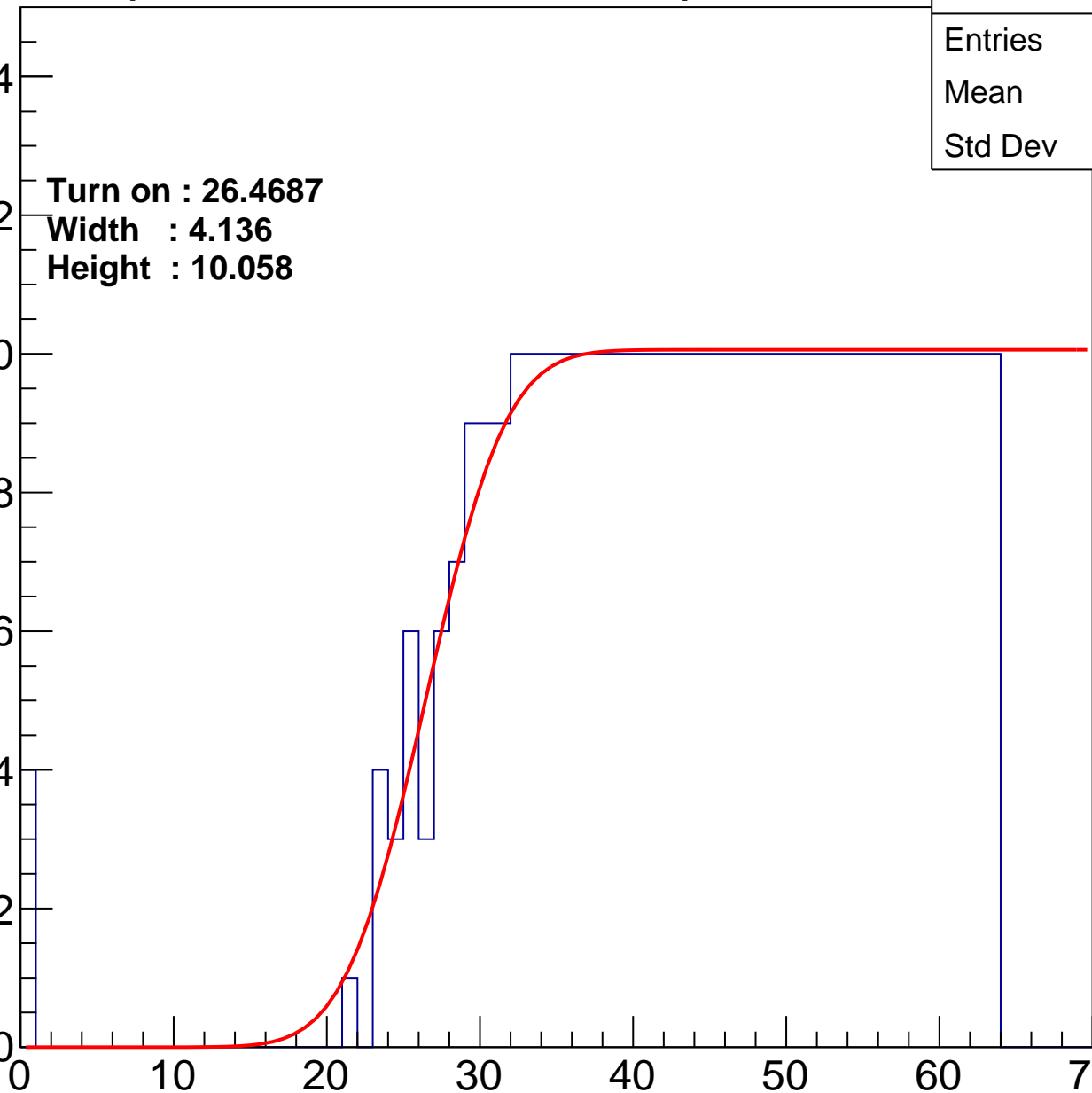
Width : 4.136

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch34

calib_packv5_042523_0143.root, FC#11, port A2

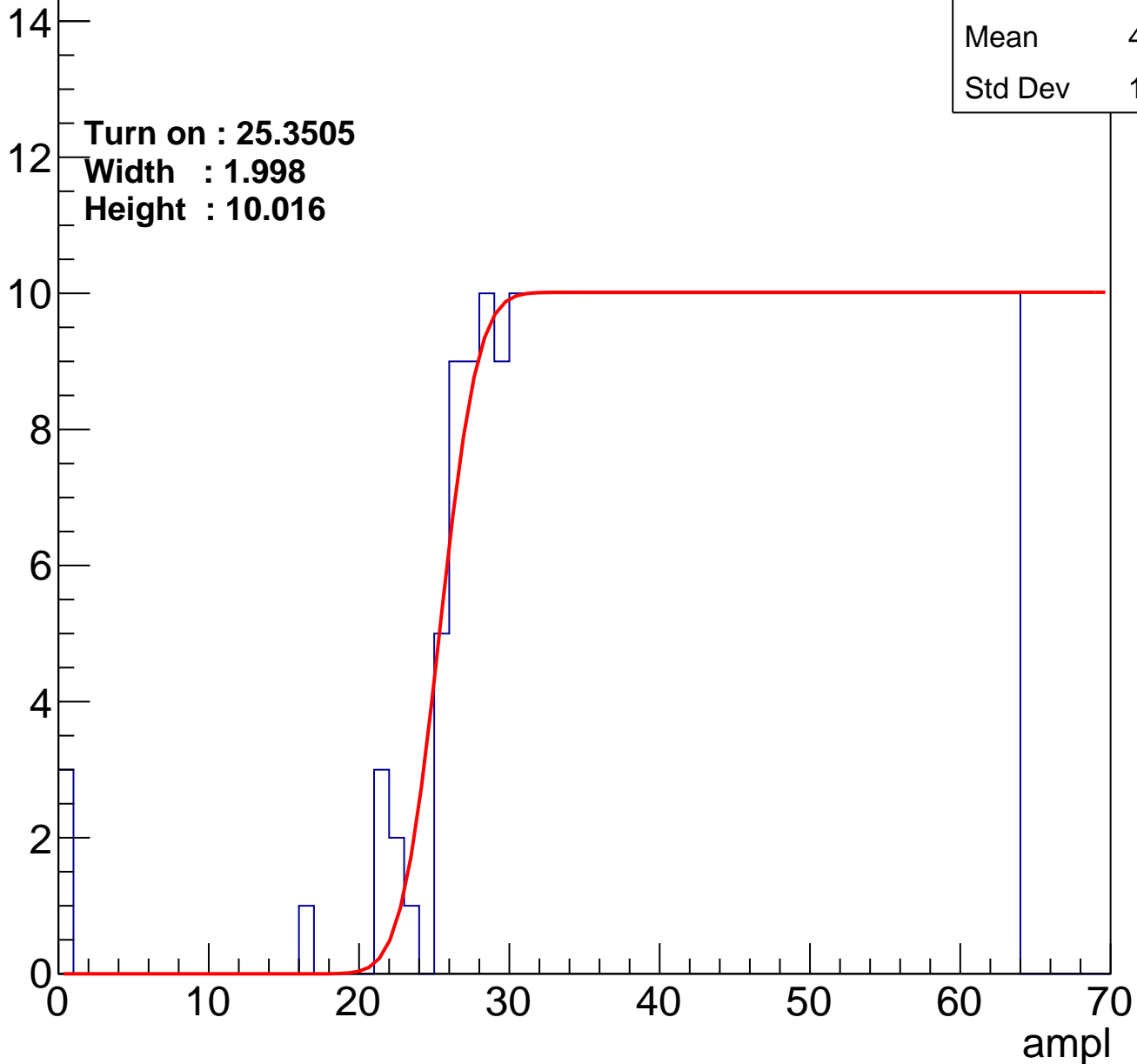
Entries	392
Mean	43.62
Std Dev	11.98

Turn on : 25.3505

Width : 1.998

Height : 10.016

Entry



B1L102S, U14-ch35

calib_packv5_042523_0143.root, FC#11, port A2

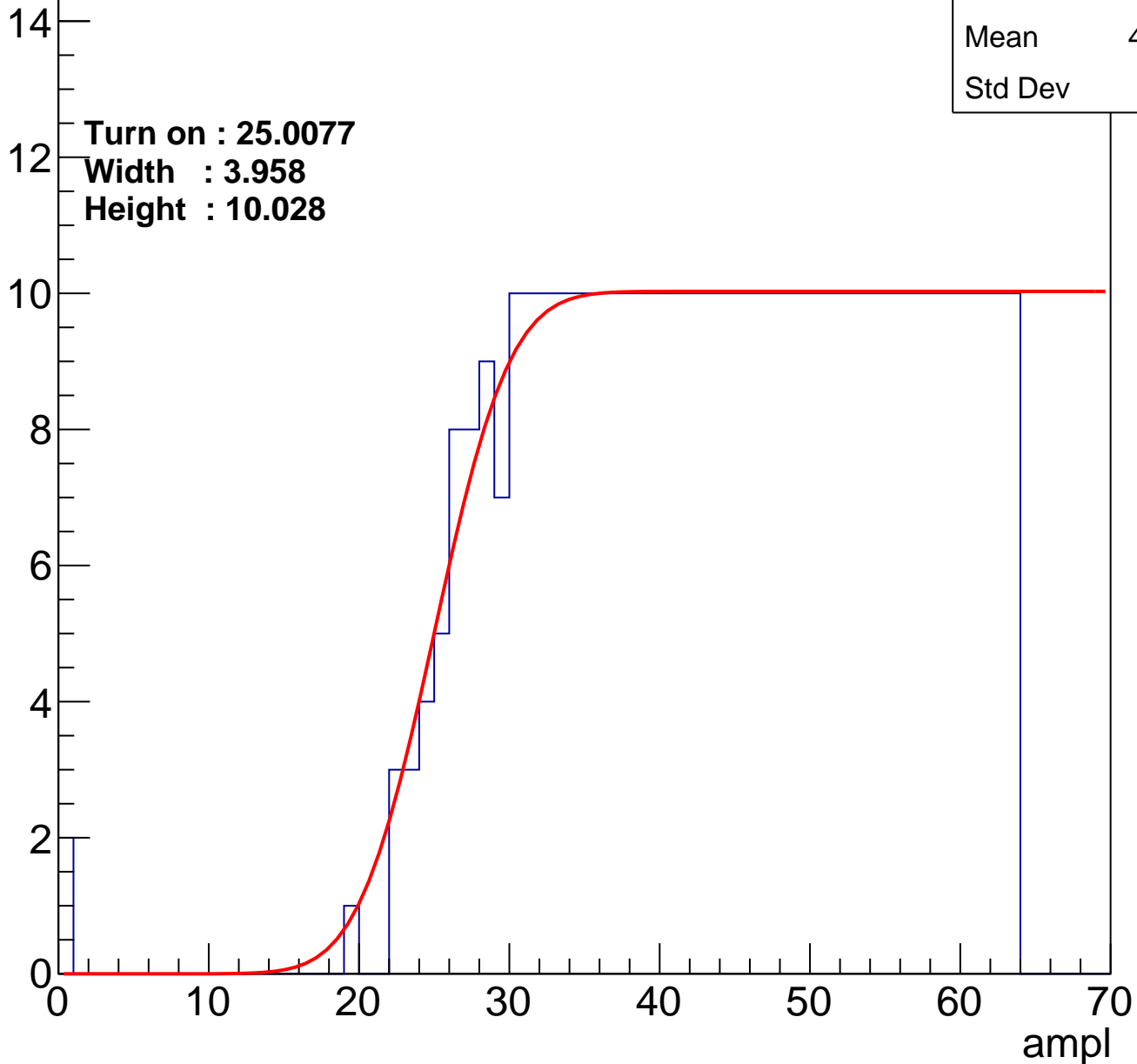
Entries	390
Mean	43.75
Std Dev	11.8

Turn on : 25.0077

Width : 3.958

Height : 10.028

Entry



B1L102S, U14-ch36

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.46
Std Dev	12.18

Turn on : 25.1375

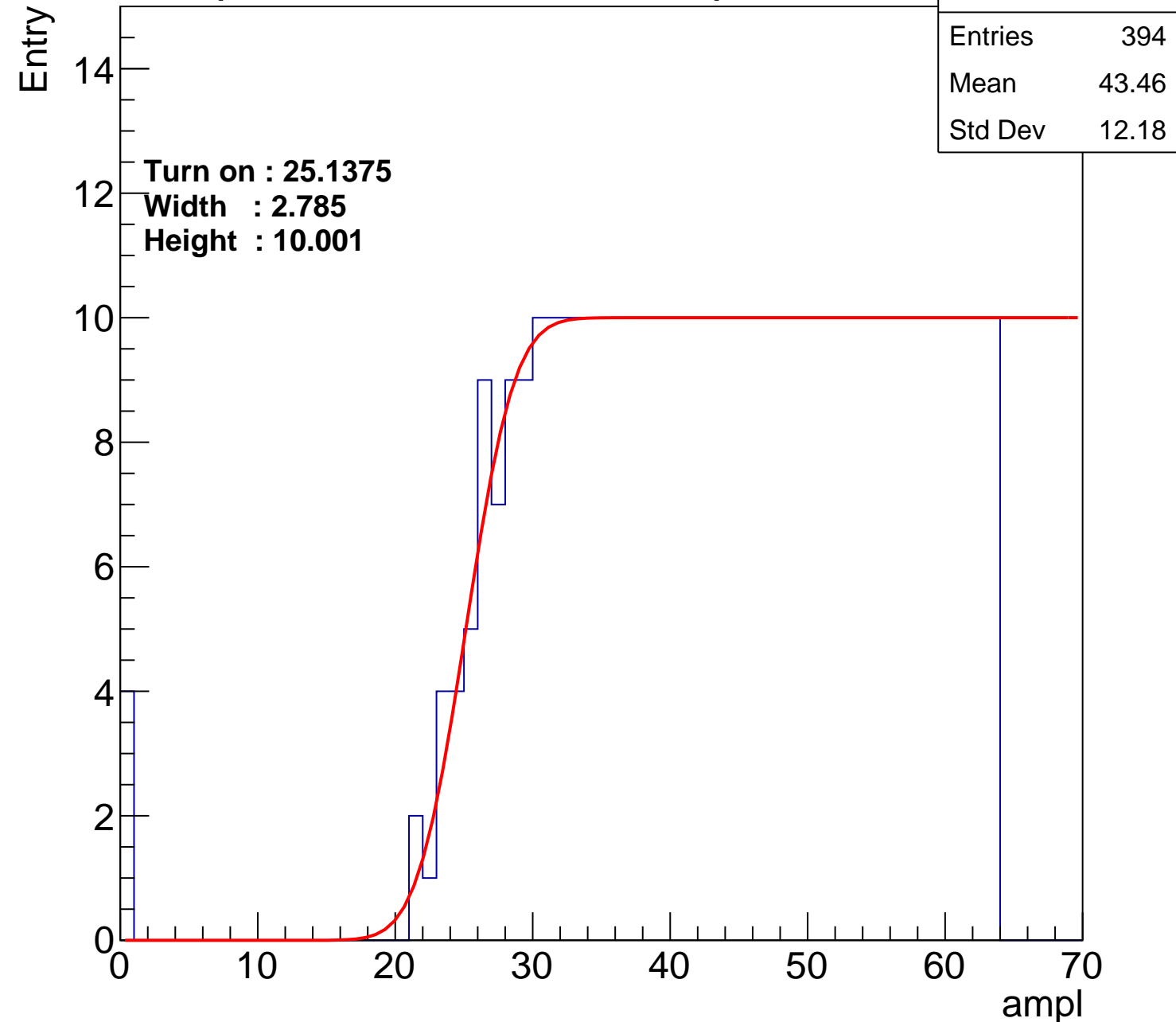
Width : 2.785

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.1155

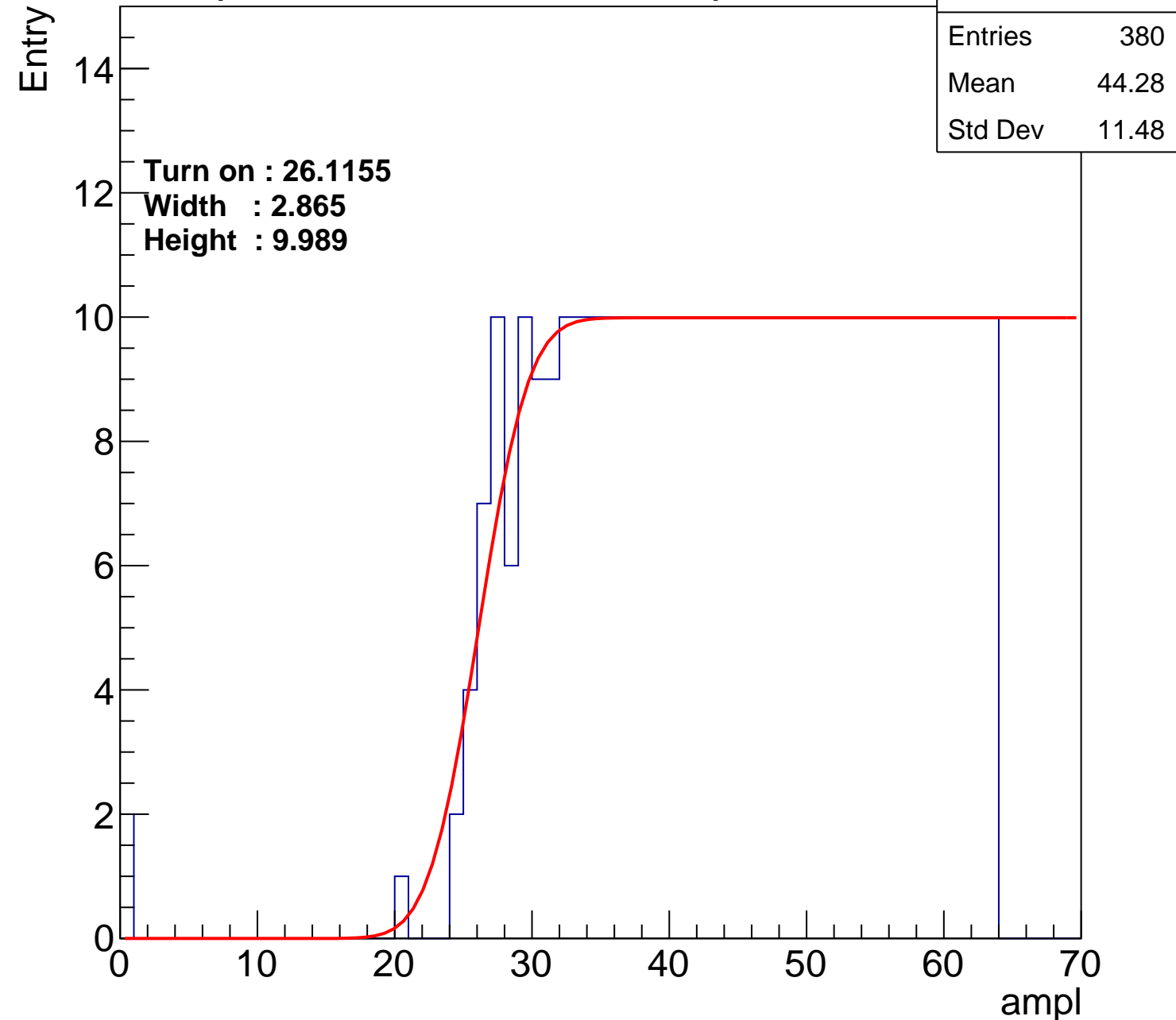
Width : 2.865

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.88
Std Dev	11.65

Turn on : 25.9013

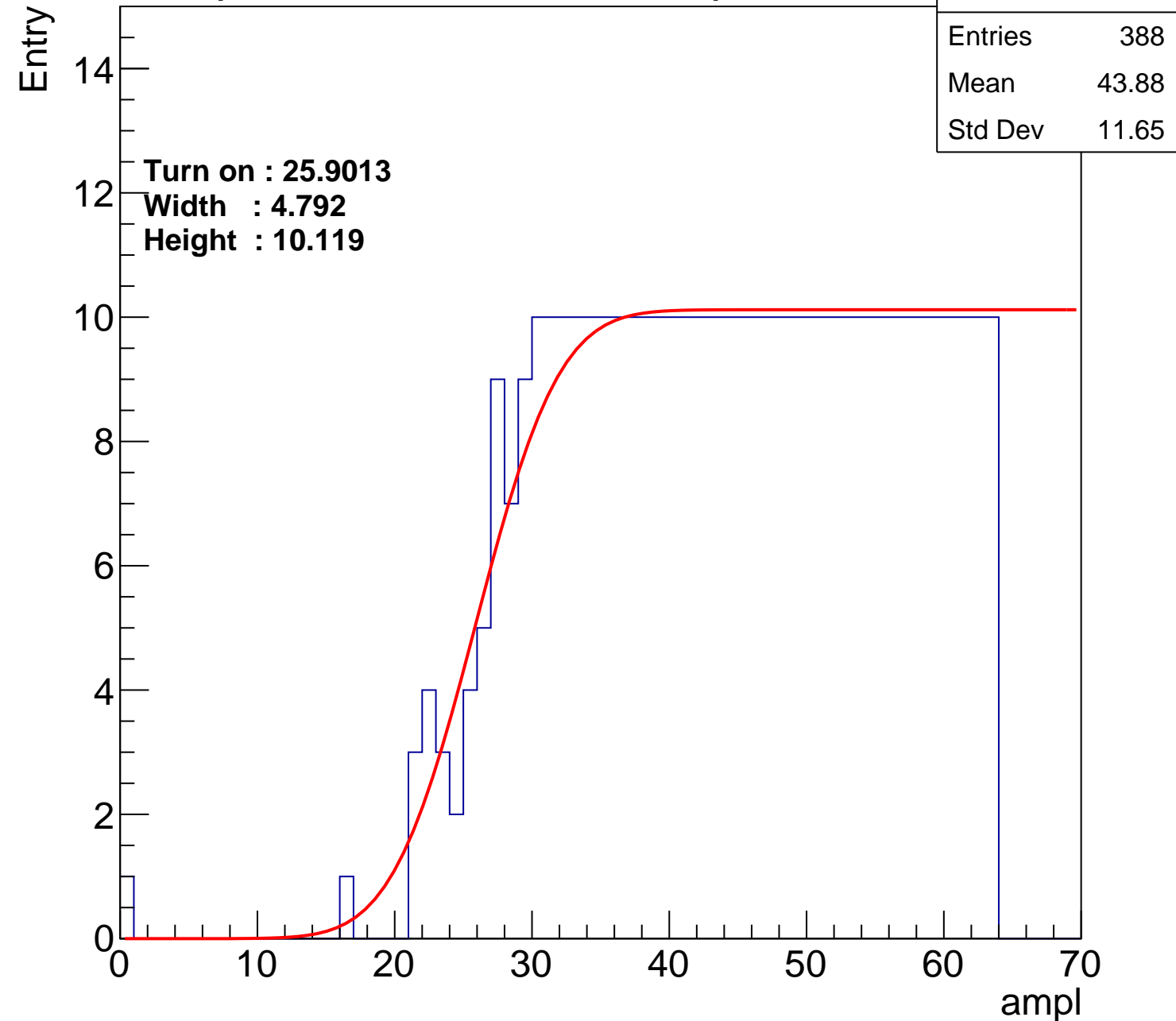
Width : 4.792

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch39

calib_packv5_042523_0143.root, FC#11, port A2

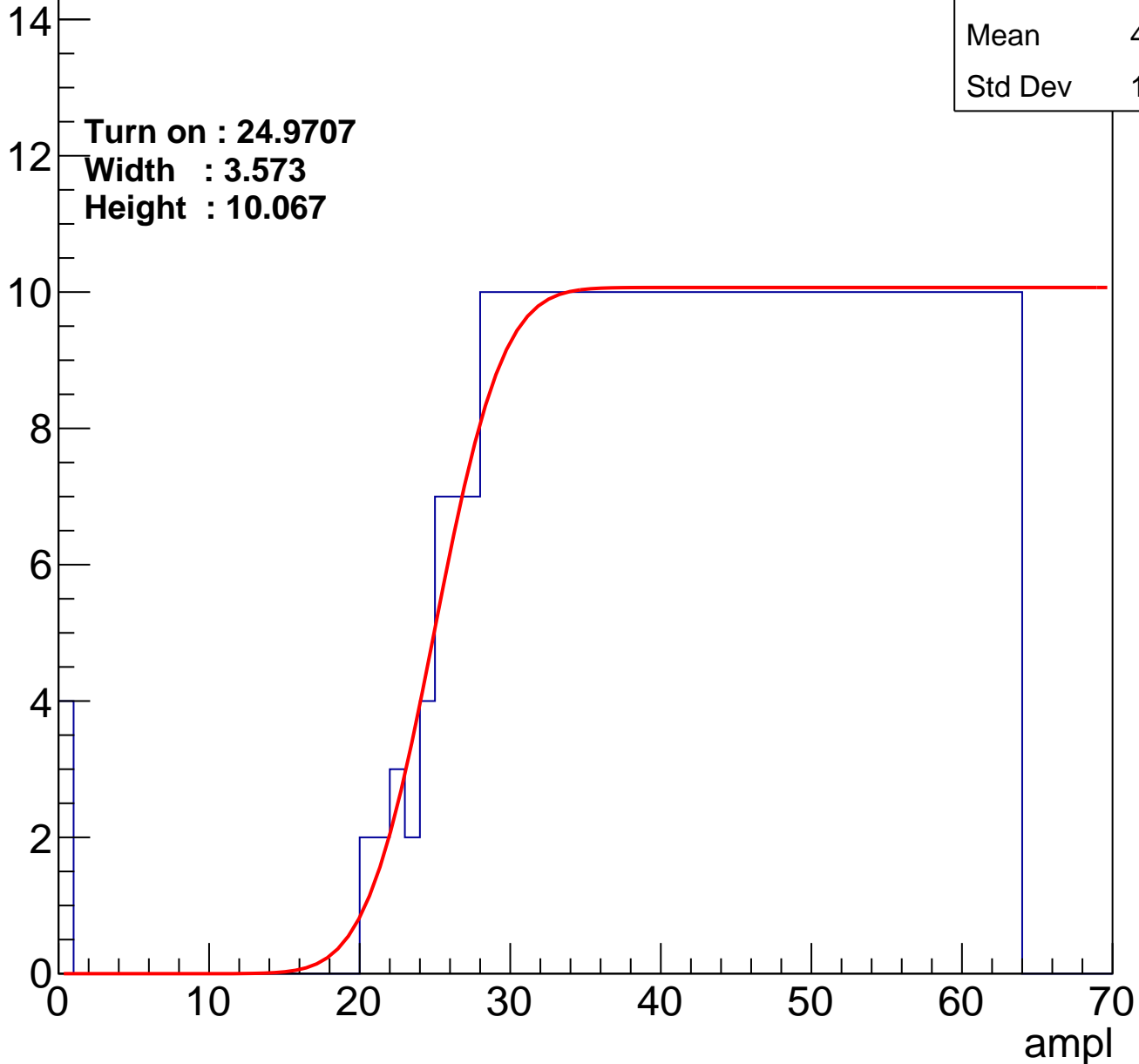
Entries	398
Mean	43.26
Std Dev	12.29

Turn on : 24.9707

Width : 3.573

Height : 10.067

Entry



B1L102S, U14-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.59
Std Dev	11.9

Turn on : 25.4293

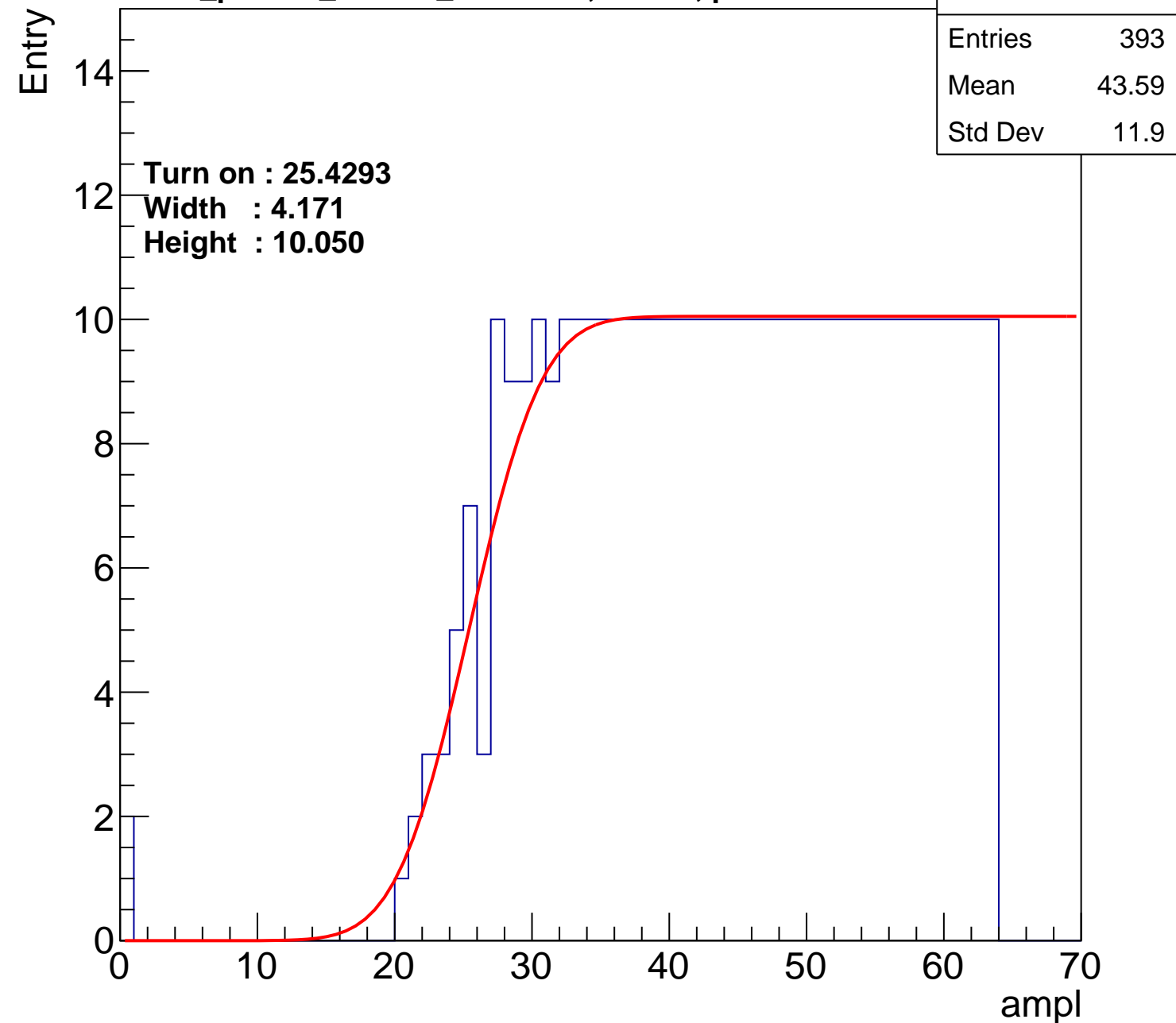
Width : 4.171

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.7
Std Dev	11.17

Turn on : 27.3386

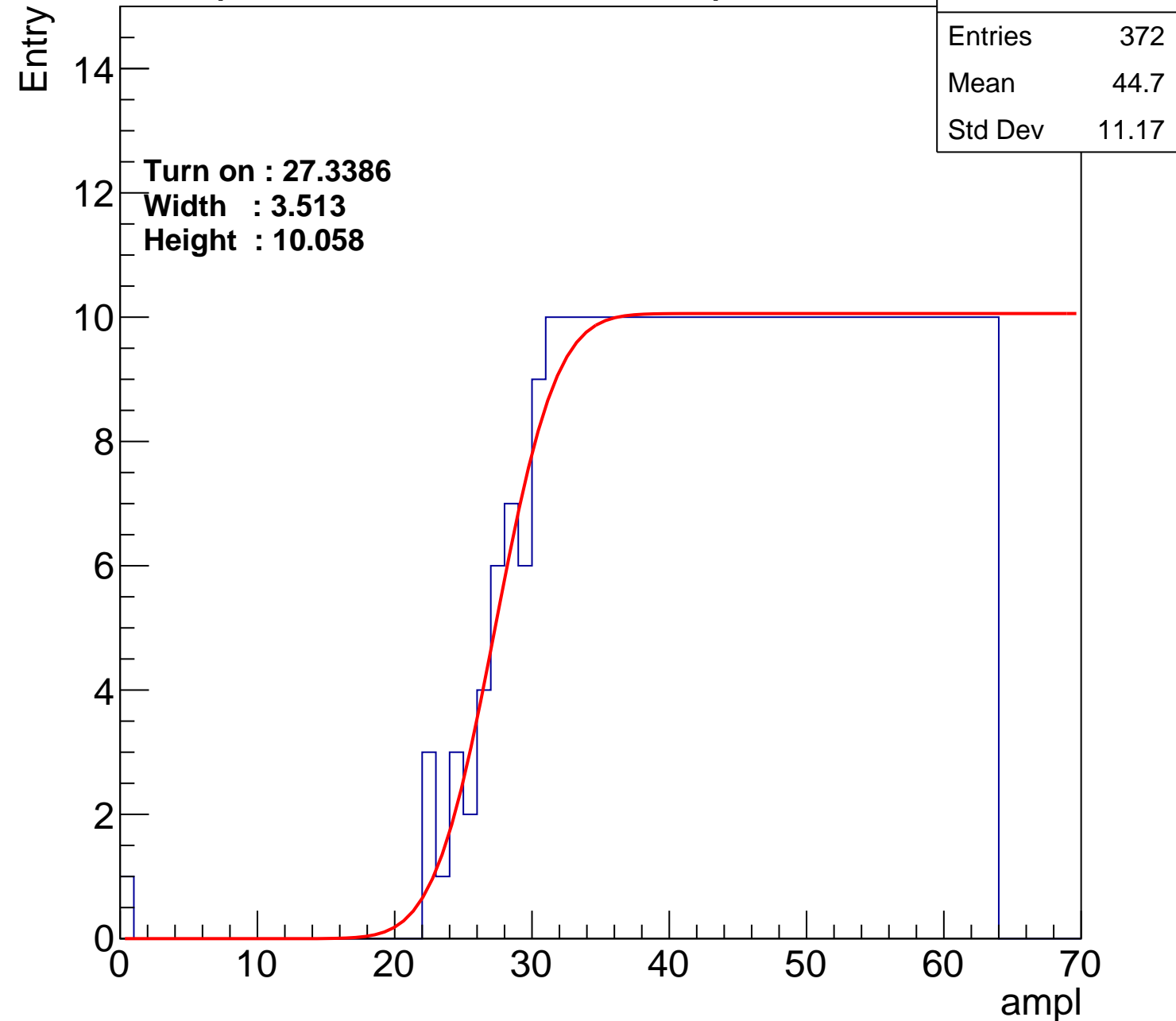
Width : 3.513

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch42

calib_packv5_042523_0143.root, FC#11, port A2

Entries	414
Mean	42.49
Std Dev	12.67

Turn on : 22.8147

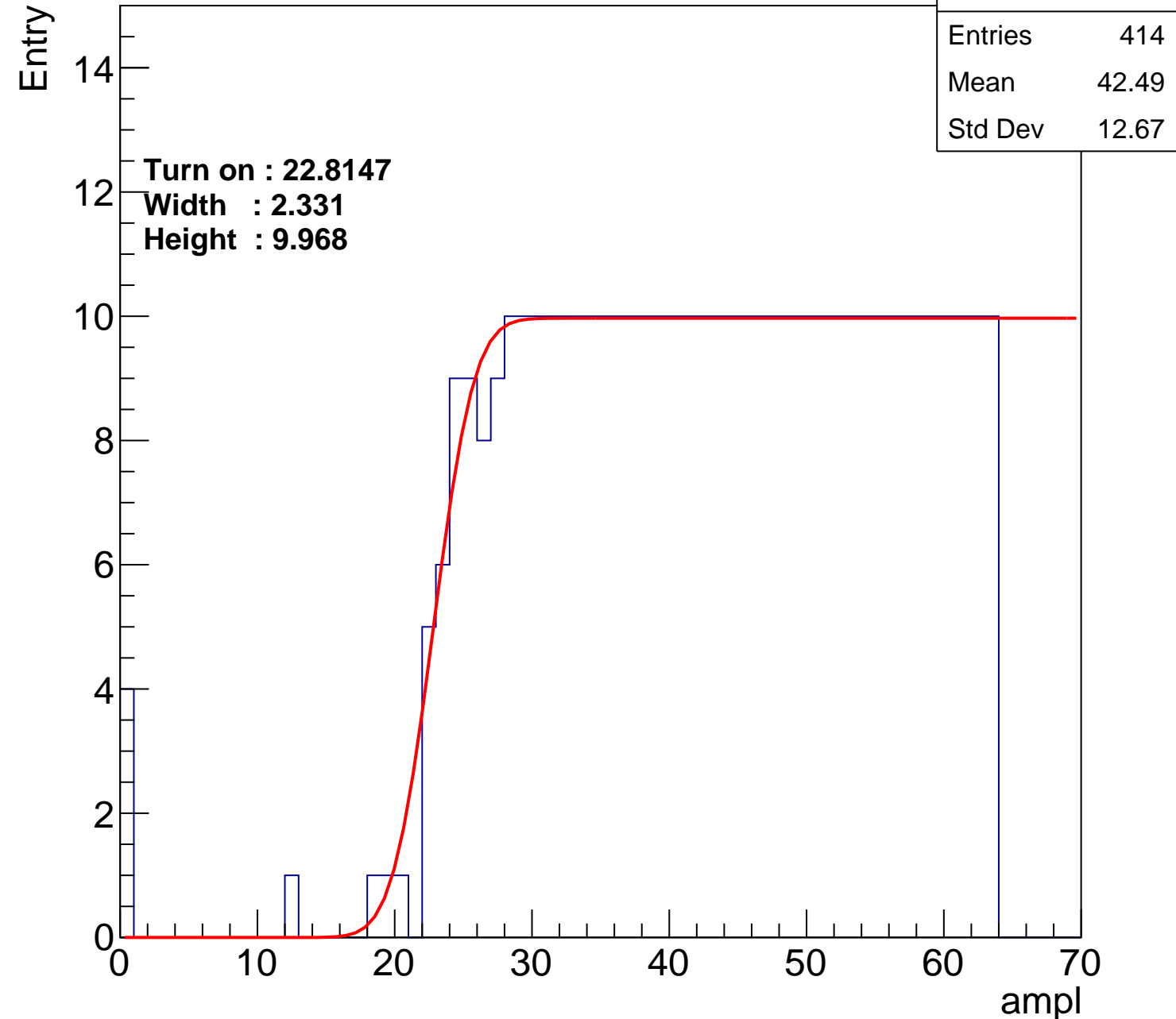
Width : 2.331

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch43

calib_packv5_042523_0143.root, FC#11, port A2

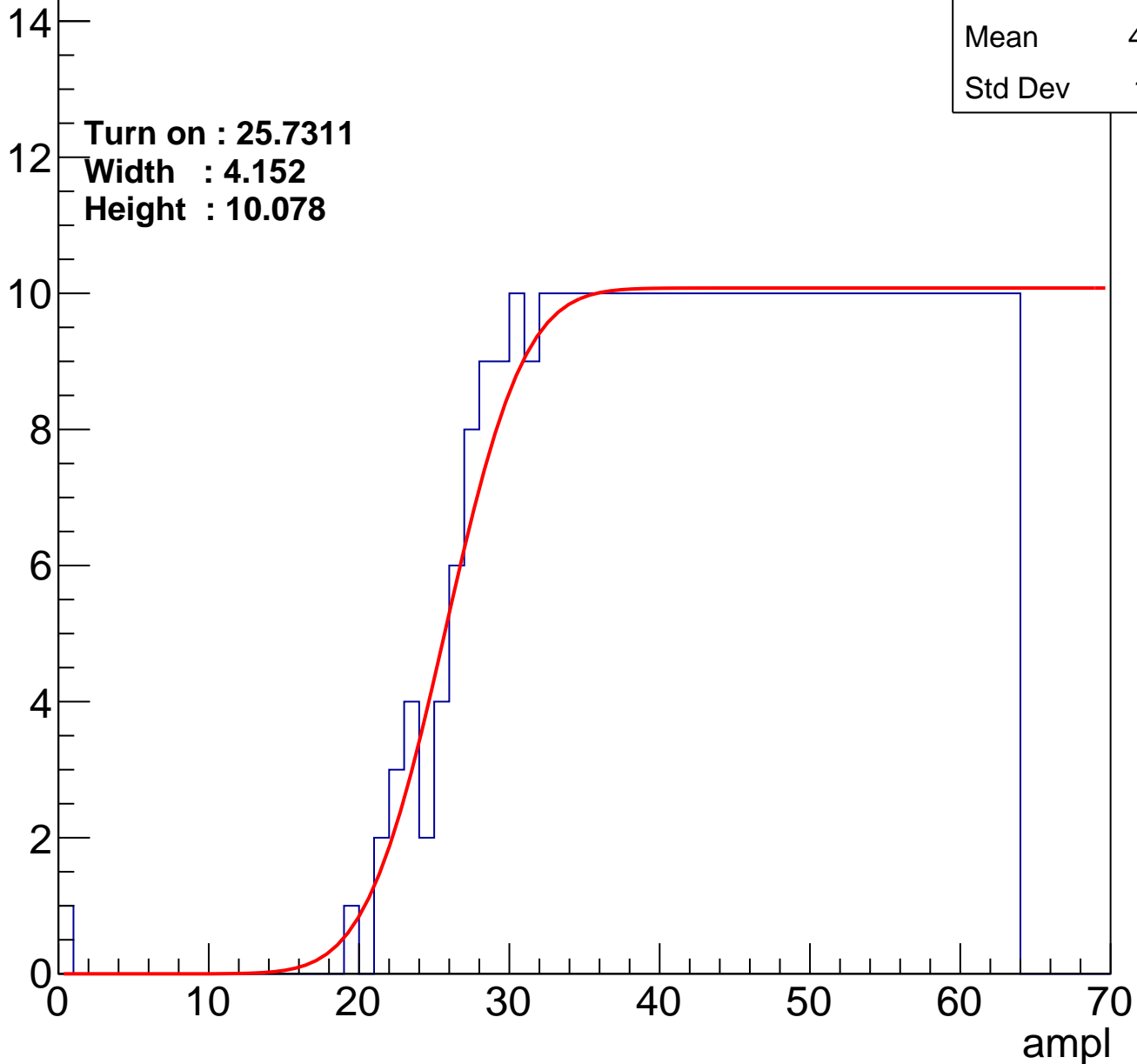
Entries	388
Mean	43.89
Std Dev	11.61

Turn on : 25.7311

Width : 4.152

Height : 10.078

Entry



B1L102S, U14-ch44

calib_packv5_042523_0143.root, FC#11, port A2

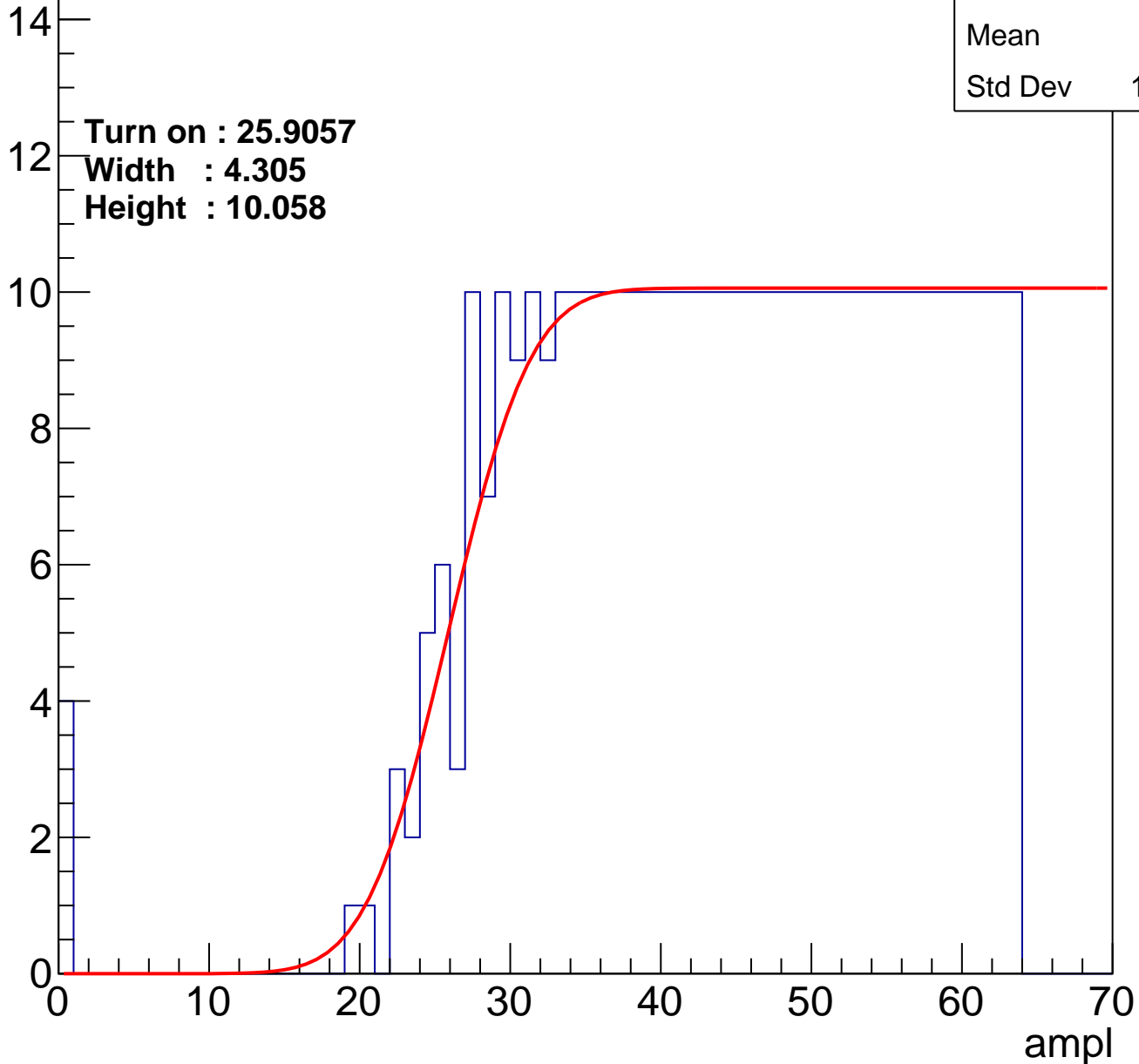
Entries	390
Mean	43.6
Std Dev	12.17

Turn on : 25.9057

Width : 4.305

Height : 10.058

Entry



B1L102S, U14-ch45

calib_packv5_042523_0143.root, FC#11, port A2

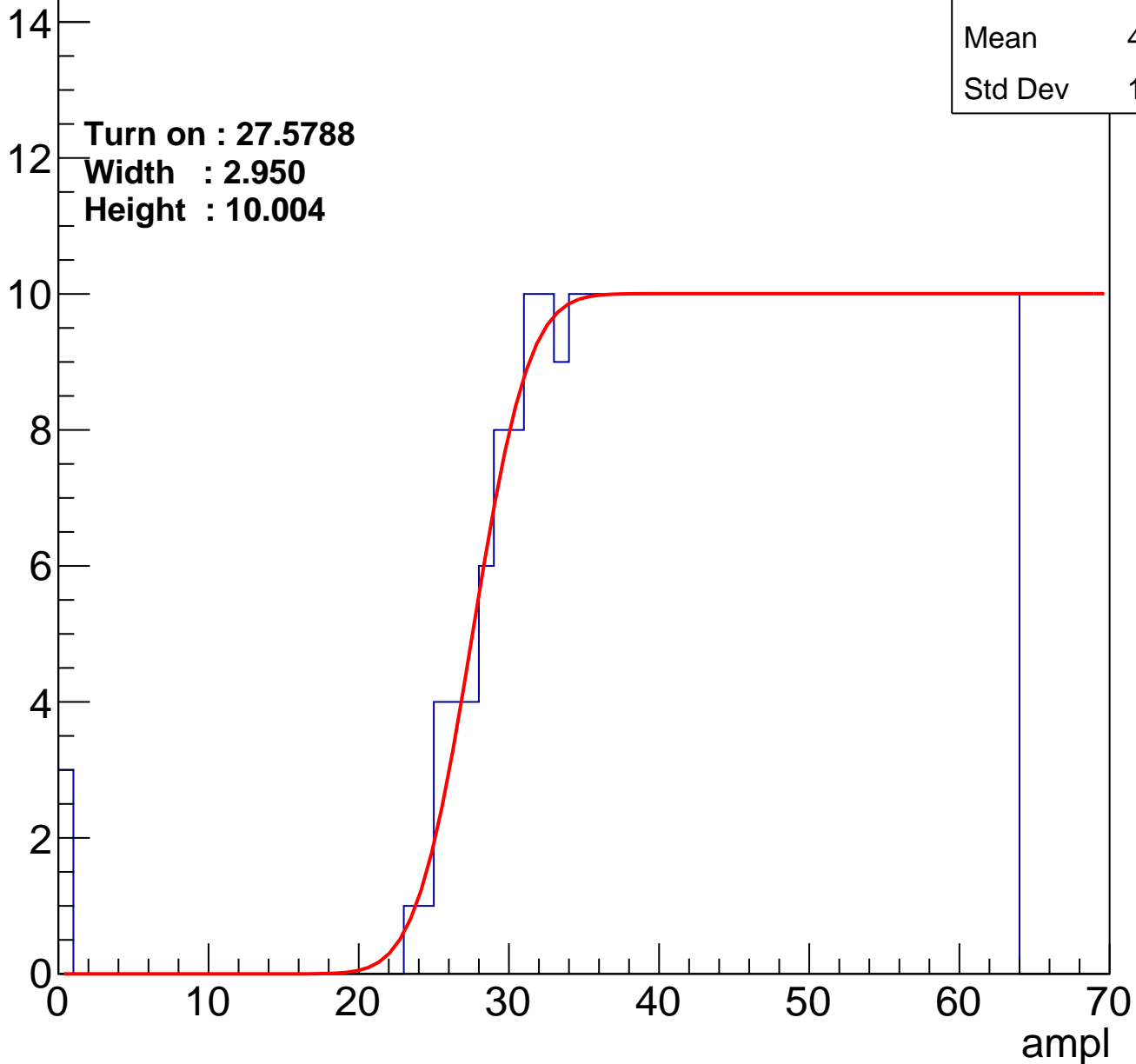
Entries	368
Mean	44.77
Std Dev	11.42

Turn on : 27.5788

Width : 2.950

Height : 10.004

Entry



B1L102S, U14-ch46

calib_packv5_042523_0143.root, FC#11, port A2

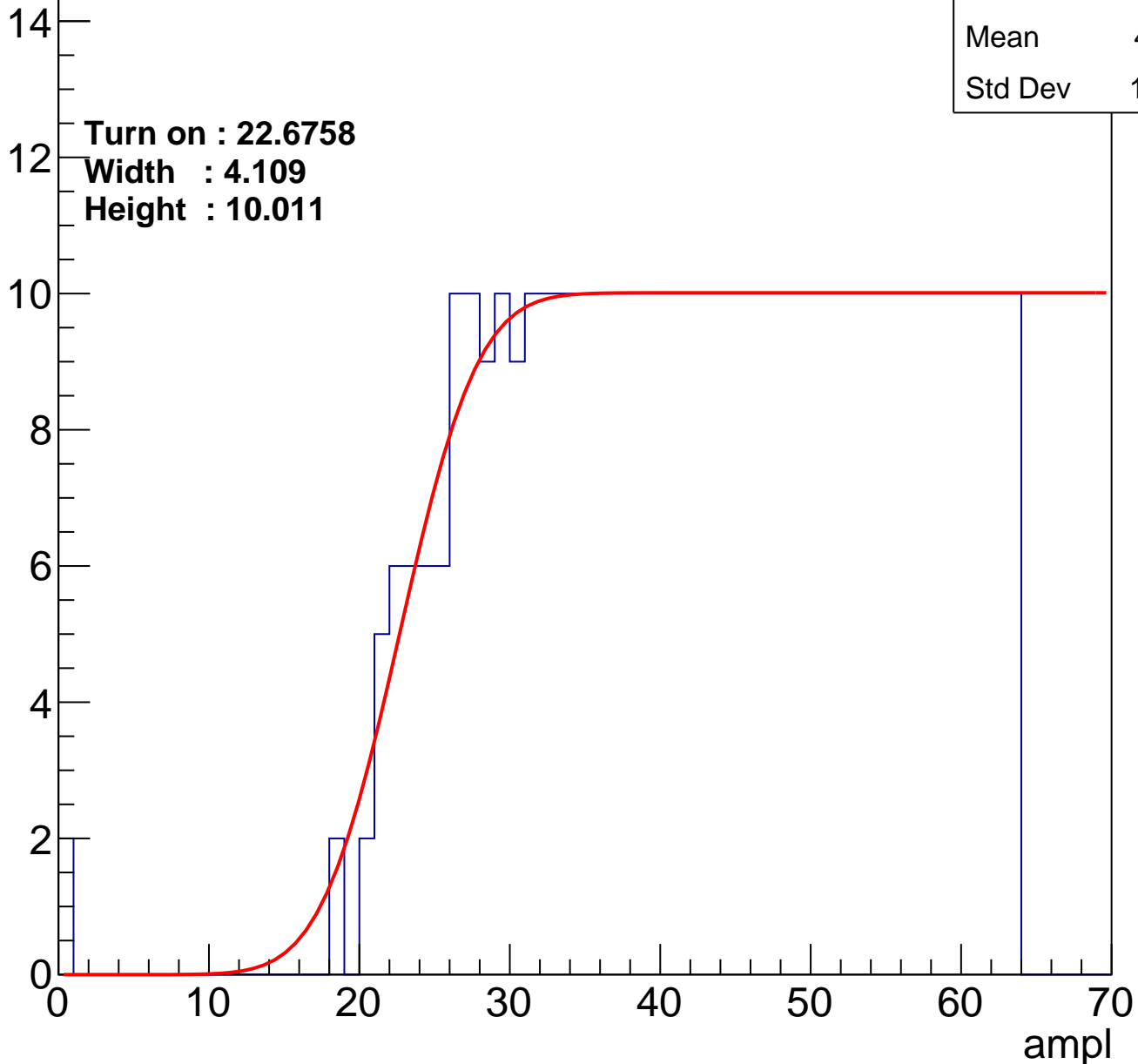
Entries	413
Mean	42.61
Std Dev	12.42

Turn on : 22.6758

Width : 4.109

Height : 10.011

Entry



B1L102S, U14-ch47

calib_packv5_042523_0143.root, FC#11, port A2

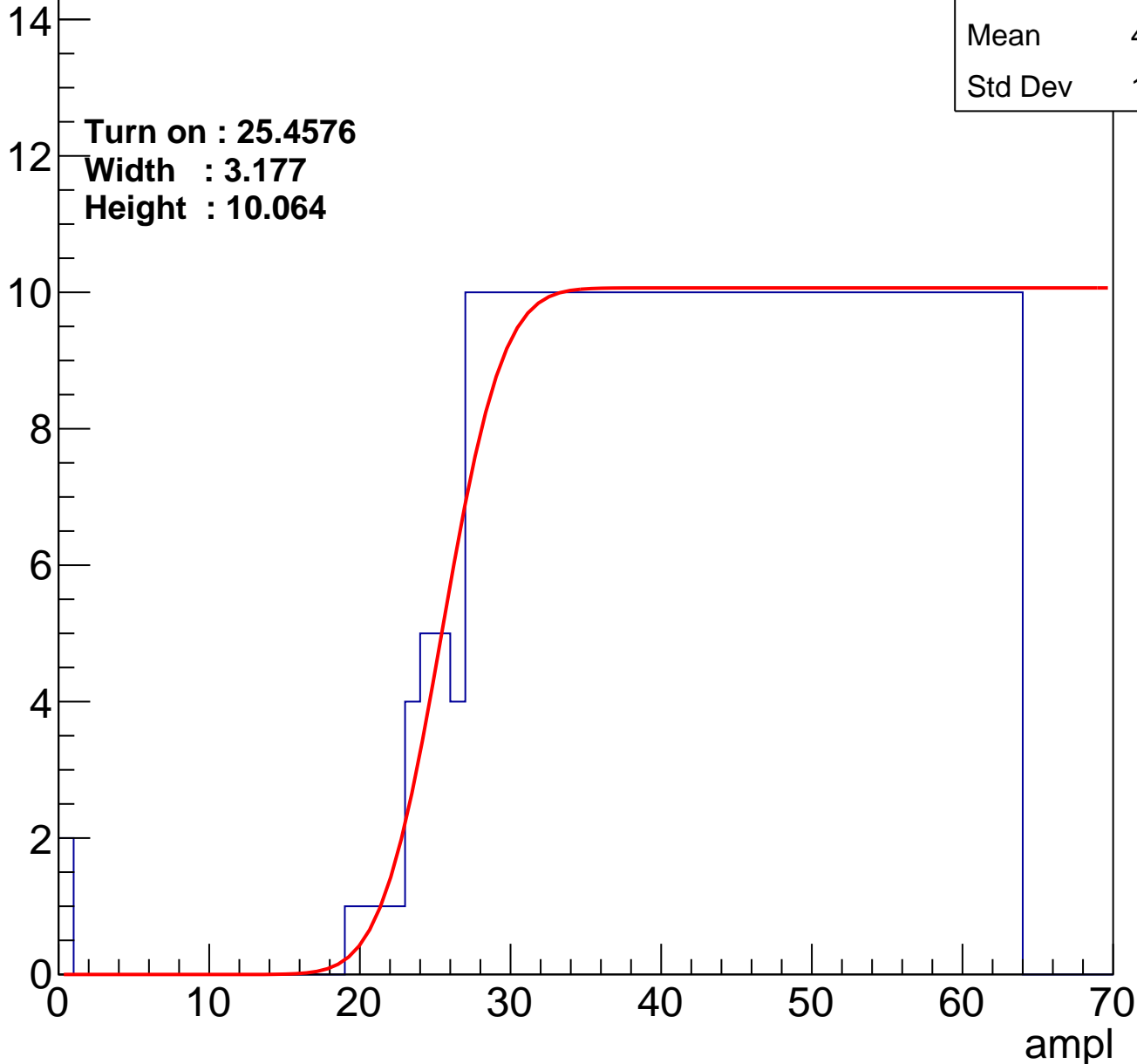
Entries	394
Mean	43.59
Std Dev	11.86

Turn on : 25.4576

Width : 3.177

Height : 10.064

Entry



B1L102S, U14-ch48

calib_packv5_042523_0143.root, FC#11, port A2

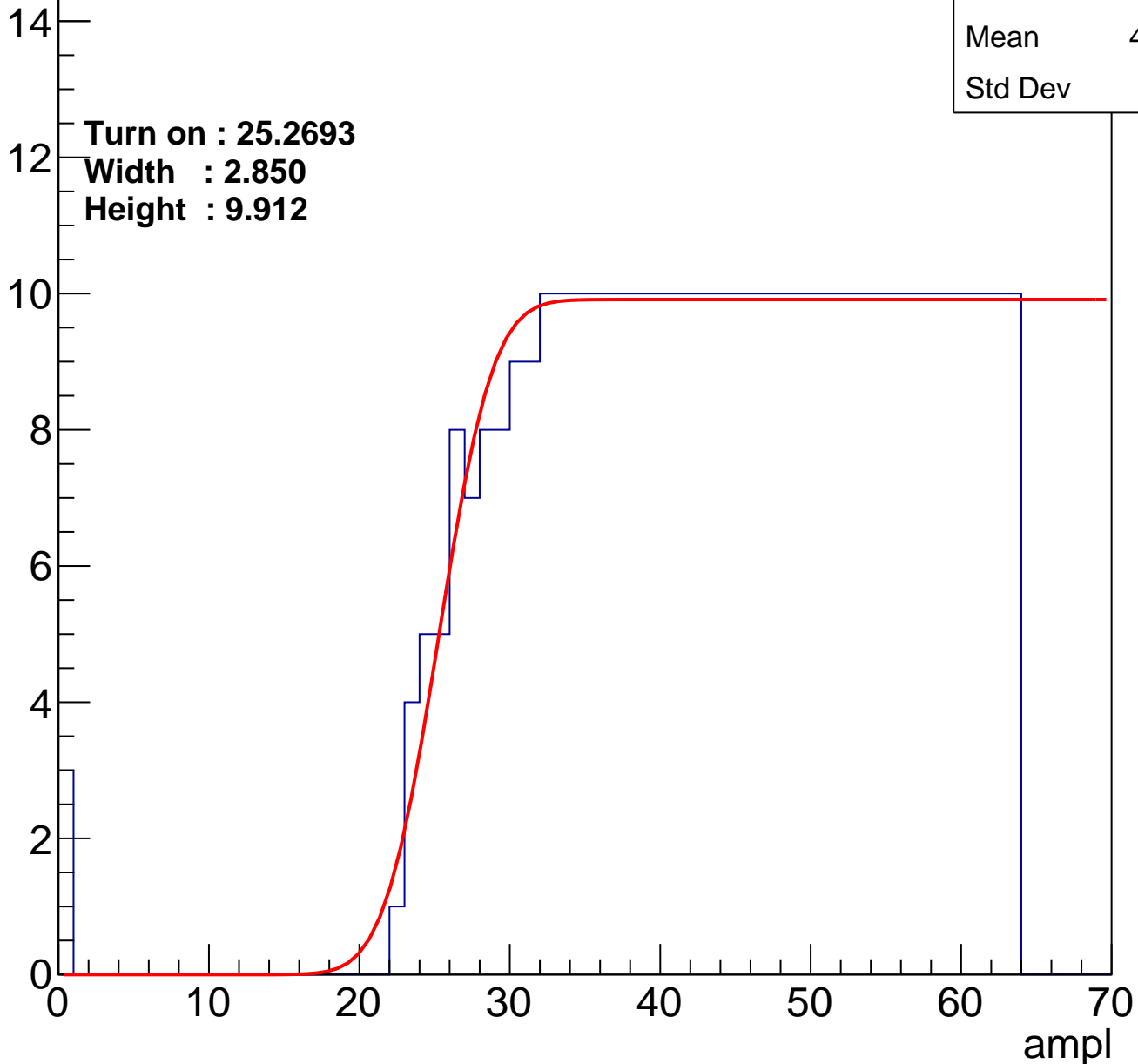
Entries	387
Mean	43.83
Std Dev	11.9

Turn on : 25.2693

Width : 2.850

Height : 9.912

Entry



B1L102S, U14-ch49

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.49
Std Dev	11.5

Turn on : 27.0331

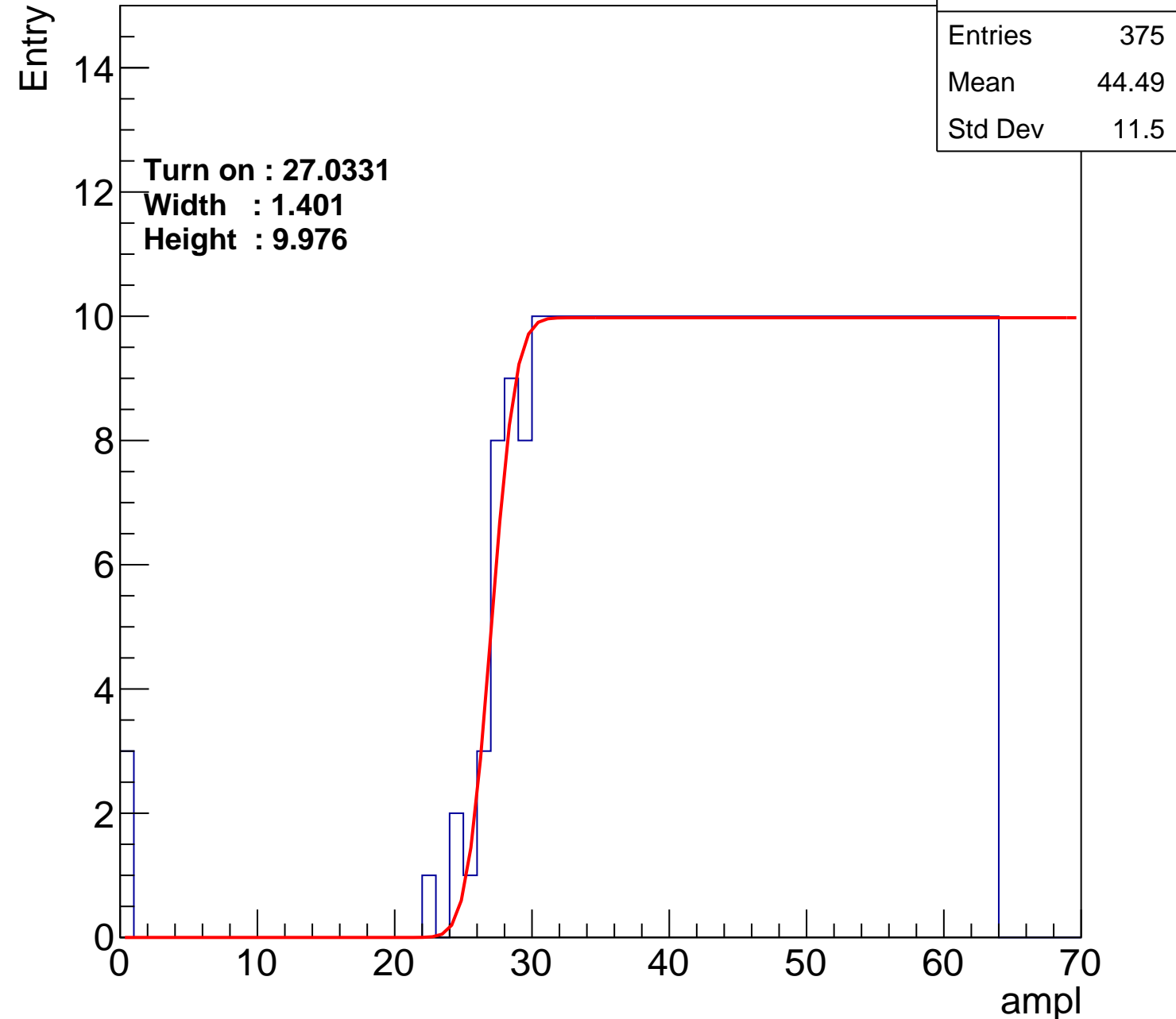
Width : 1.401

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch50

calib_packv5_042523_0143.root, FC#11, port A2

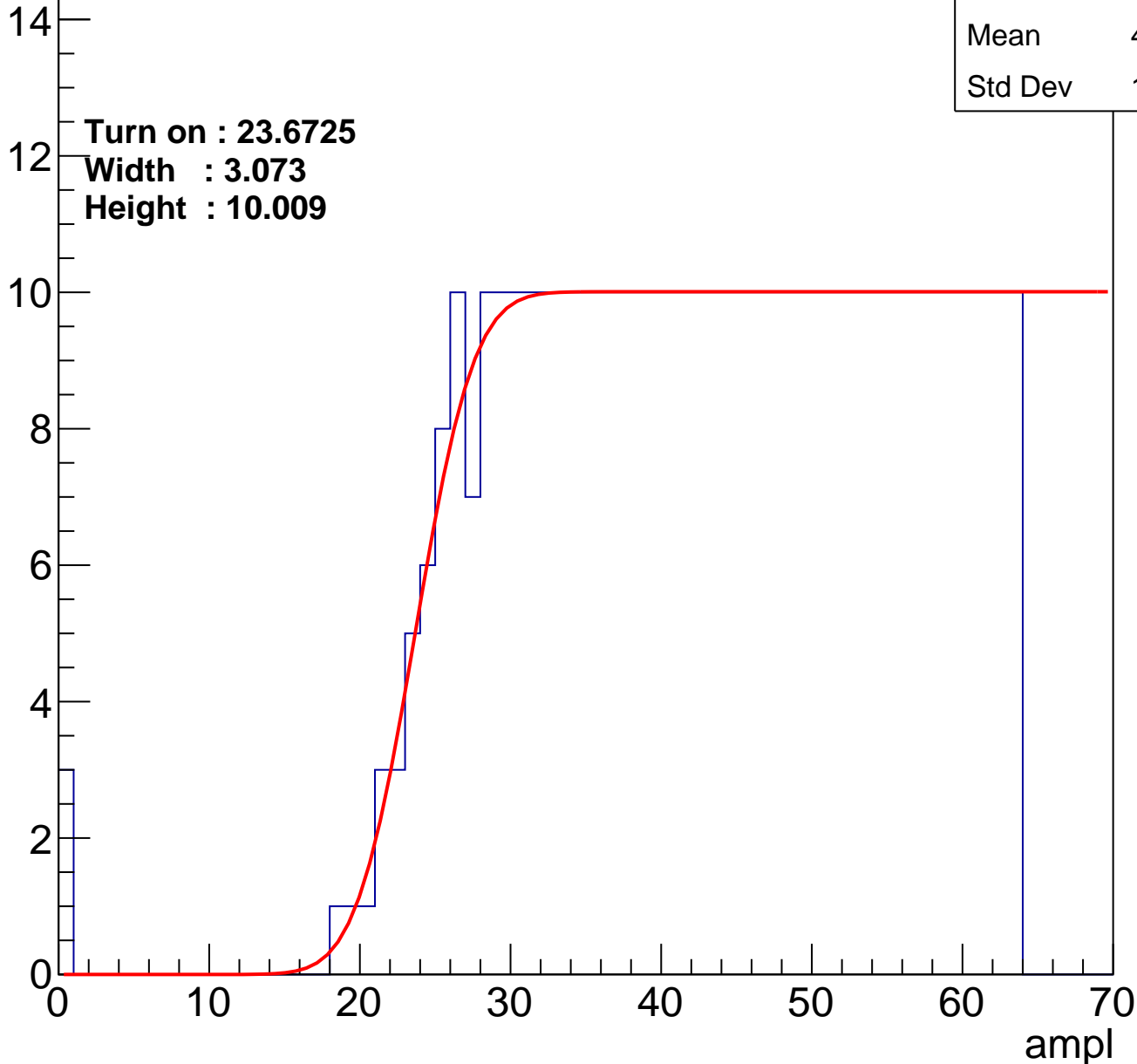
Entries	408
Mean	42.83
Std Dev	12.38

Turn on : 23.6725

Width : 3.073

Height : 10.009

Entry



B1L102S, U14-ch51

calib_packv5_042523_0143.root, FC#11, port A2

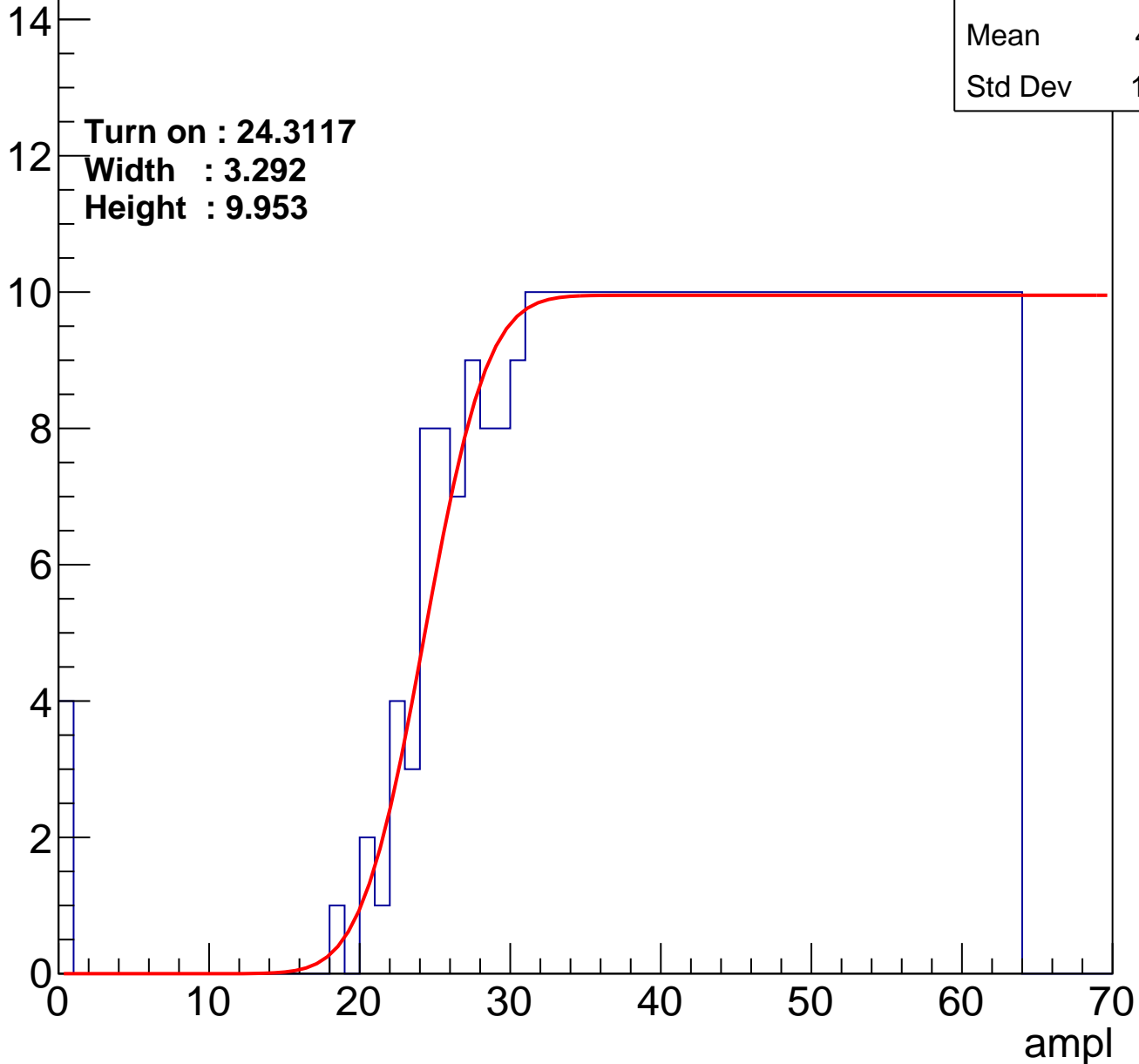
Entries	402
Mean	43.01
Std Dev	12.46

Turn on : 24.3117

Width : 3.292

Height : 9.953

Entry



B1L102S, U14-ch52

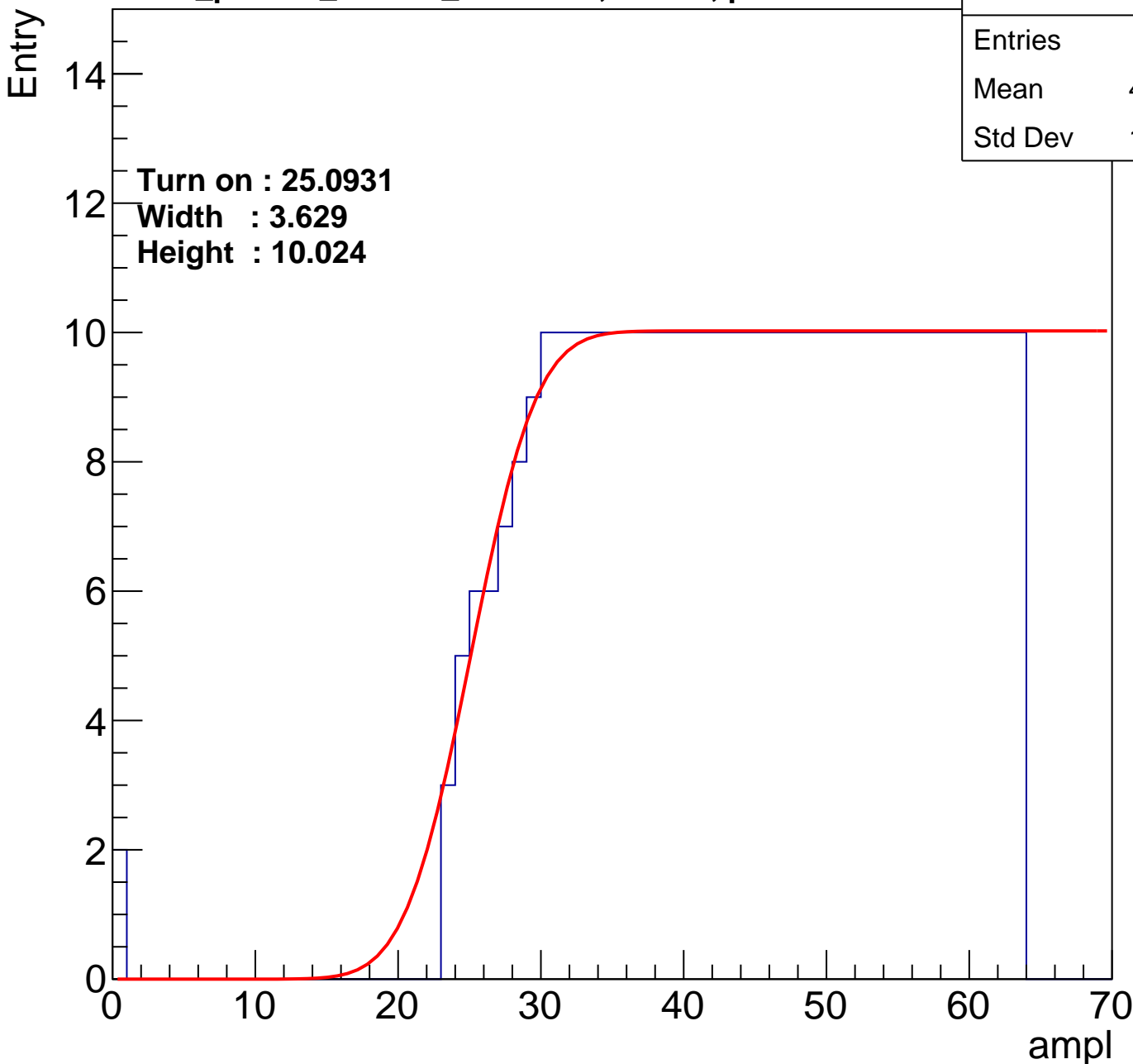
calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.0931

Width : 3.629

Height : 10.024



B1L102S, U14-ch53

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.4
Std Dev	12.11

Turn on : 24.9066

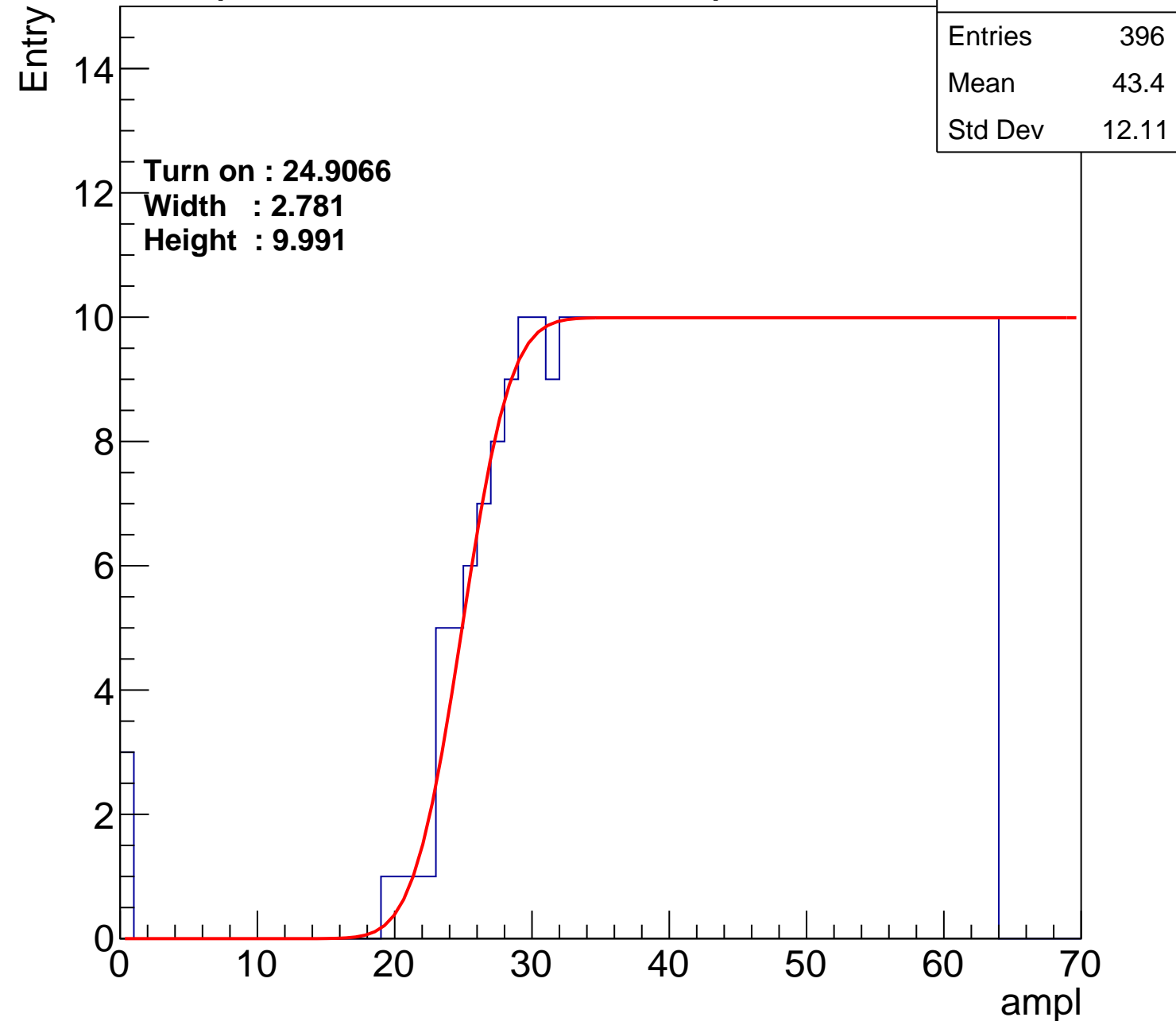
Width : 2.781

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.43
Std Dev	11.82

Turn on : 24.5504

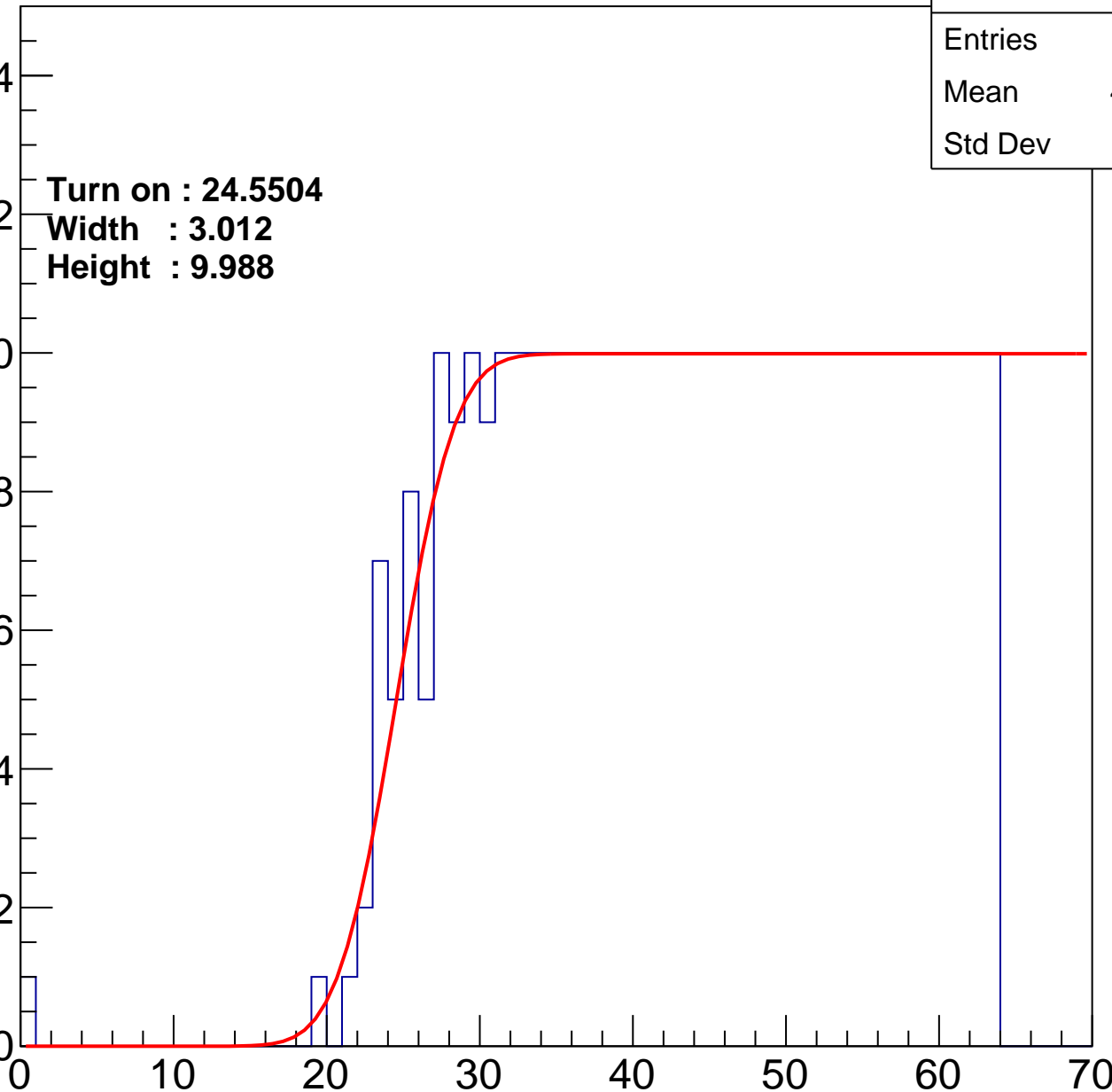
Width : 3.012

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.8
Std Dev	11.94

Turn on : 25.8955

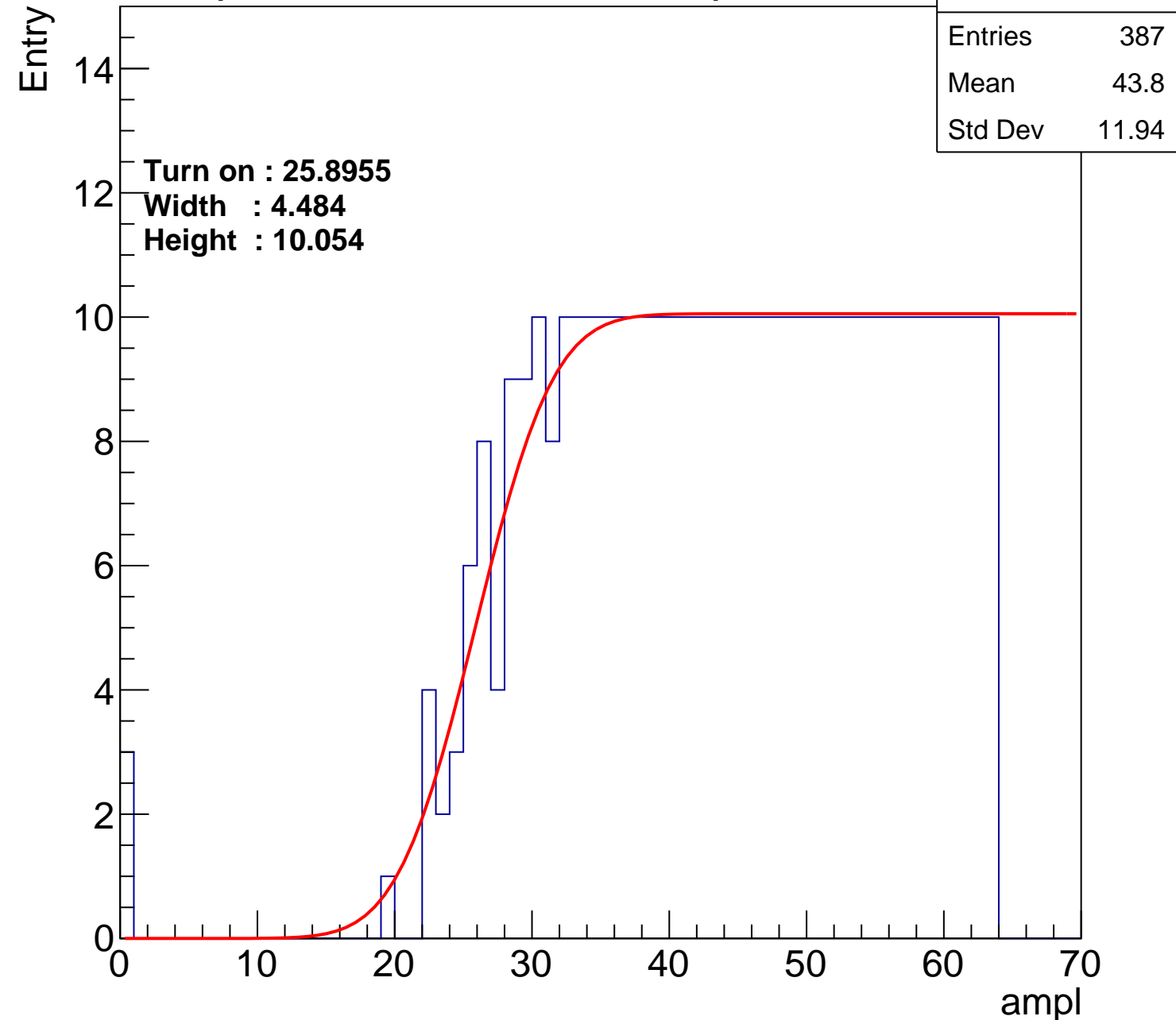
Width : 4.484

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch56

calib_packv5_042523_0143.root, FC#11, port A2

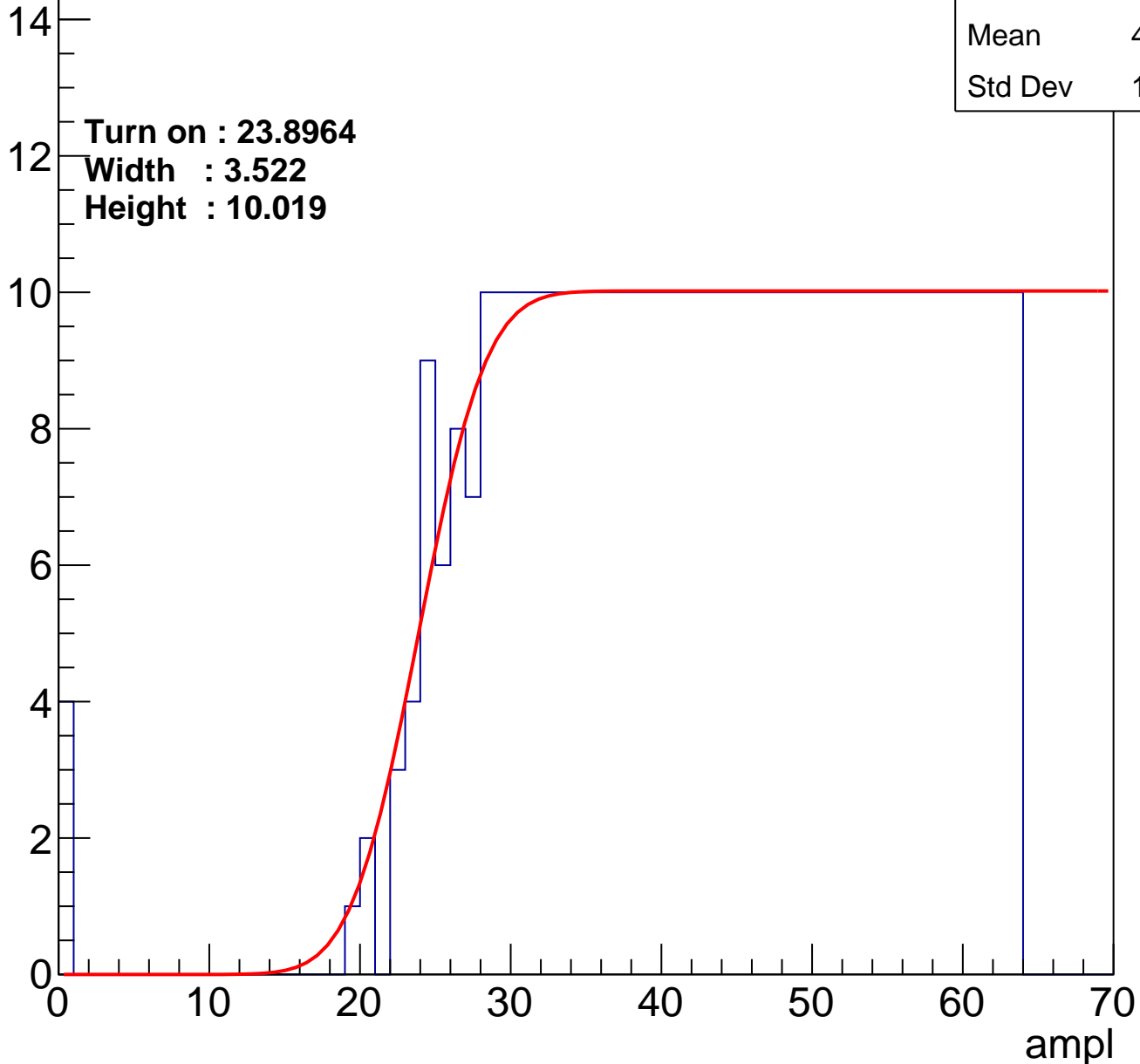
Entries	404
Mean	42.97
Std Dev	12.42

Turn on : 23.8964

Width : 3.522

Height : 10.019

Entry



B1L102S, U14-ch57

calib_packv5_042523_0143.root, FC#11, port A2

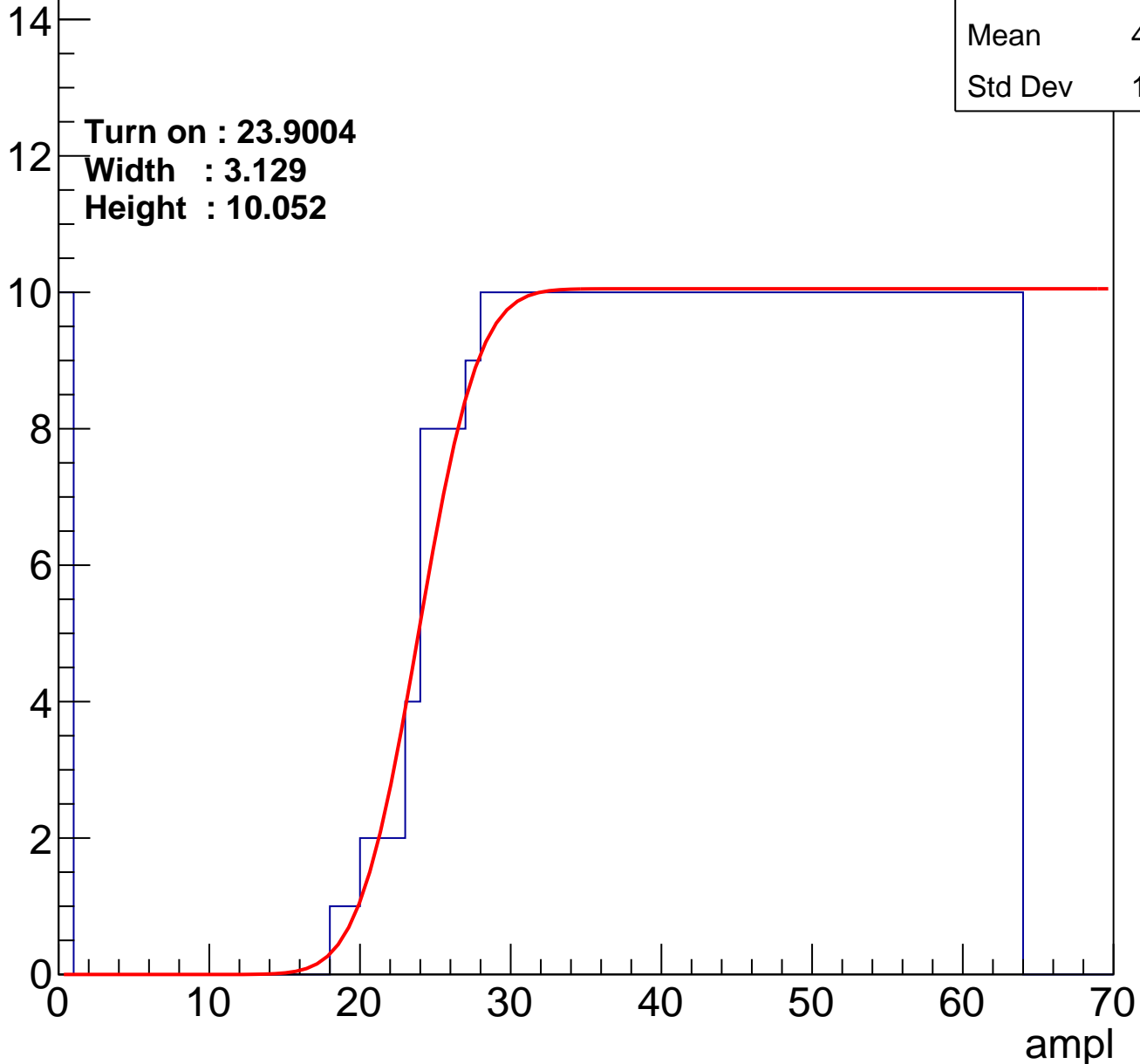
Entries	415
Mean	42.12
Std Dev	13.45

Turn on : 23.9004

Width : 3.129

Height : 10.052

Entry



B1L102S, U14-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.3926

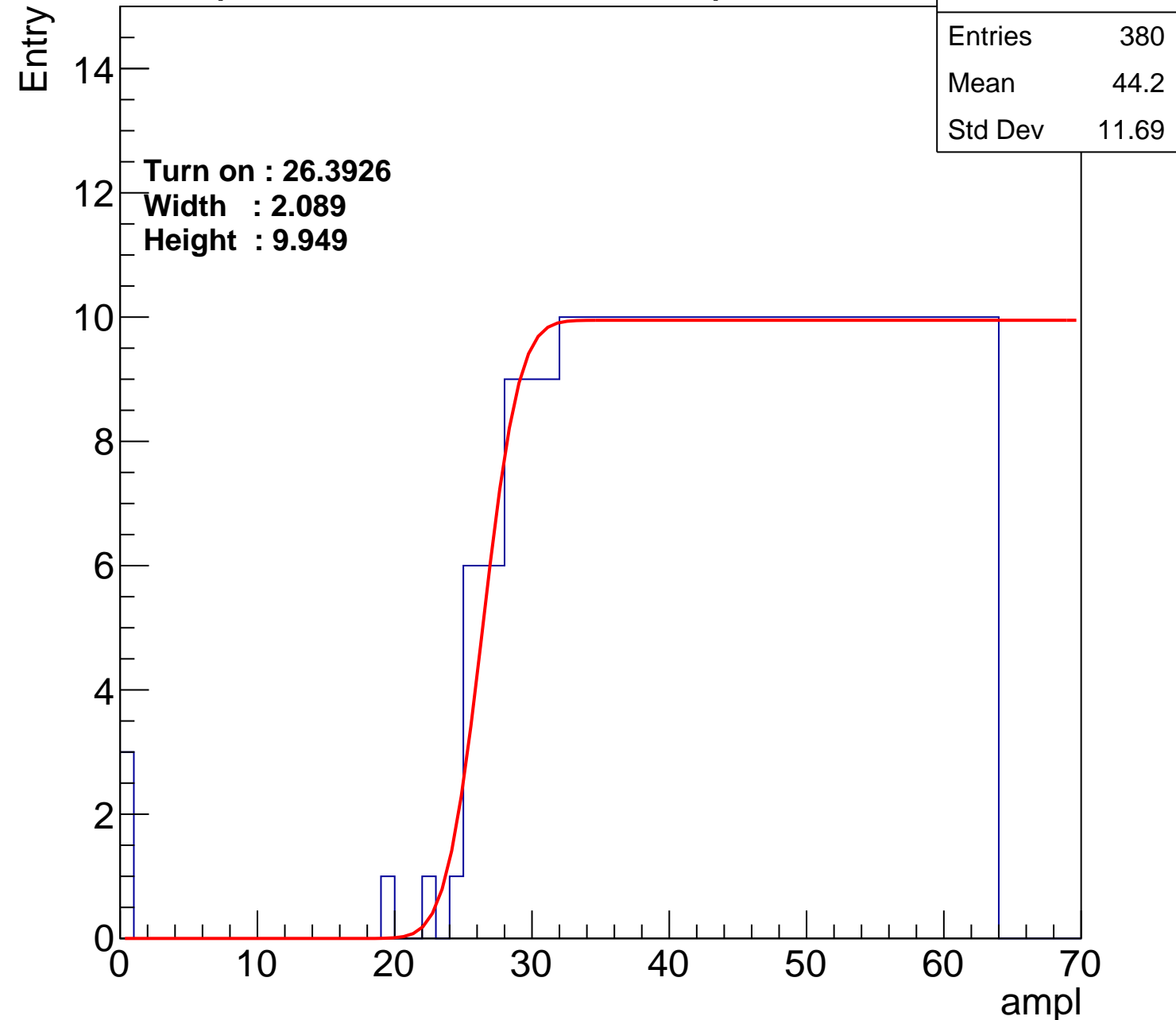
Width : 2.089

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.48
Std Dev	11.81

Turn on : 24.5309

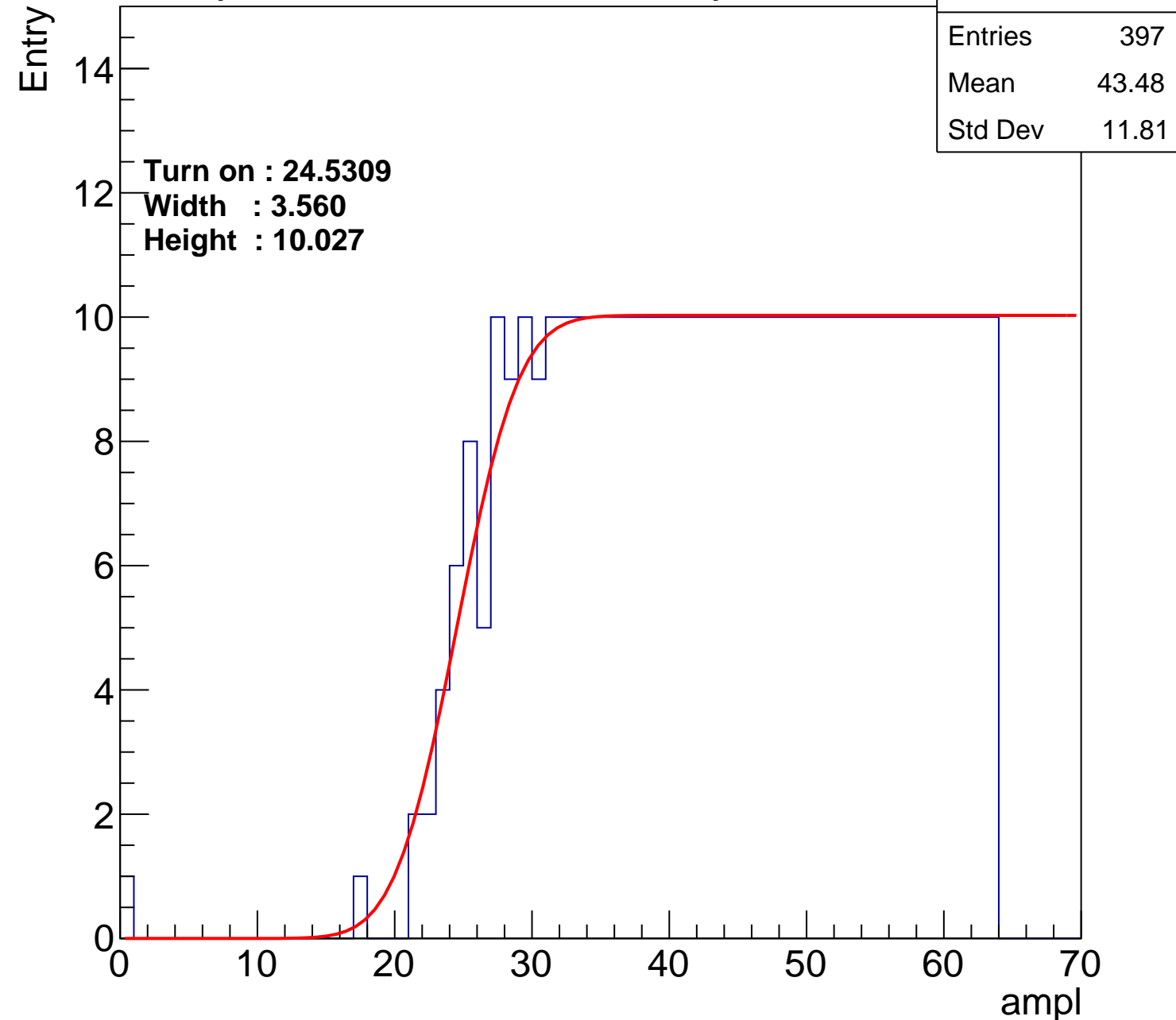
Width : 3.560

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch60

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.43
Std Dev	12.12

Turn on : 25.1771

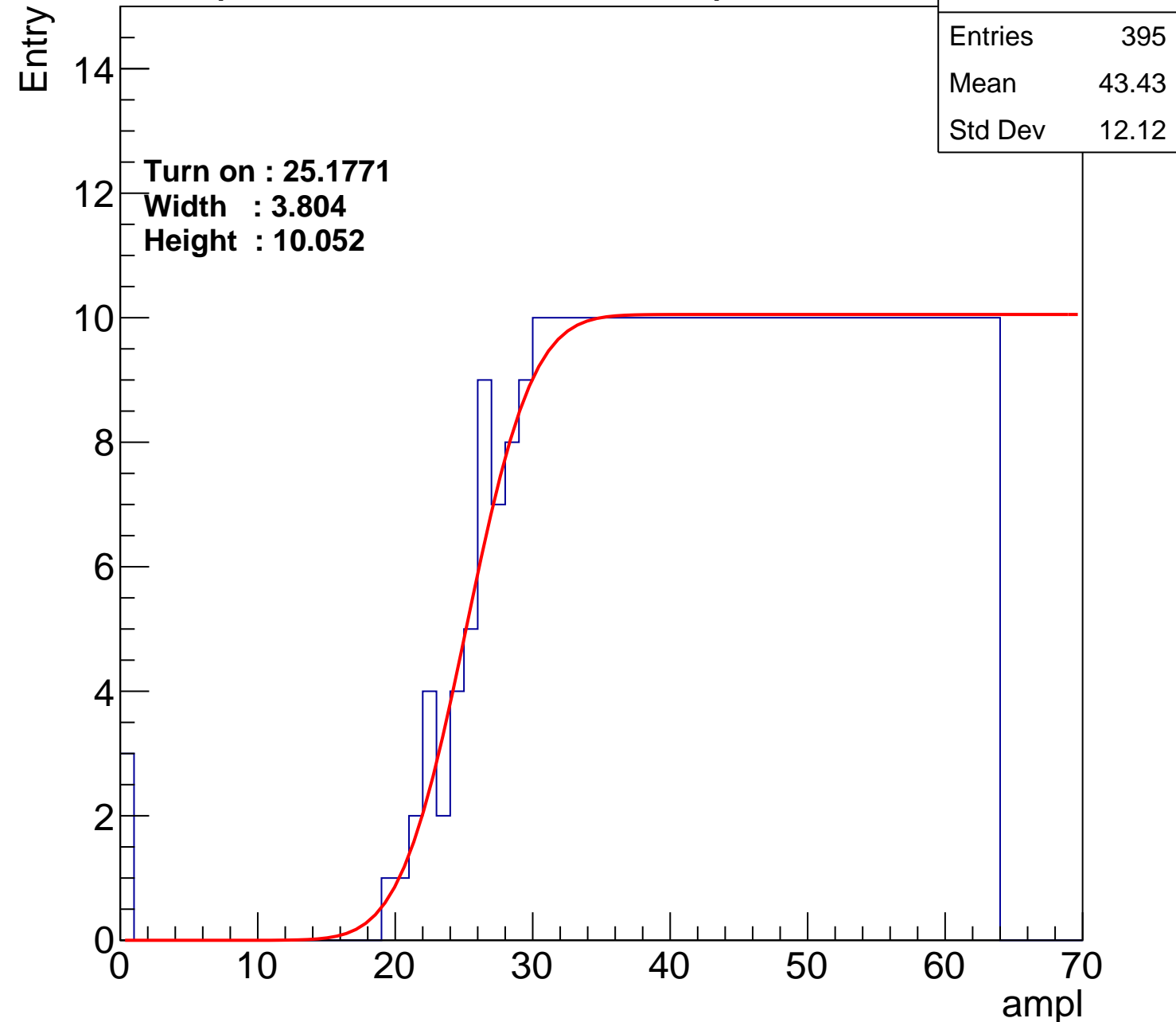
Width : 3.804

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.47
Std Dev	12.28

Turn on : 25.3409

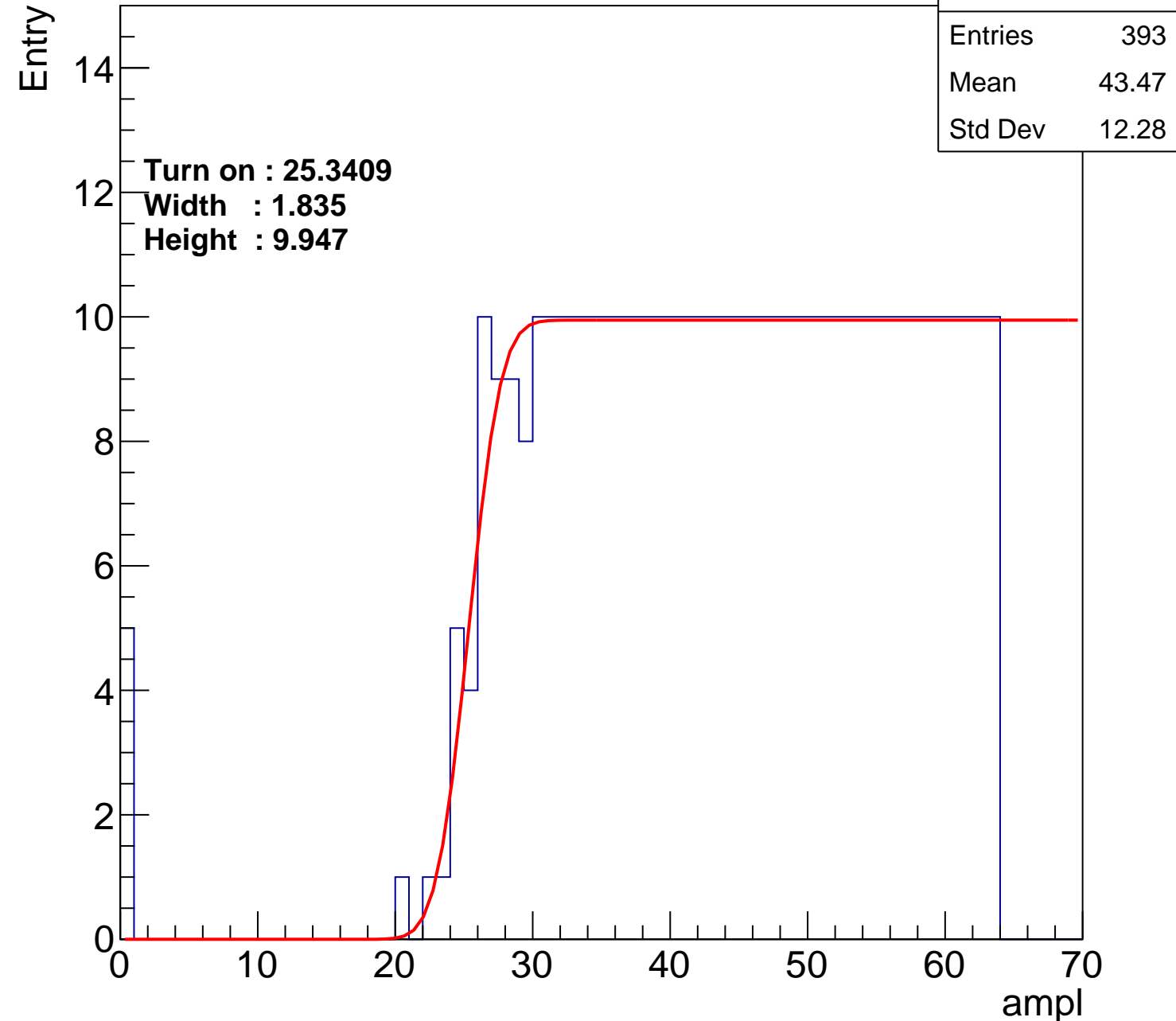
Width : 1.835

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch62

calib_packv5_042523_0143.root, FC#11, port A2

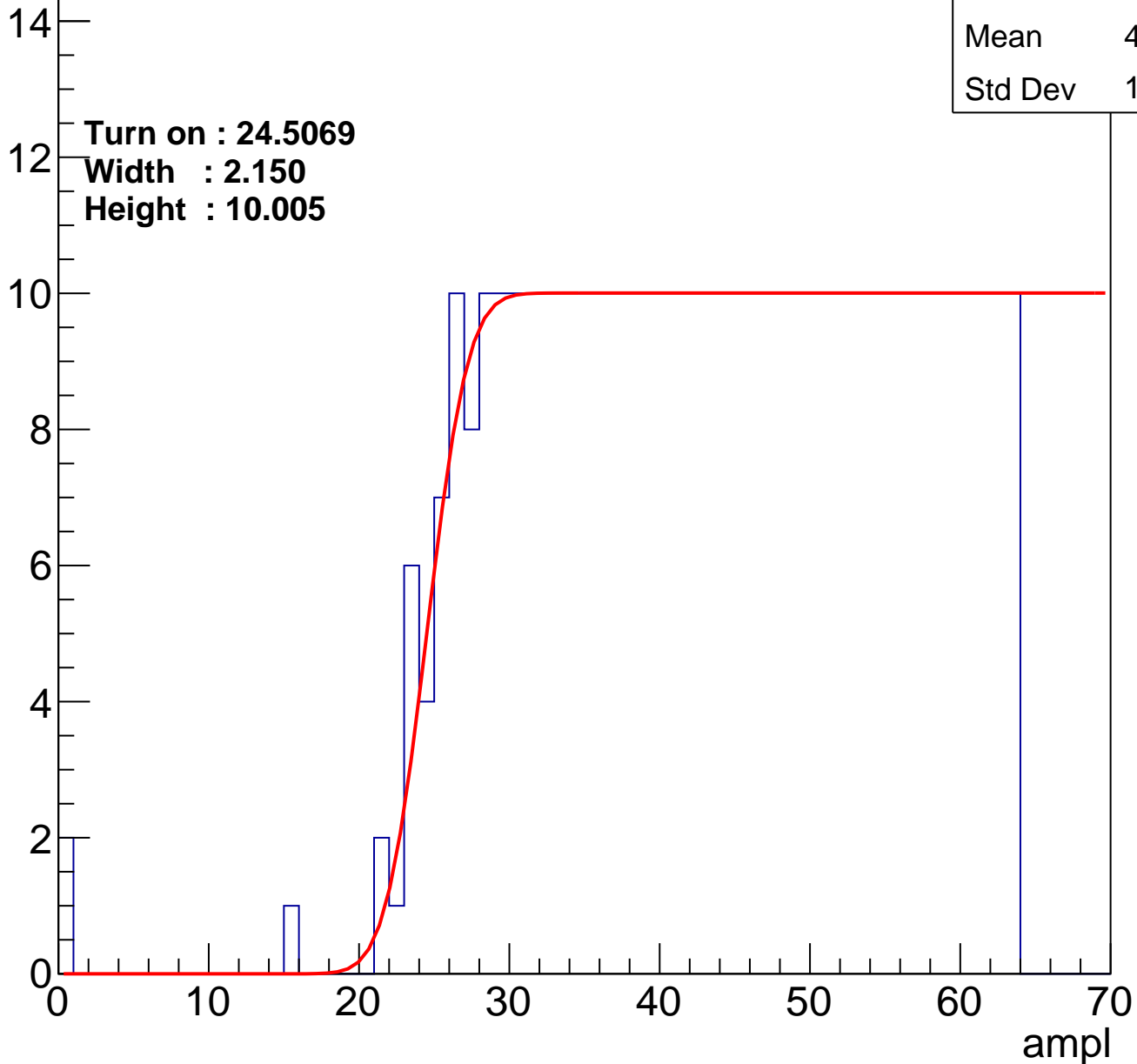
Entries	401
Mean	43.25
Std Dev	12.03

Turn on : 24.5069

Width : 2.150

Height : 10.005

Entry



B1L102S, U14-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 24.1763

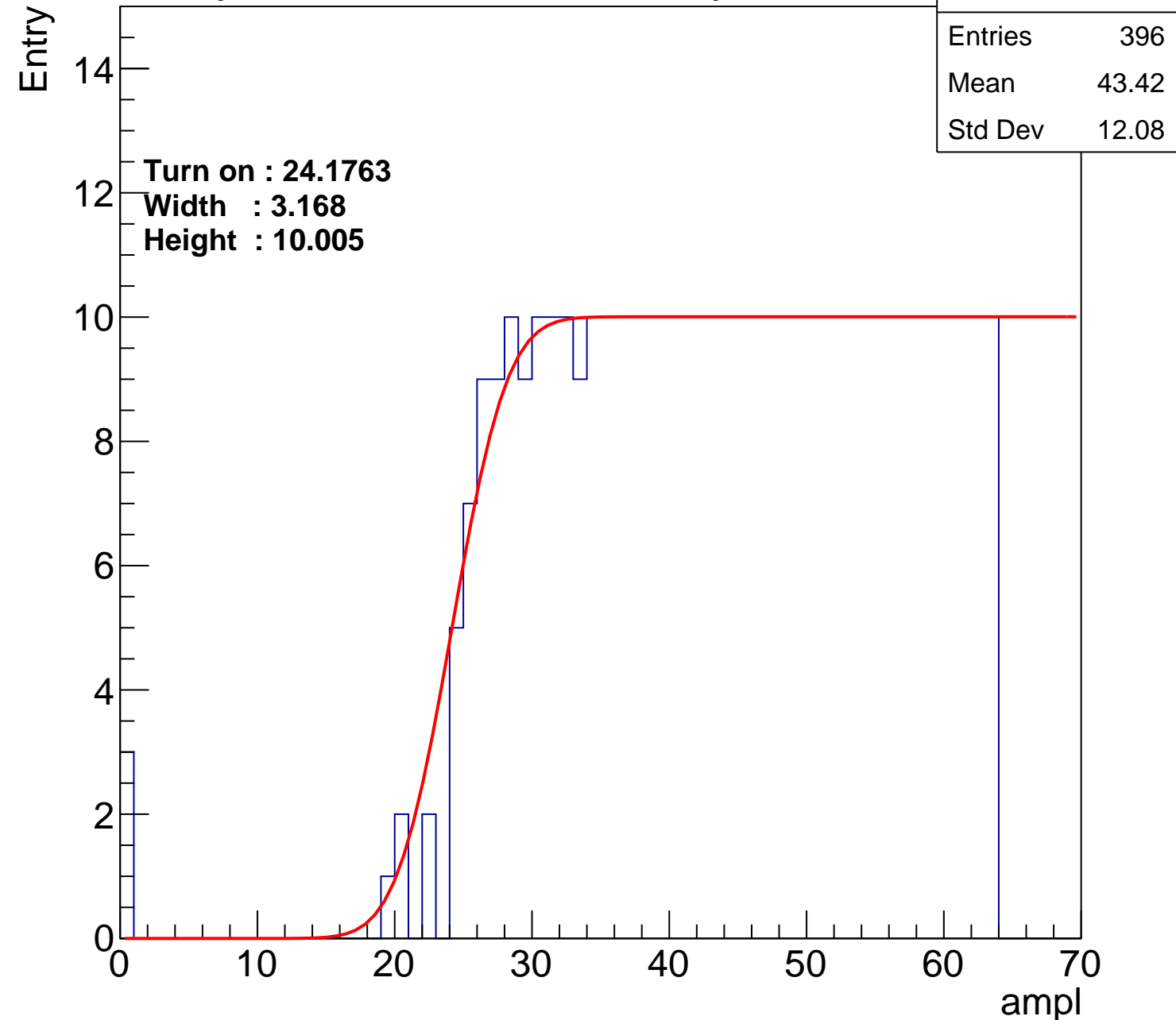
Width : 3.168

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch64

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.39
Std Dev	11.41

Turn on : 26.0945

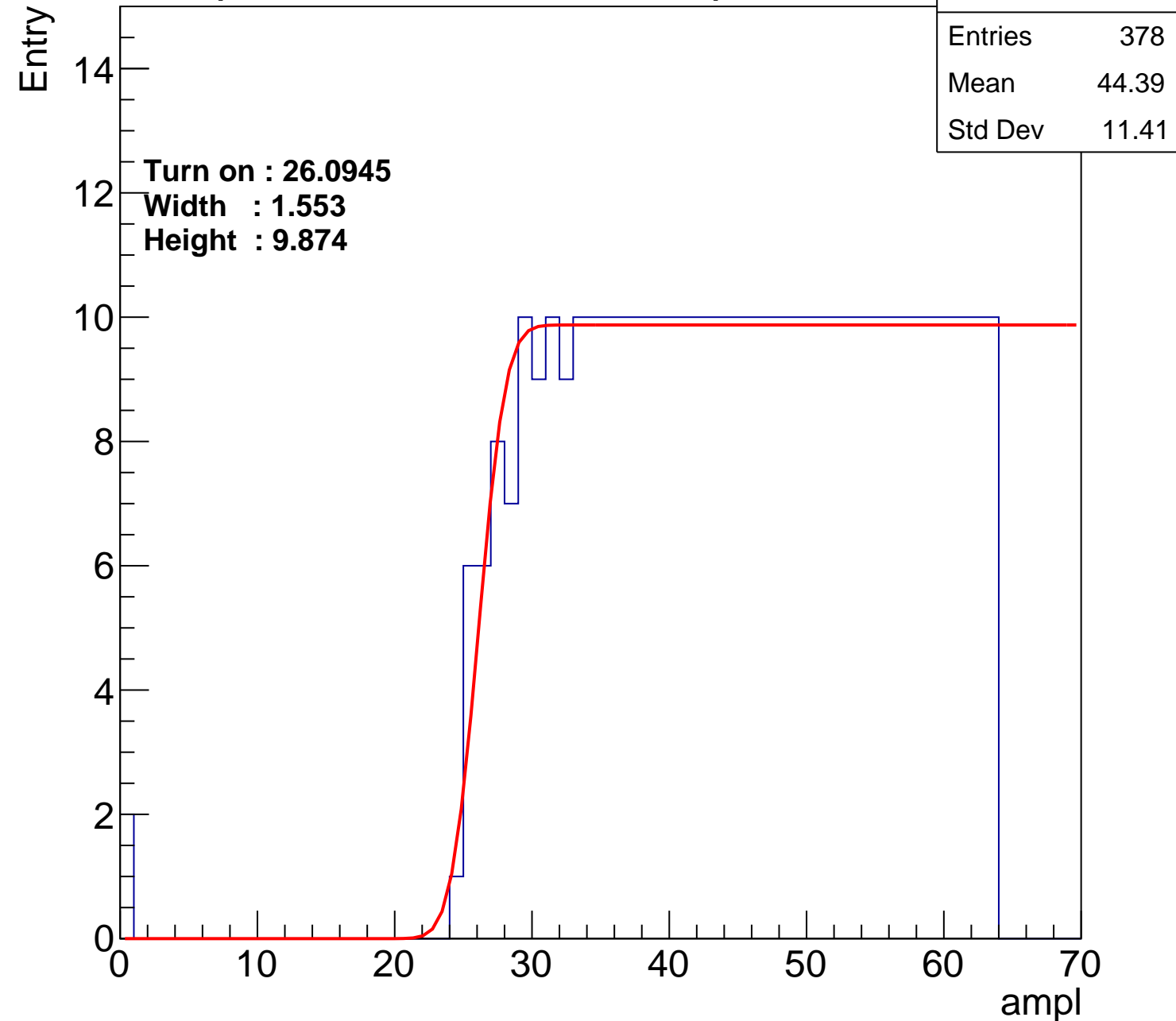
Width : 1.553

Height : 9.874

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.0460

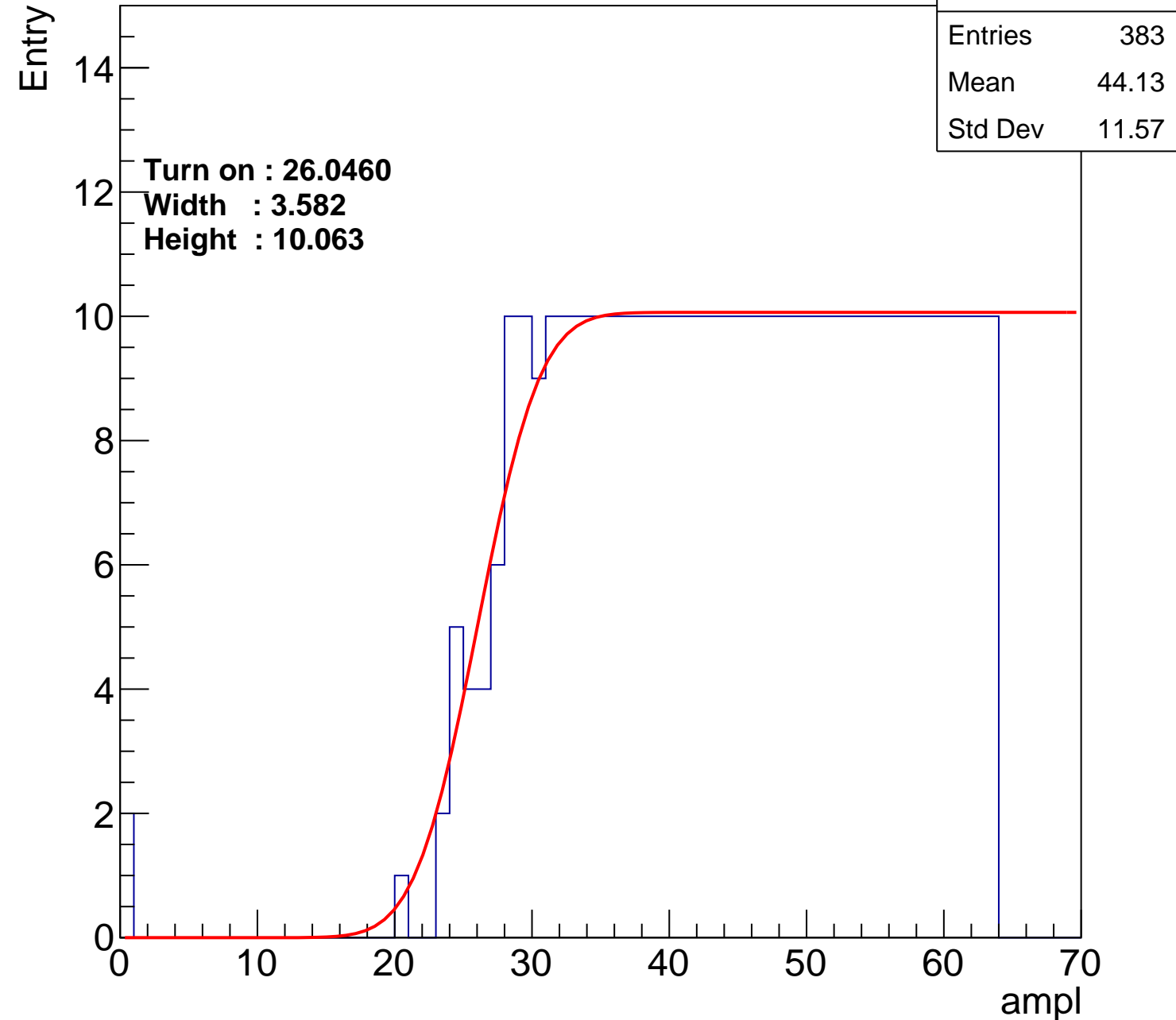
Width : 3.582

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch66

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.32
Std Dev	12.41

Turn on : 24.7136

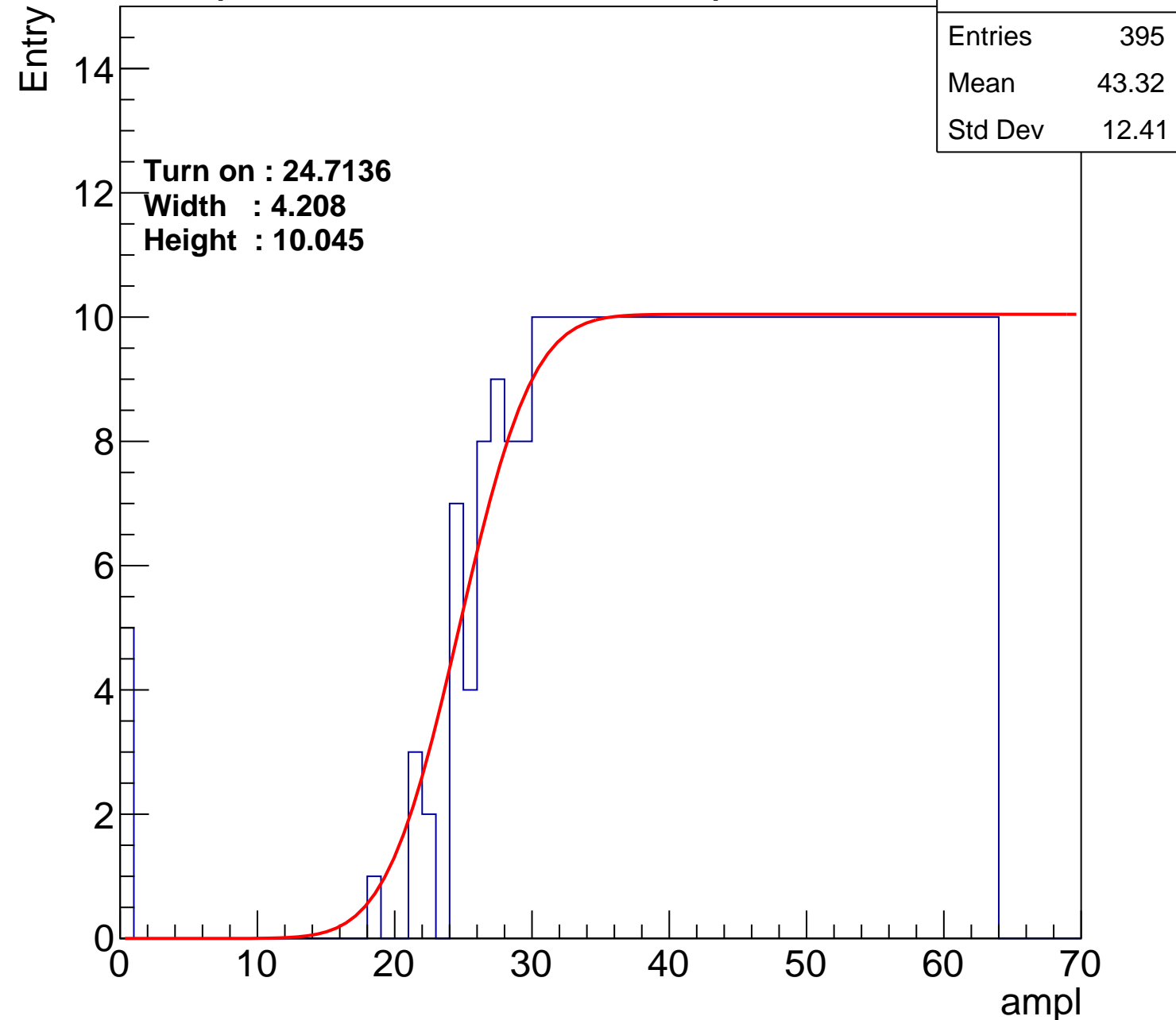
Width : 4.208

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch67

calib_packv5_042523_0143.root, FC#11, port A2

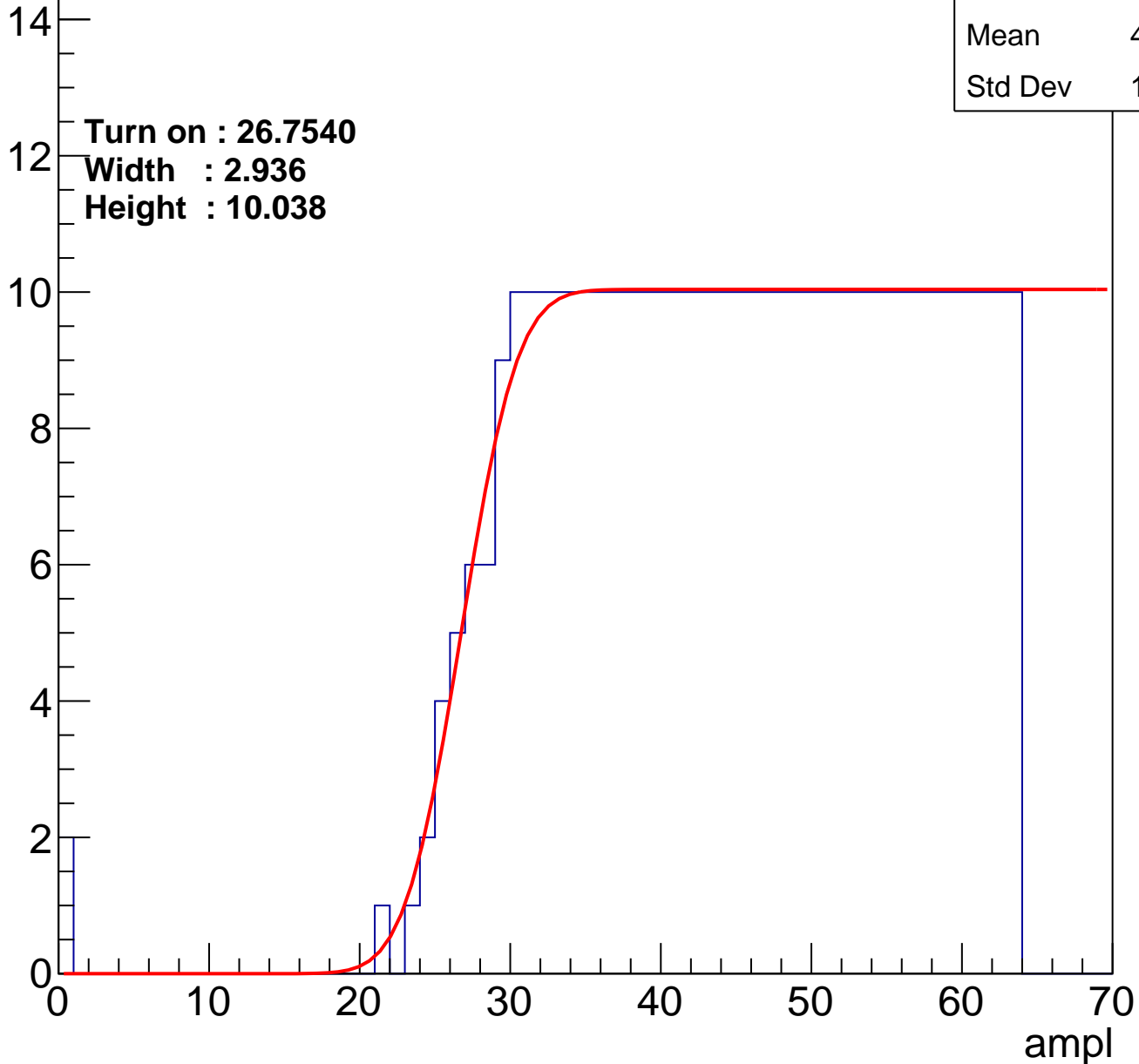
Entries	376
Mean	44.48
Std Dev	11.39

Turn on : 26.7540

Width : 2.936

Height : 10.038

Entry



B1L102S, U14-ch68

calib_packv5_042523_0143.root, FC#11, port A2

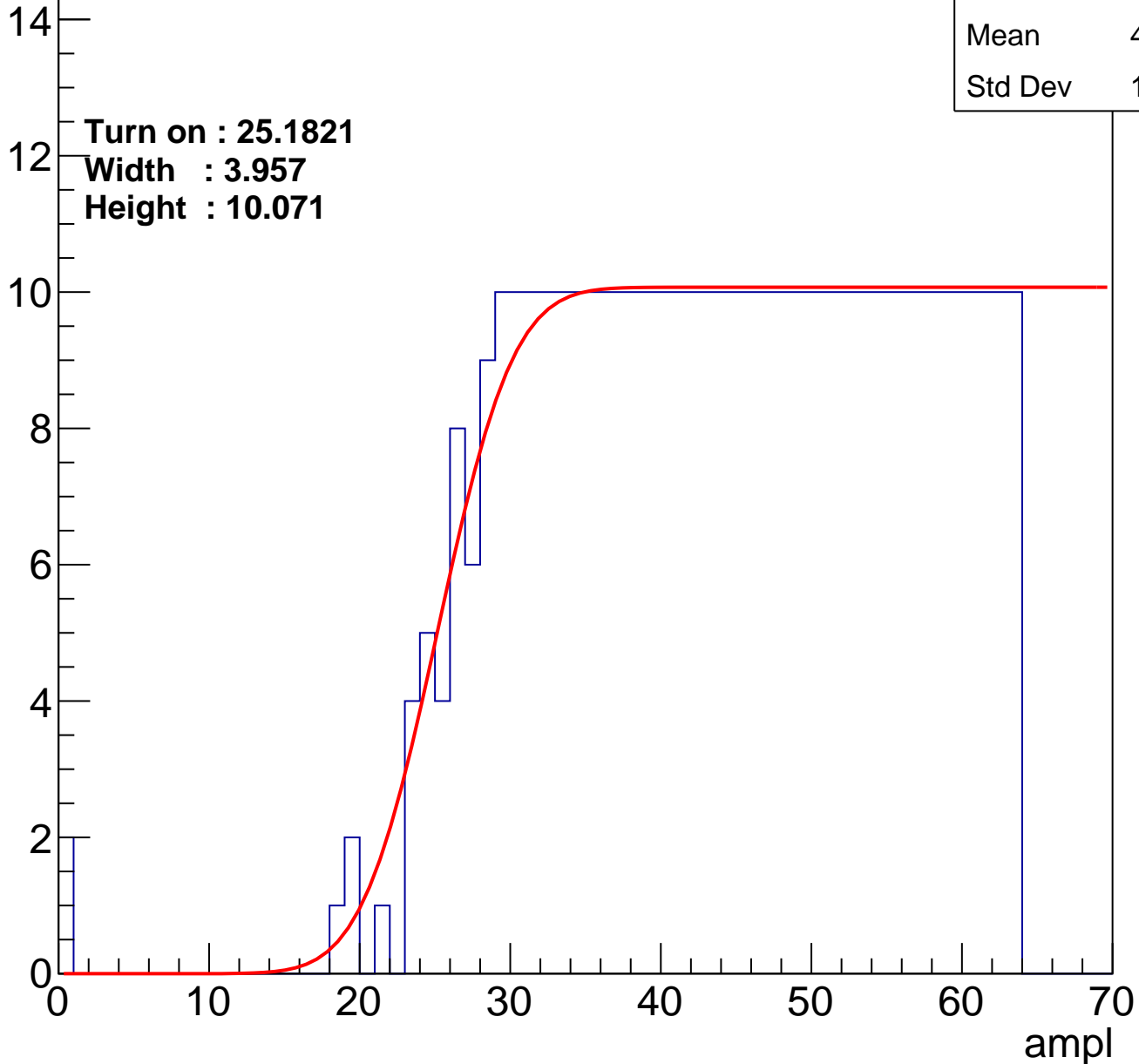
Entries	392
Mean	43.65
Std Dev	11.87

Turn on : 25.1821

Width : 3.957

Height : 10.071

Entry



B1L102S, U14-ch69

calib_packv5_042523_0143.root, FC#11, port A2

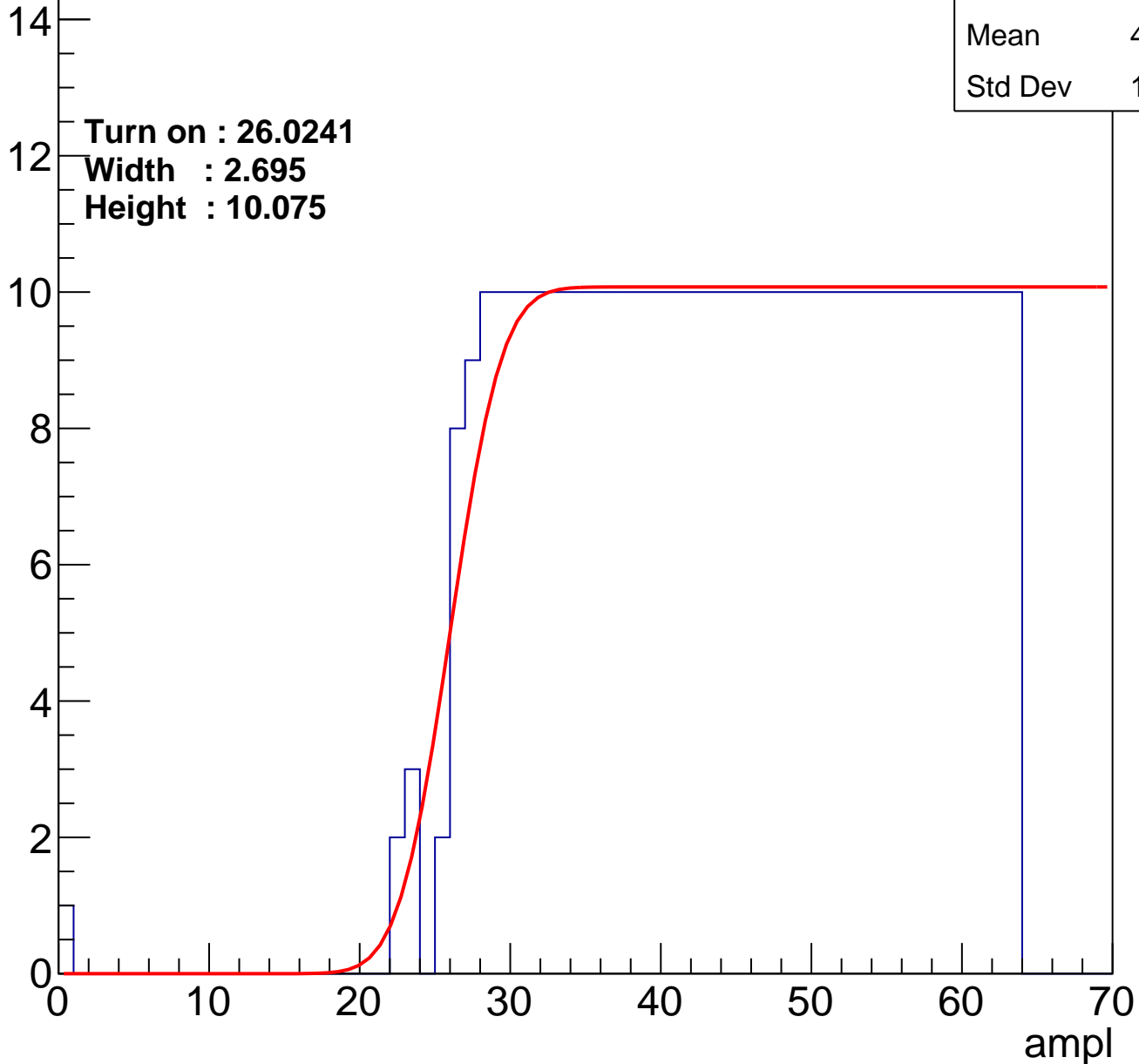
Entries	385
Mean	44.14
Std Dev	11.37

Turn on : 26.0241

Width : 2.695

Height : 10.075

Entry



B1L102S, U14-ch70

calib_packv5_042523_0143.root, FC#11, port A2

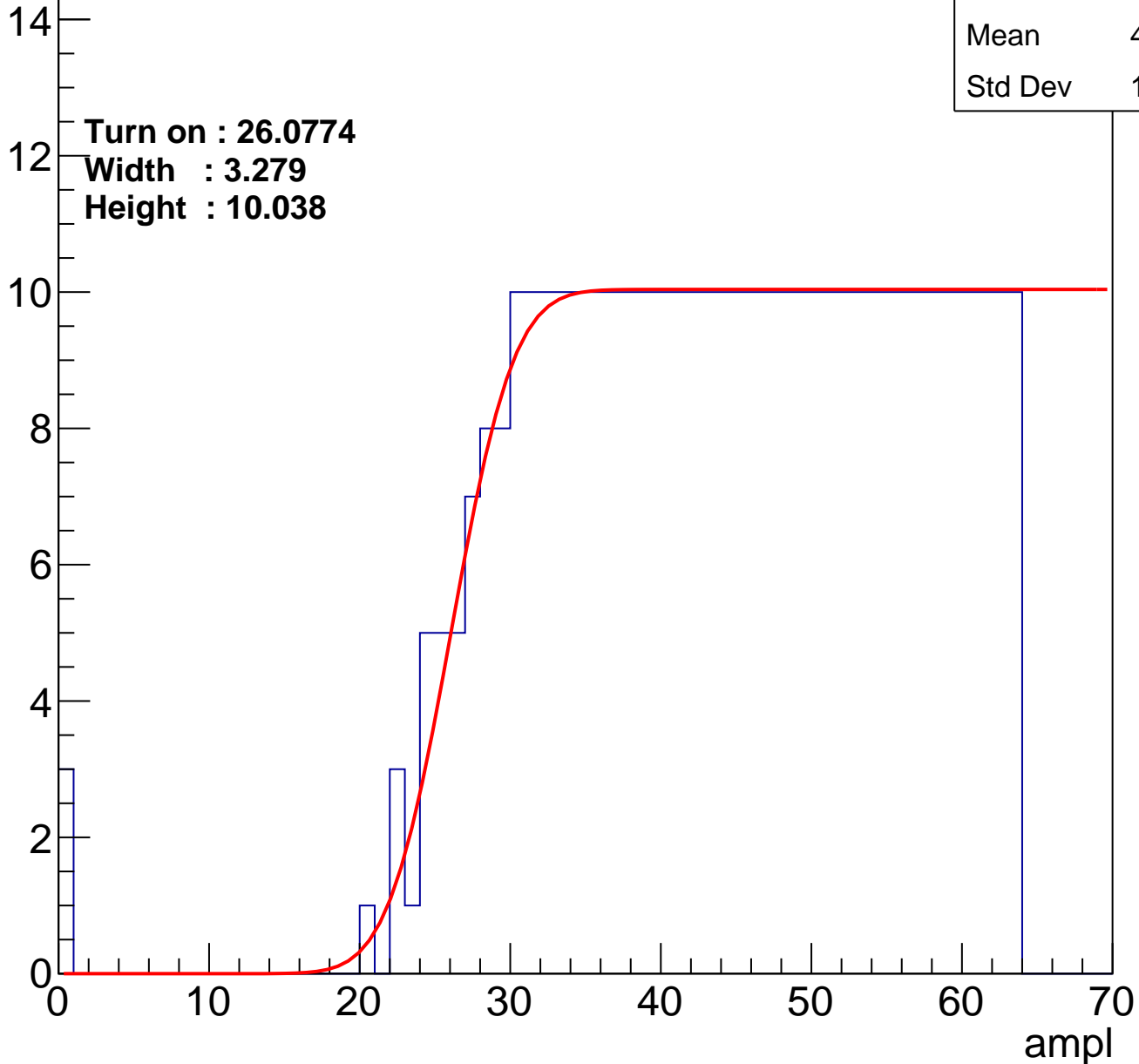
Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 26.0774

Width : 3.279

Height : 10.038

Entry



B1L102S, U14-ch71

calib_packv5_042523_0143.root, FC#11, port A2

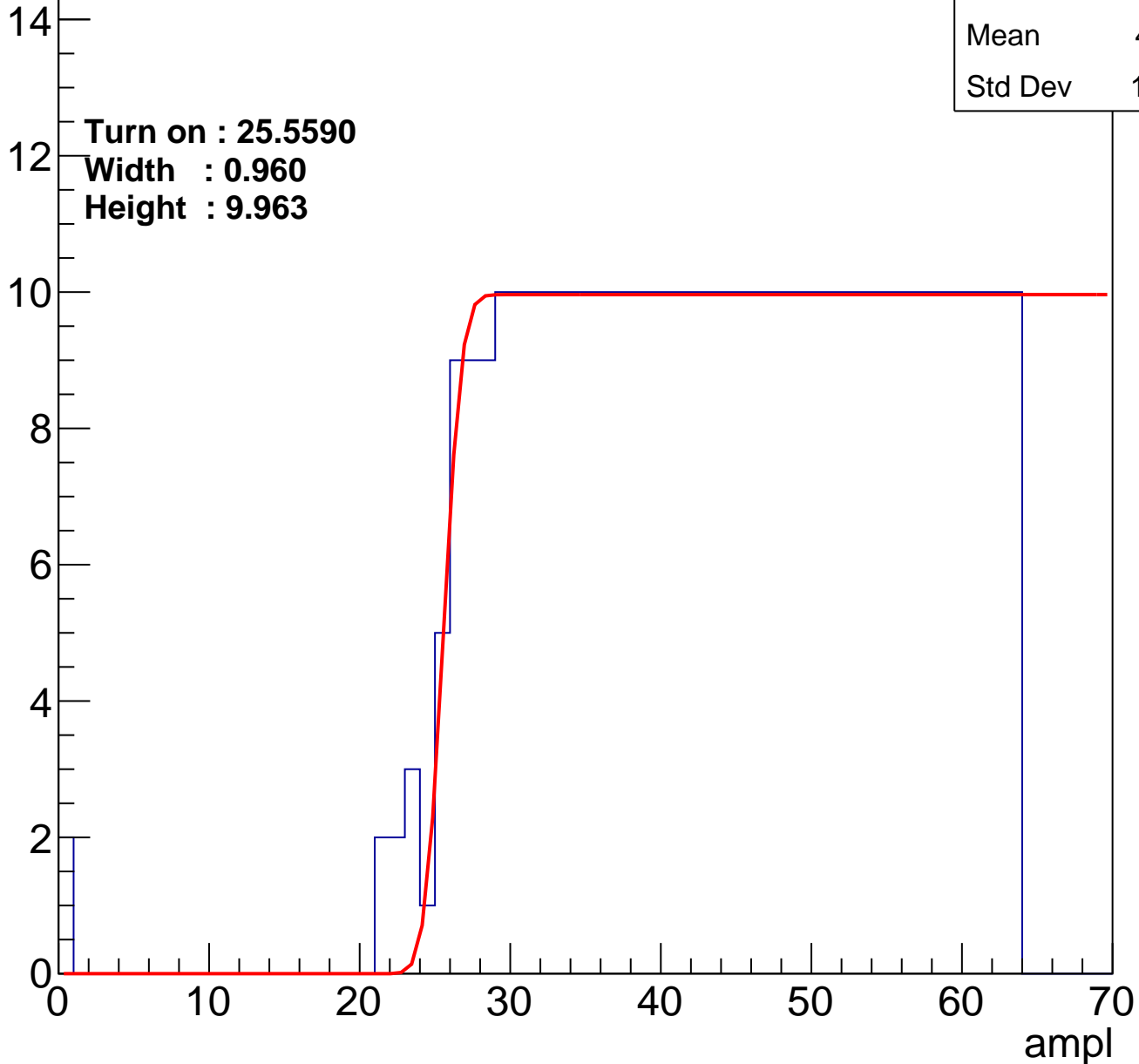
Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.5590

Width : 0.960

Height : 9.963

Entry



B1L102S, U14-ch72

calib_packv5_042523_0143.root, FC#11, port A2

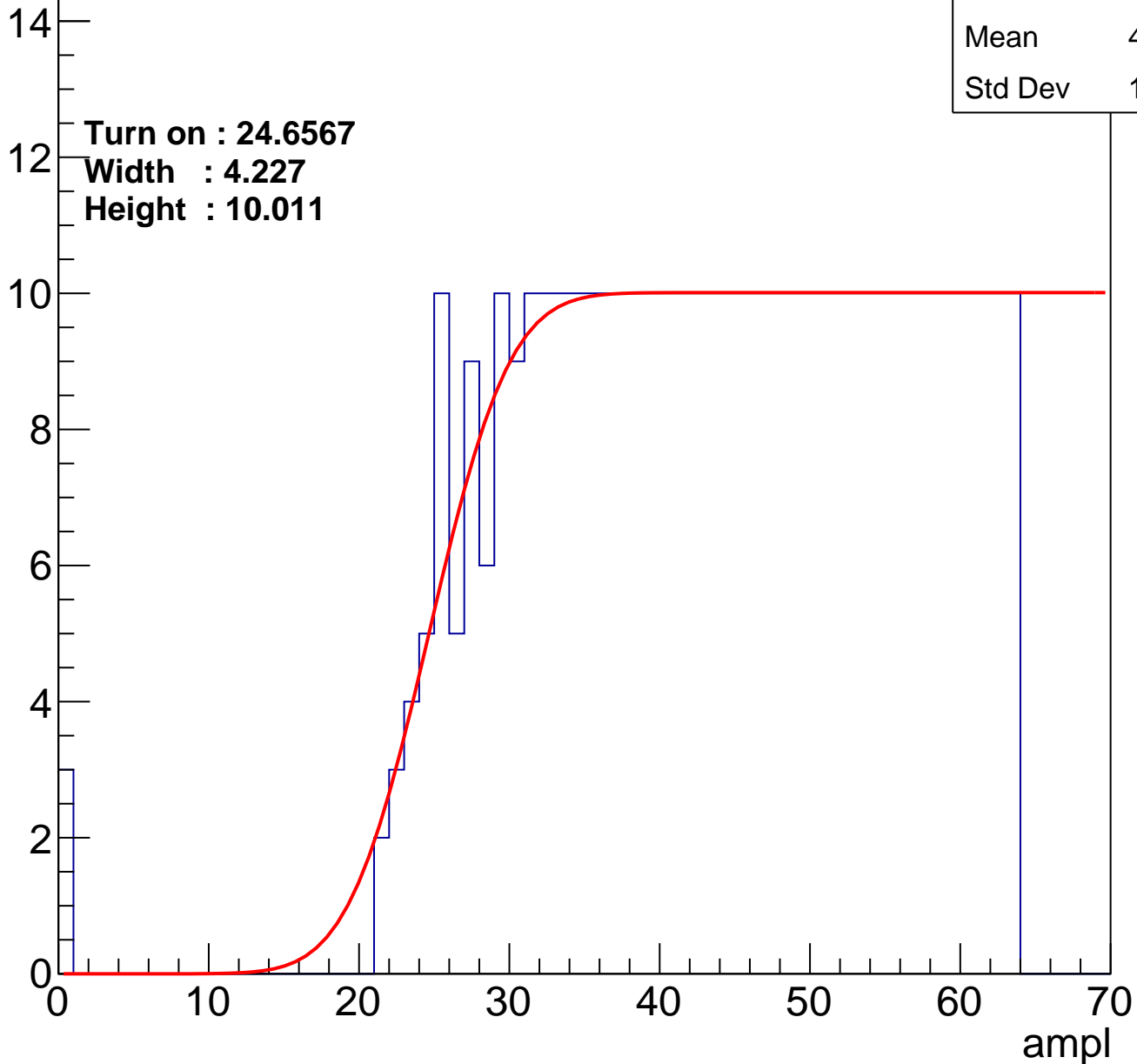
Entries	396
Mean	43.39
Std Dev	12.12

Turn on : 24.6567

Width : 4.227

Height : 10.011

Entry



B1L102S, U14-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.6221

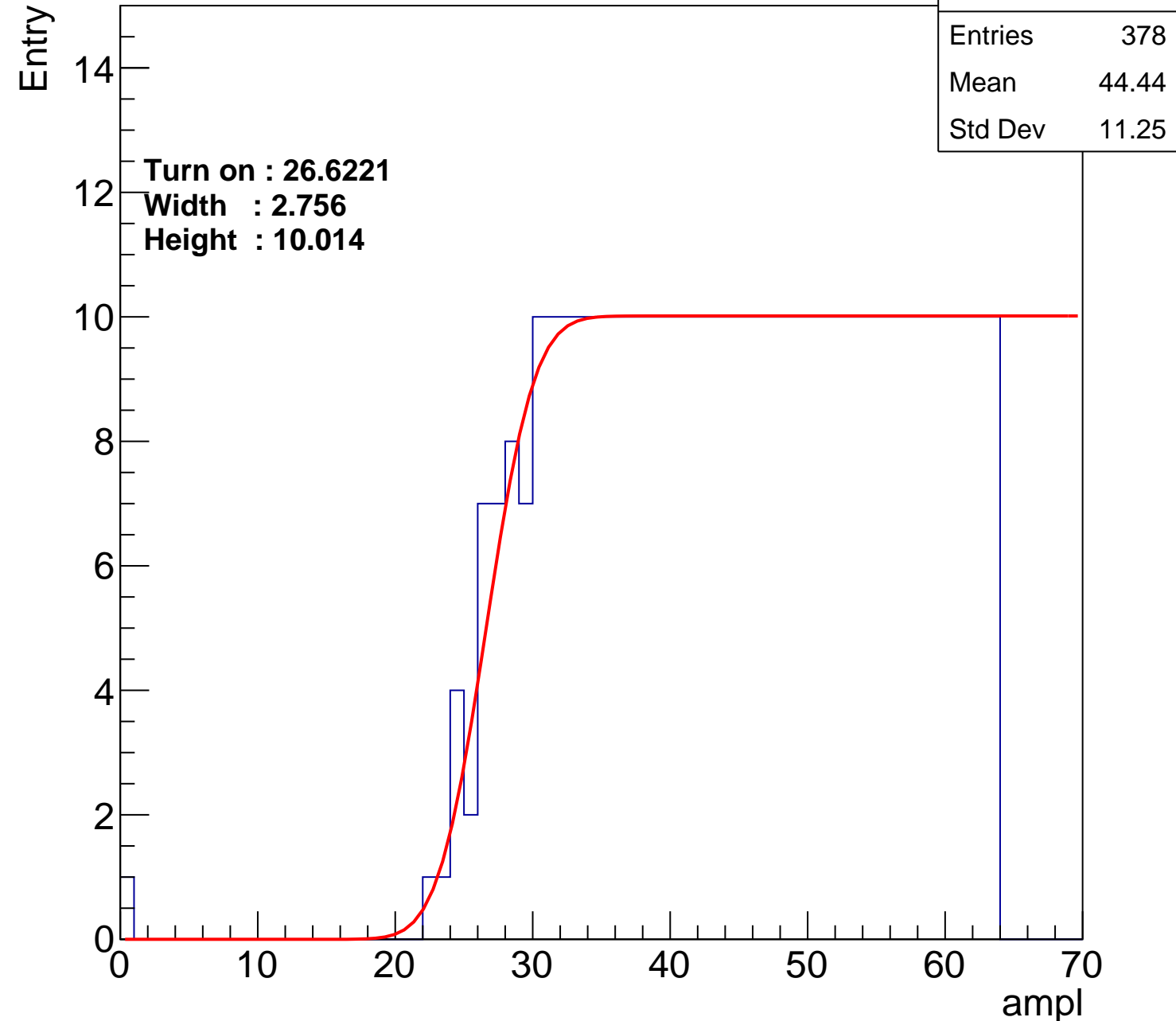
Width : 2.756

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch74

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	43.08
Std Dev	12.14

Turn on : 24.2295

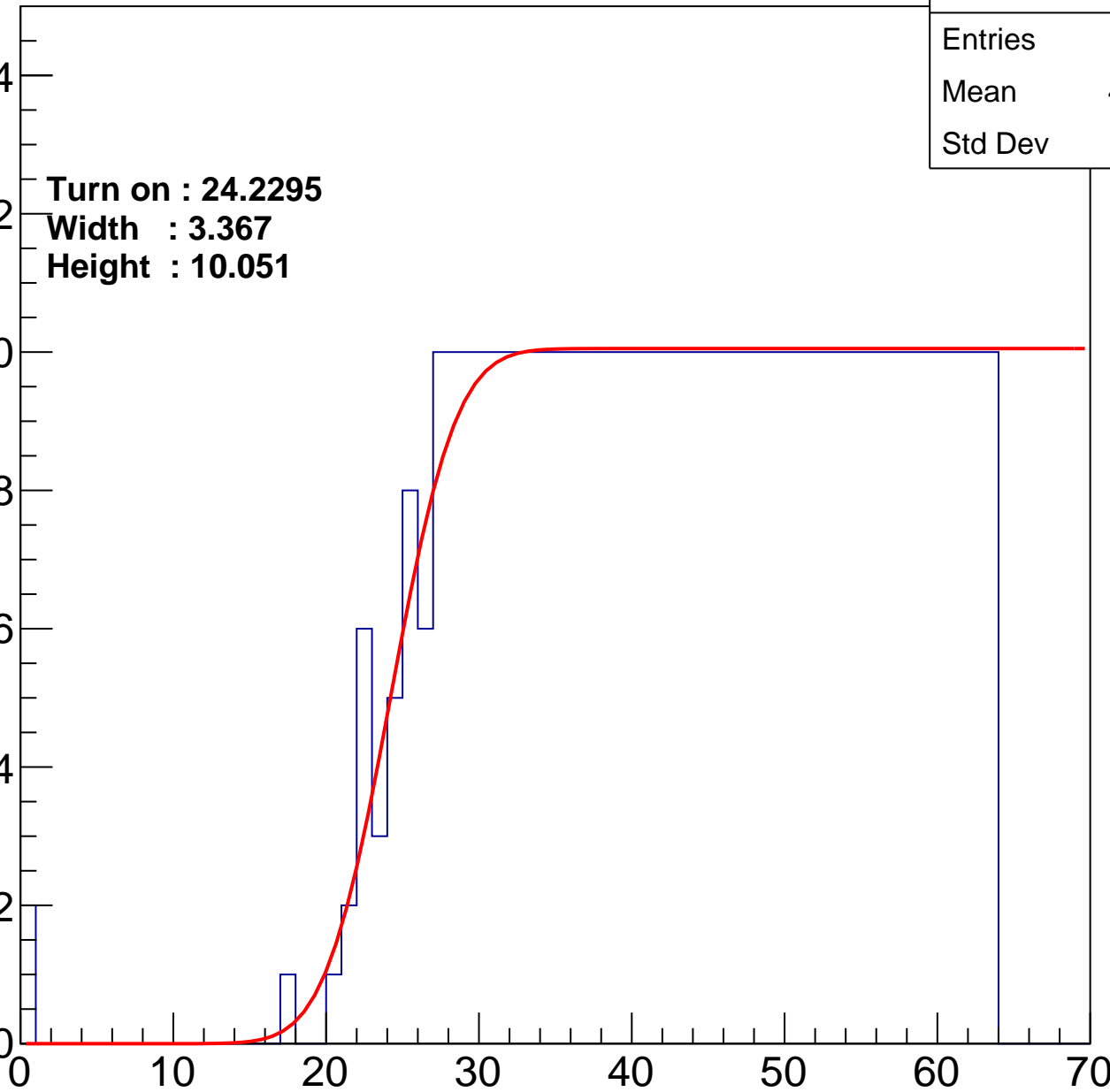
Width : 3.367

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	413
Mean	42.55
Std Dev	12.61

Turn on : 22.8193

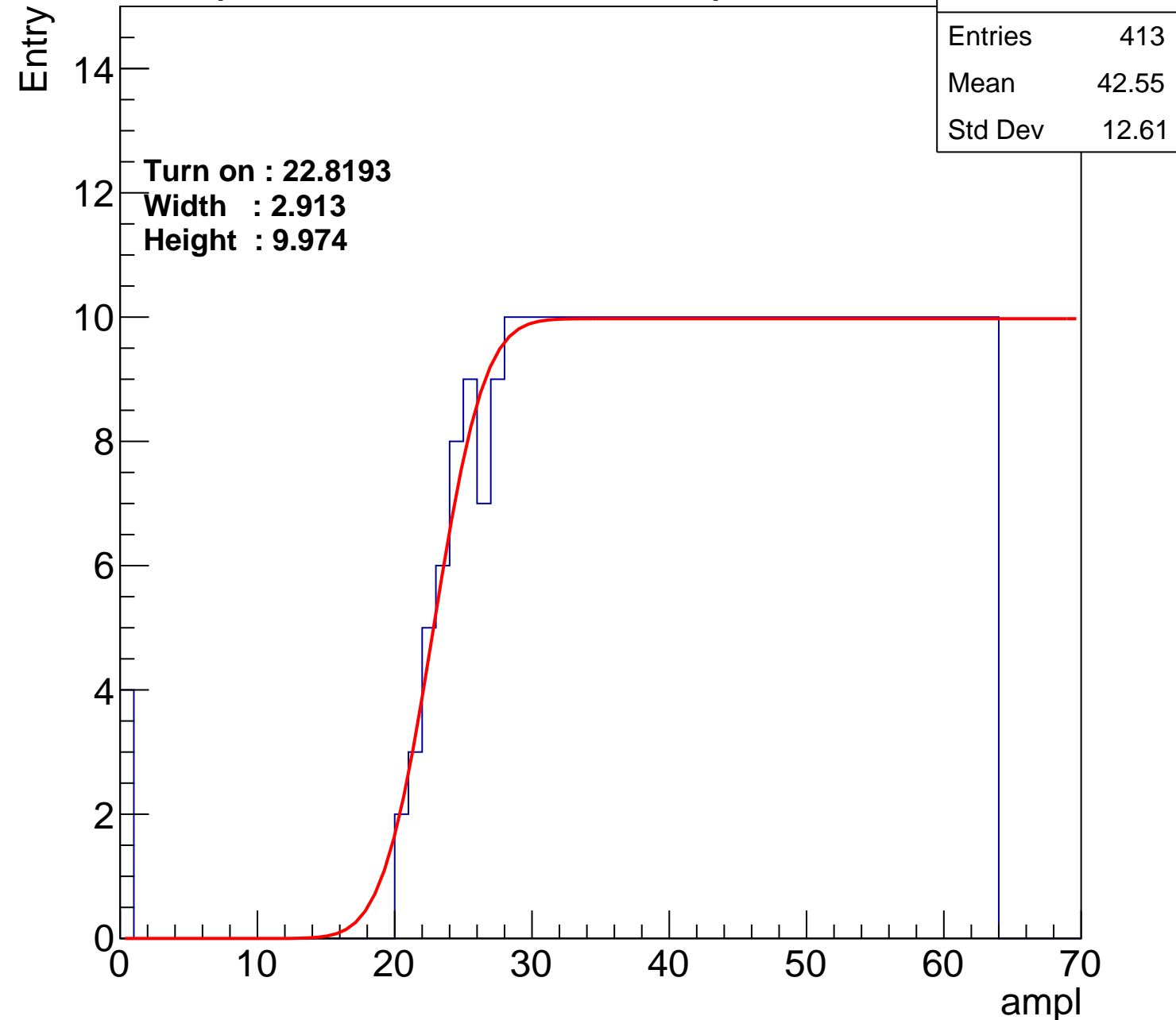
Width : 2.913

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch76

calib_packv5_042523_0143.root, FC#11, port A2

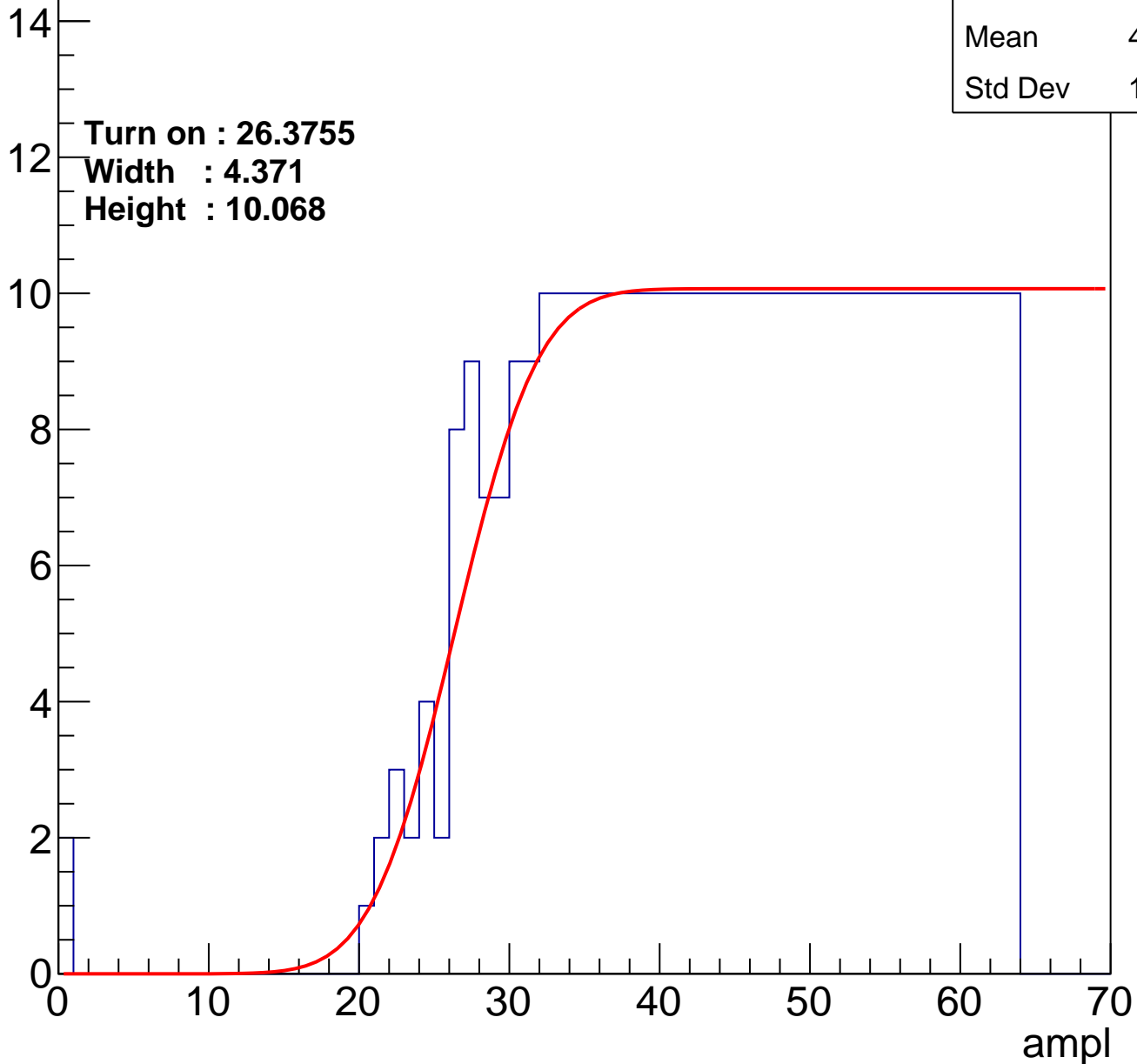
Entries	385
Mean	43.95
Std Dev	11.76

Turn on : 26.3755

Width : 4.371

Height : 10.068

Entry



B1L102S, U14-ch77

calib_packv5_042523_0143.root, FC#11, port A2

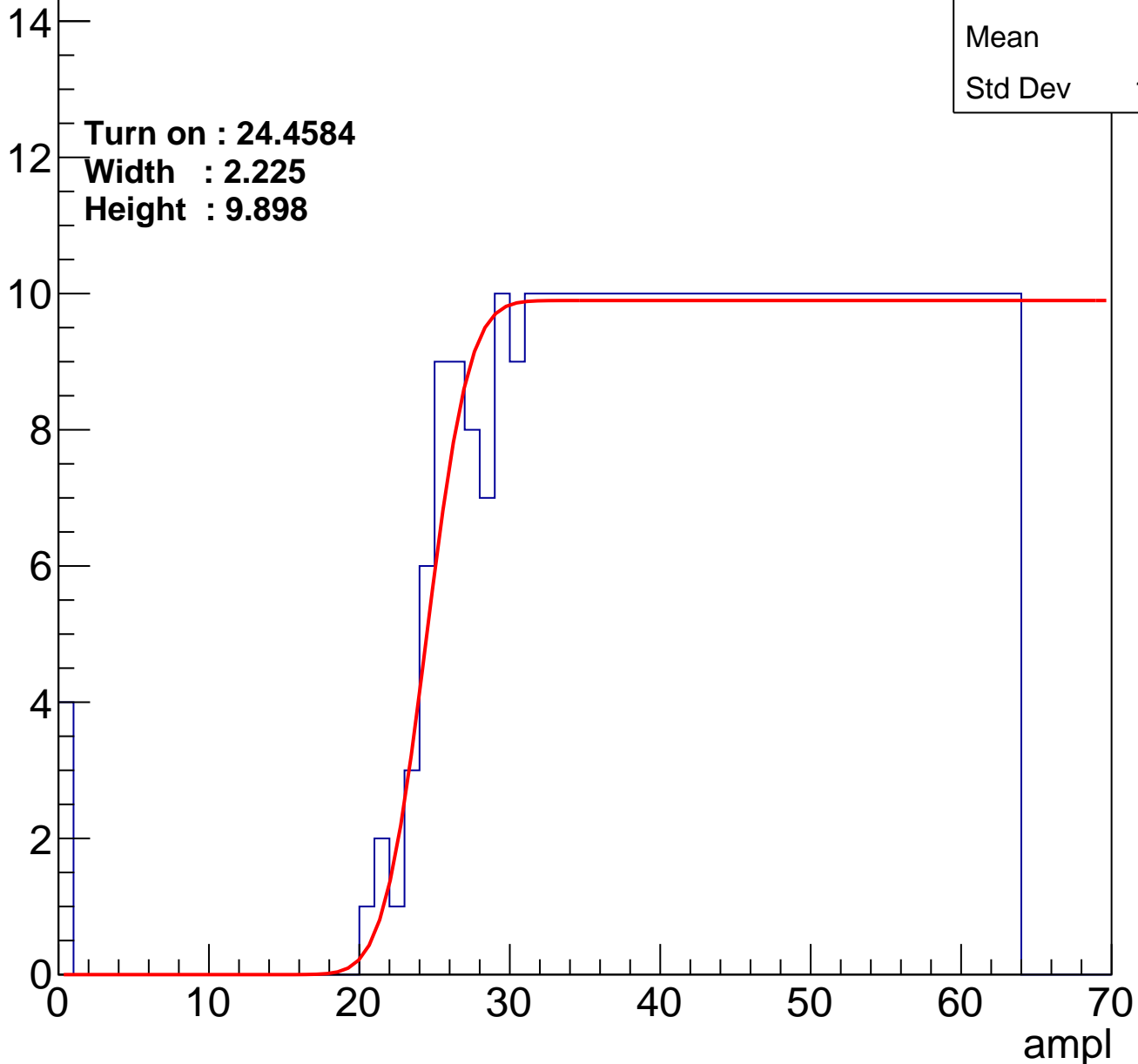
Entries	399
Mean	43.2
Std Dev	12.31

Turn on : 24.4584

Width : 2.225

Height : 9.898

Entry



B1L102S, U14-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.82
Std Dev	12.75

Turn on : 24.3297

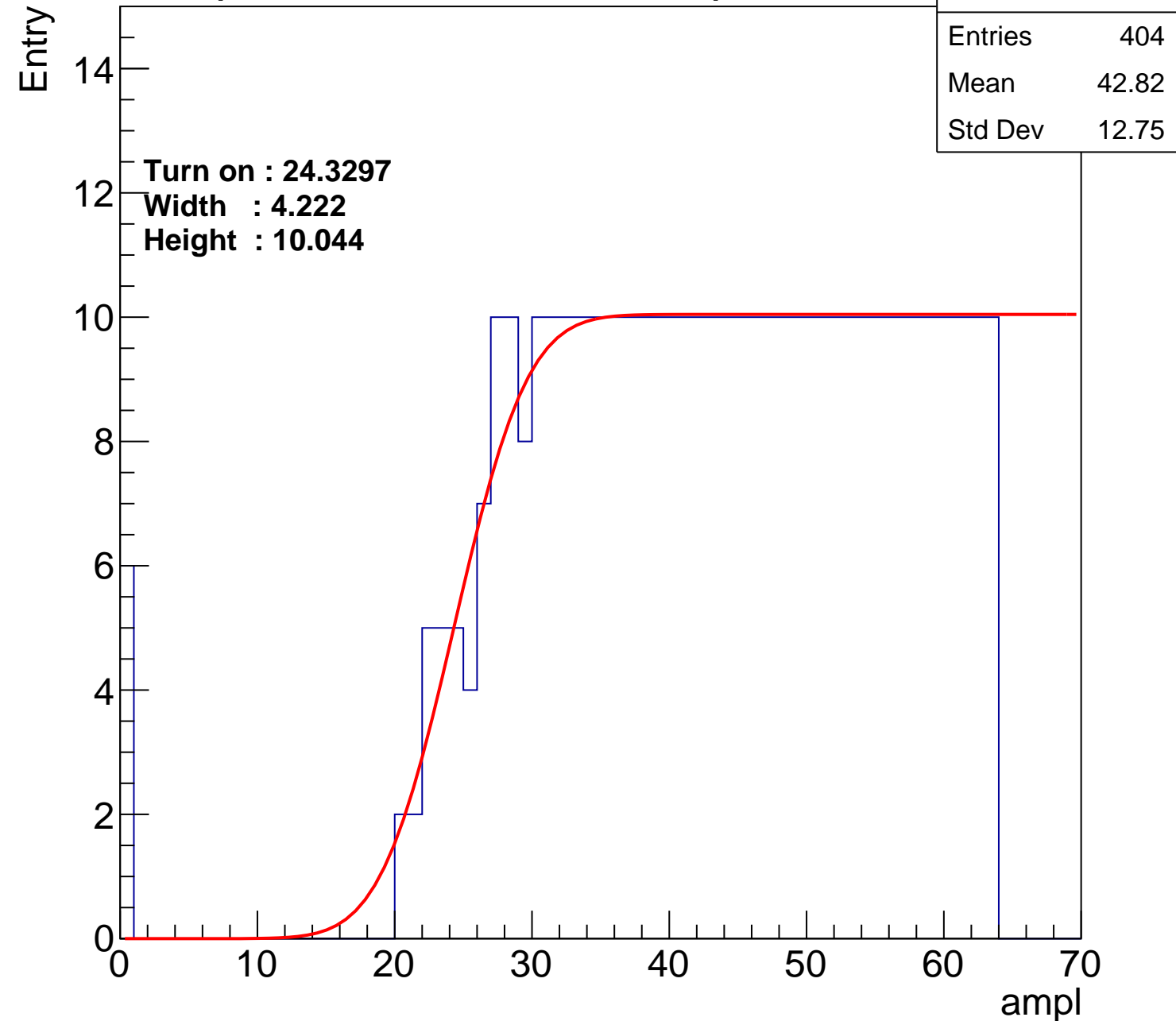
Width : 4.222

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.86
Std Dev	11.87

Turn on : 26.2827

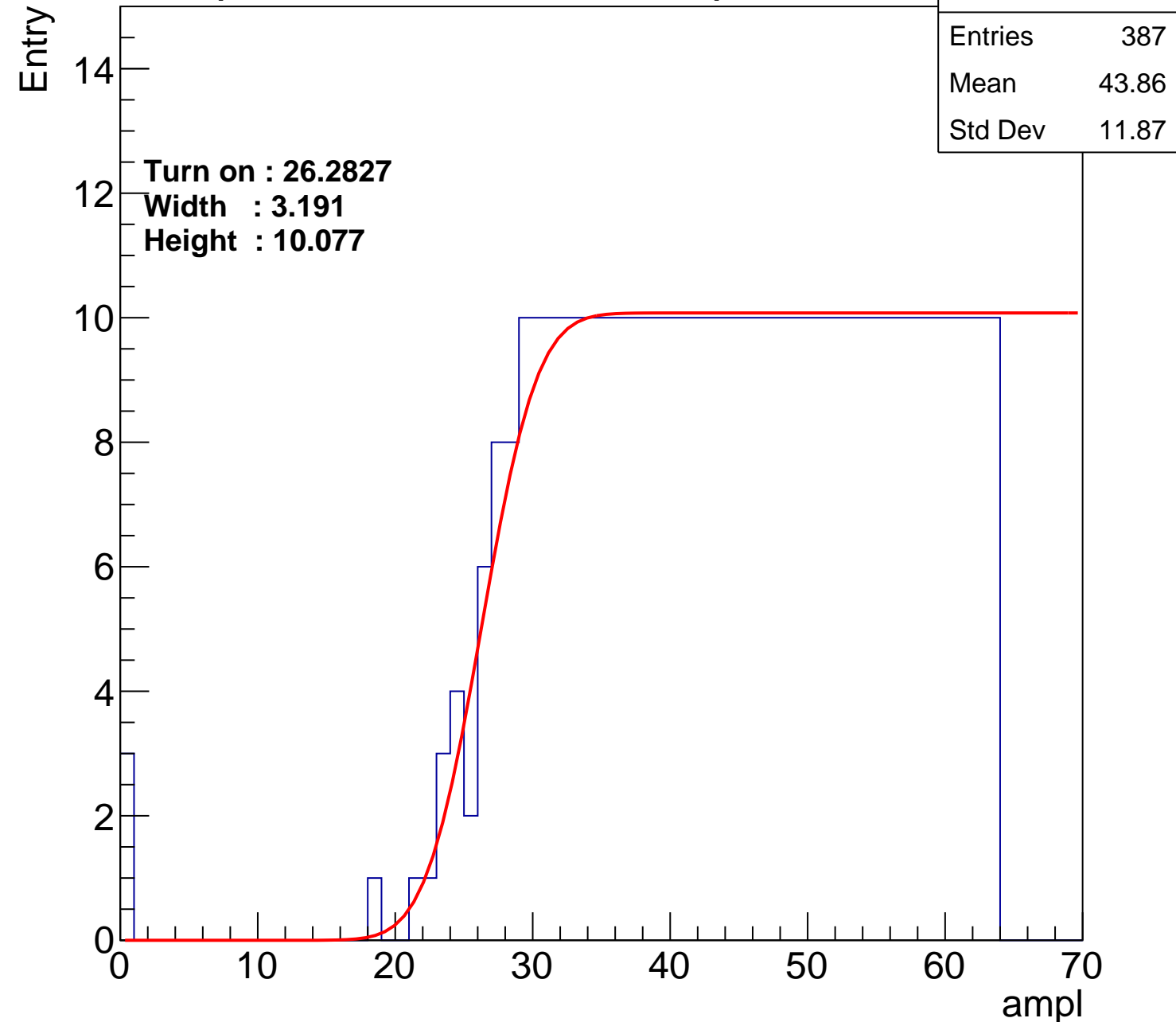
Width : 3.191

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch80

calib_packv5_042523_0143.root, FC#11, port A2

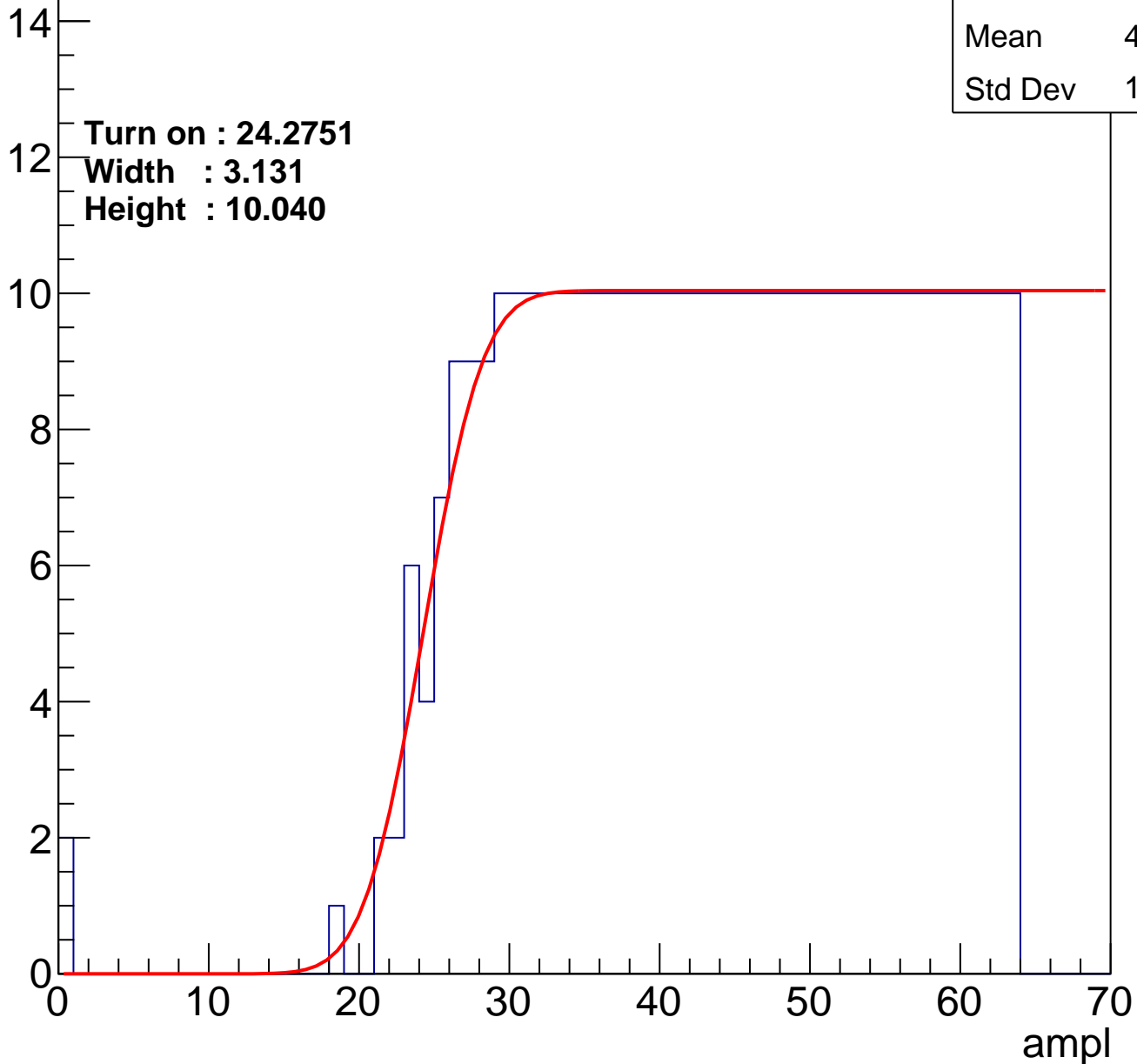
Entries	401
Mean	43.25
Std Dev	12.03

Turn on : 24.2751

Width : 3.131

Height : 10.040

Entry



B1L102S, U14-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.93
Std Dev	11.96

Turn on : 26.3565

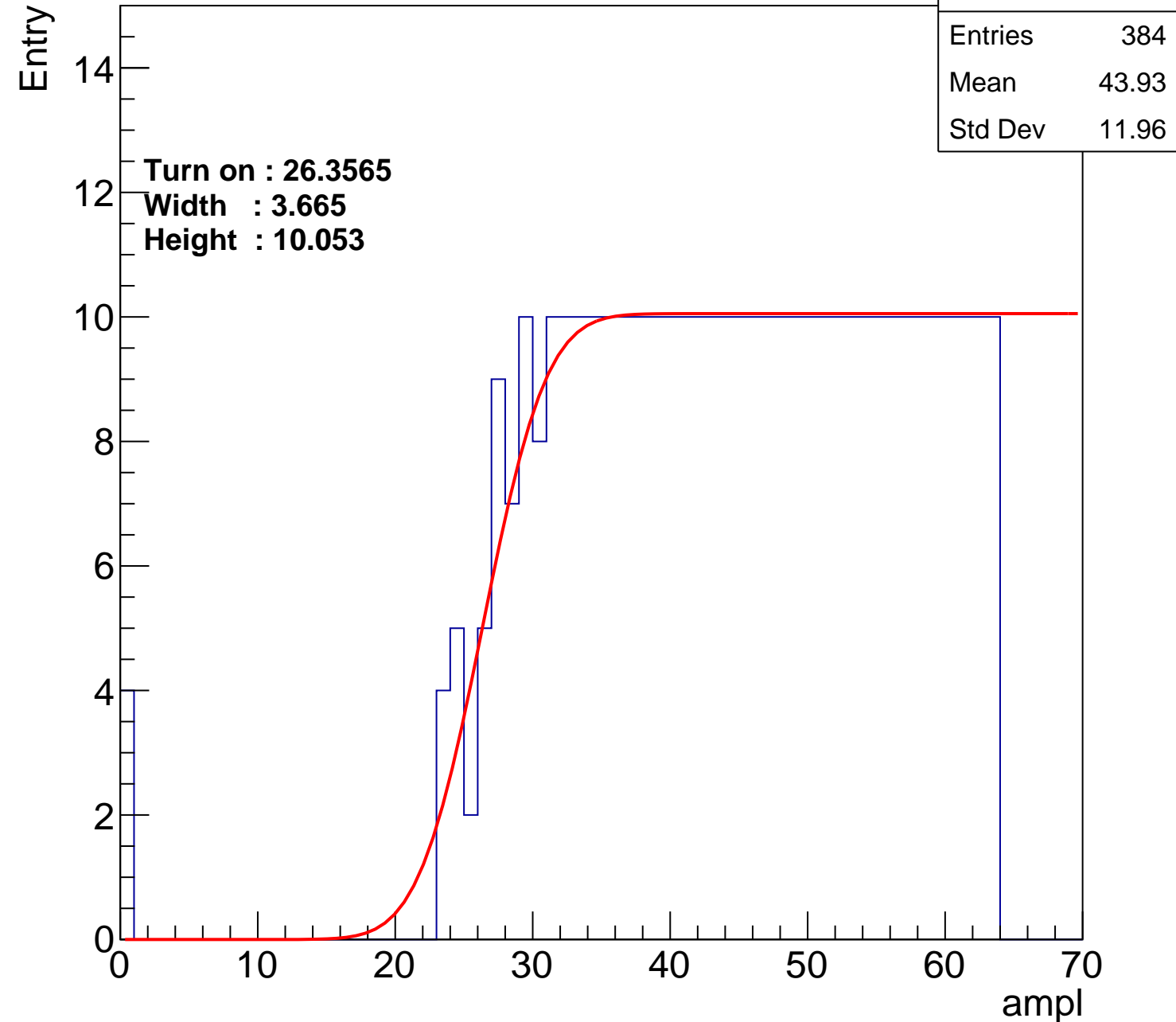
Width : 3.665

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	43.8
Std Dev	12.43

Turn on : 26.6702

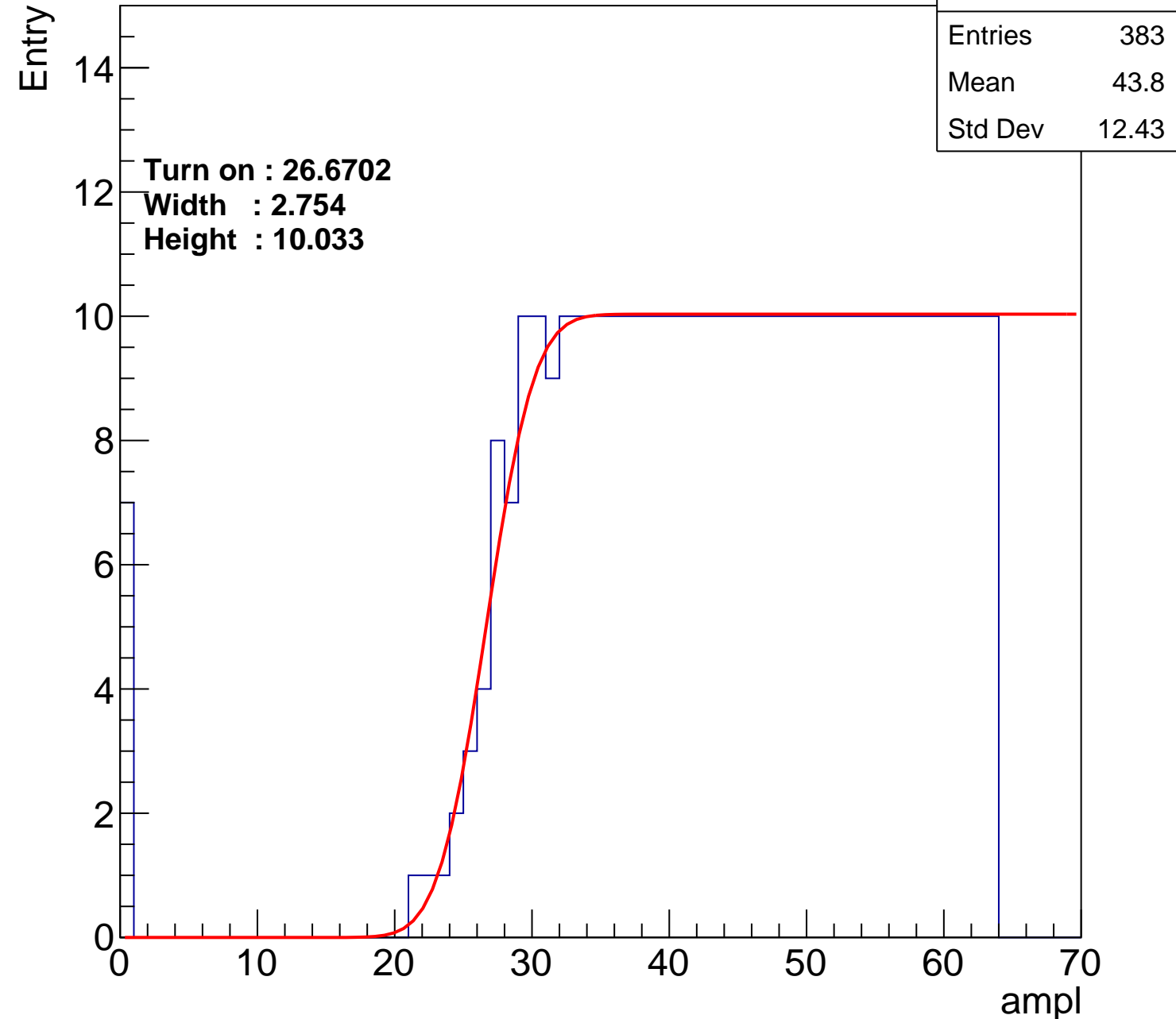
Width : 2.754

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch83

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 26.1931

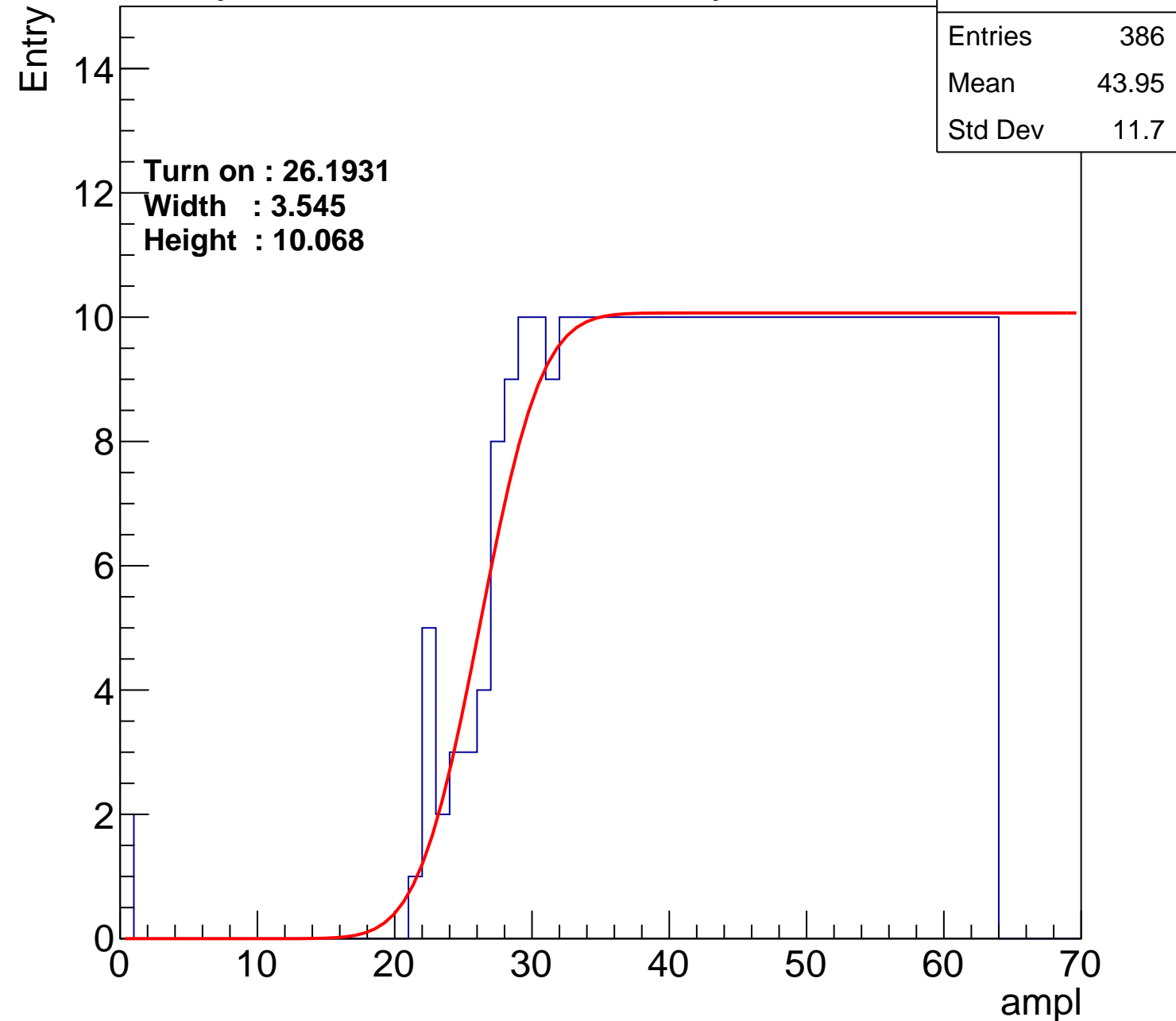
Width : 3.545

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.95
Std Dev	11.69

Turn on : 25.7817

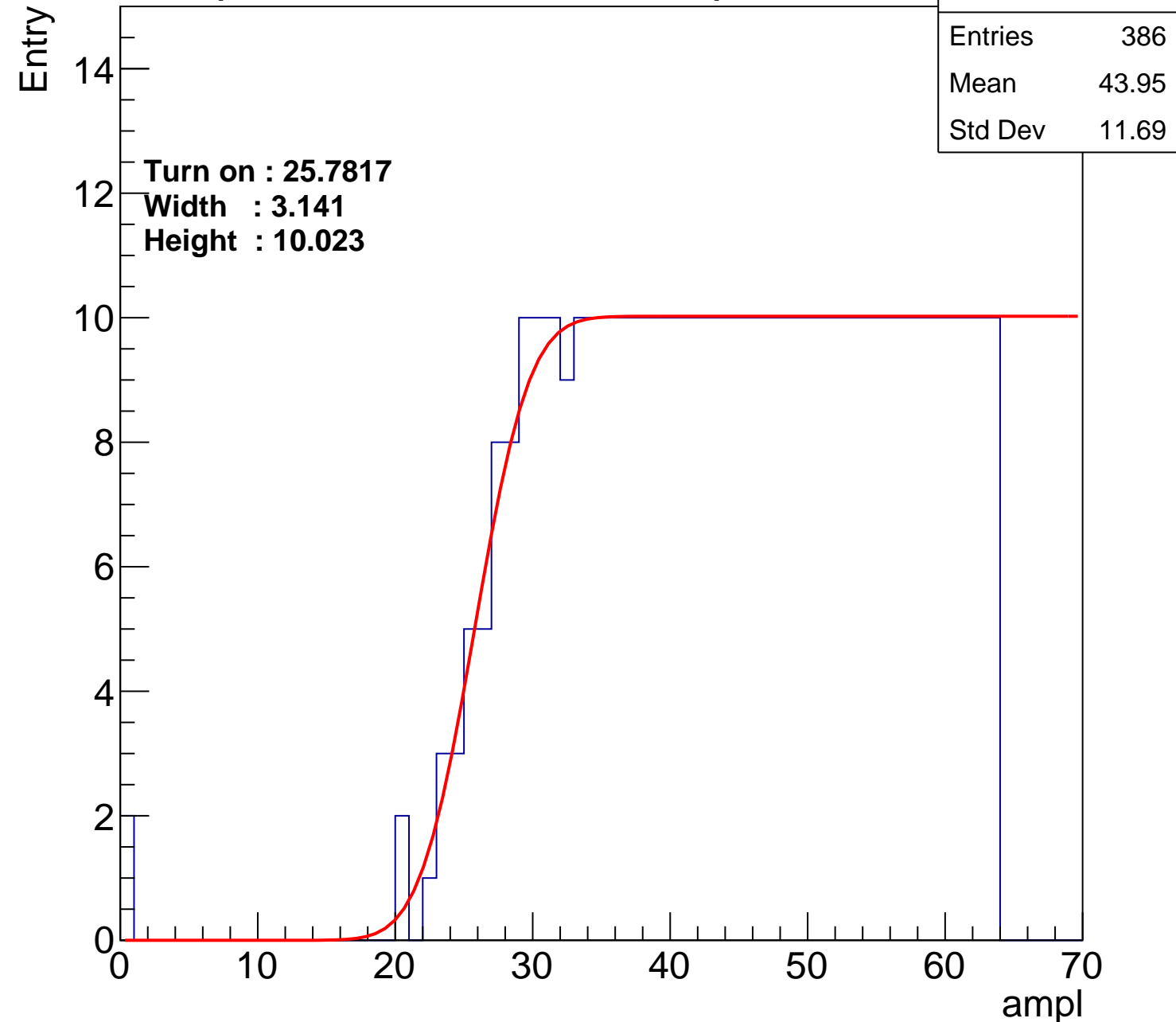
Width : 3.141

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch85

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.97
Std Dev	11.54

Turn on : 25.5806

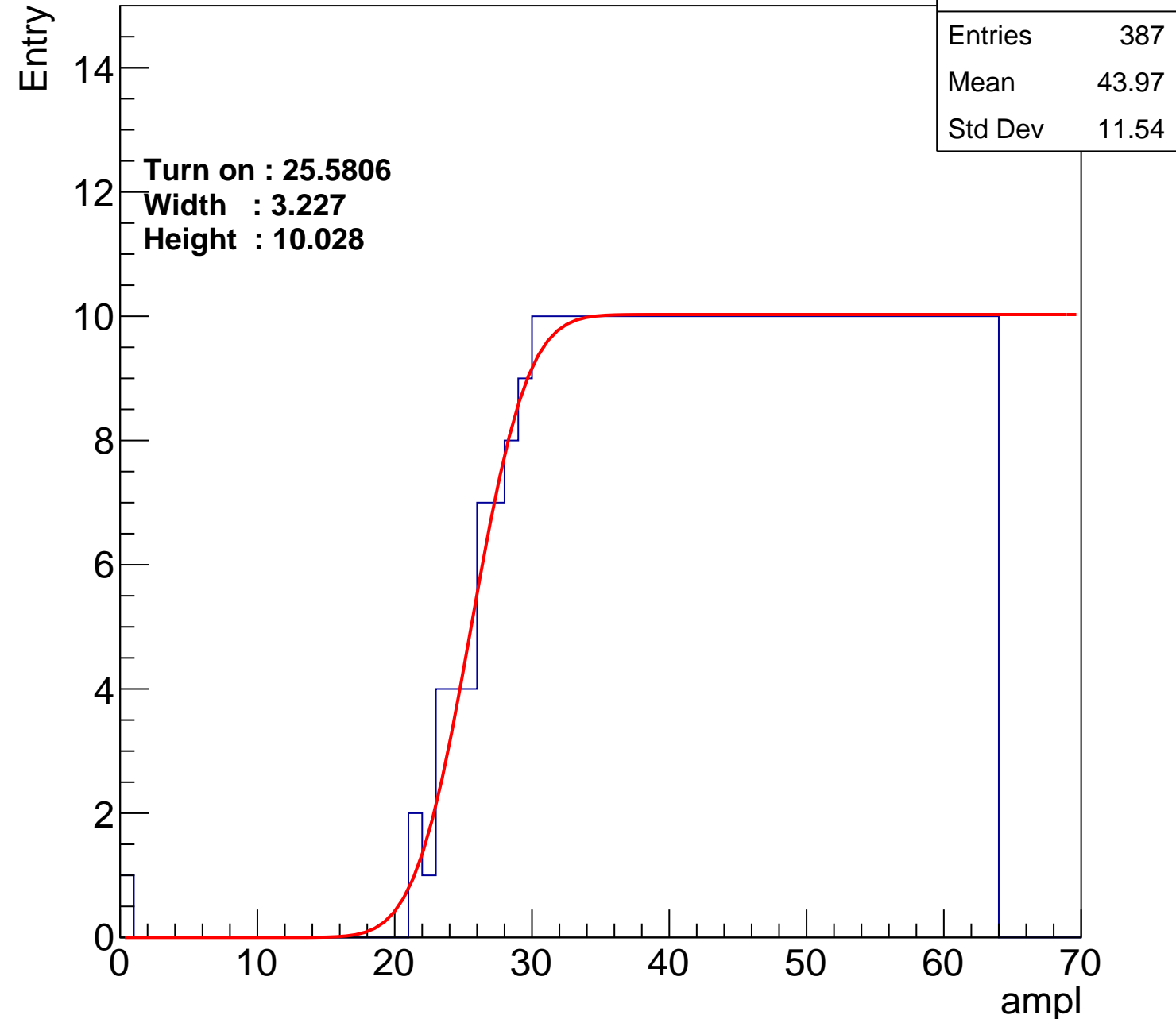
Width : 3.227

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.9586

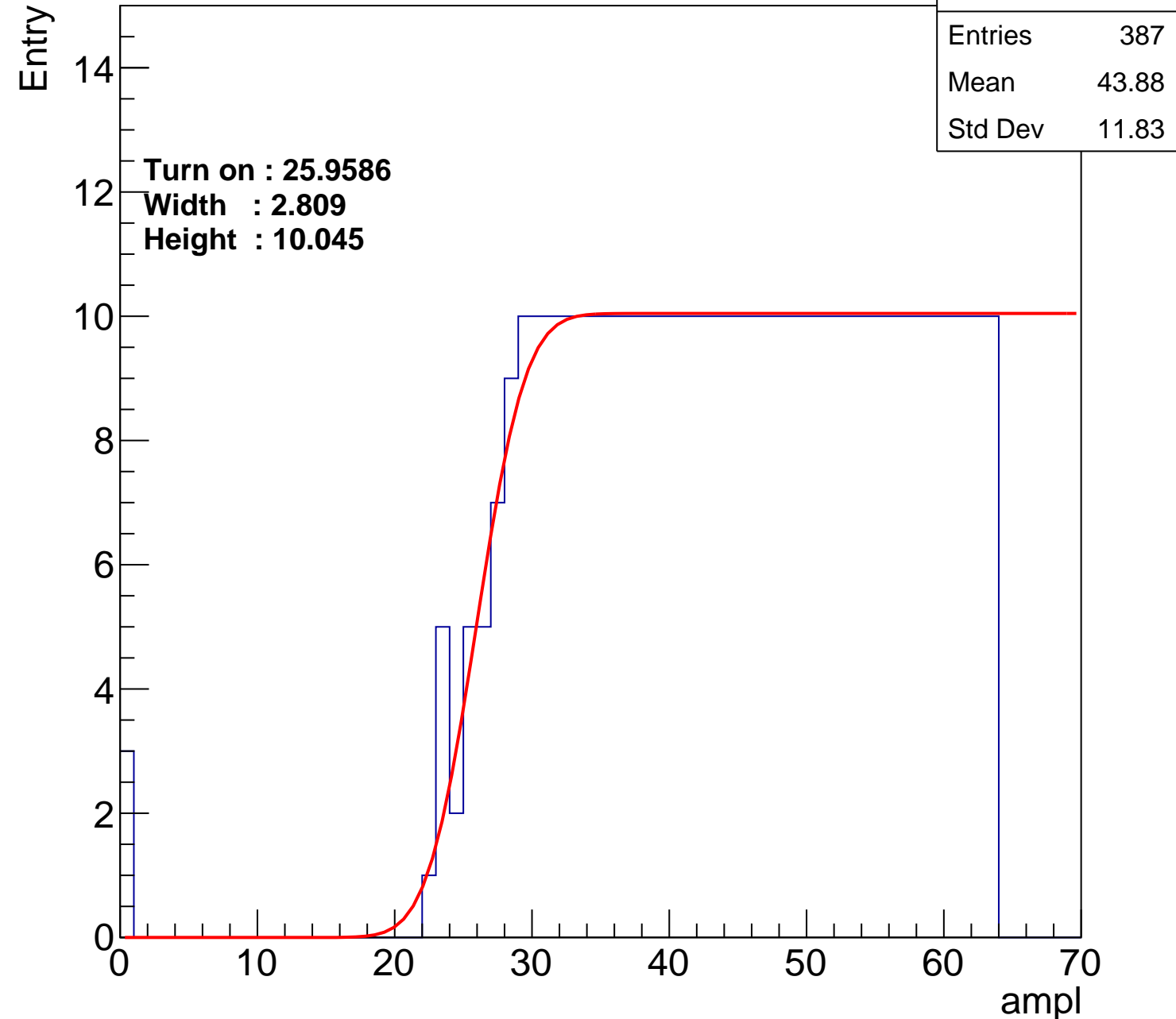
Width : 2.809

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch87

calib_packv5_042523_0143.root, FC#11, port A2

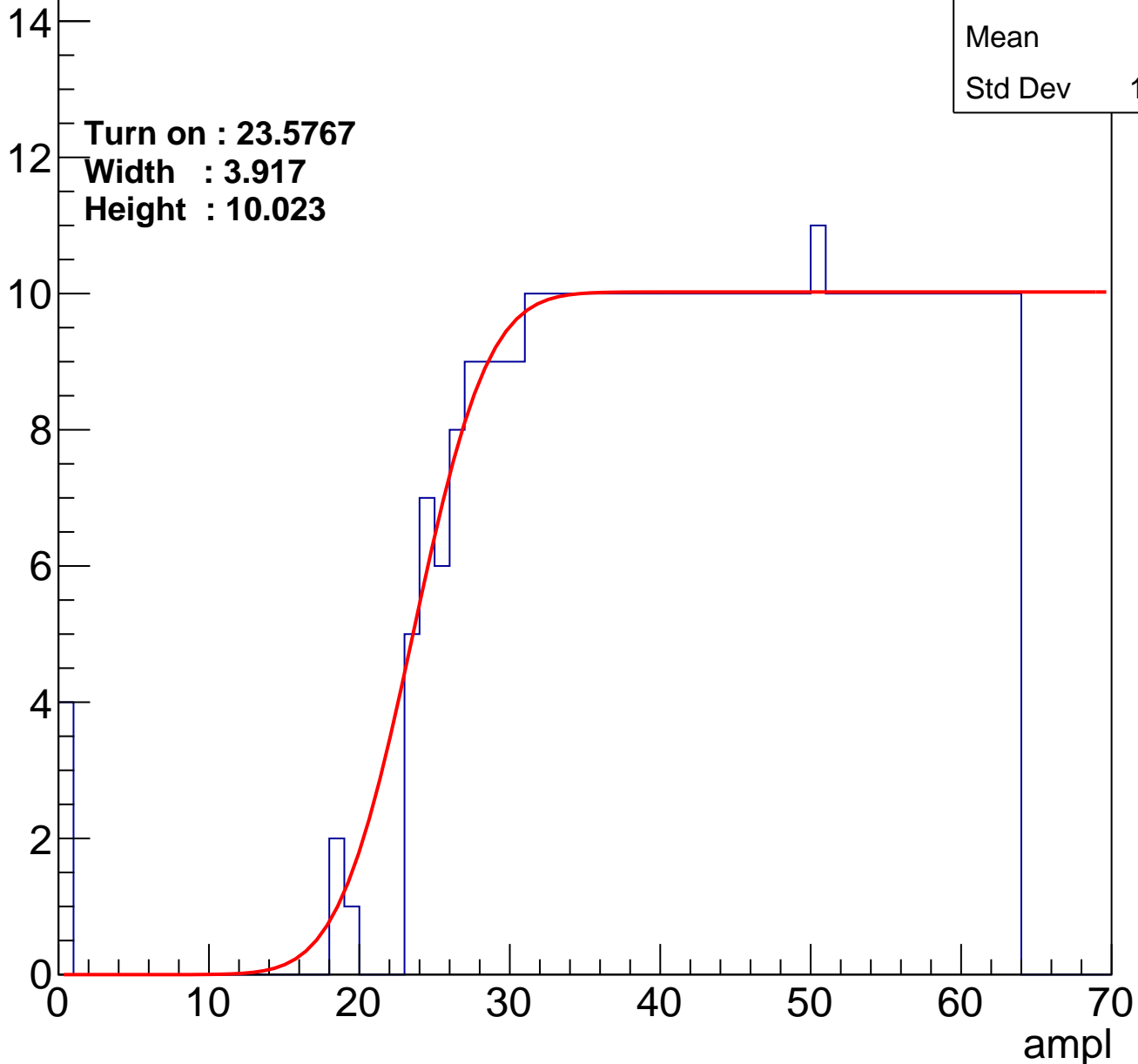
Entries	400
Mean	43.2
Std Dev	12.33

Turn on : 23.5767

Width : 3.917

Height : 10.023

Entry



B1L102S, U14-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.32
Std Dev	12.29

Turn on : 25.1797

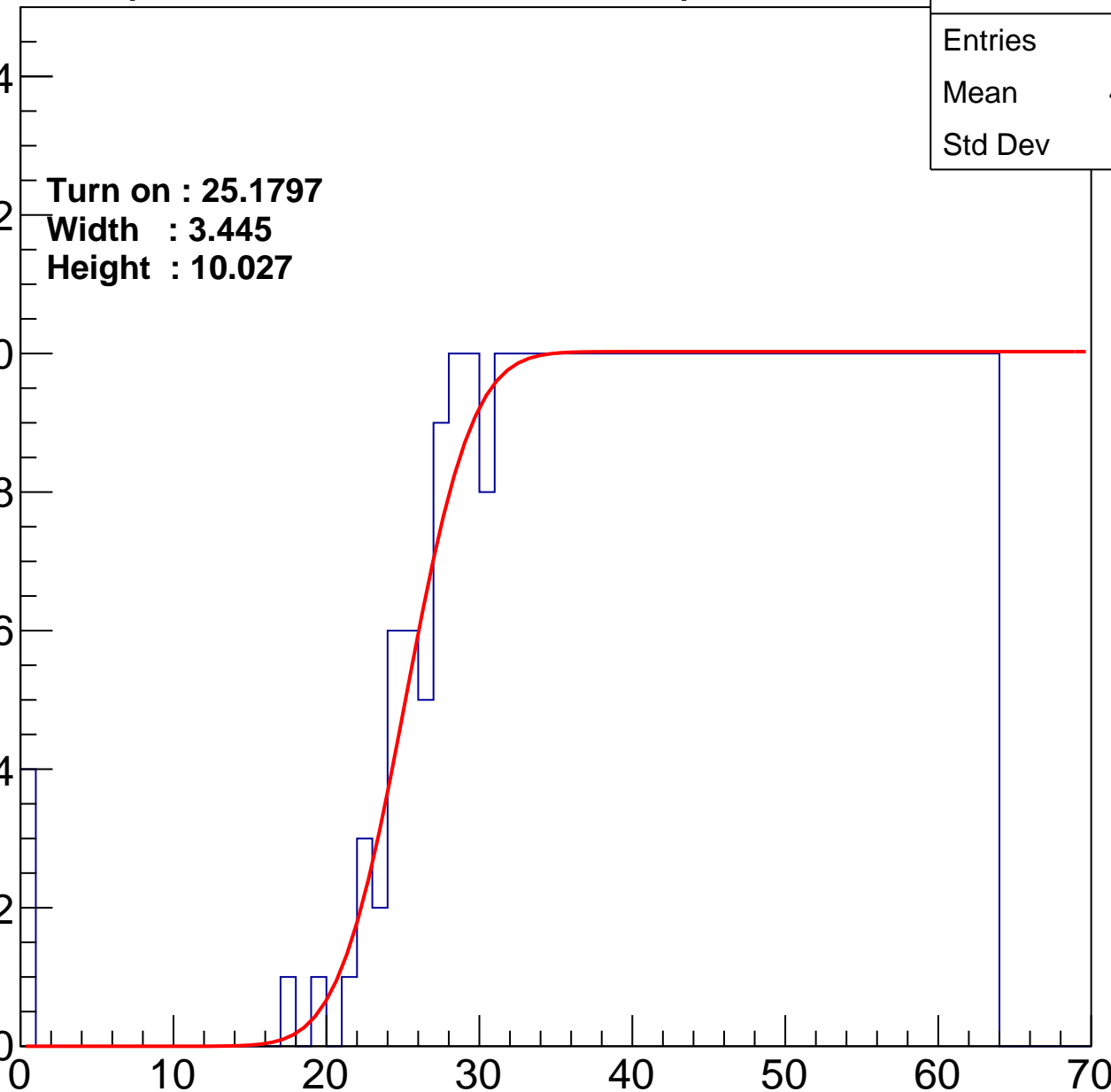
Width : 3.445

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch89

calib_packv5_042523_0143.root, FC#11, port A2

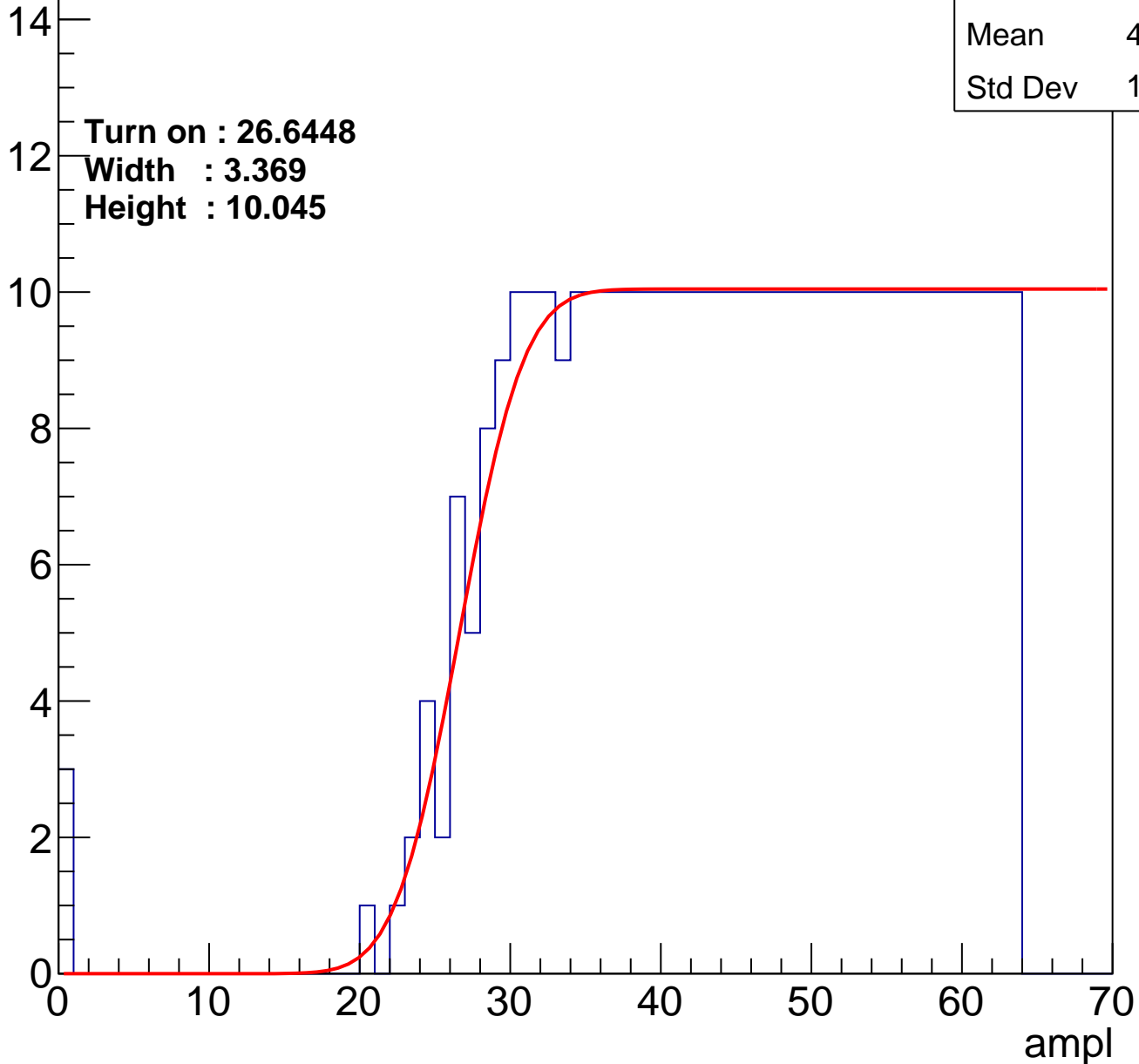
Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 26.6448

Width : 3.369

Height : 10.045

Entry



B1L102S, U14-ch90

calib_packv5_042523_0143.root, FC#11, port A2

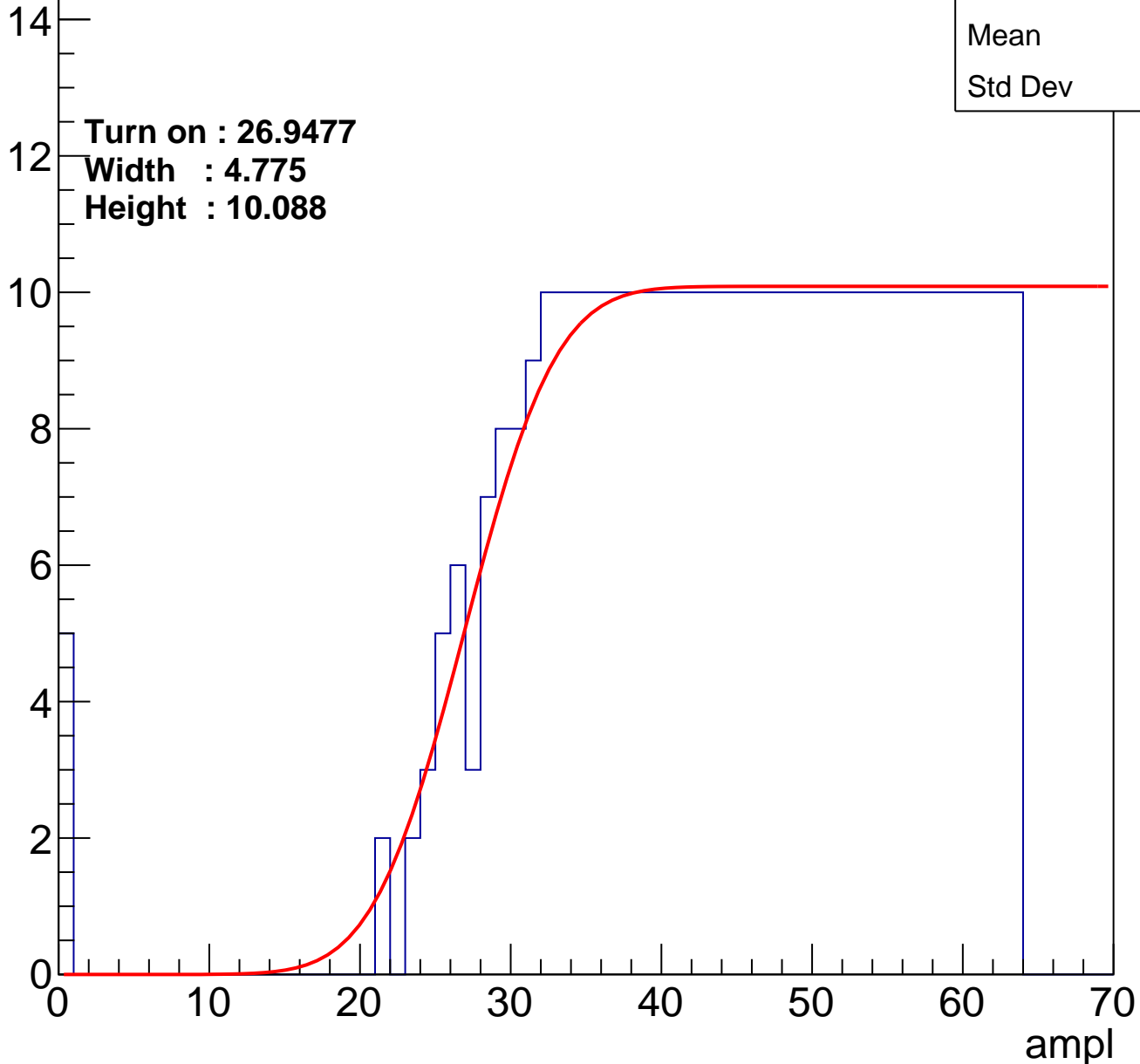
Entries	378
Mean	44.1
Std Dev	12.1

Turn on : 26.9477

Width : 4.775

Height : 10.088

Entry



B1L102S, U14-ch91

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.44
Std Dev	11.63

Turn on : 27.0820

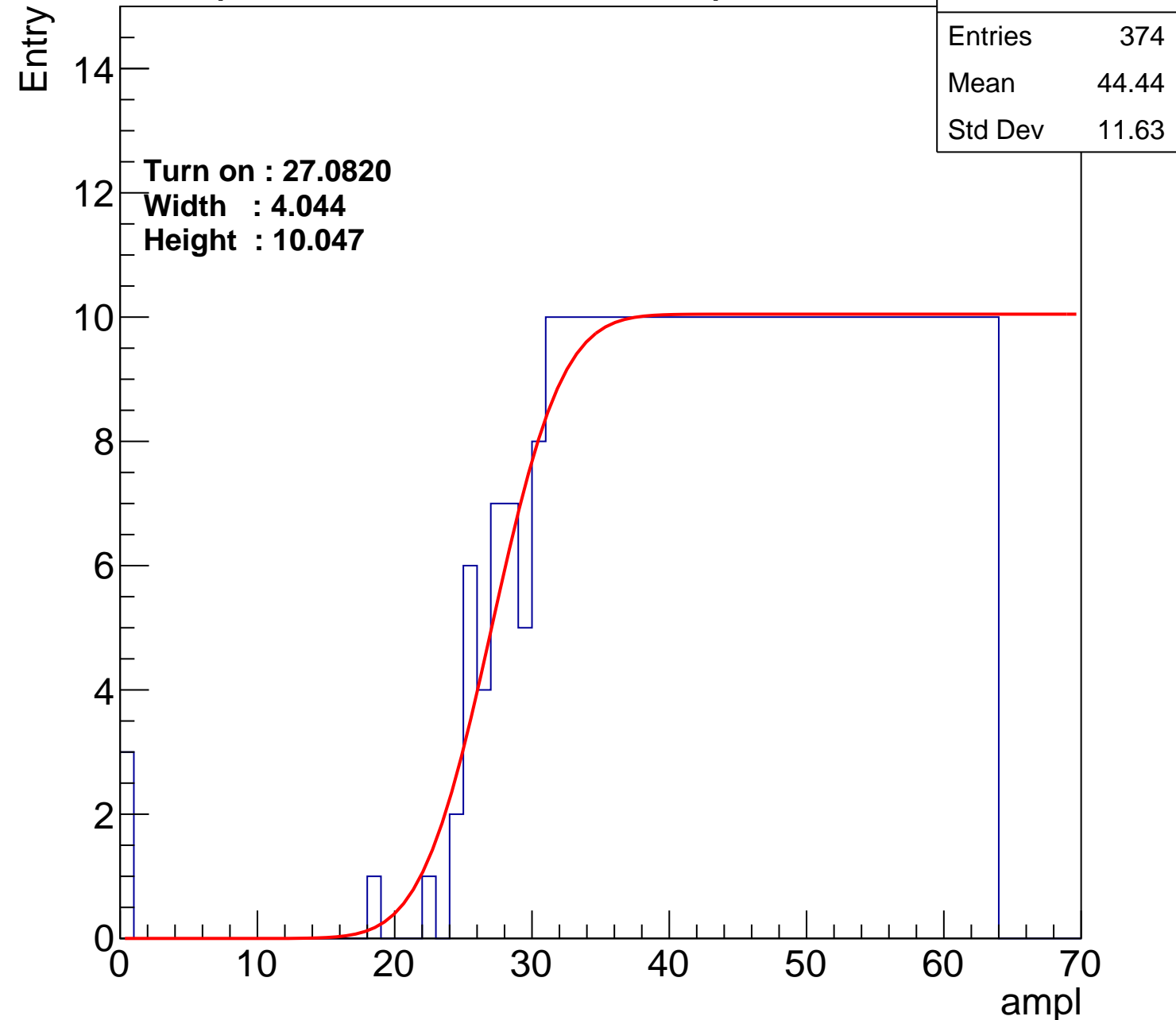
Width : 4.044

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch92

calib_packv5_042523_0143.root, FC#11, port A2

Entries	424
Mean	41.99
Std Dev	12.91

Turn on : 22.4984

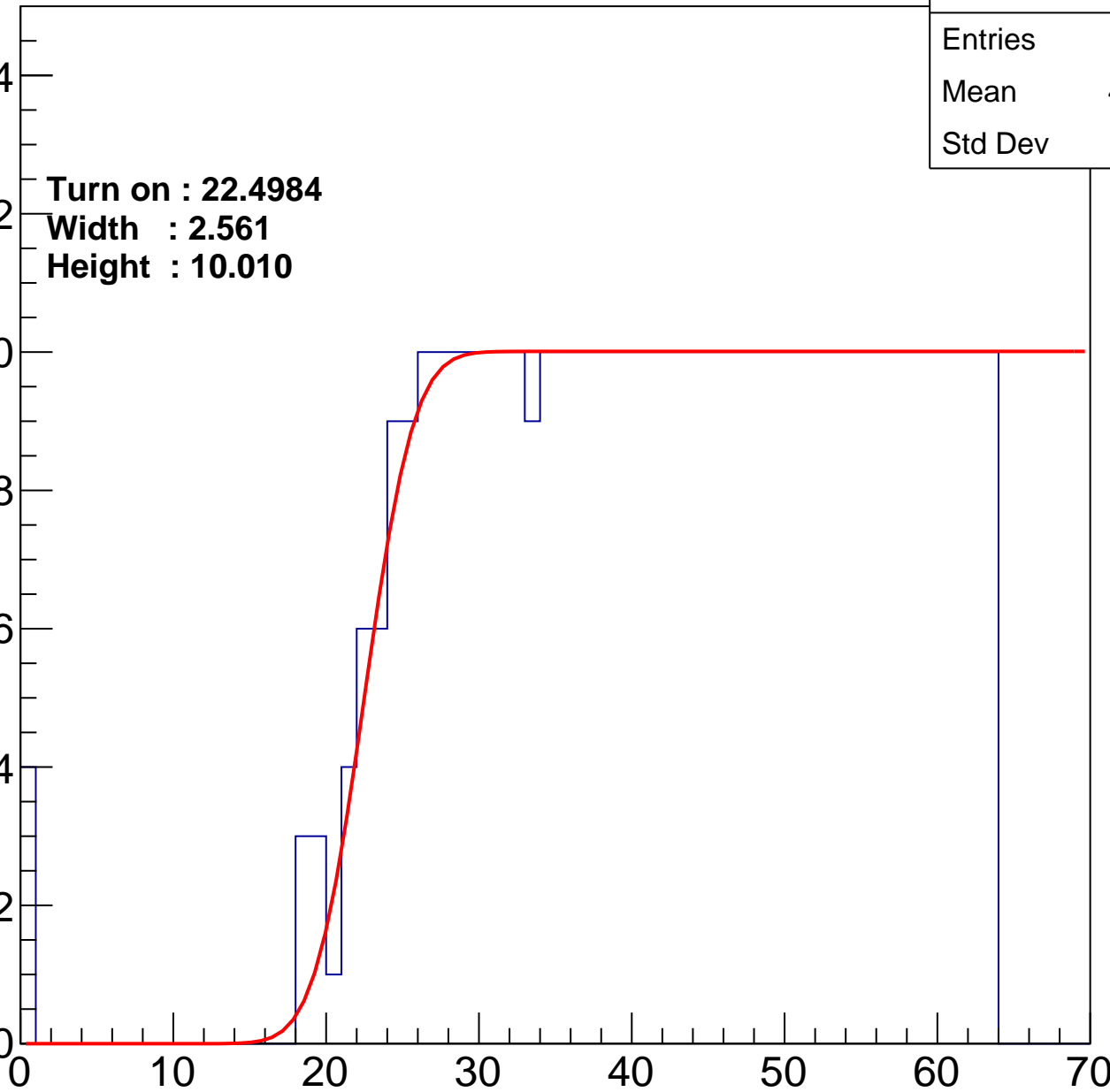
Width : 2.561

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.14
Std Dev	11.79

Turn on : 26.2688

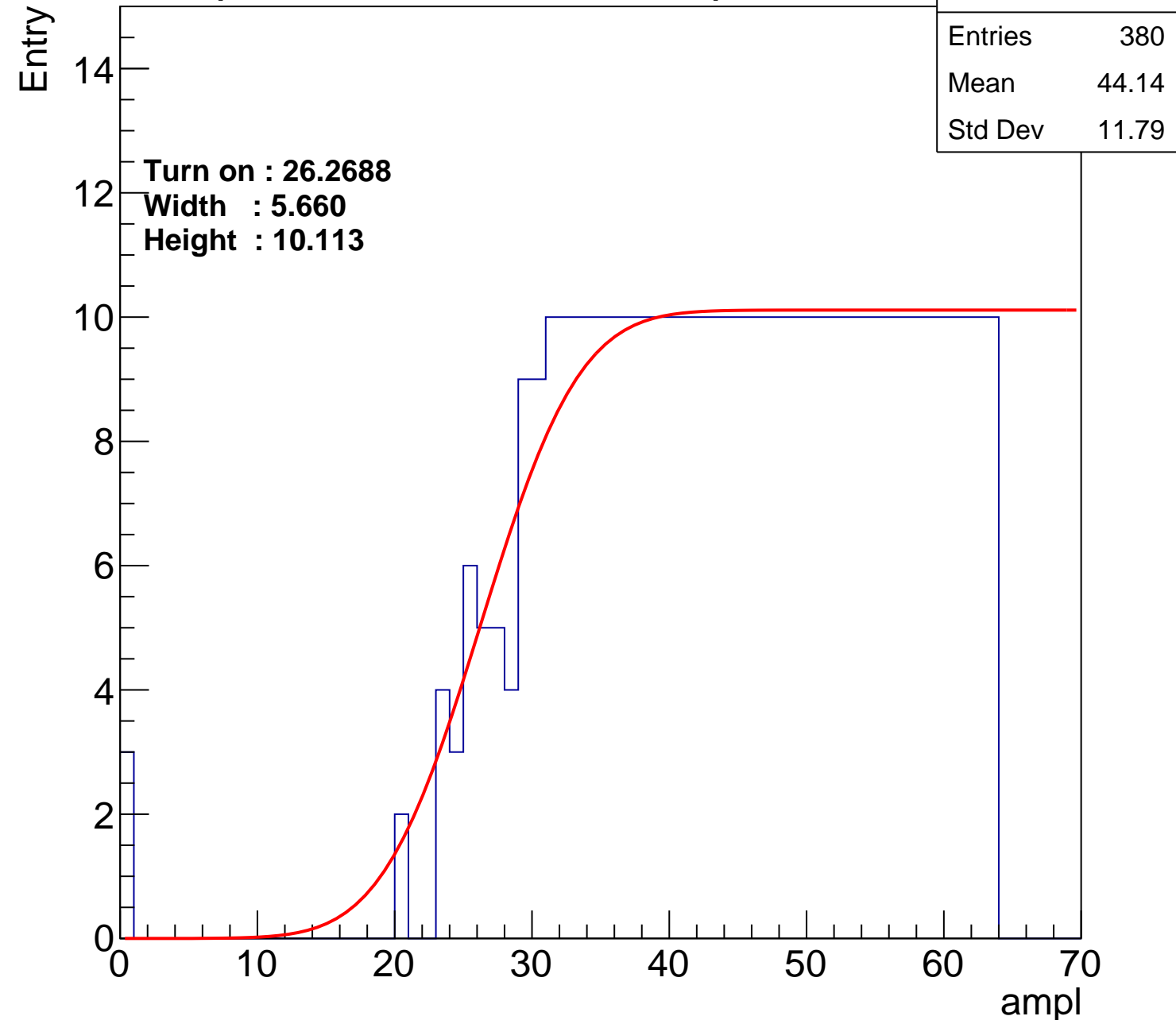
Width : 5.660

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch94

calib_packv5_042523_0143.root, FC#11, port A2

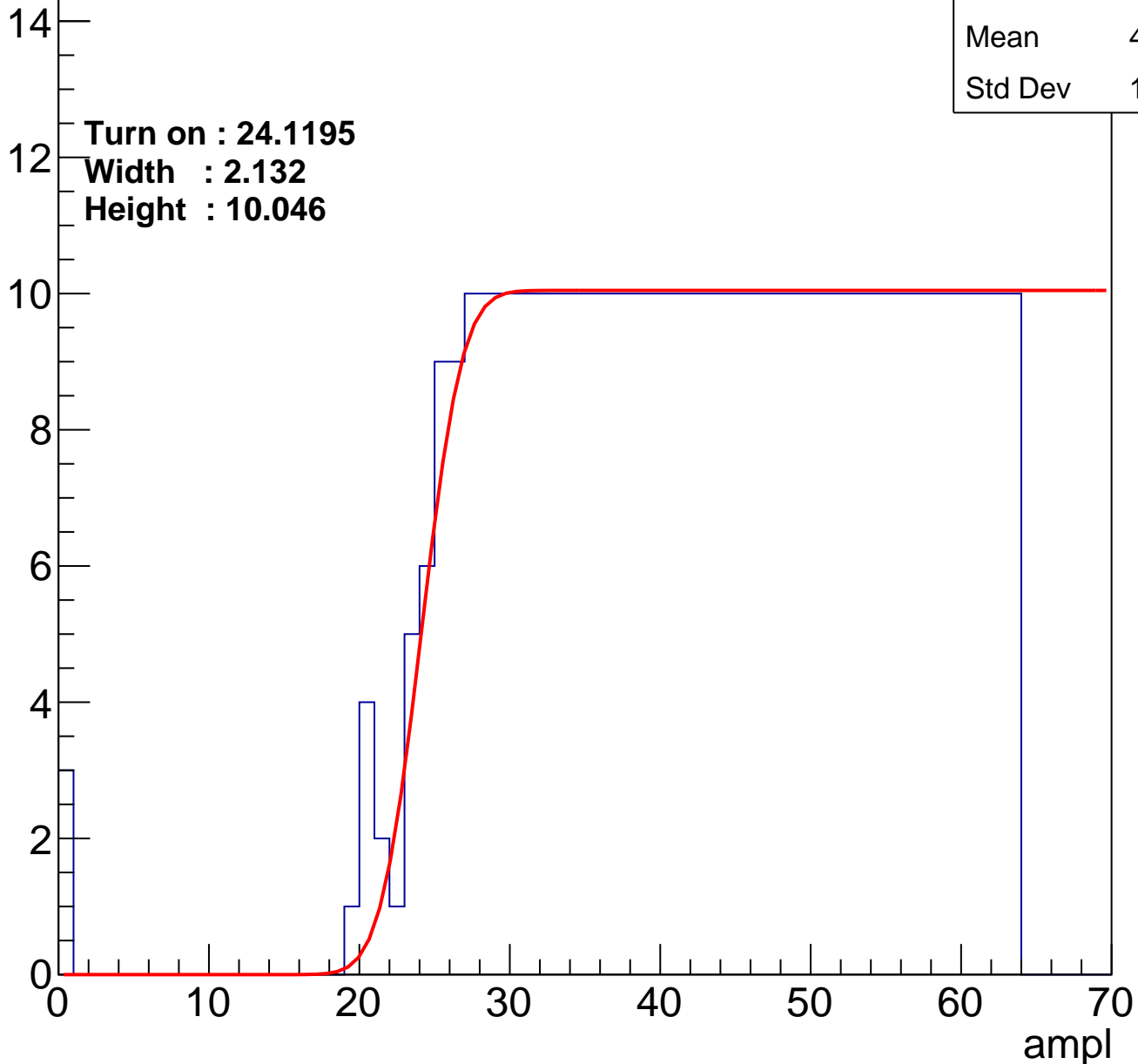
Entries	410
Mean	42.76
Std Dev	12.39

Turn on : 24.1195

Width : 2.132

Height : 10.046

Entry



B1L102S, U14-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.71
Std Dev	11.66

Turn on : 28.7187

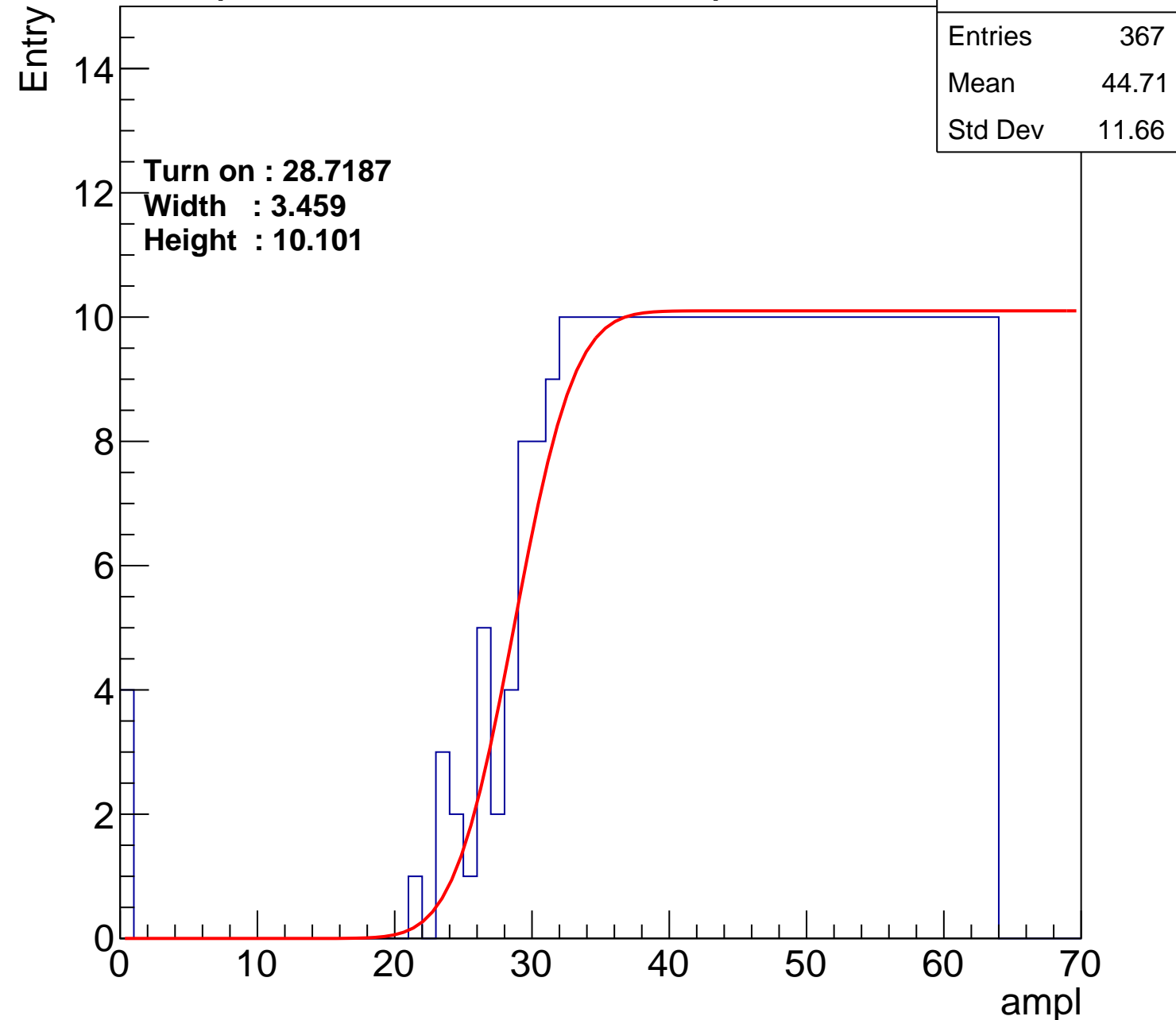
Width : 3.459

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch96

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.42
Std Dev	12.14

Turn on : 25.1765

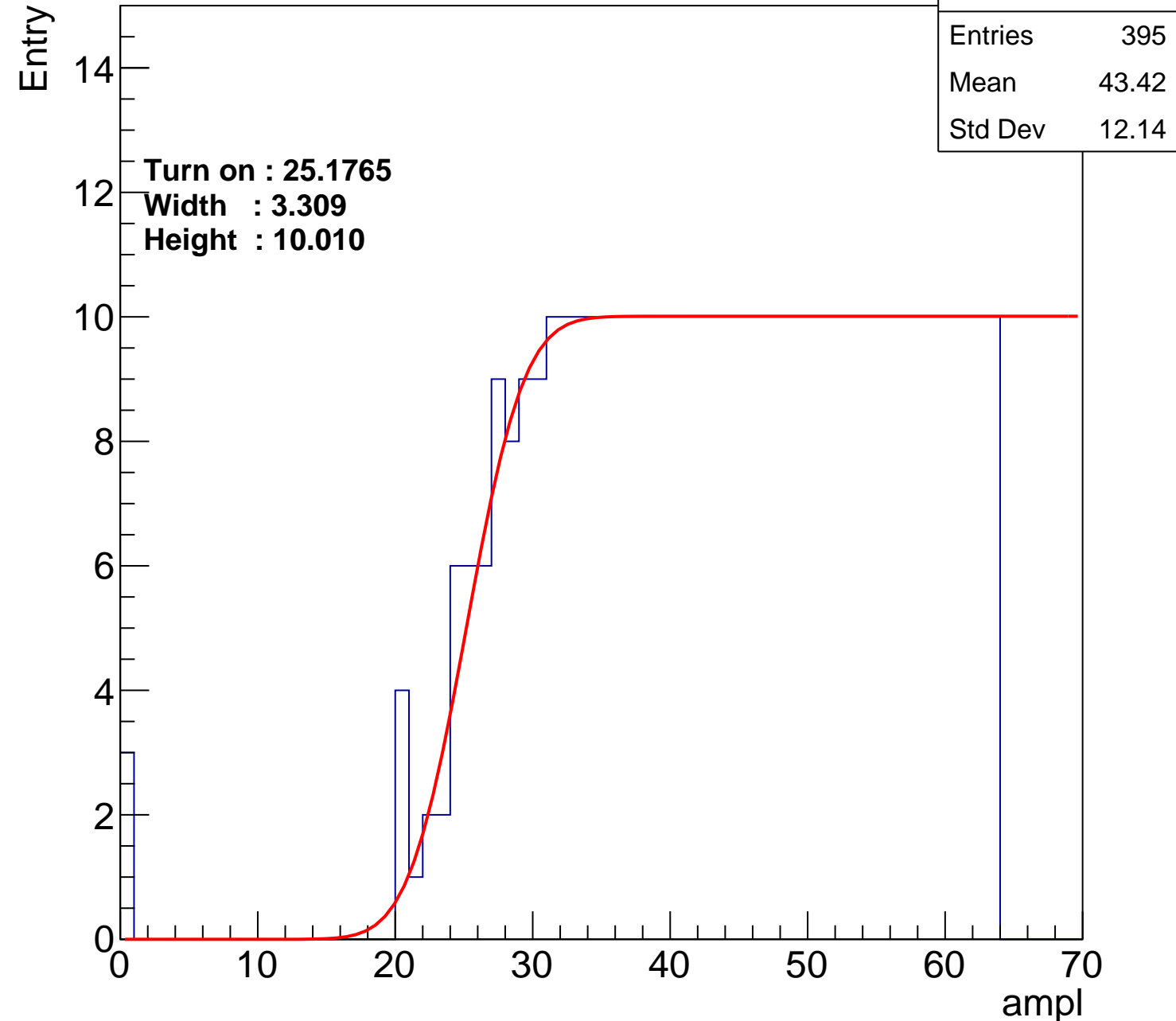
Width : 3.309

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.57
Std Dev	12.32

Turn on : 25.6747

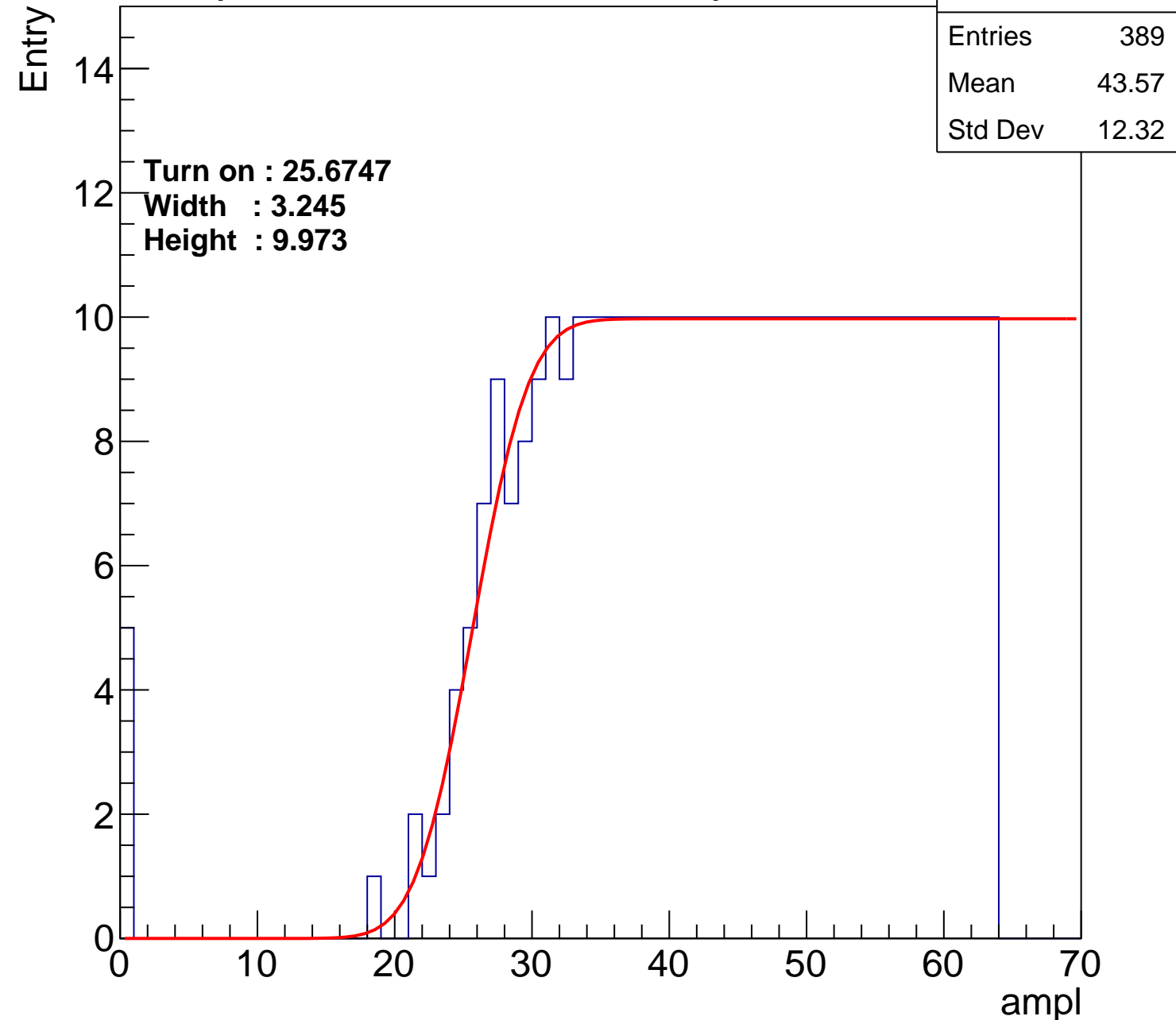
Width : 3.245

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.28
Std Dev	11.54

Turn on : 26.3031

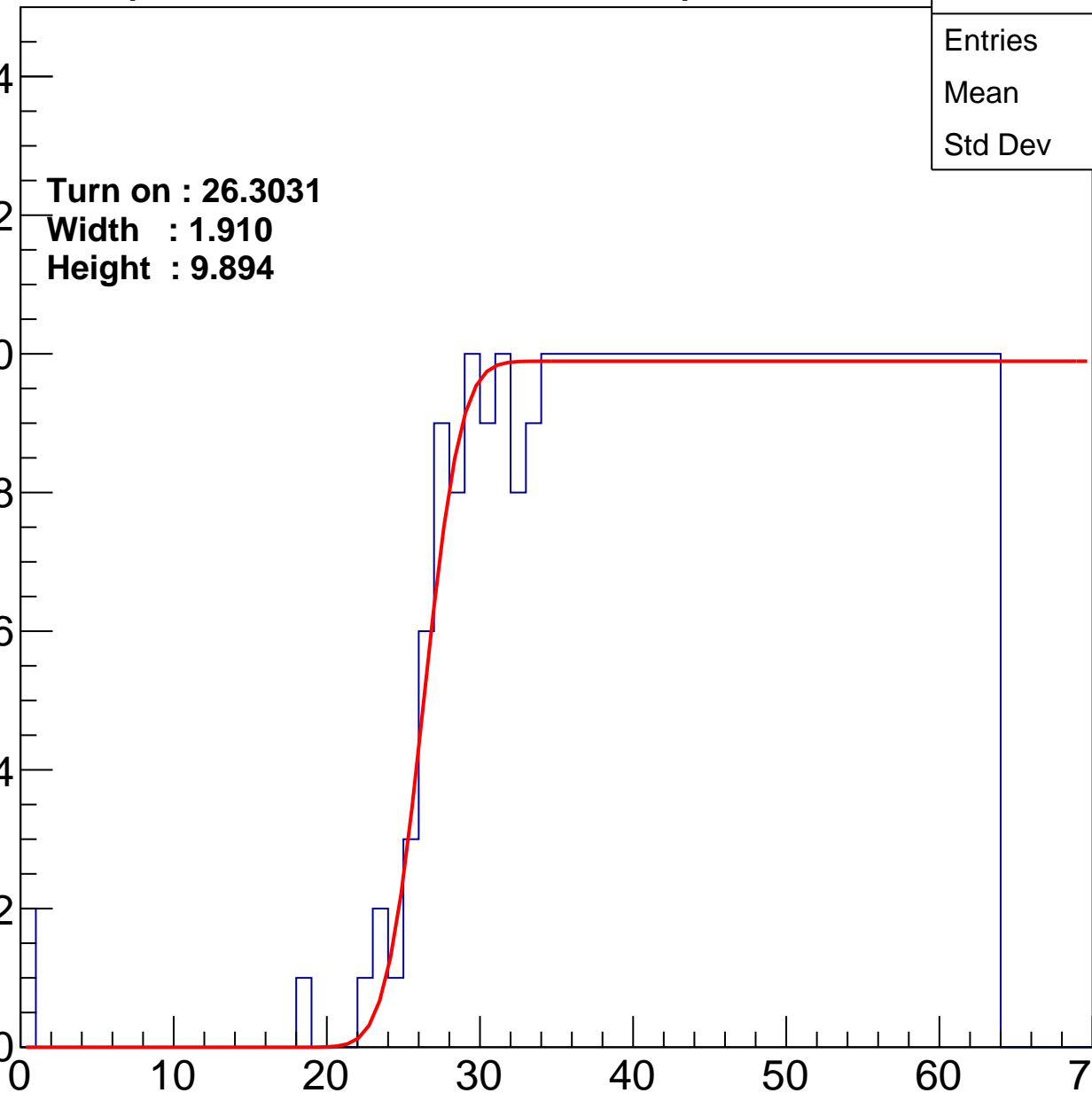
Width : 1.910

Height : 9.894

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.59
Std Dev	12.01

Turn on : 25.2943

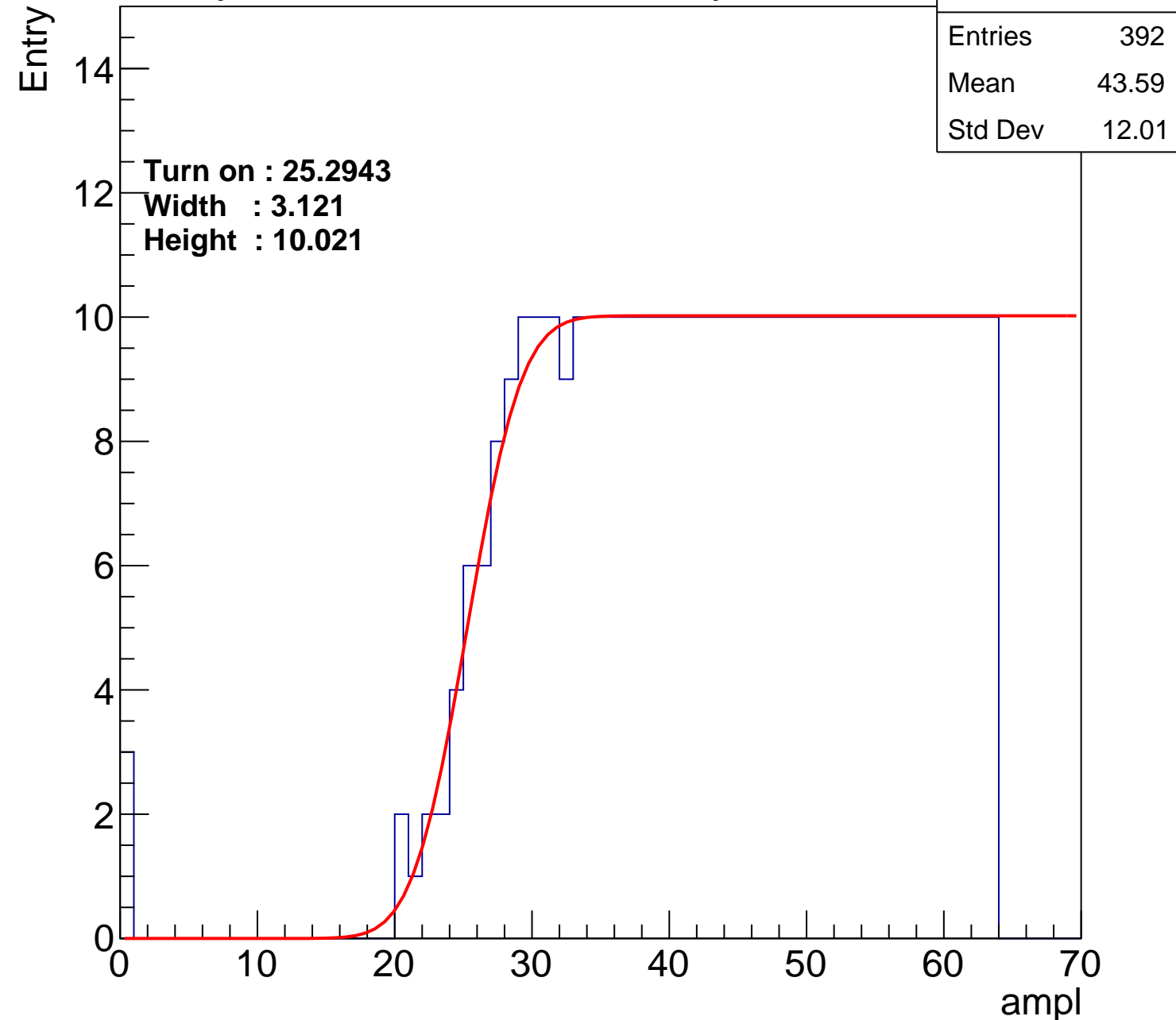
Width : 3.121

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch100

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.68
Std Dev	12.22

Turn on : 25.9616

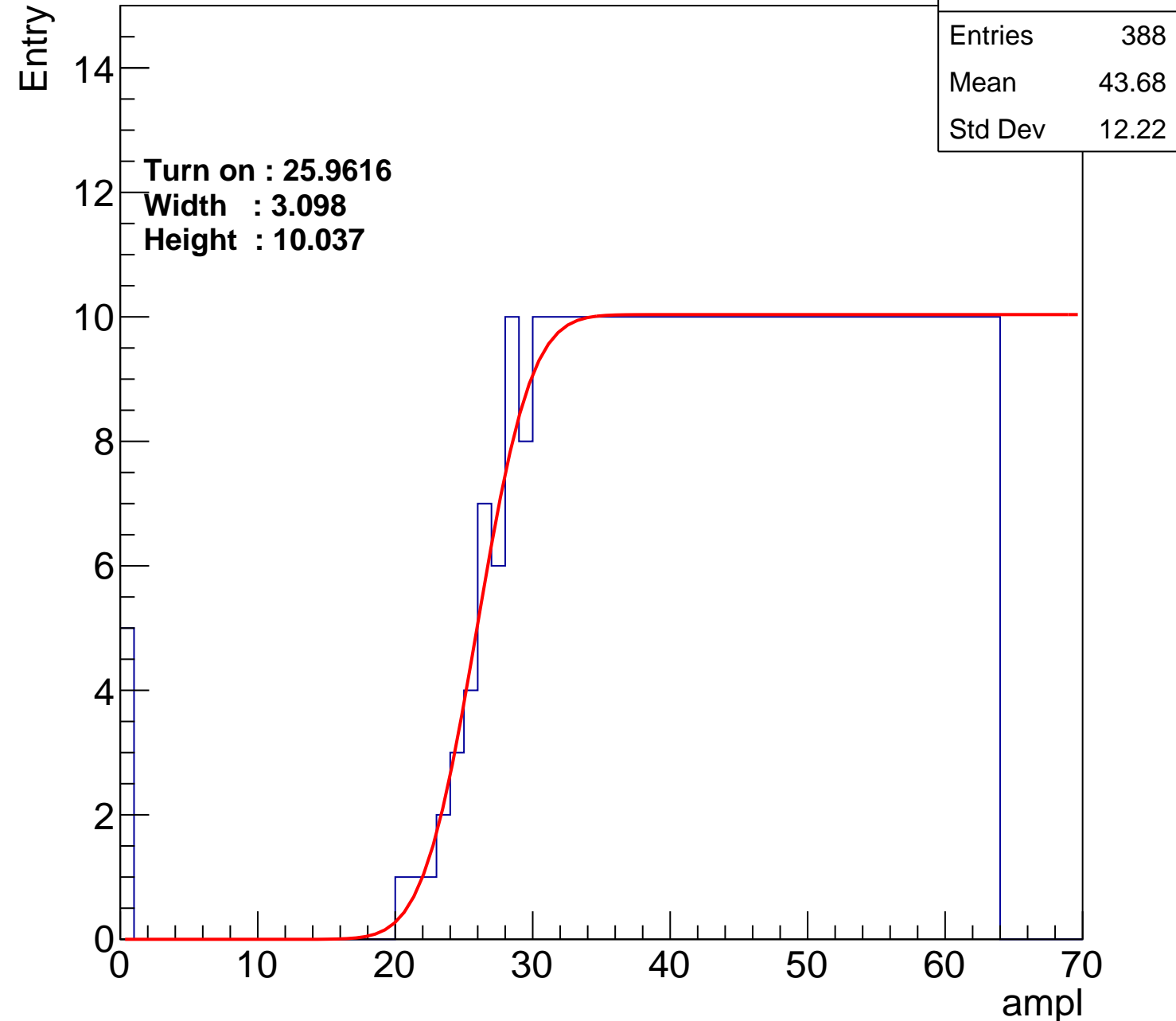
Width : 3.098

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.64
Std Dev	11.66

Turn on : 24.6988

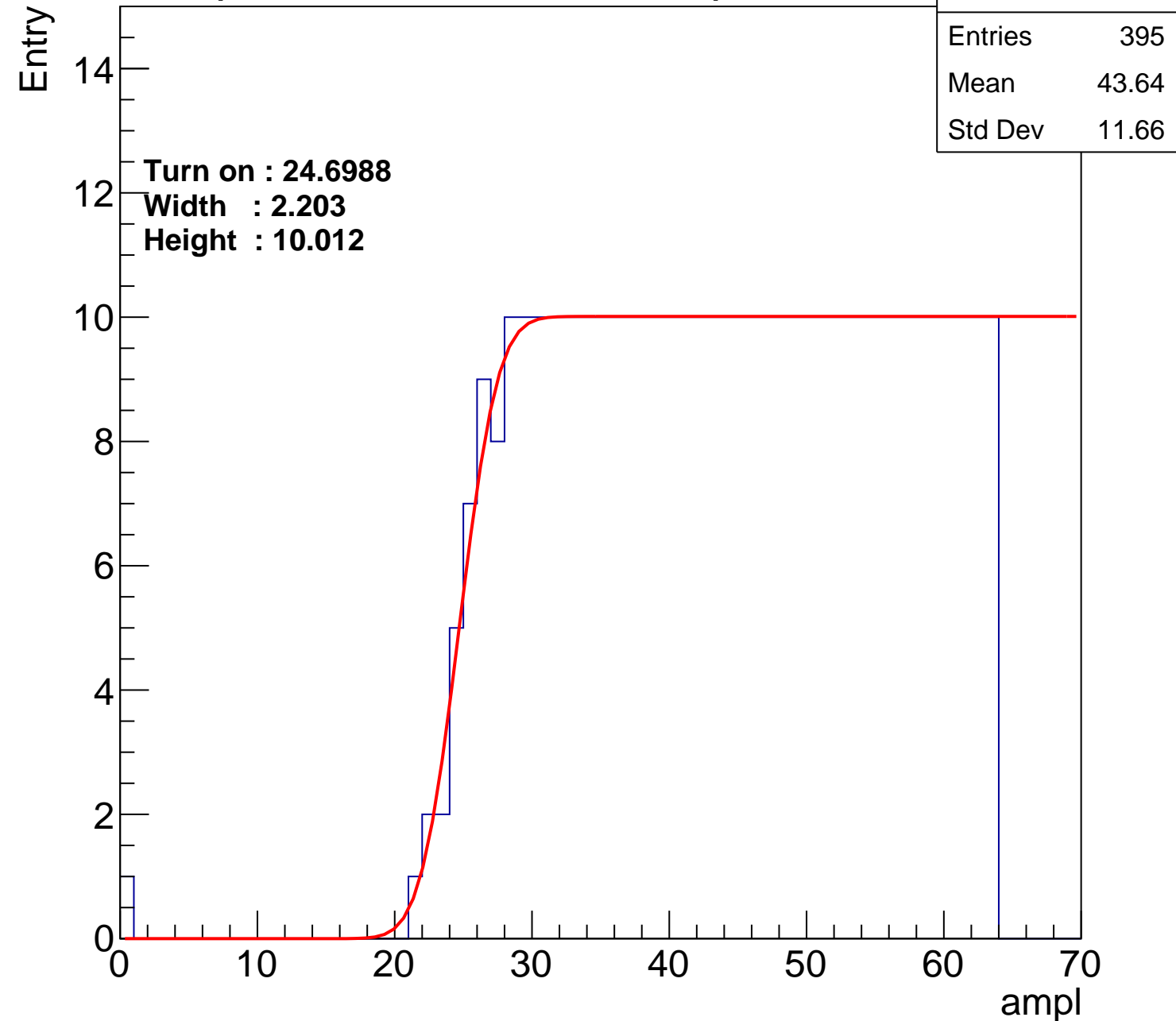
Width : 2.203

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.35
Std Dev	12.04

Turn on : 25.2687

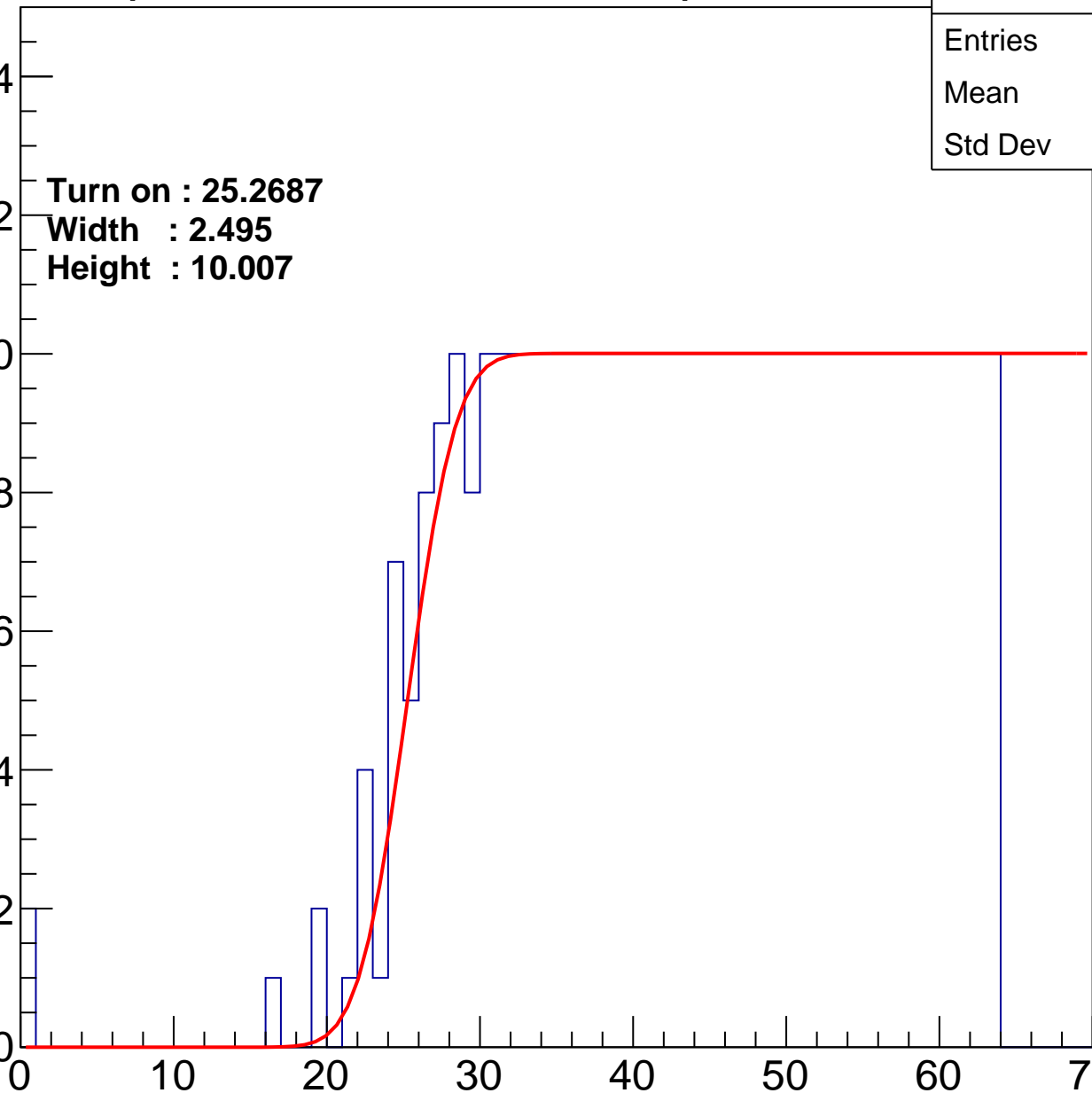
Width : 2.495

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch103

calib_packv5_042523_0143.root, FC#11, port A2

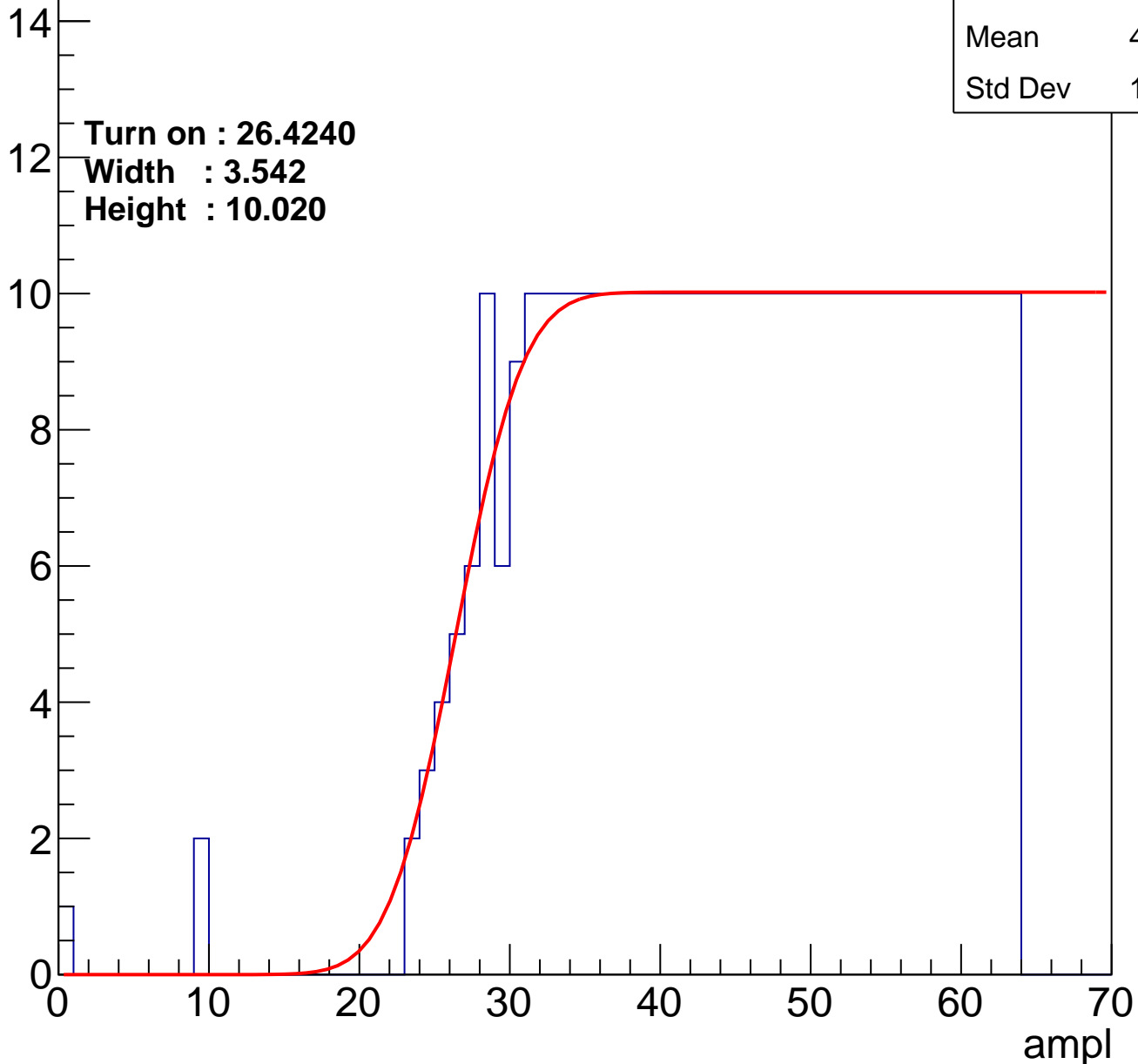
Entries	378
Mean	44.34
Std Dev	11.48

Turn on : 26.4240

Width : 3.542

Height : 10.020

Entry



B1L102S, U14-ch104

calib_packv5_042523_0143.root, FC#11, port A2

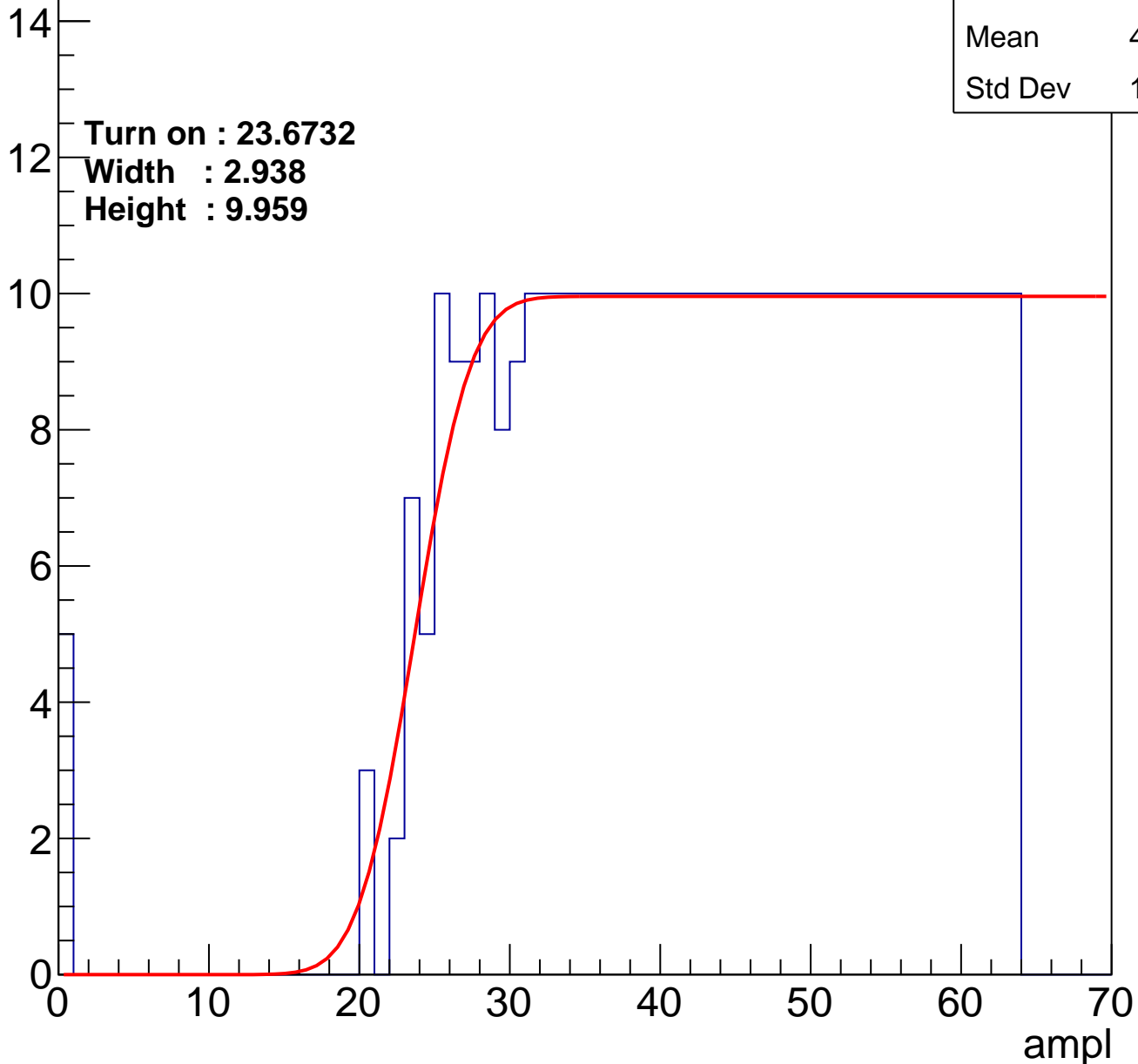
Entries	407
Mean	42.76
Std Dev	12.64

Turn on : 23.6732

Width : 2.938

Height : 9.959

Entry



B1L102S, U14-ch105

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 25.8698

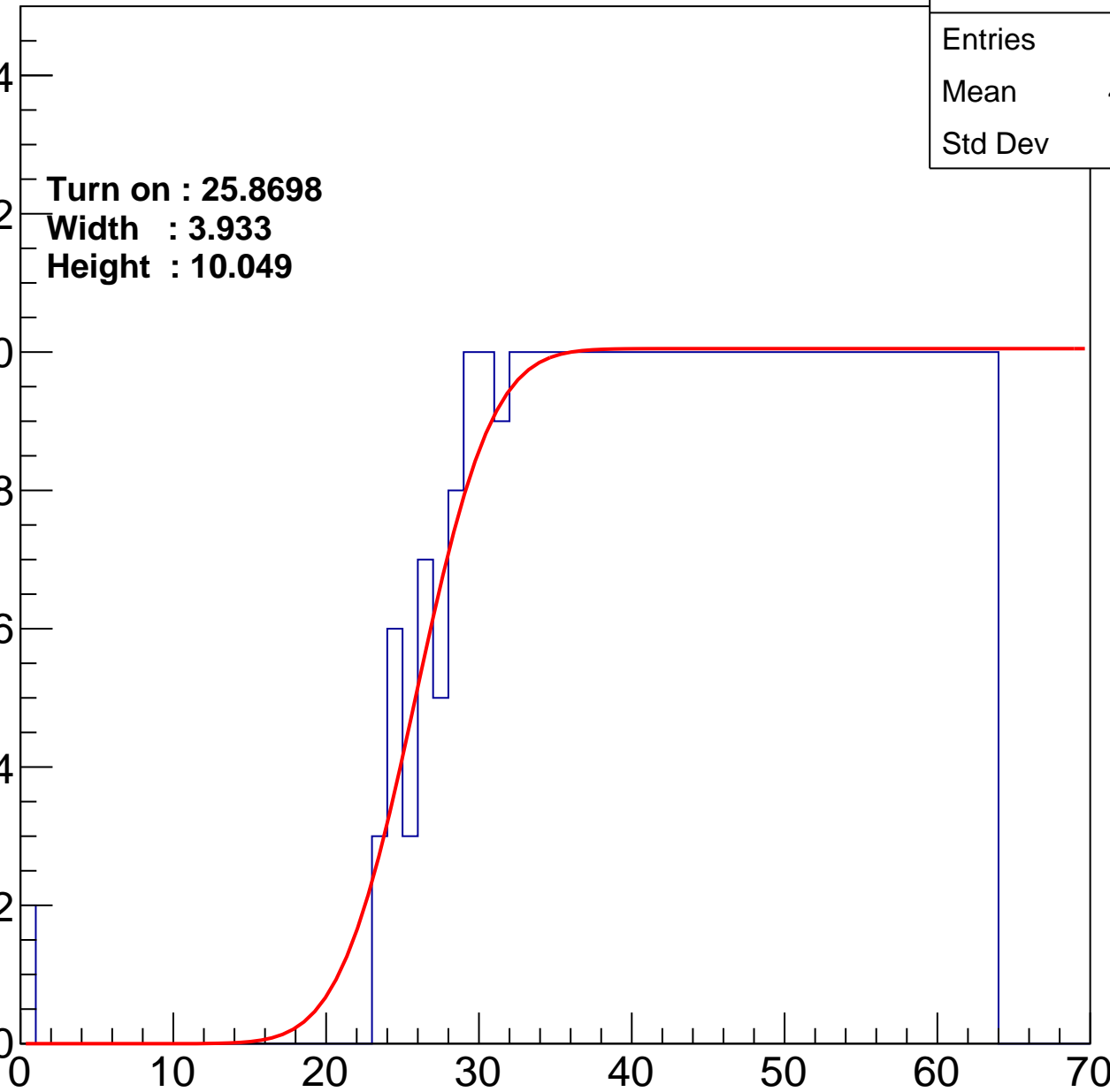
Width : 3.933

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch106

calib_packv5_042523_0143.root, FC#11, port A2

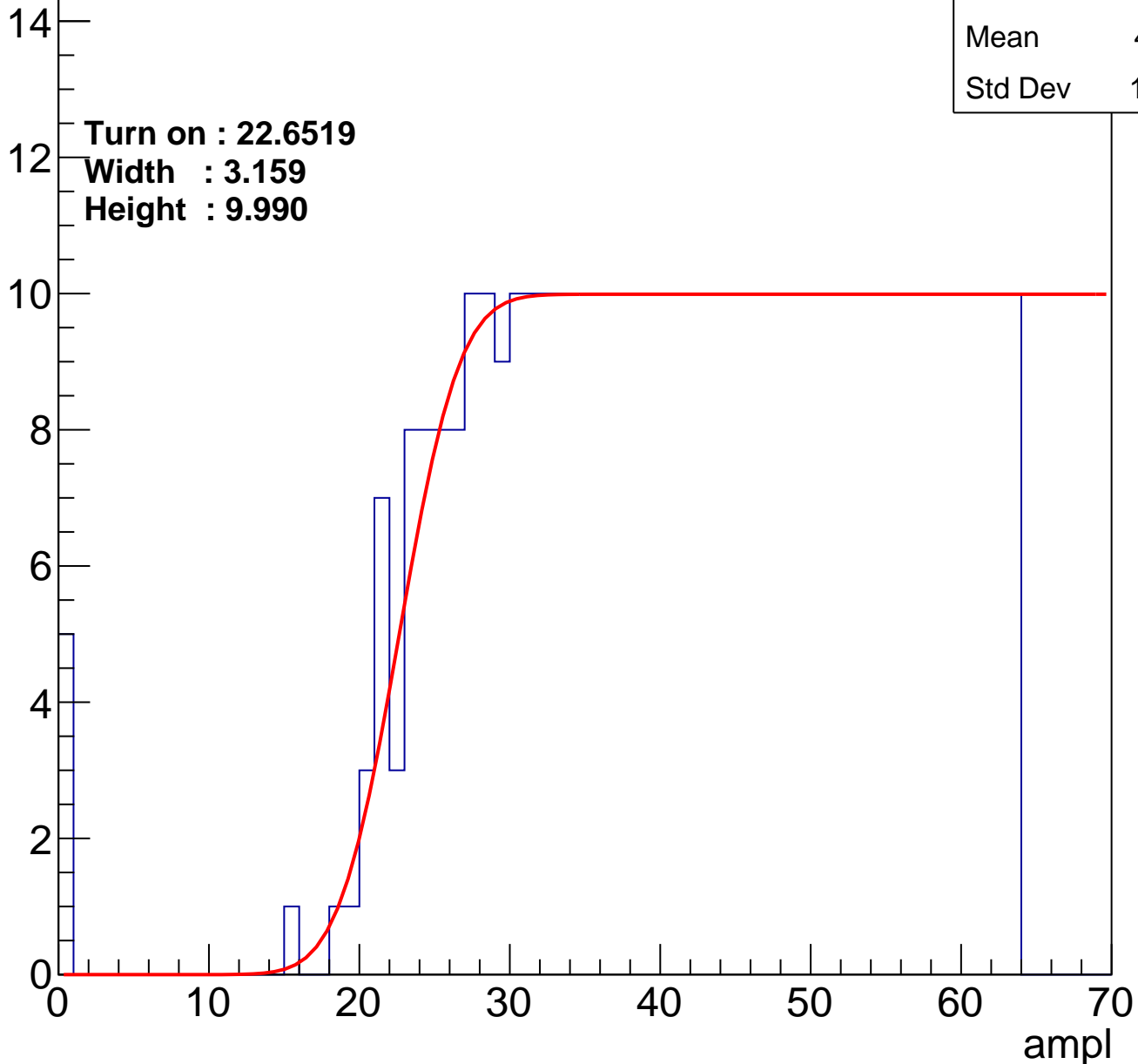
Entries	422
Mean	42.01
Std Dev	13.02

Turn on : 22.6519

Width : 3.159

Height : 9.990

Entry



B1L102S, U14-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.73
Std Dev	11.96

Turn on : 24.9842

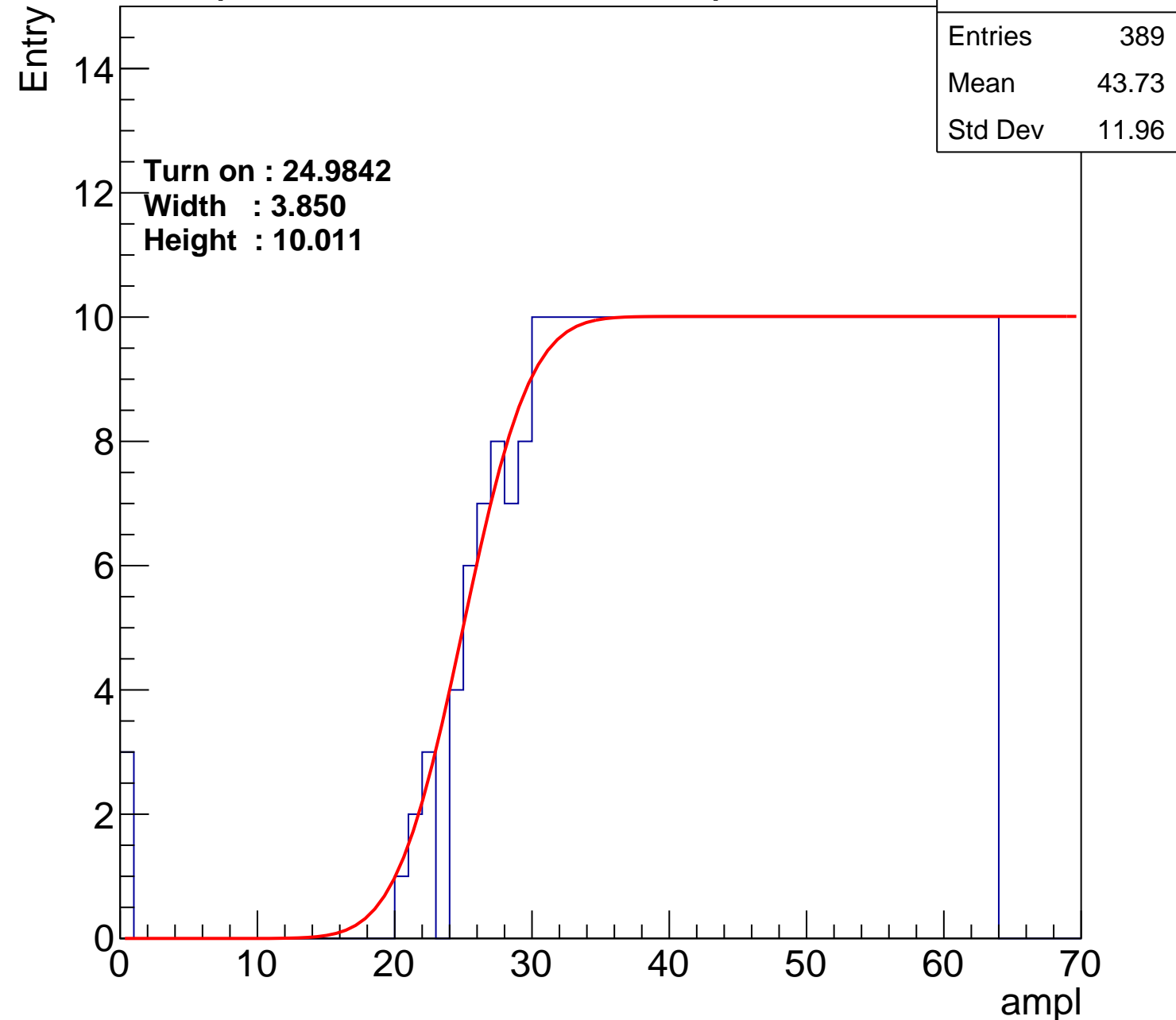
Width : 3.850

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch108

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.3898

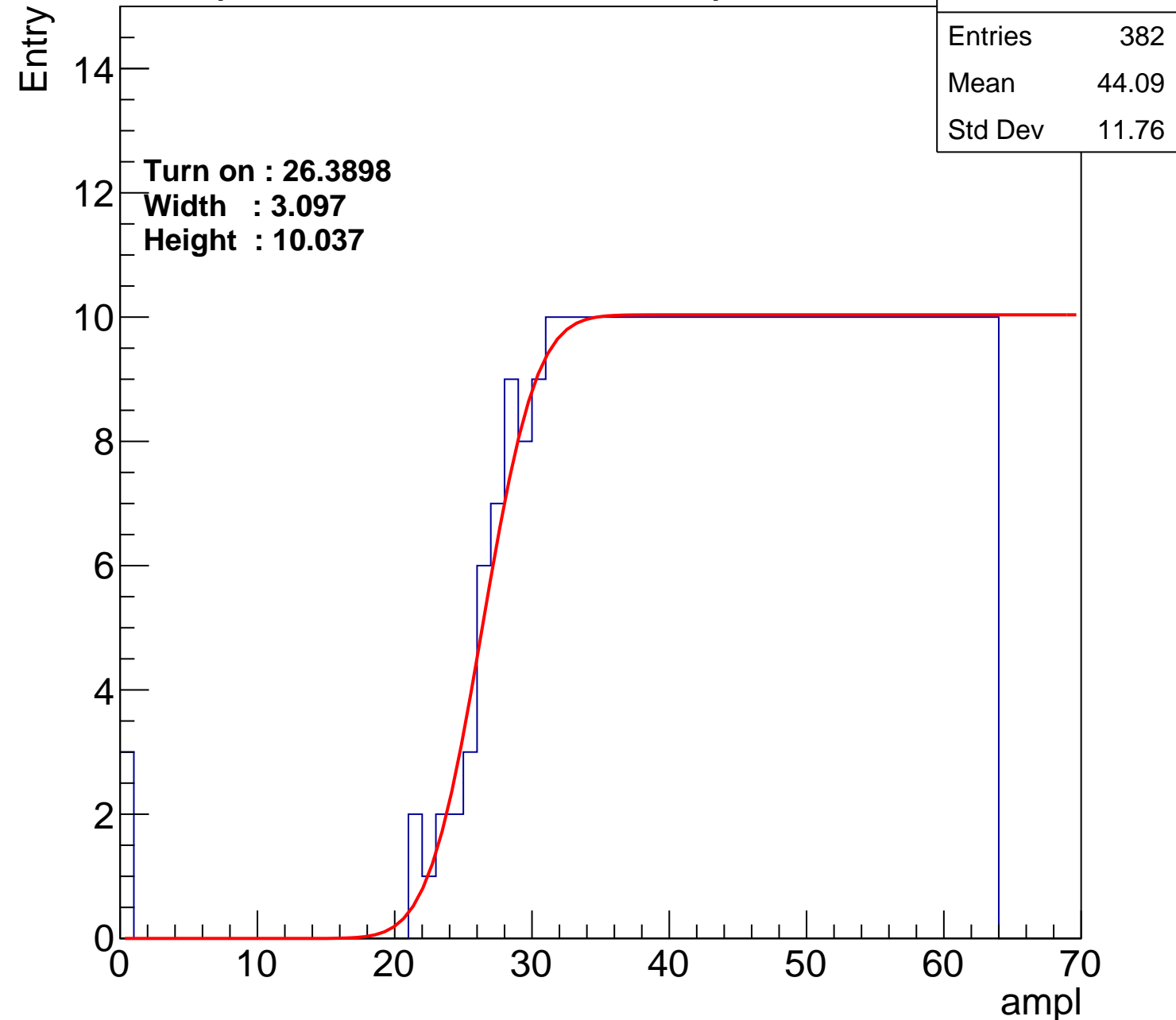
Width : 3.097

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch109

calib_packv5_042523_0143.root, FC#11, port A2

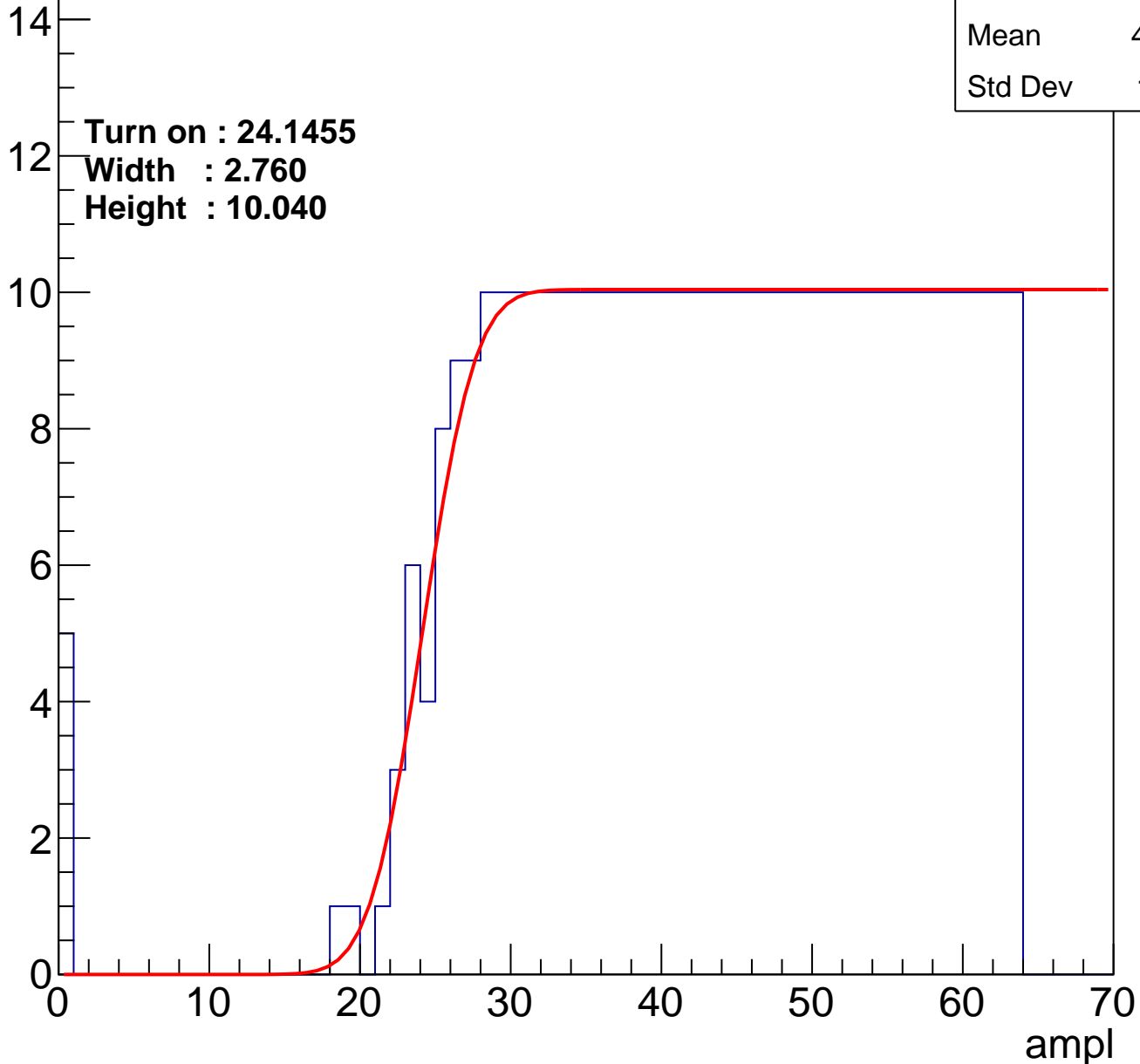
Entries	407
Mean	42.79
Std Dev	12.61

Turn on : 24.1455

Width : 2.760

Height : 10.040

Entry



B1L102S, U14-ch110

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	42.96
Std Dev	12.59

Turn on : 24.2673

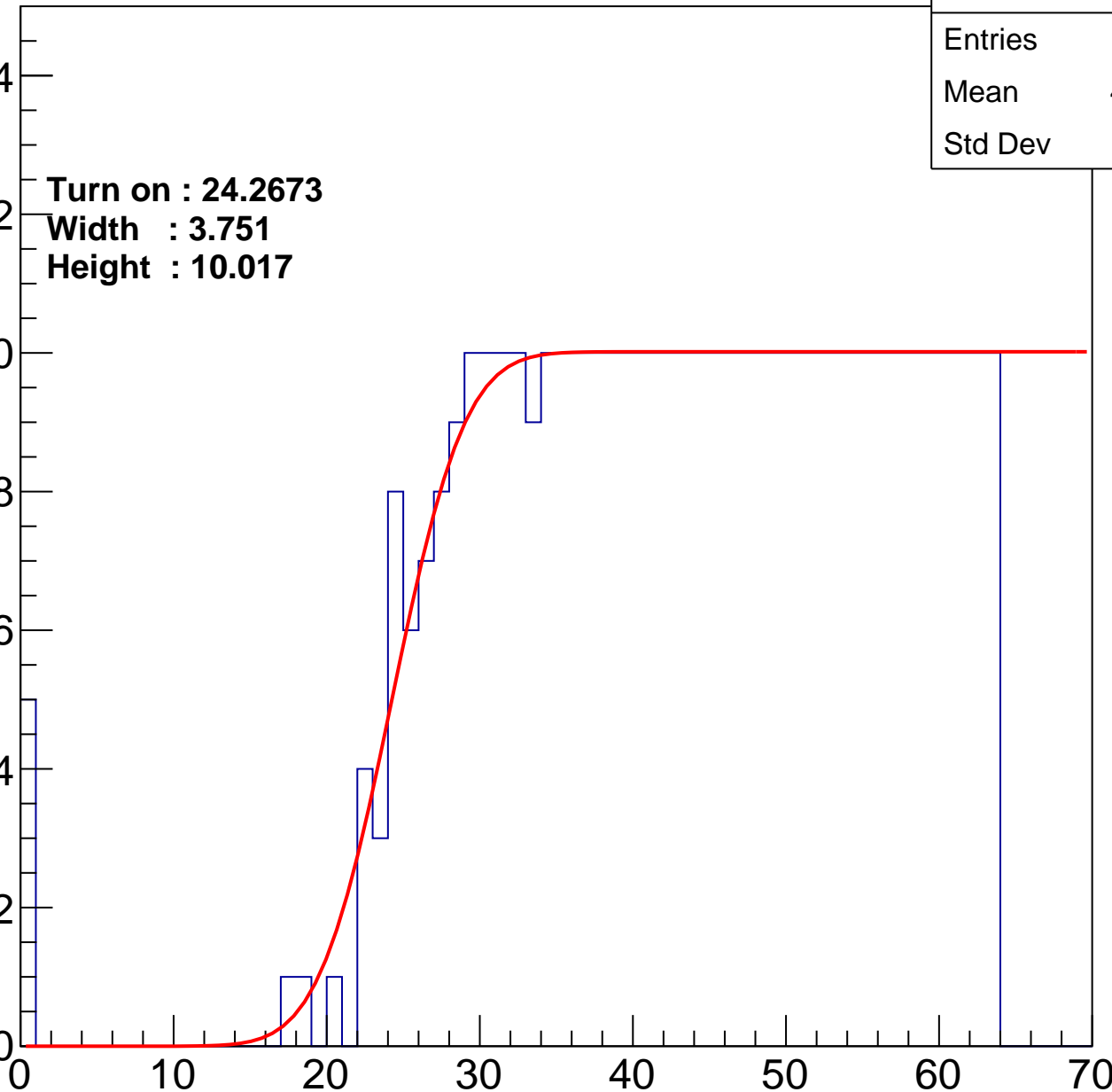
Width : 3.751

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch111

calib_packv5_042523_0143.root, FC#11, port A2

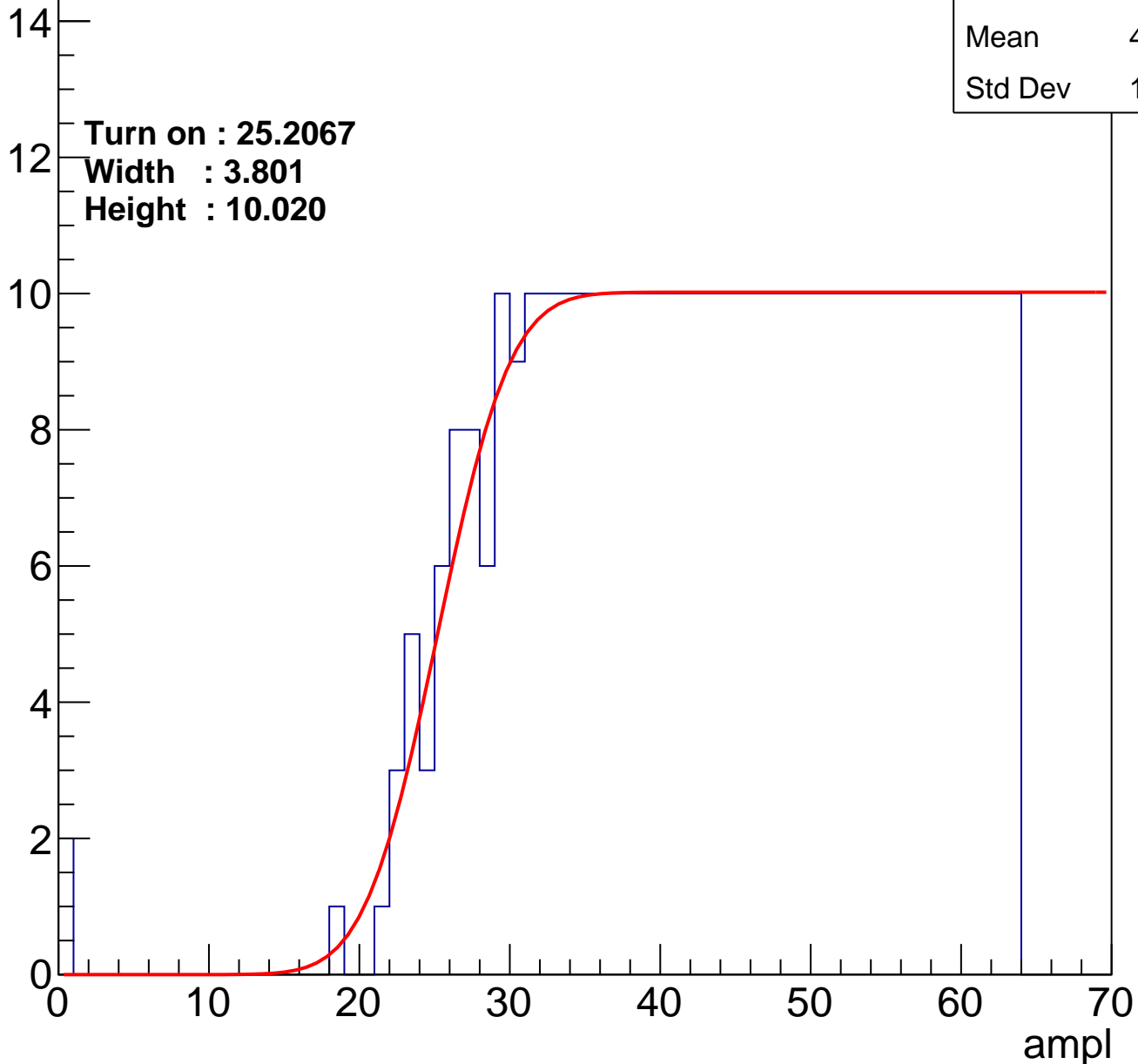
Entries	392
Mean	43.63
Std Dev	11.89

Turn on : 25.2067

Width : 3.801

Height : 10.020

Entry



B1L102S, U14-ch112

calib_packv5_042523_0143.root, FC#11, port A2

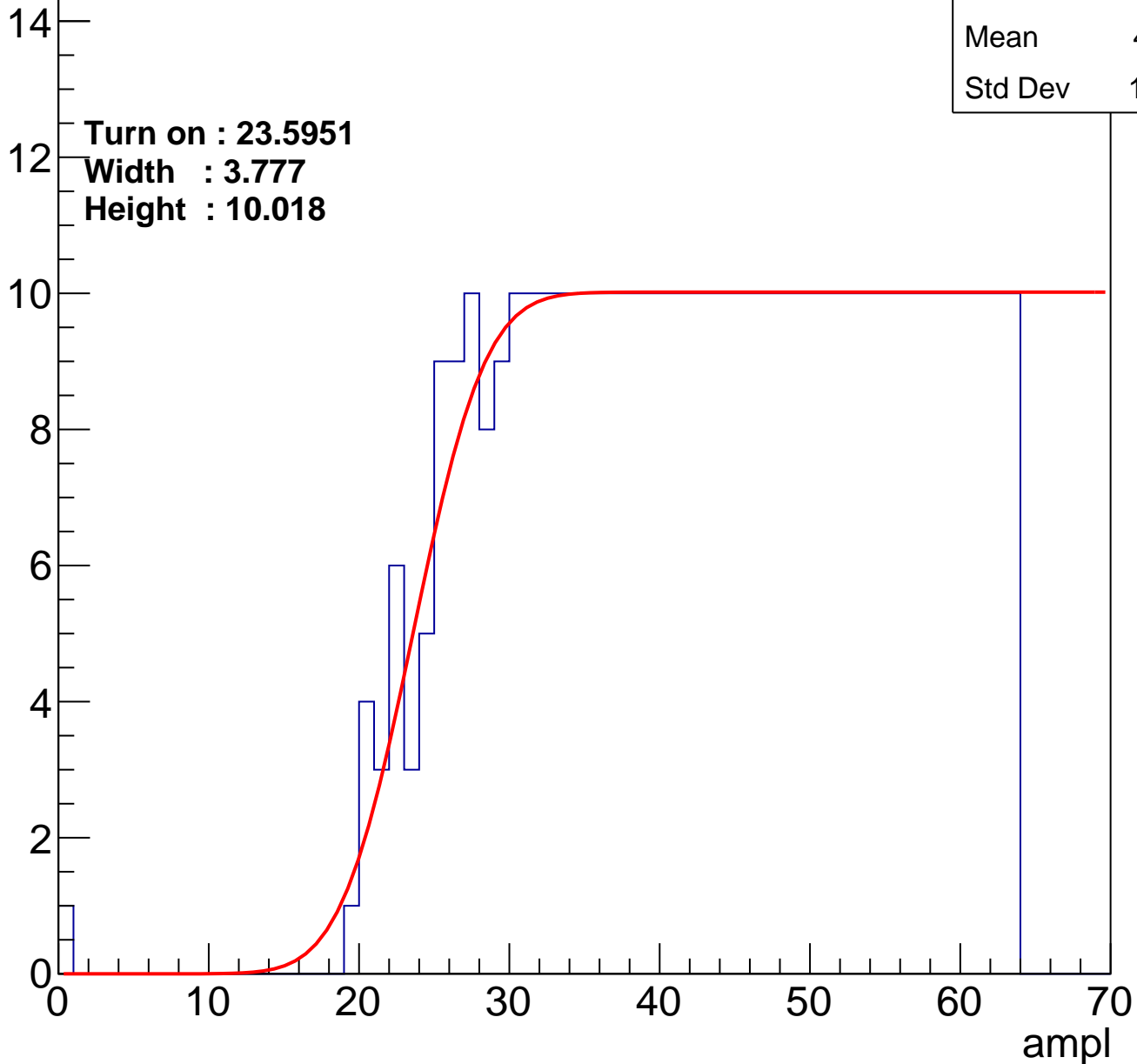
Entries	408
Mean	42.91
Std Dev	12.14

Turn on : 23.5951

Width : 3.777

Height : 10.018

Entry



B1L102S, U14-ch113

calib_packv5_042523_0143.root, FC#11, port A2

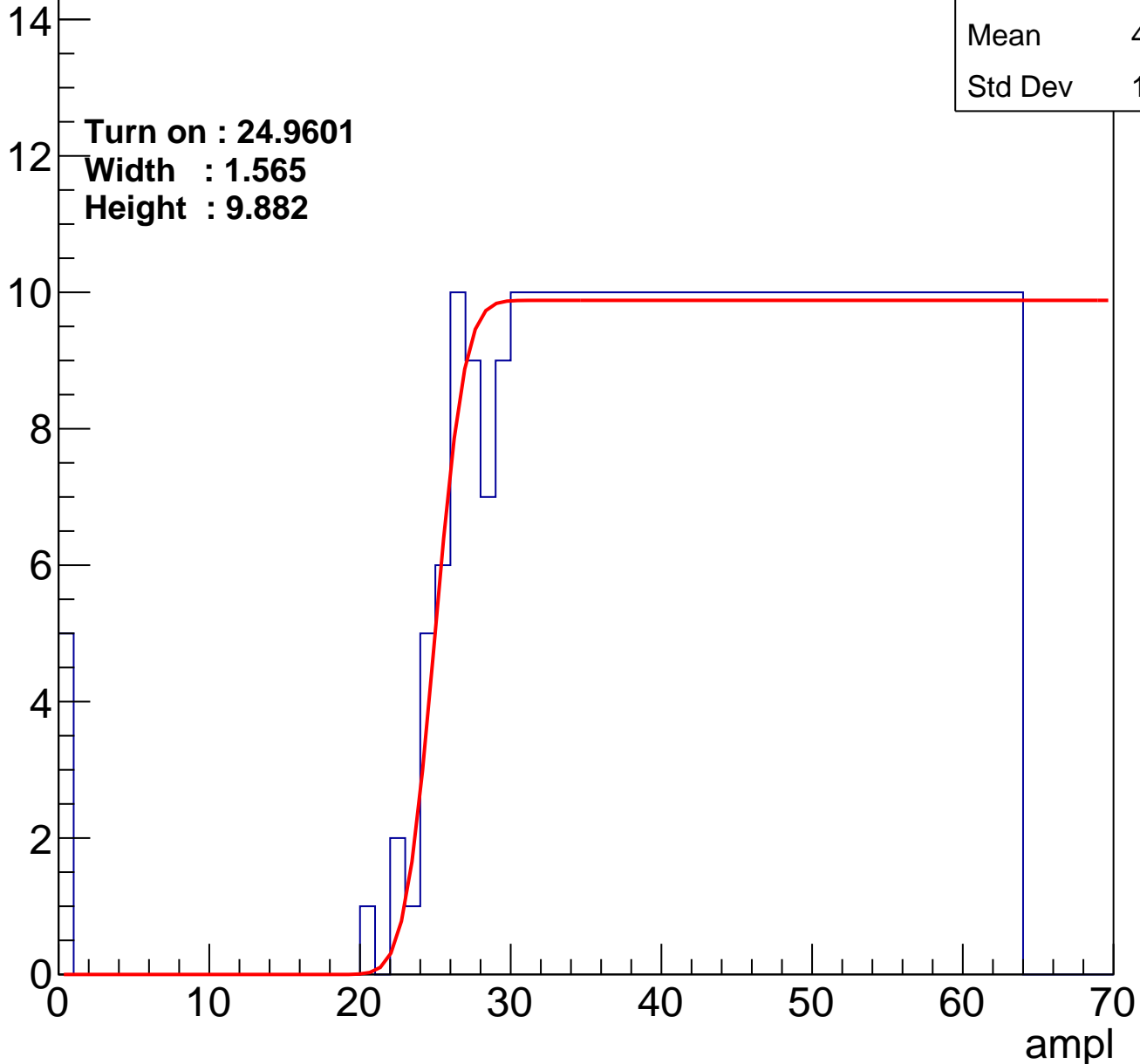
Entries	395
Mean	43.36
Std Dev	12.34

Turn on : 24.9601

Width : 1.565

Height : 9.882

Entry



B1L102S, U14-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	43.04
Std Dev	12.52

Turn on : 23.9908

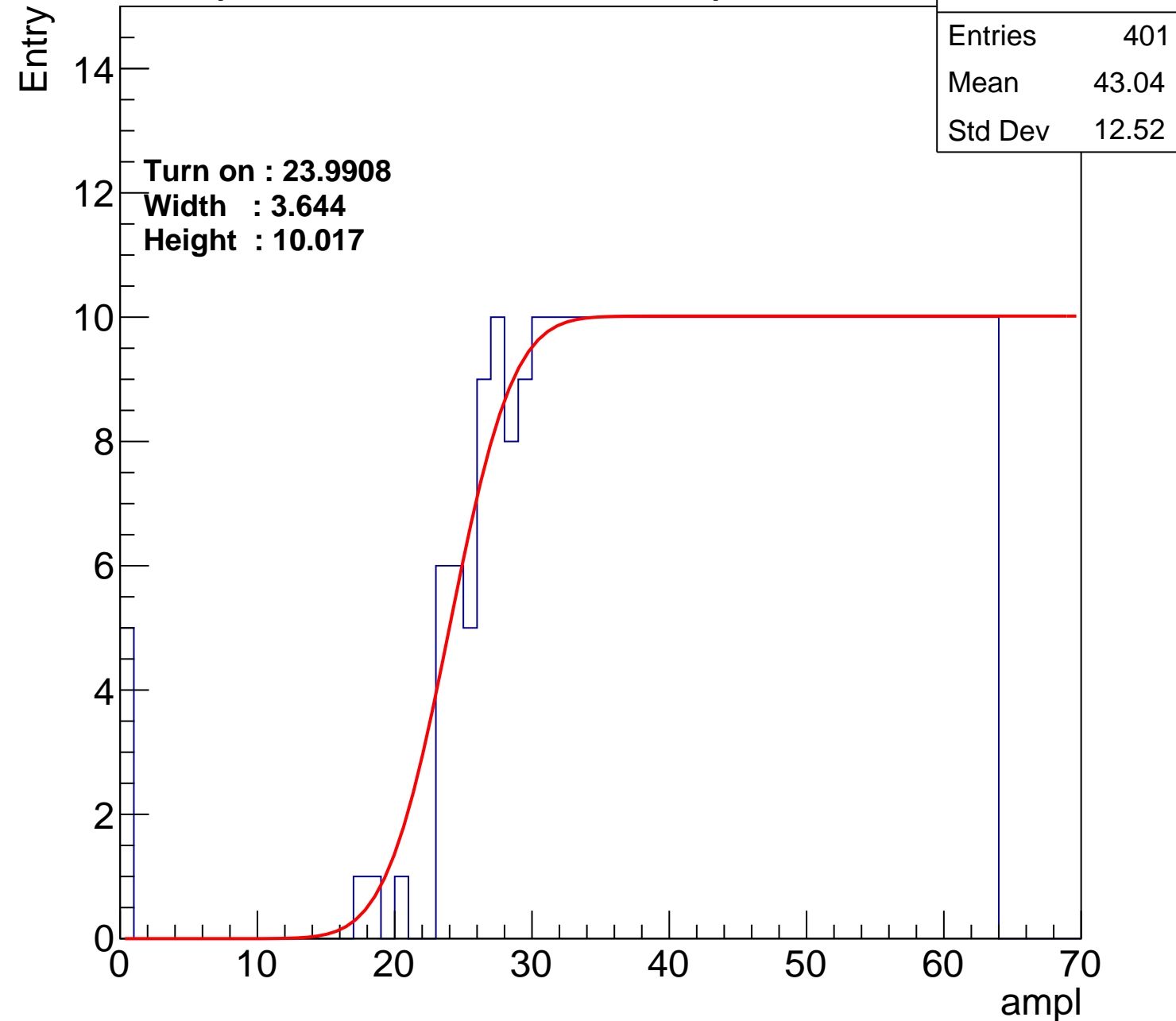
Width : 3.644

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch115

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.31
Std Dev	11.79

Turn on : 26.9114

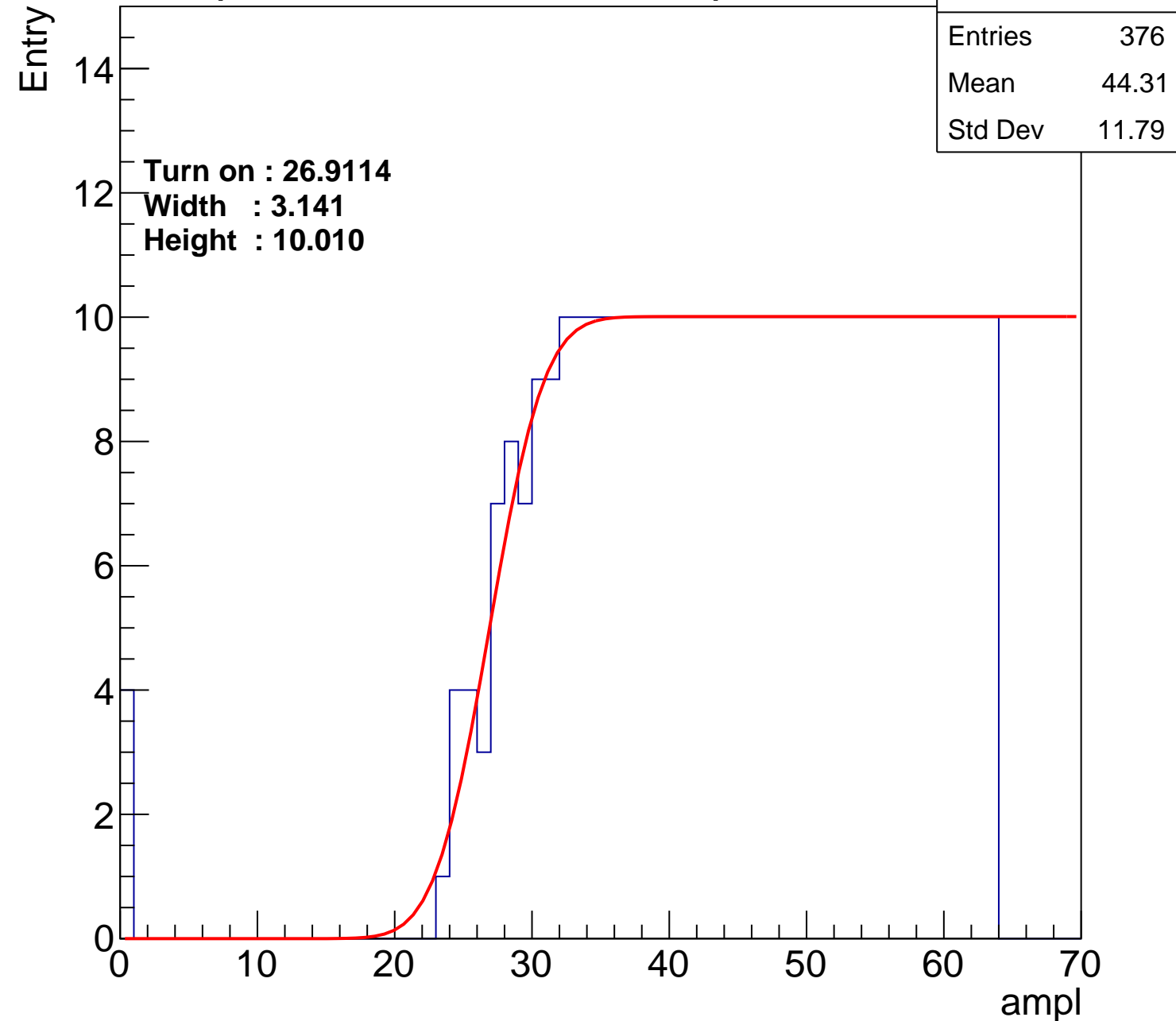
Width : 3.141

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.32
Std Dev	12.36

Turn on : 25.0510

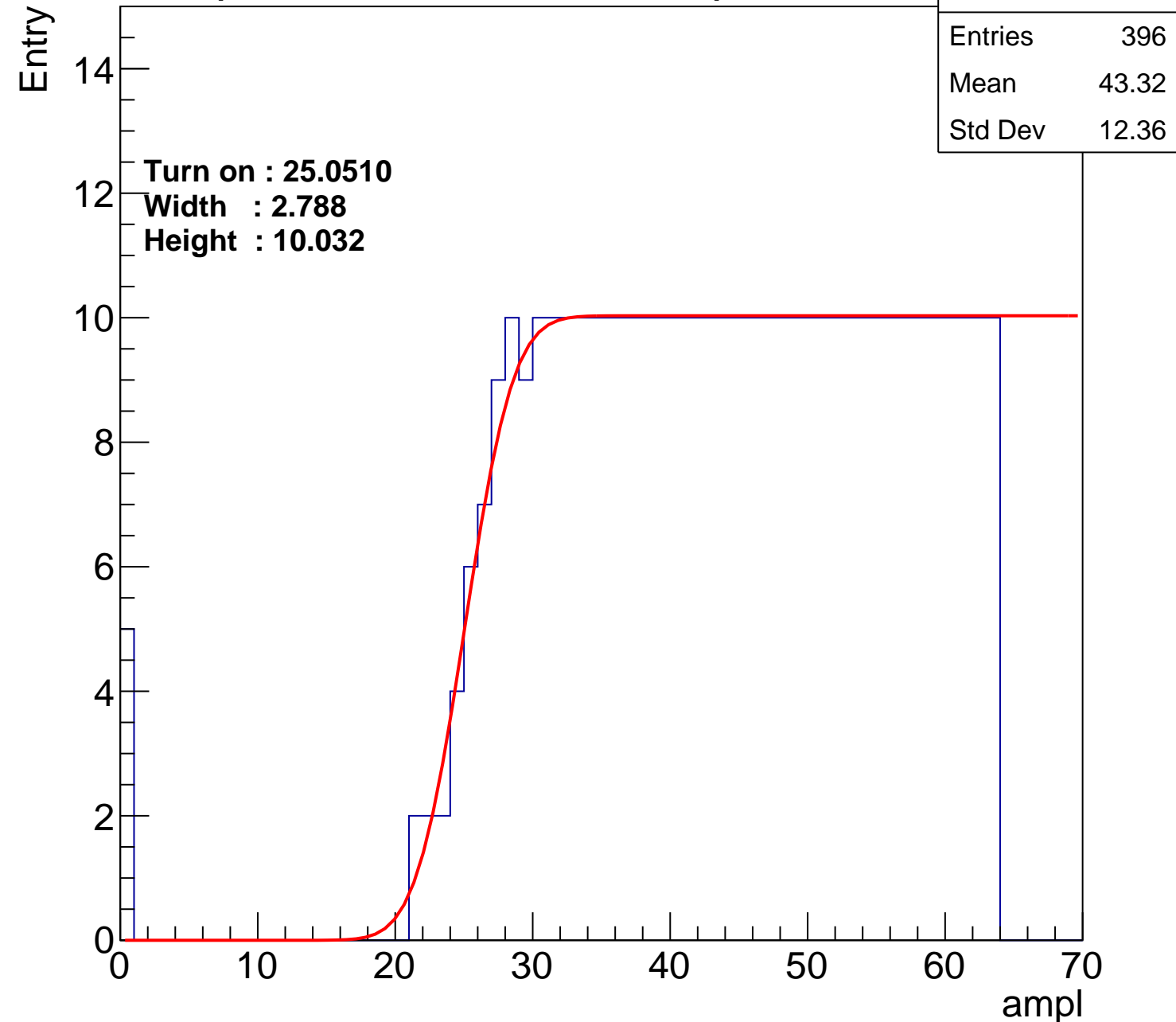
Width : 2.788

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.44
Std Dev	12.33

Turn on : 25.2952

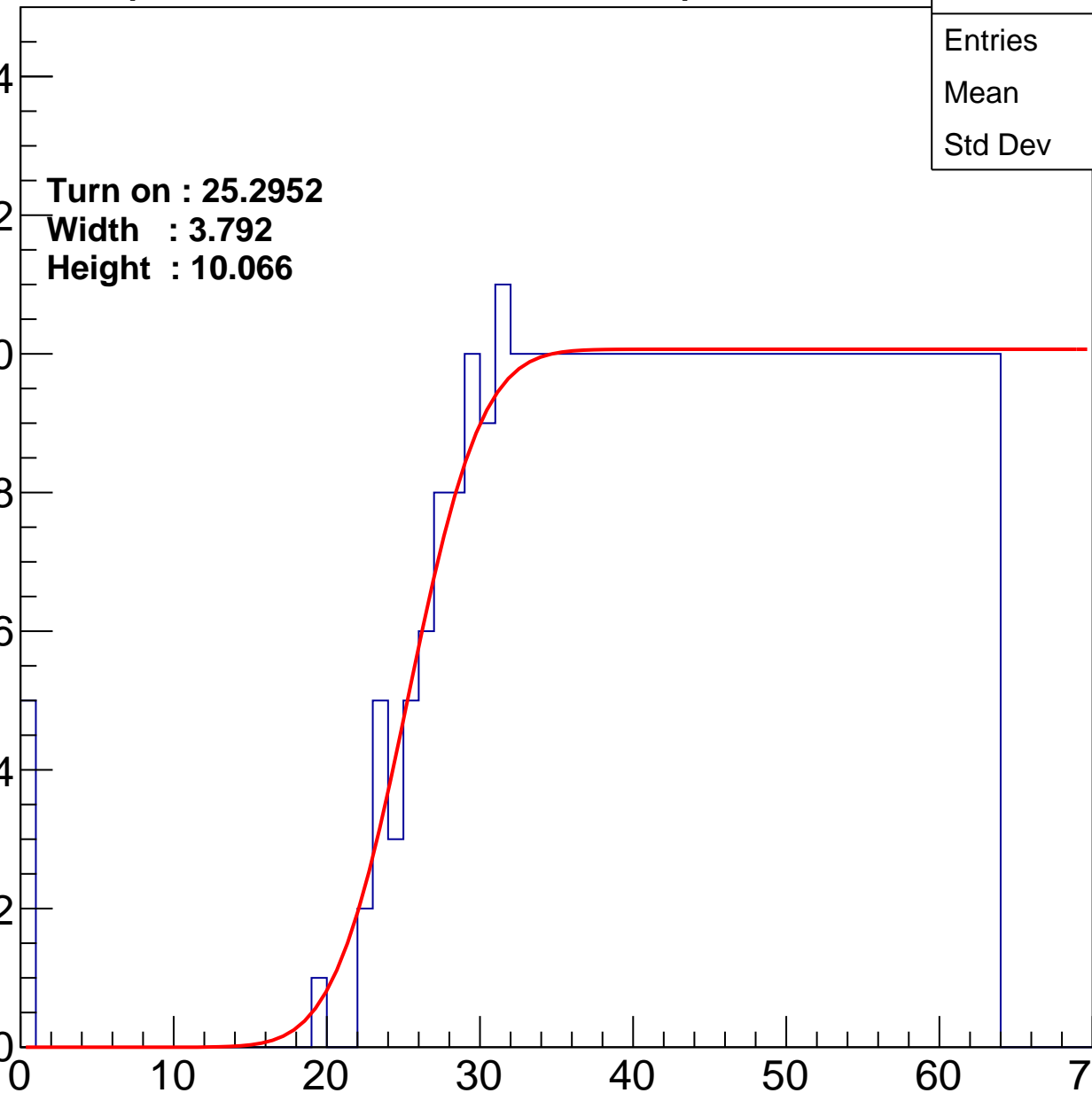
Width : 3.792

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch118

calib_packv5_042523_0143.root, FC#11, port A2

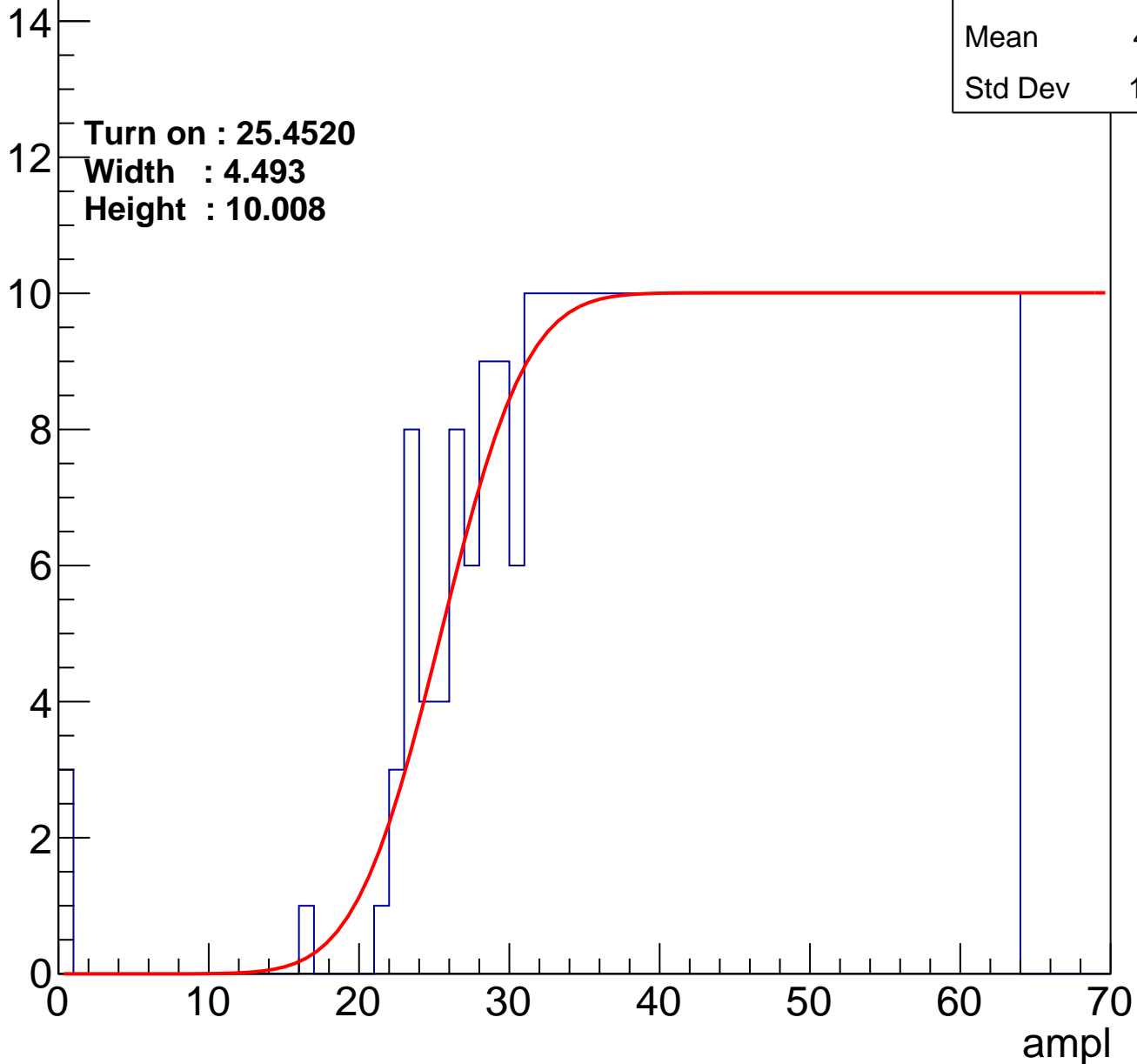
Entries	392
Mean	43.51
Std Dev	12.14

Turn on : 25.4520

Width : 4.493

Height : 10.008

Entry



B1L102S, U14-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.8922

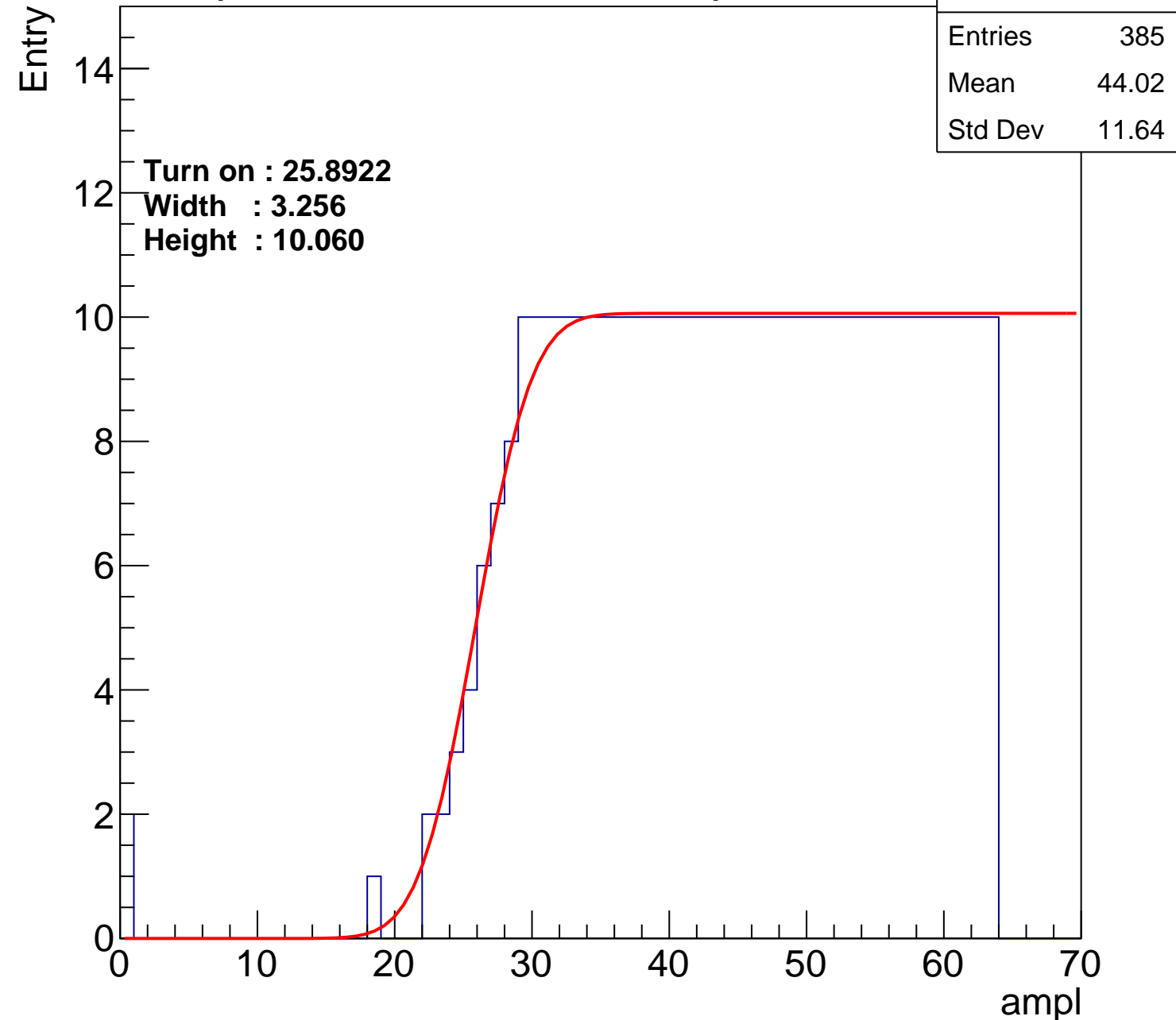
Width : 3.256

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch120

calib_packv5_042523_0143.root, FC#11, port A2

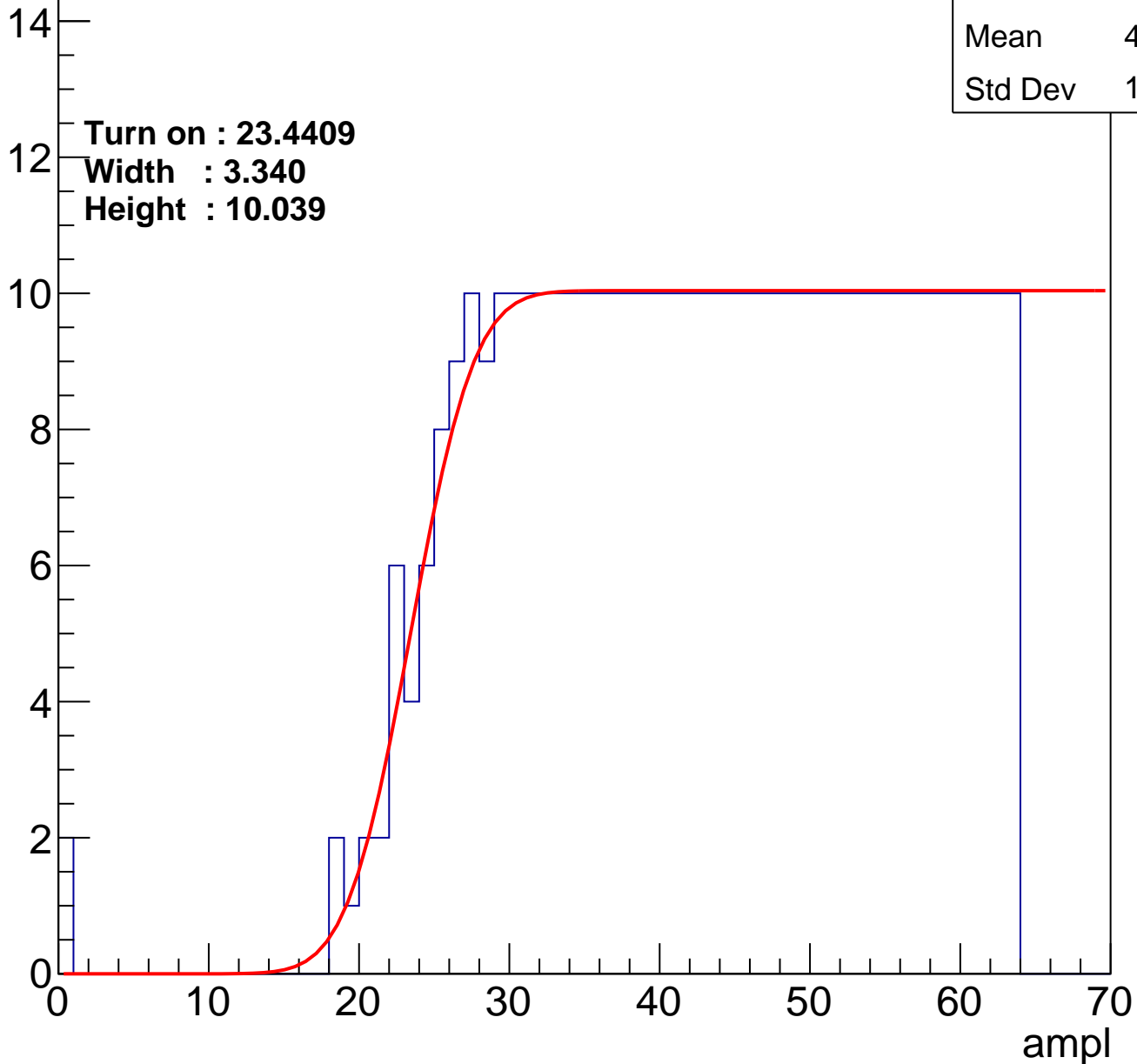
Entries	411
Mean	42.73
Std Dev	12.34

Turn on : 23.4409

Width : 3.340

Height : 10.039

Entry



B1L102S, U14-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.9444

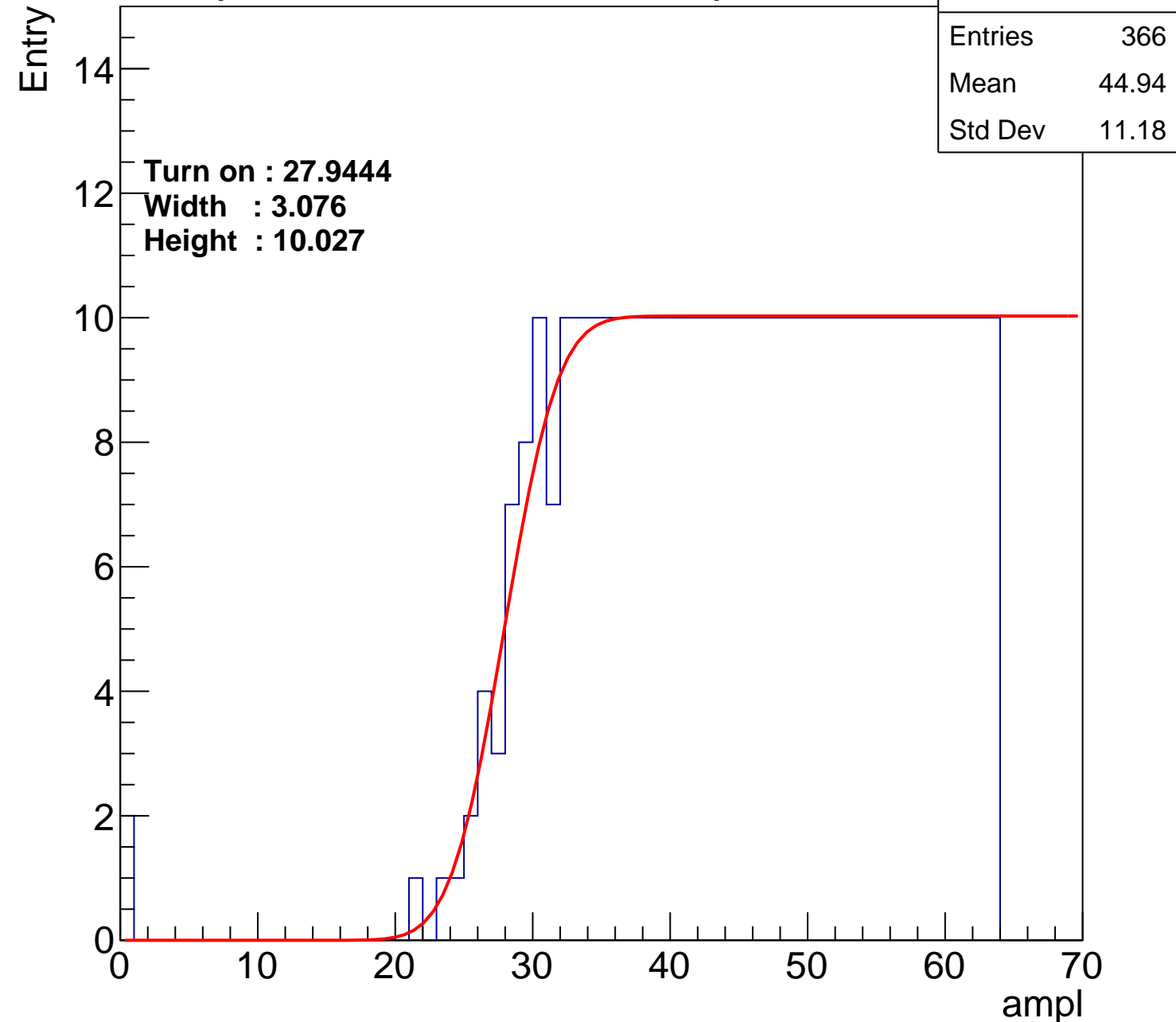
Width : 3.076

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch122

calib_packv5_042523_0143.root, FC#11, port A2

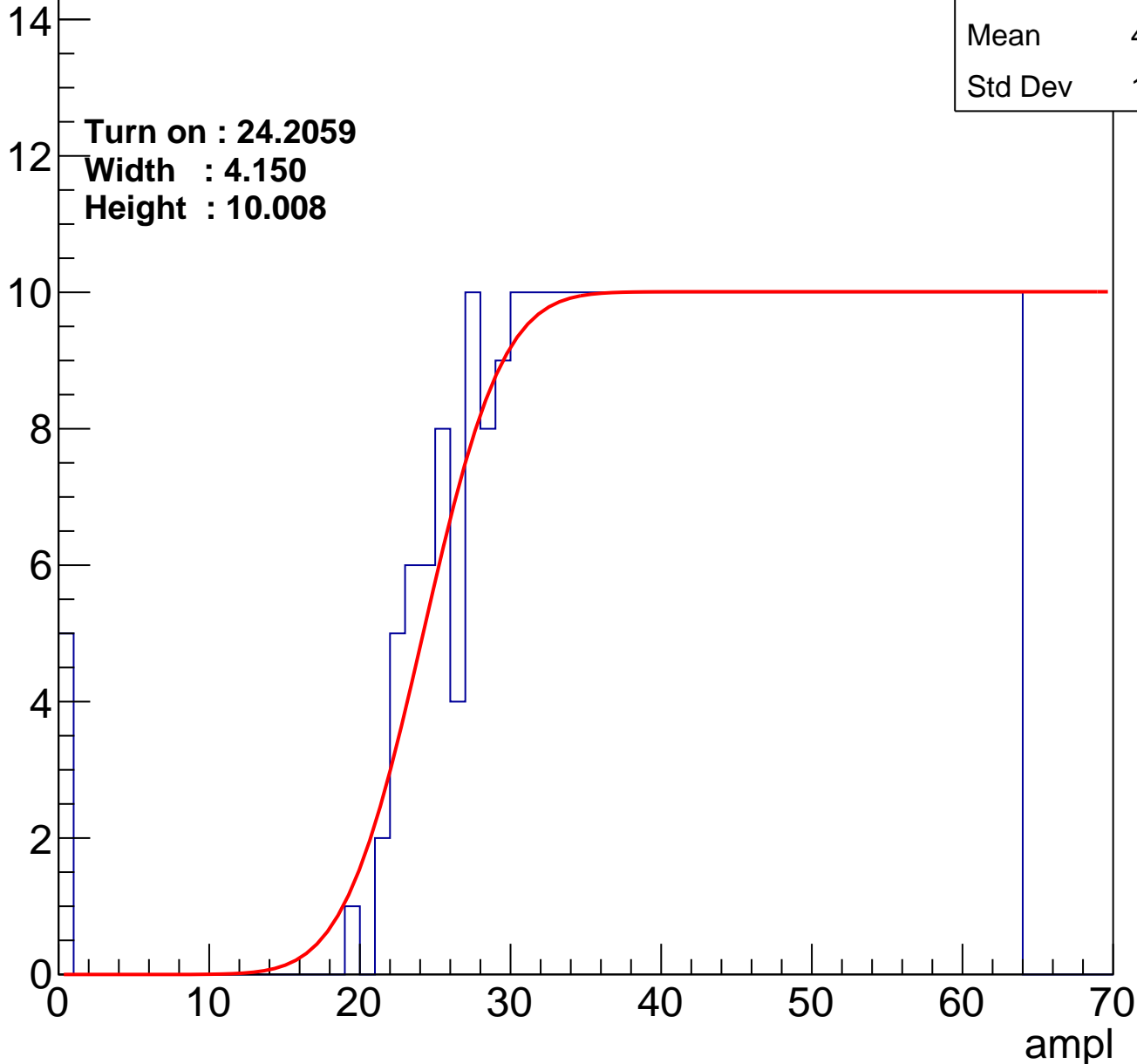
Entries	404
Mean	42.88
Std Dev	12.62

Turn on : 24.2059

Width : 4.150

Height : 10.008

Entry



B1L102S, U14-ch123

calib_packv5_042523_0143.root, FC#11, port A2

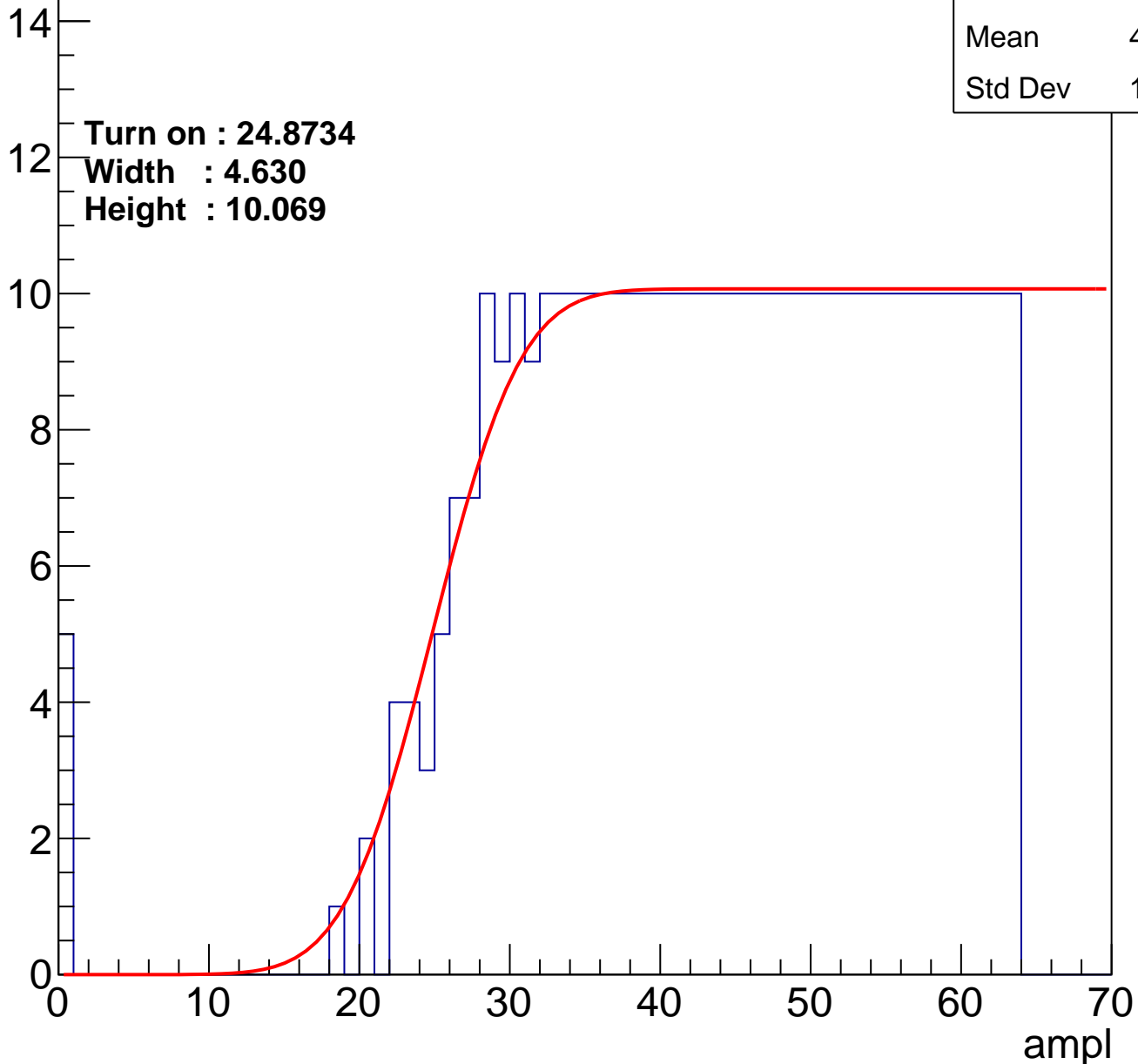
Entries	396
Mean	43.25
Std Dev	12.47

Turn on : 24.8734

Width : 4.630

Height : 10.069

Entry



B1L102S, U14-ch124

calib_packv5_042523_0143.root, FC#11, port A2

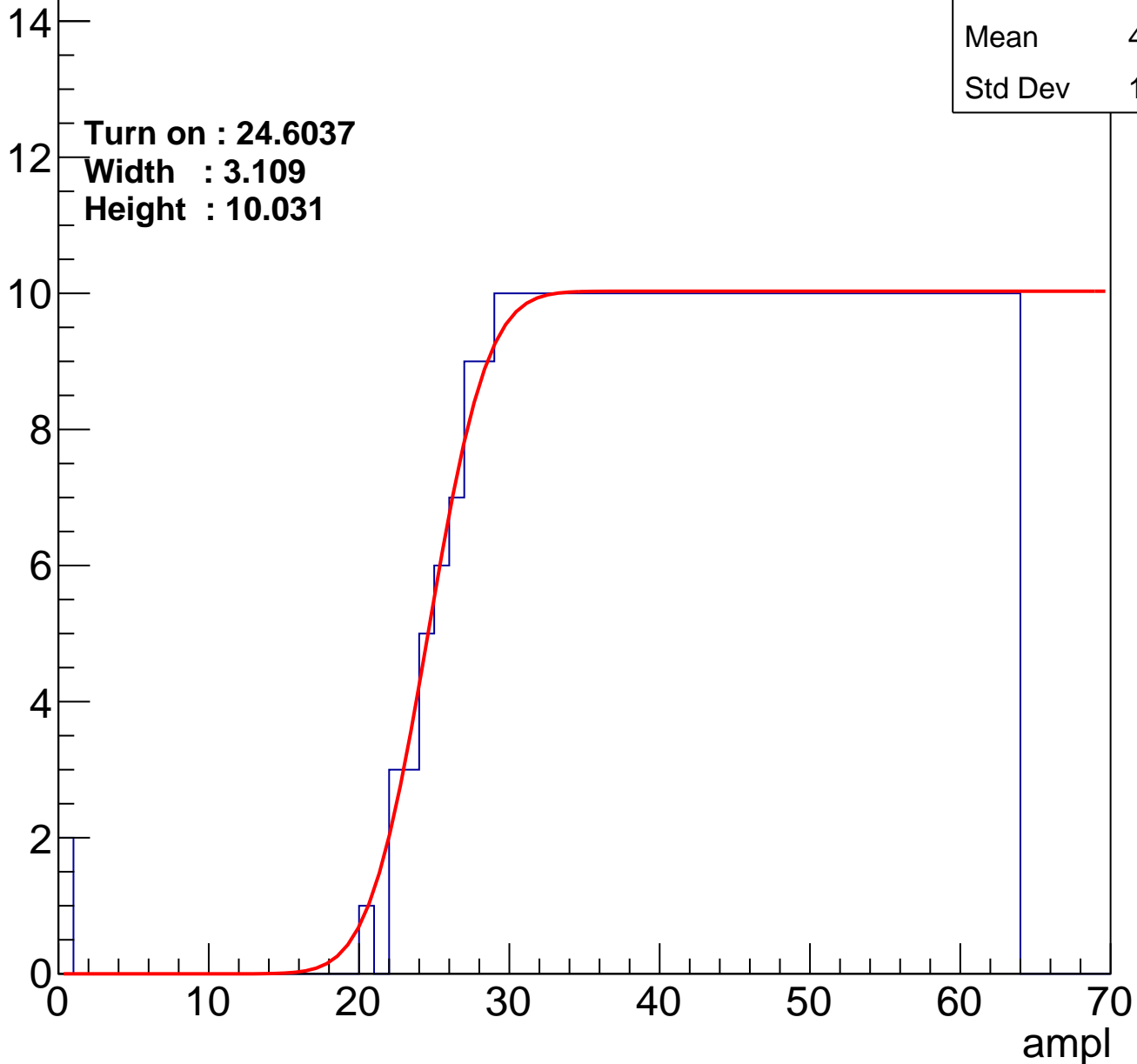
Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 24.6037

Width : 3.109

Height : 10.031

Entry



B1L102S, U14-ch125

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.3269

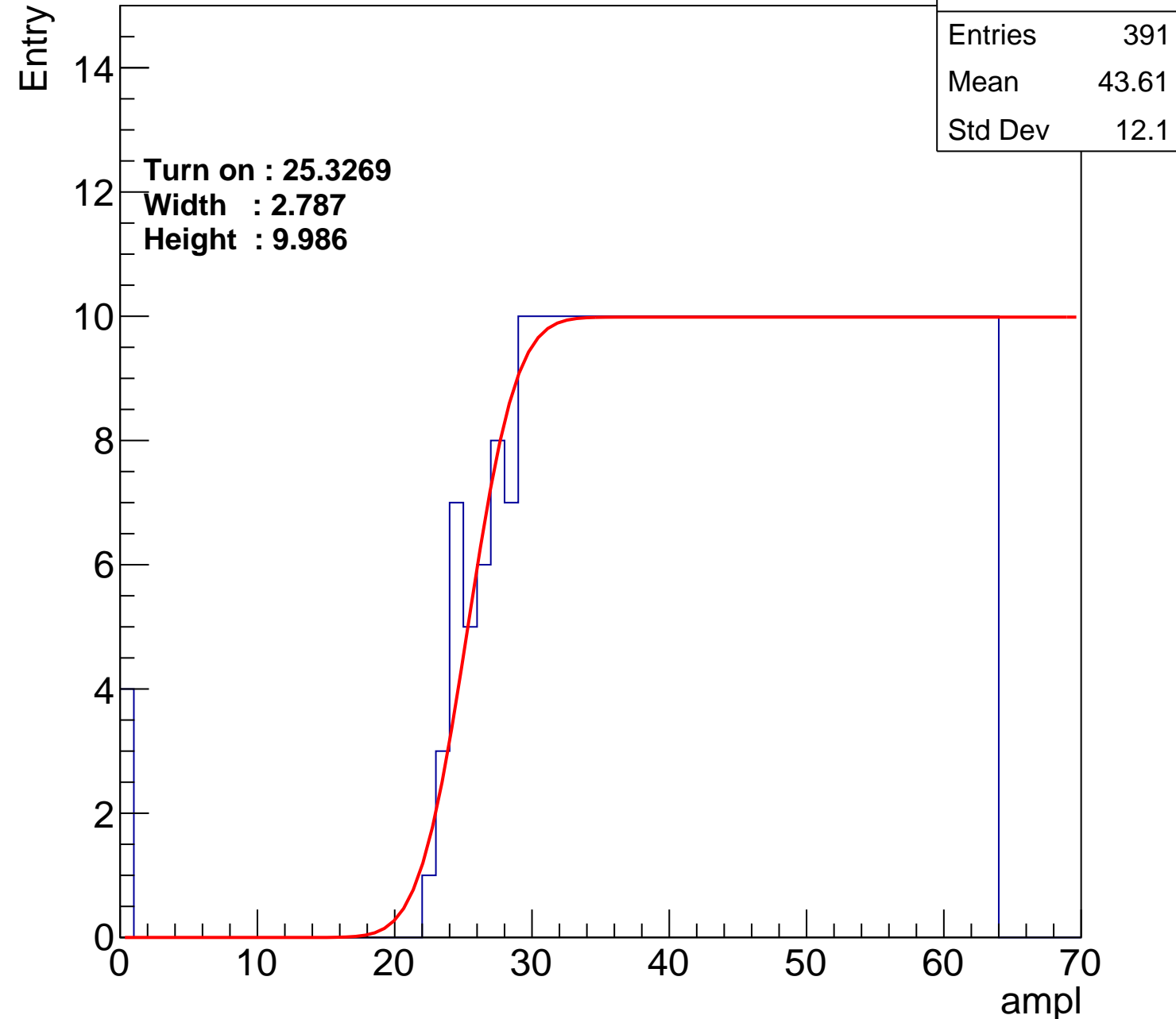
Width : 2.787

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	43
Std Dev	12.54

Turn on : 25.3265

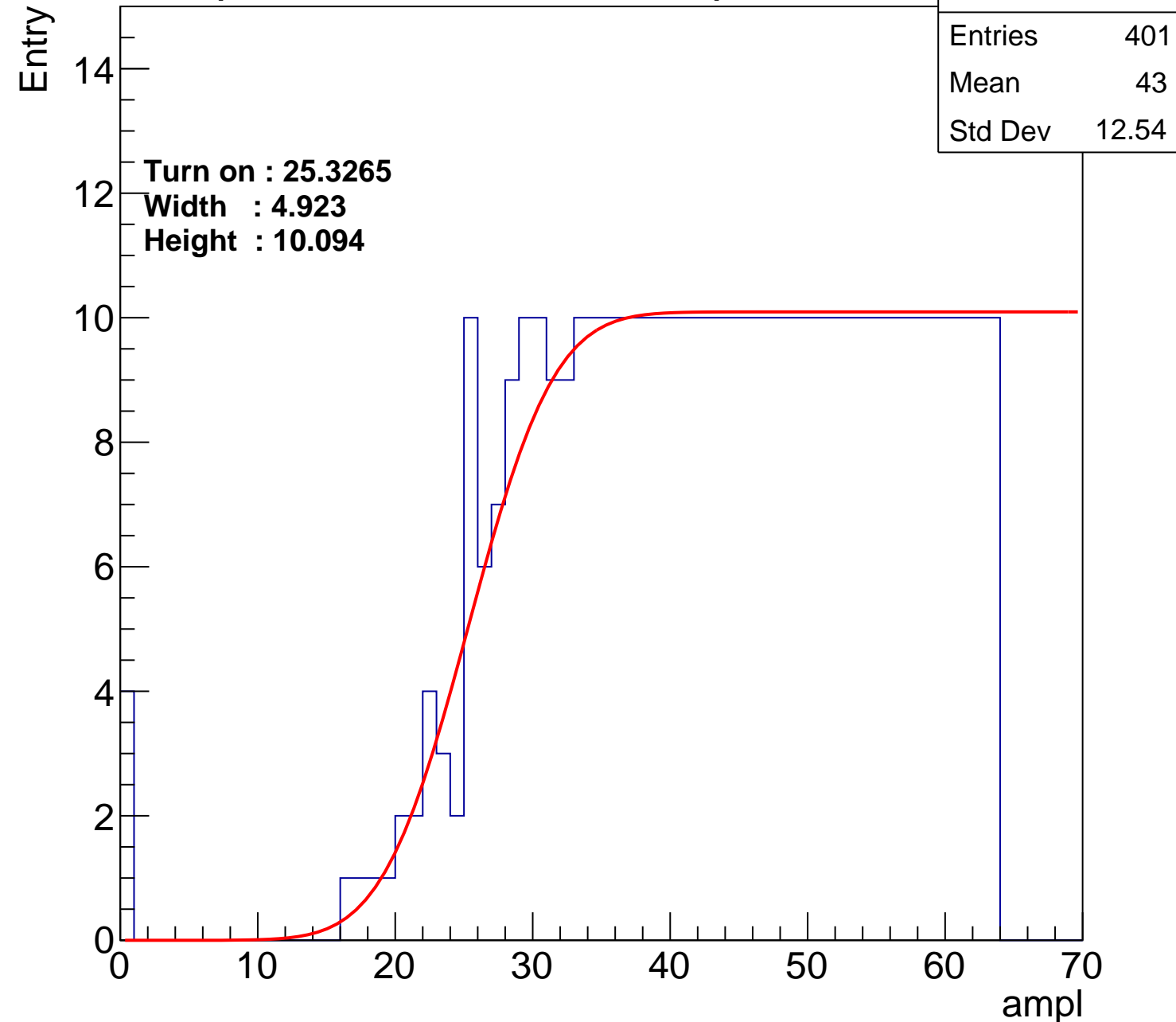
Width : 4.923

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.28
Std Dev	12.3

Turn on : 24.6359

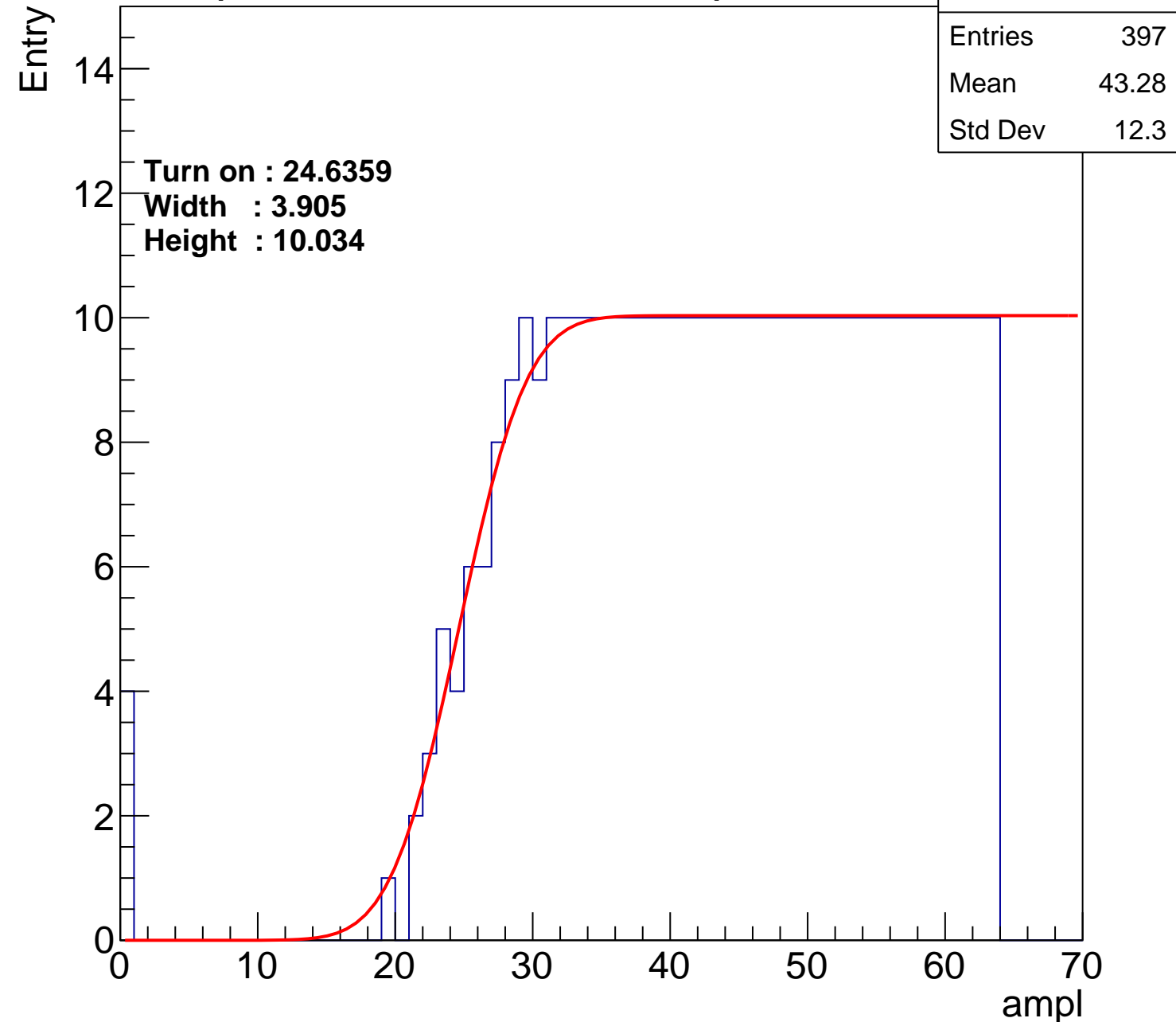
Width : 3.905

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U14-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.28
Std Dev	12.3

Turn on : 24.6359

Width : 3.905

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

