

B1L104S, U10-ch0

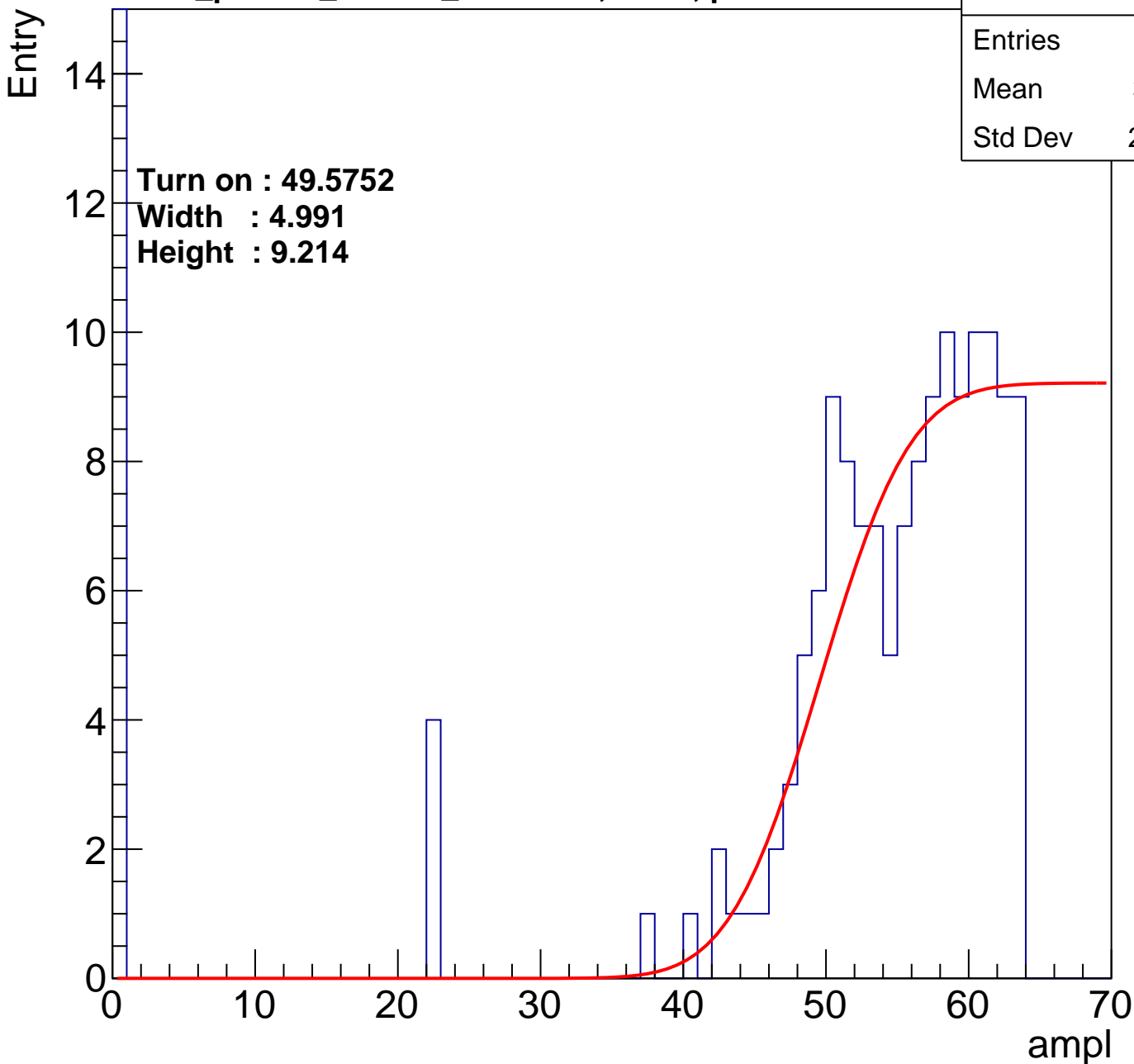
calib_packv5_033123_0516.root, FC#4, port A1

Entries	250
Mean	31.21
Std Dev	27.42

Turn on : 49.5752

Width : 4.991

Height : 9.214



B1L104S, U10-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.79
Std Dev	28.82

Turn on : 54.1113

Width : 2.941

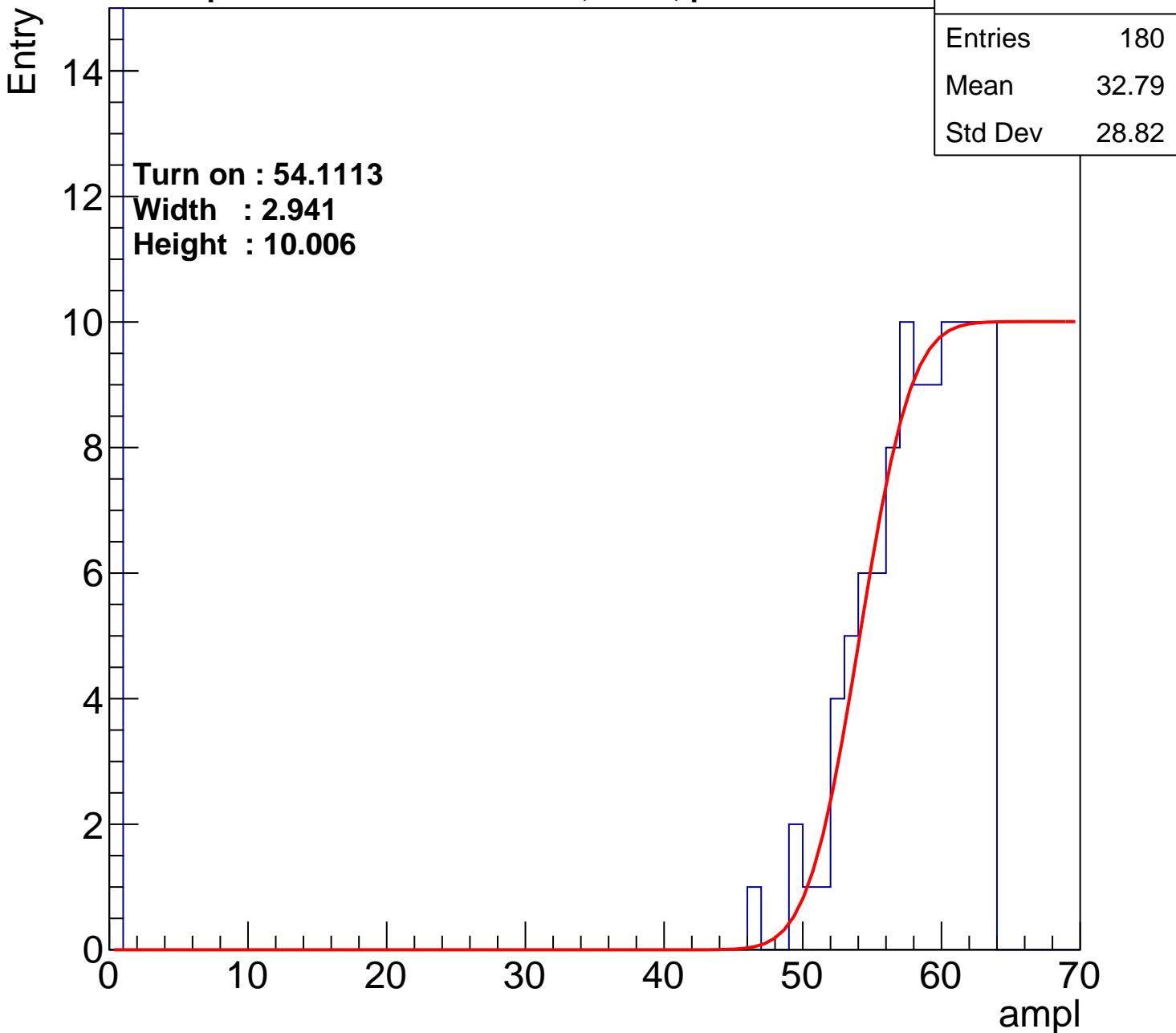
Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U10-ch2

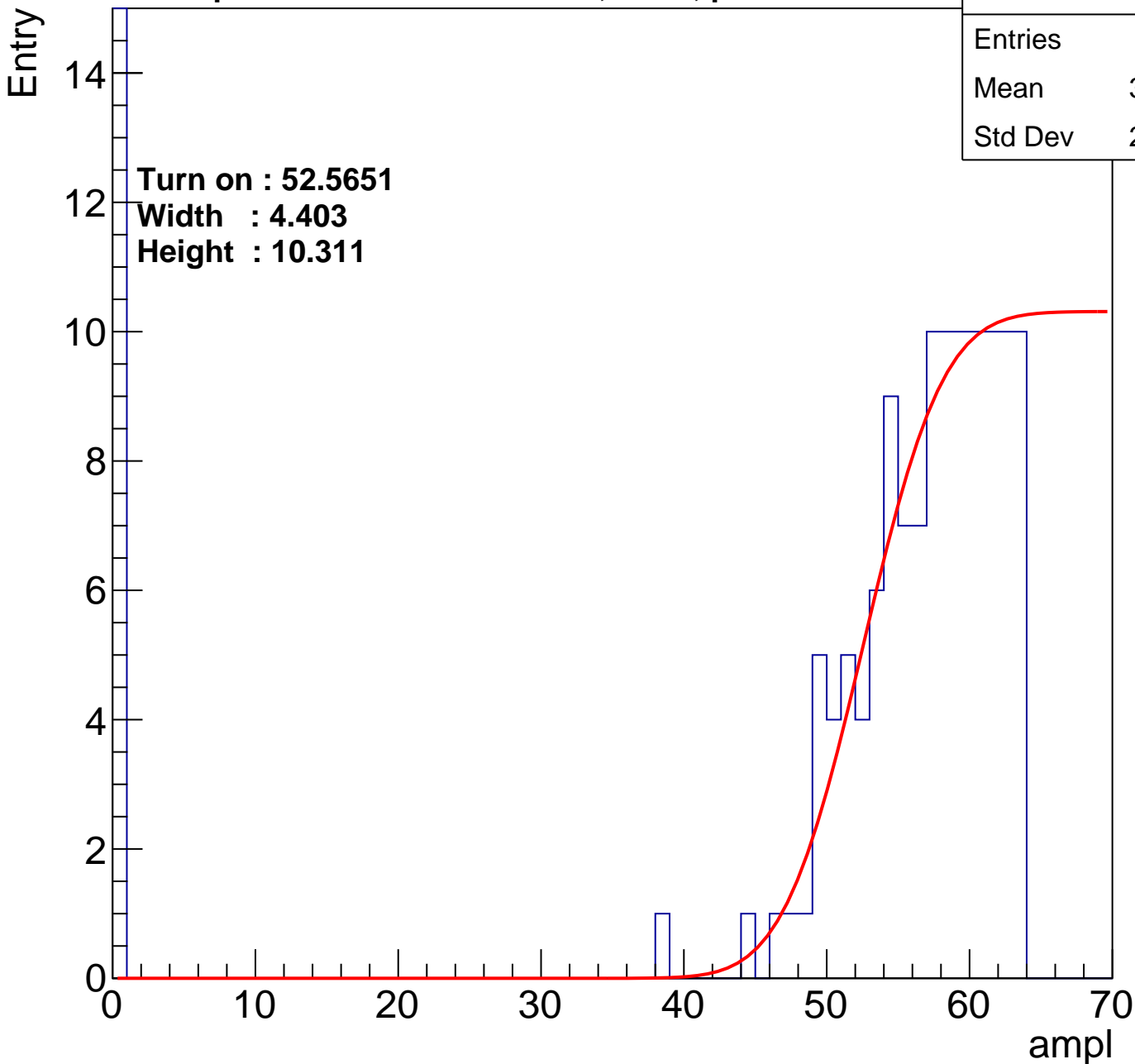
calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	32.45
Std Dev	28.25

Turn on : 52.5651

Width : 4.403

Height : 10.311



B1L104S, U10-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	35.94
Std Dev	28.51

Turn on : 54.9420

Width : 3.138

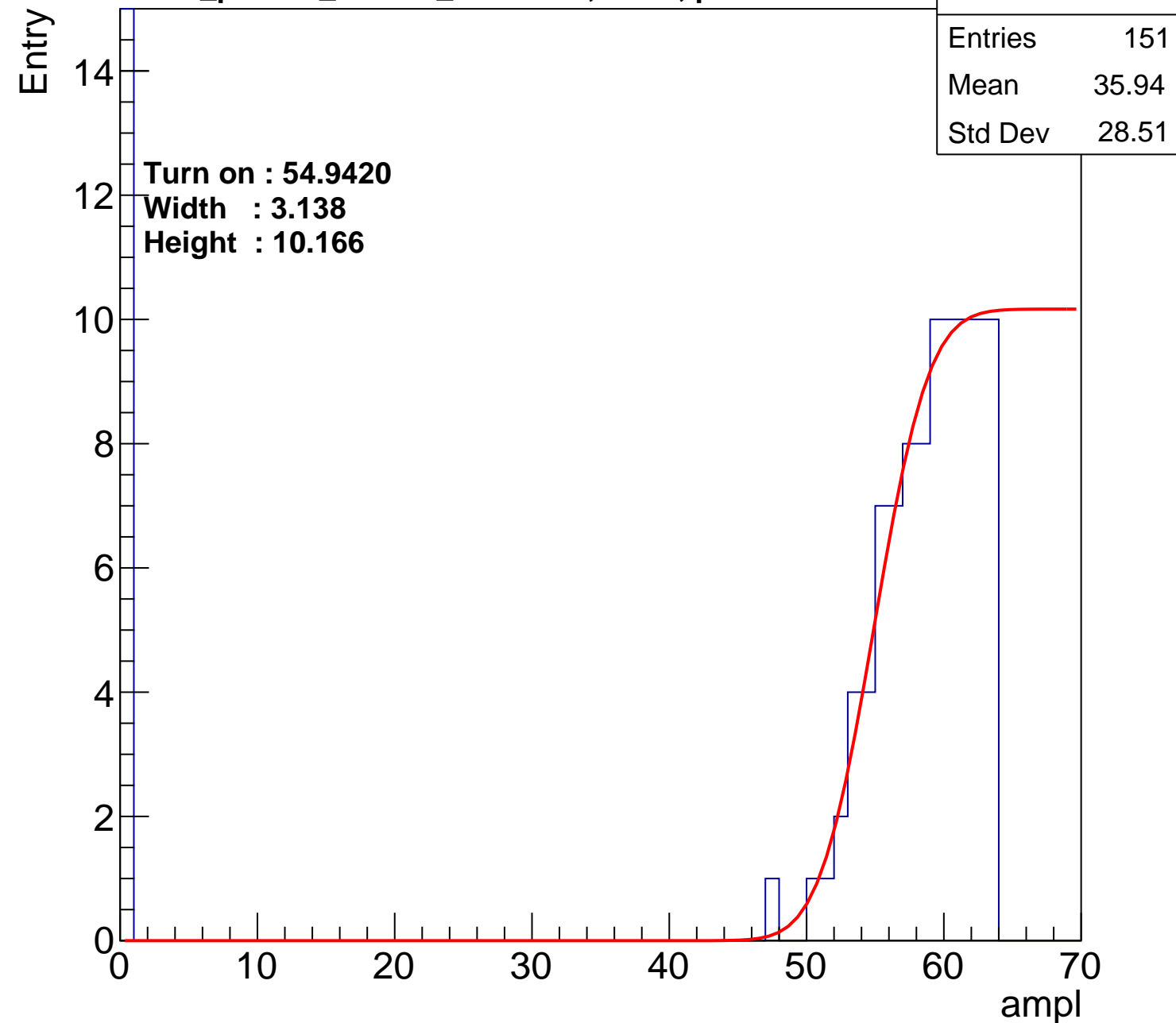
Height : 10.166

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U10-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	35.62
Std Dev	27.61

Turn on : 52.7262

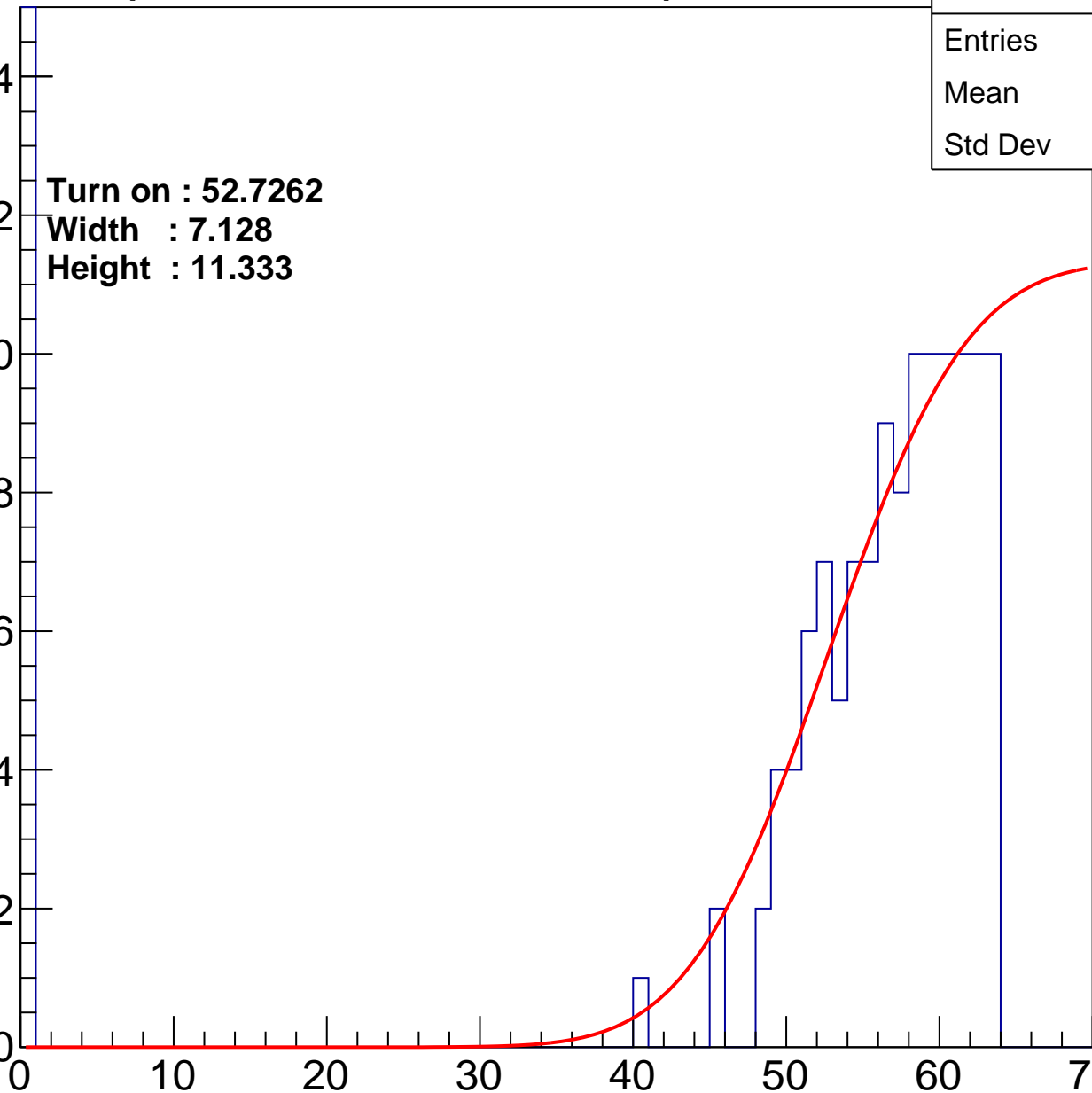
Width : 7.128

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	33.7
Std Dev	28.55

Turn on : 54.6527

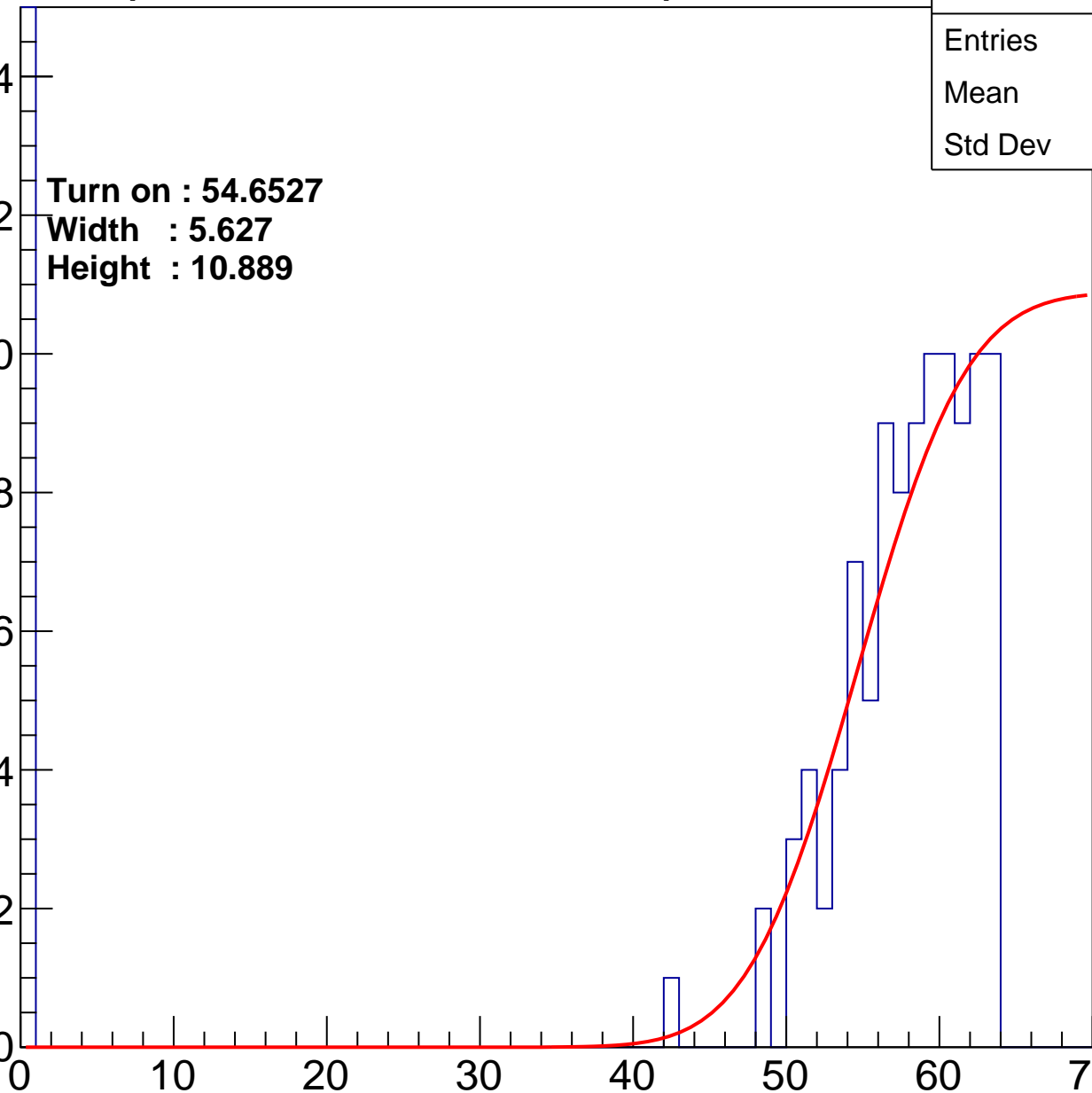
Width : 5.627

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	29.12
Std Dev	29.09

Turn on : 55.6461

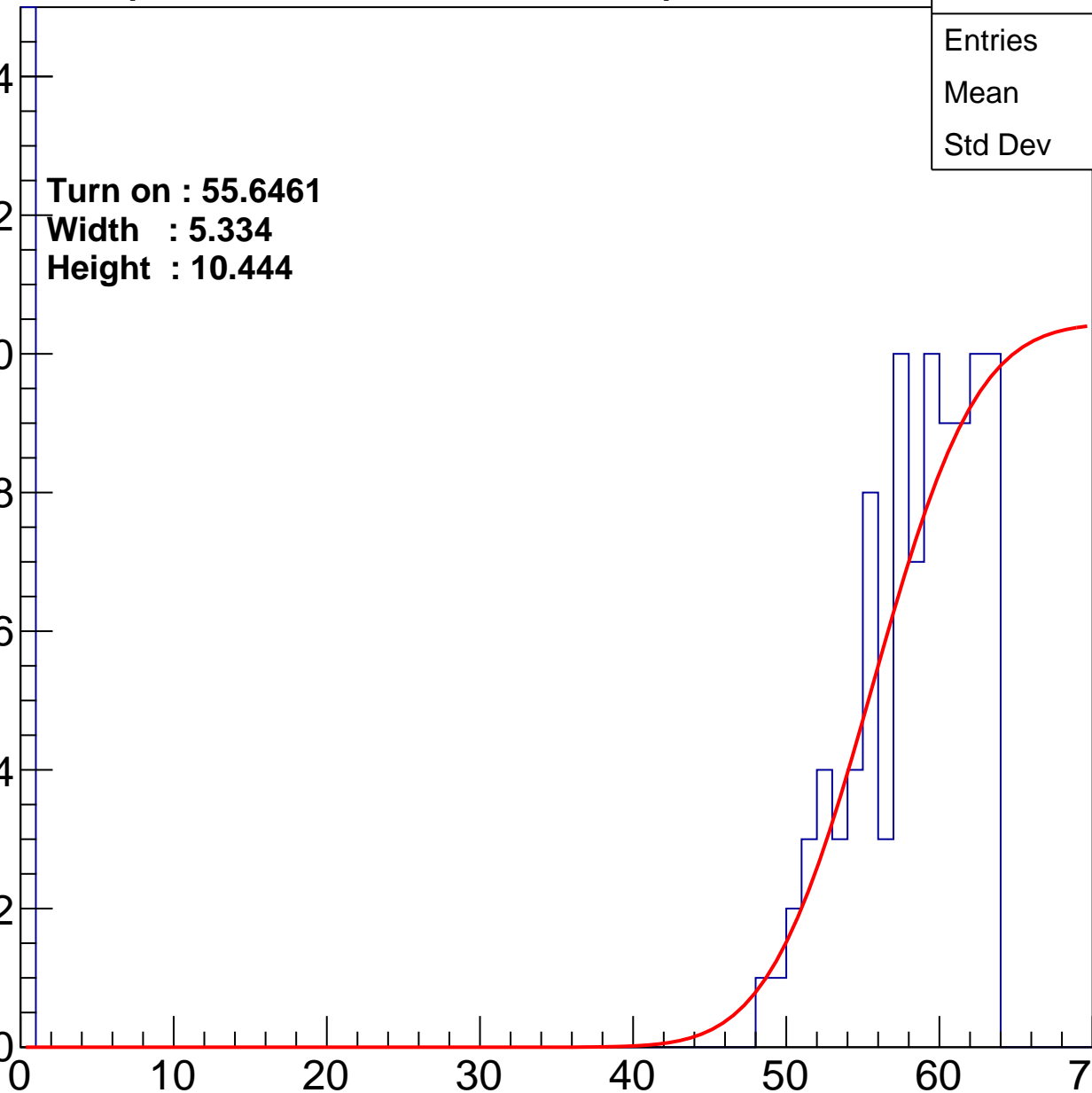
Width : 5.334

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	31.75
Std Dev	28.94

Turn on : 53.3543

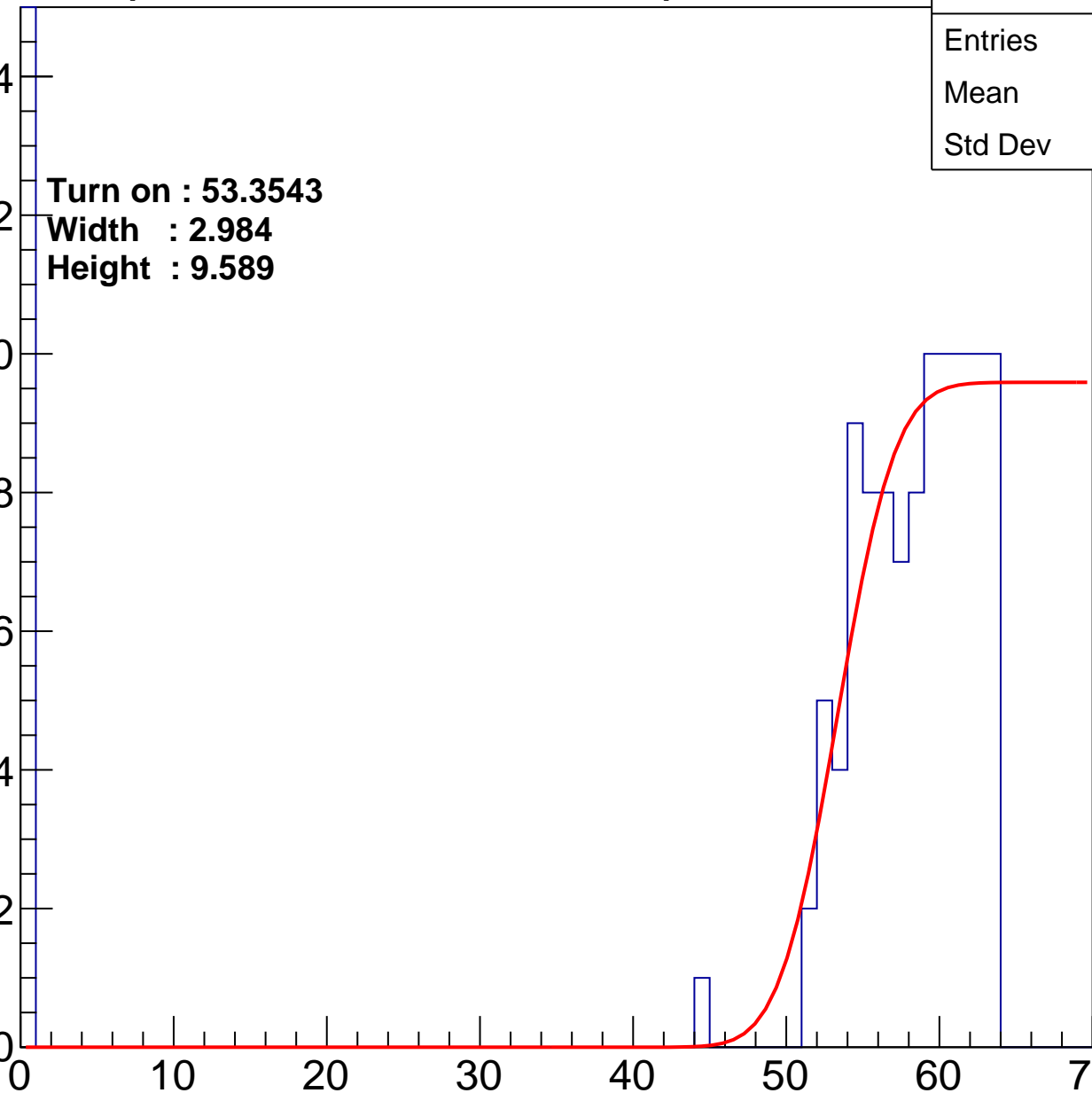
Width : 2.984

Height : 9.589

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	34.26
Std Dev	28.23

Turn on : 54.0546

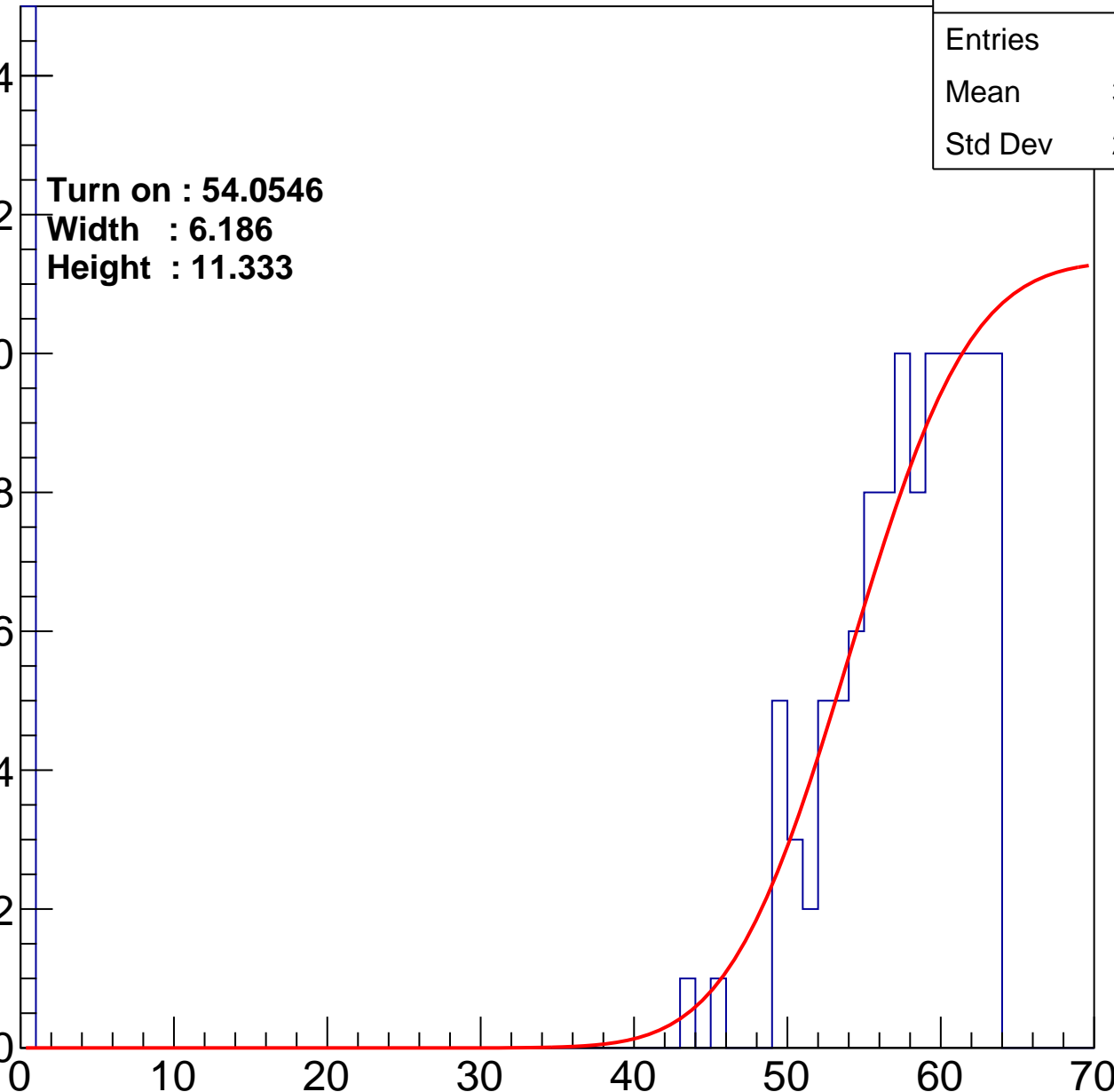
Width : 6.186

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch9

calib_packv5_033123_0516.root, FC#4, port A1

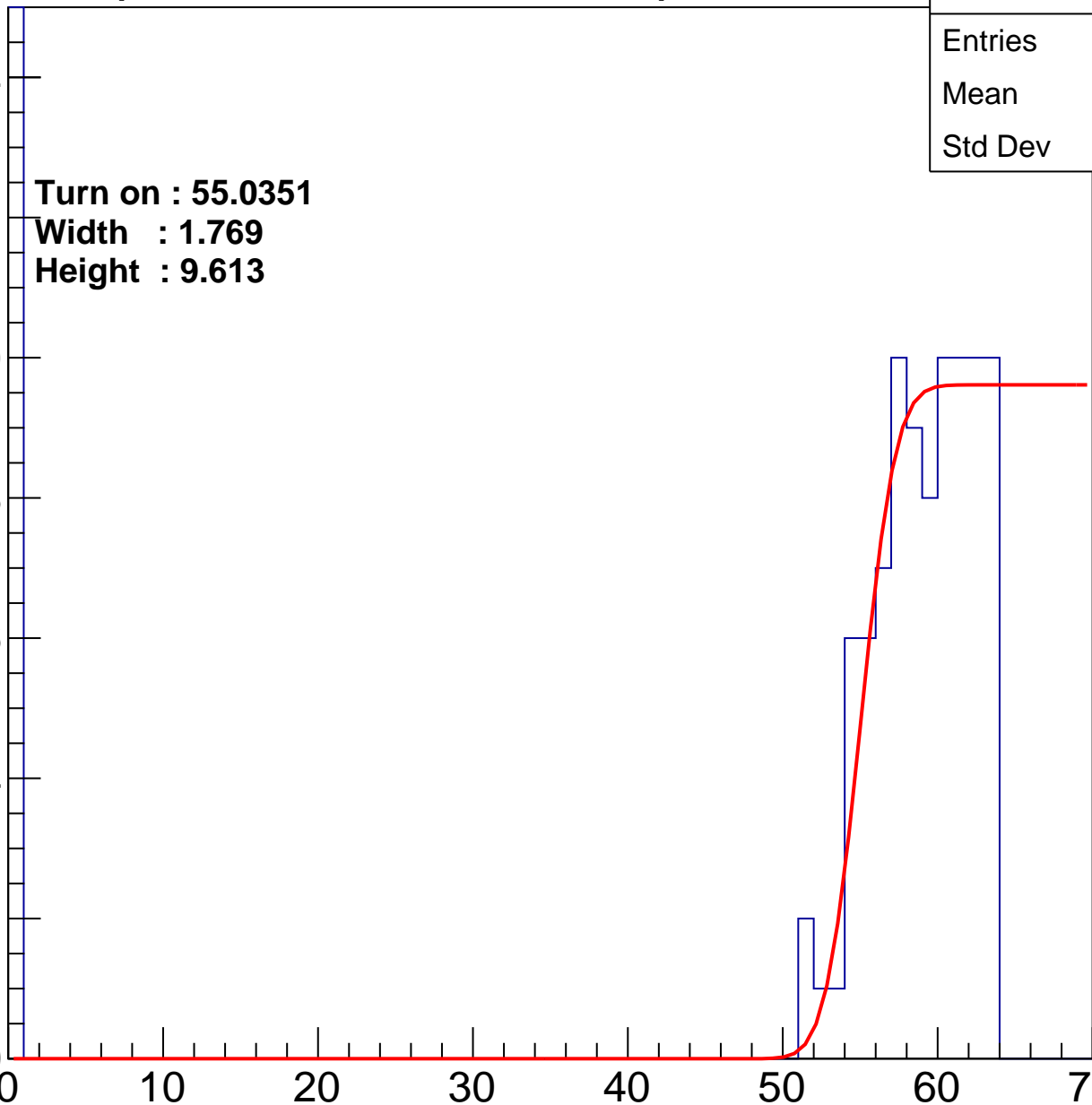
Entry

14
12
10
8
6
4
2
0

Turn on : 55.0351
Width : 1.769
Height : 9.613

Entries	170
Mean	31.04
Std Dev	29.35

ampl



B1L104S, U10-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	31.11
Std Dev	28.64

Turn on : 52.6148

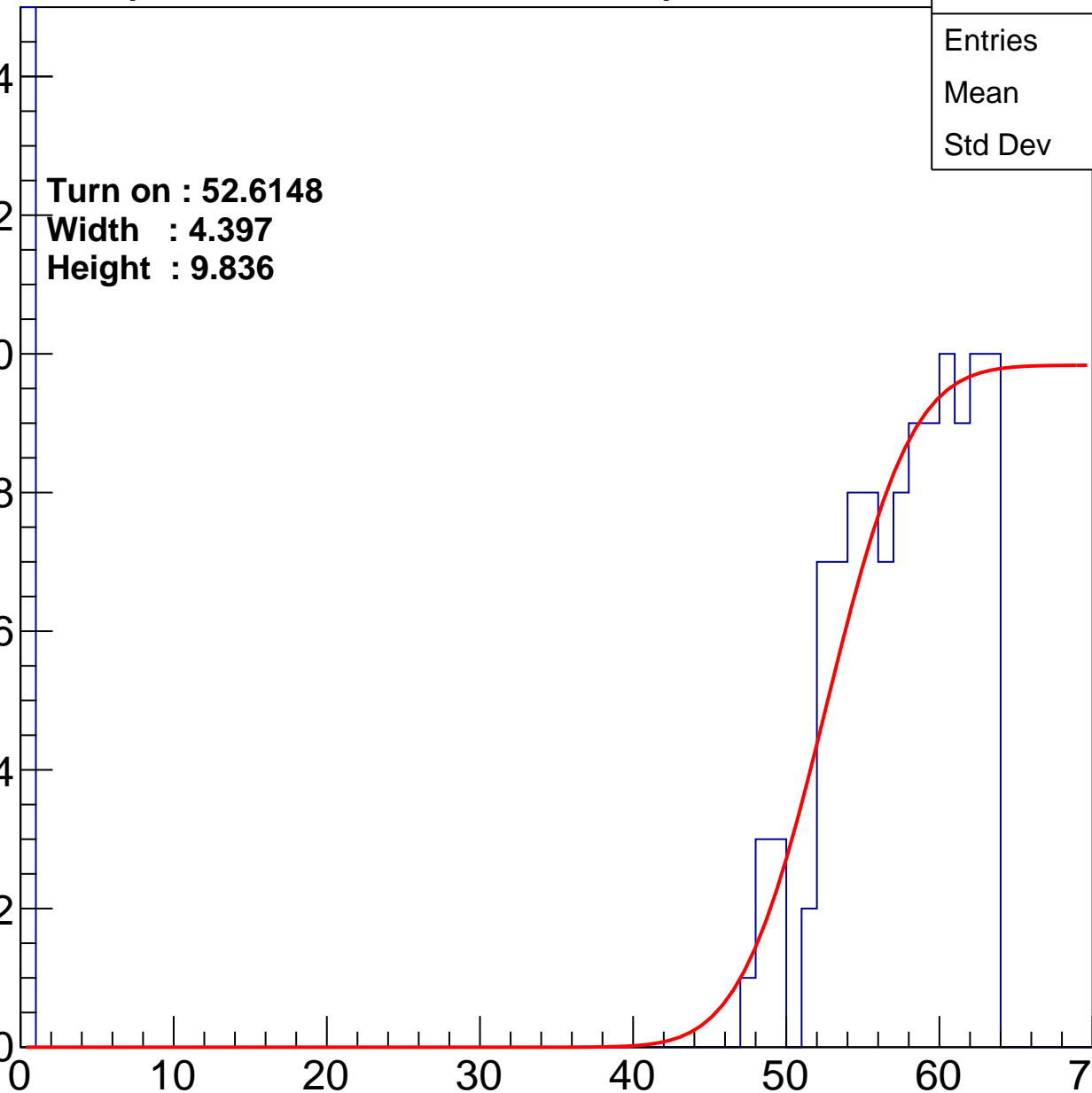
Width : 4.397

Height : 9.836

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch11

calib_packv5_033123_0516.root, FC#4, port A1

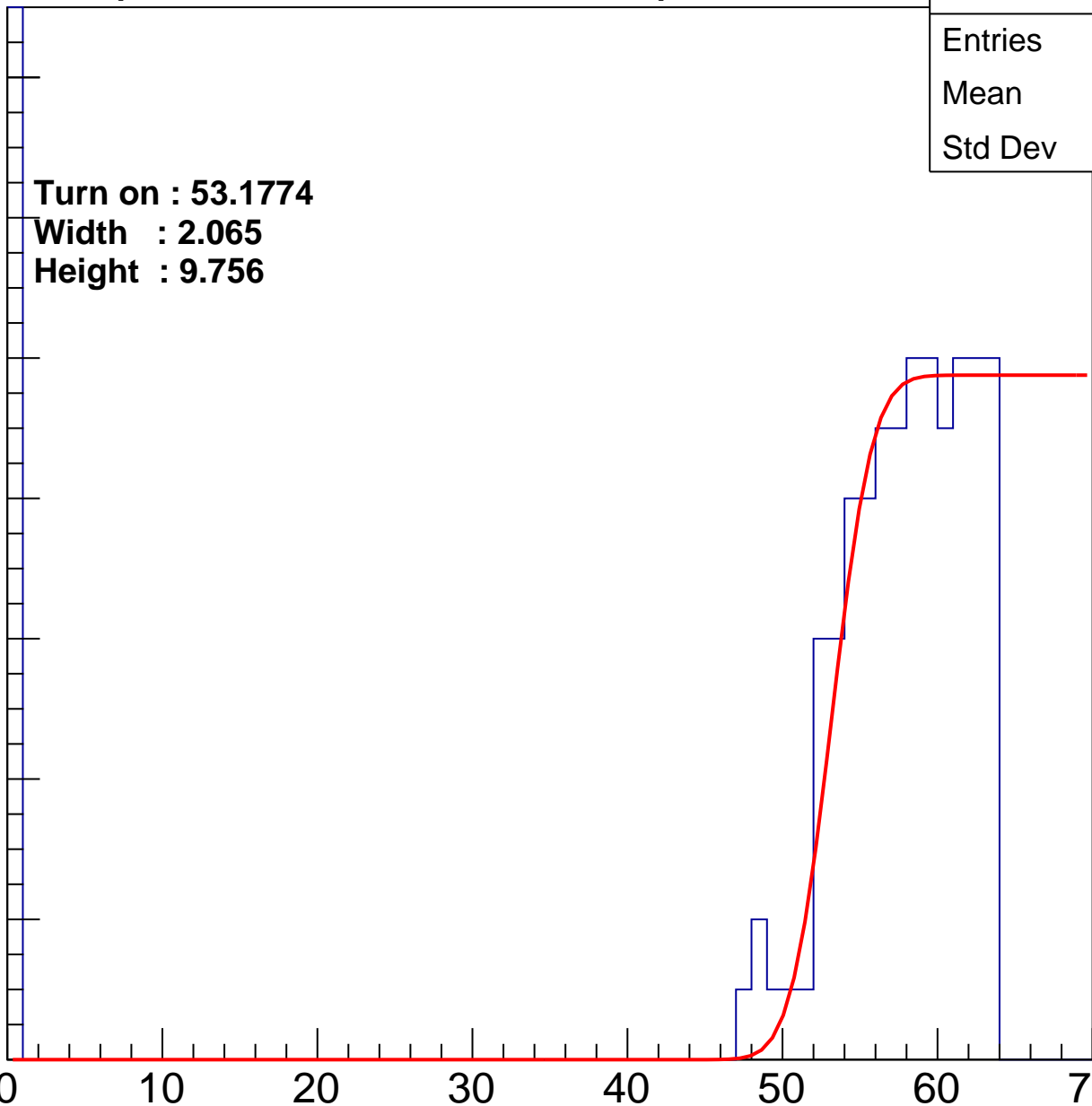
Entry

14
12
10
8
6
4
2
0

Turn on : 53.1774
Width : 2.065
Height : 9.756

Entries	171
Mean	37.32
Std Dev	27.61

ampl



B1L104S, U10-ch12

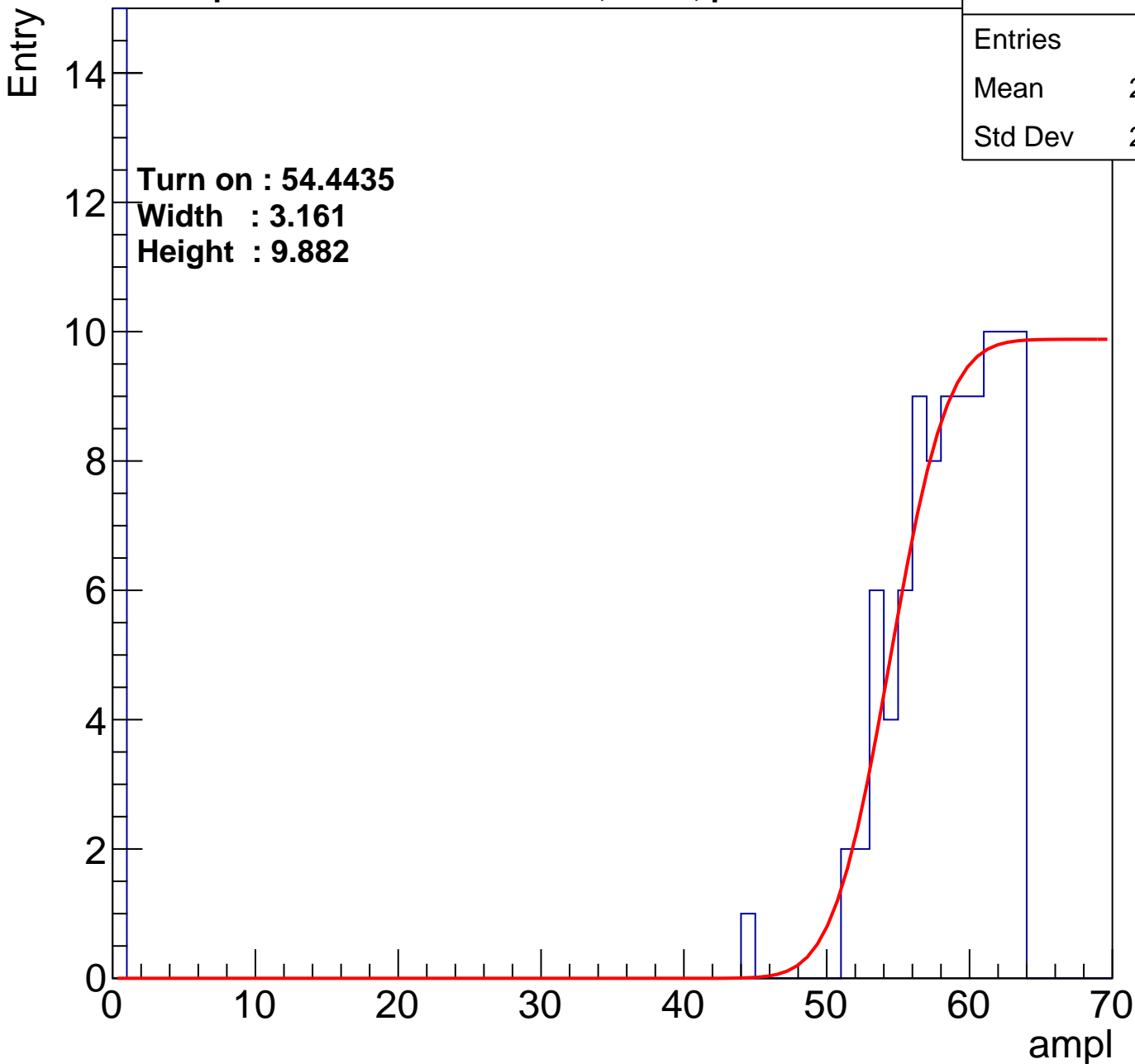
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	28.34
Std Dev	29.19

Turn on : 54.4435

Width : 3.161

Height : 9.882



B1L104S, U10-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	33.27
Std Dev	28.76

Turn on : 54.3027

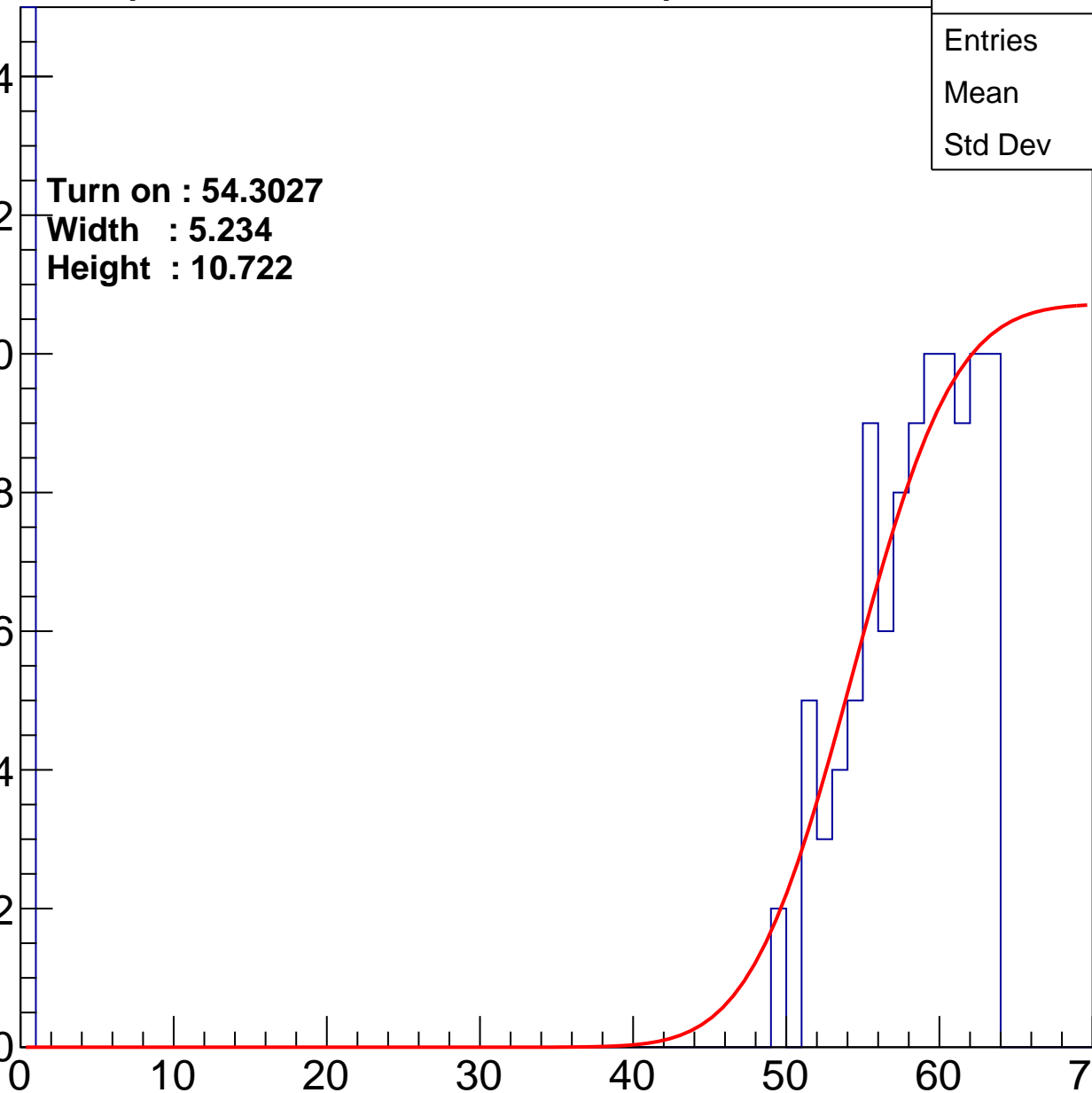
Width : 5.234

Height : 10.722

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	31.64
Std Dev	28.29

Turn on : 51.2301

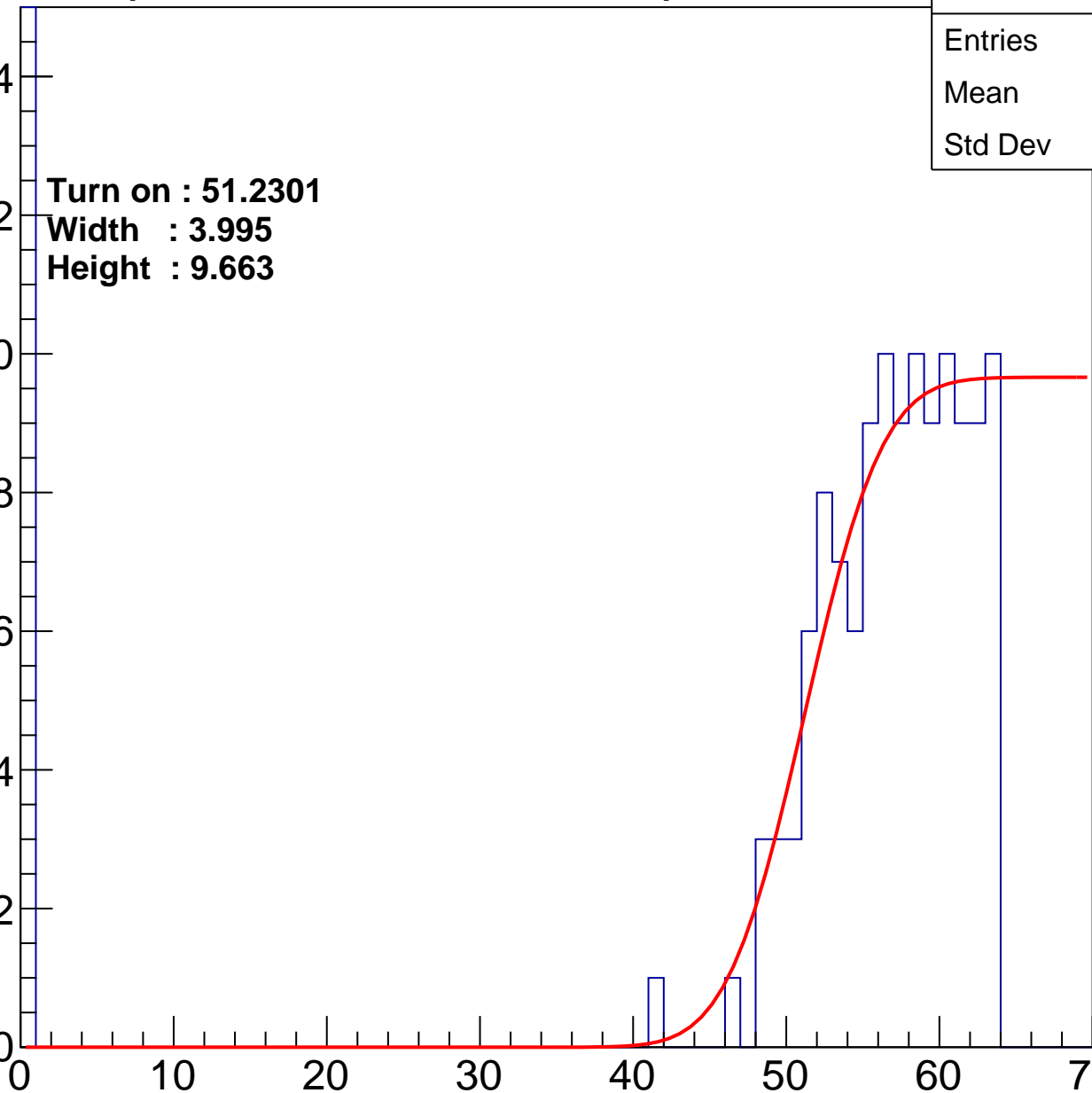
Width : 3.995

Height : 9.663

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	34.63
Std Dev	28.61

Turn on : 54.6834

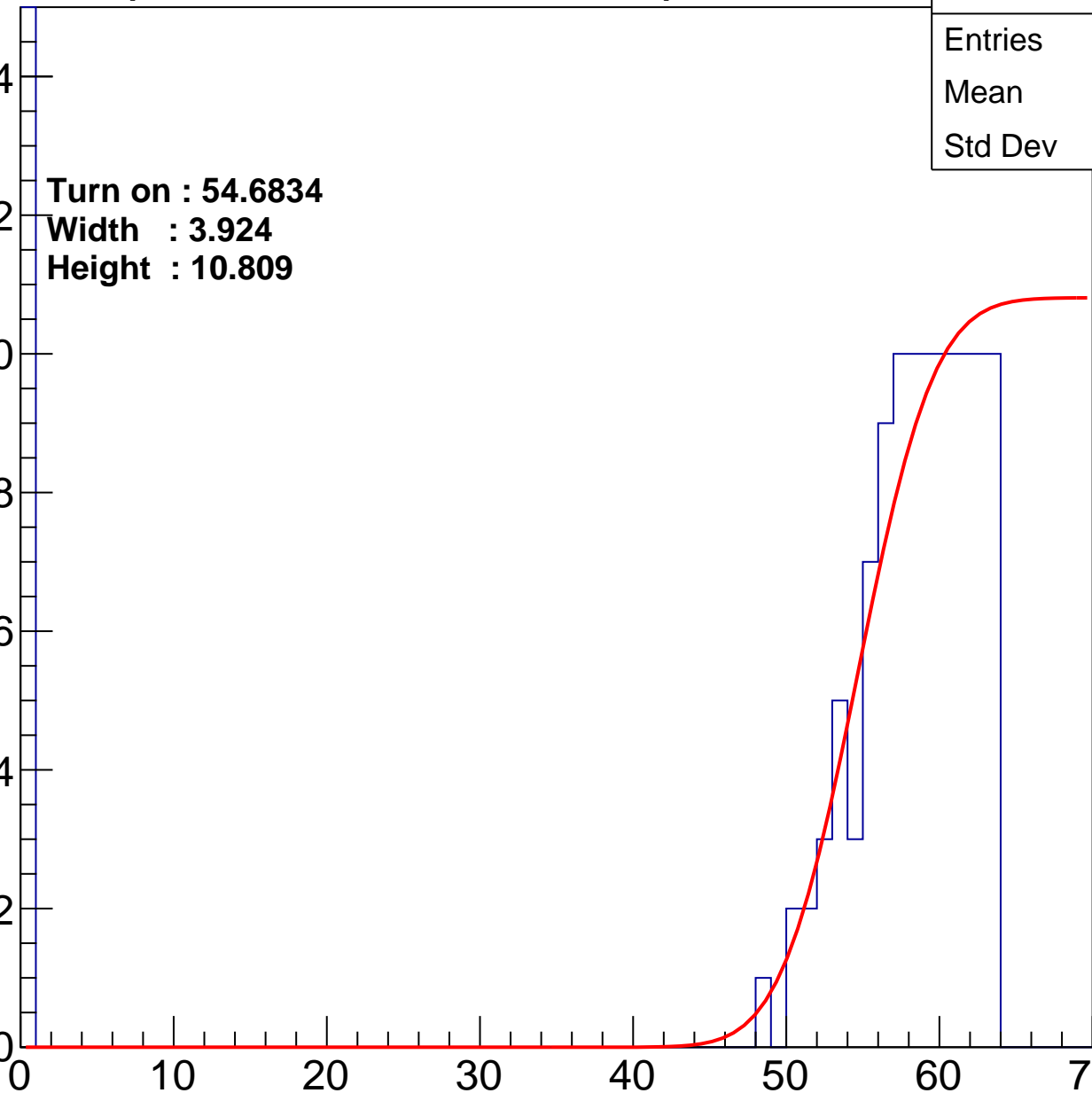
Width : 3.924

Height : 10.809

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	259
Mean	29.01
Std Dev	27.7

Turn on : 54.6007

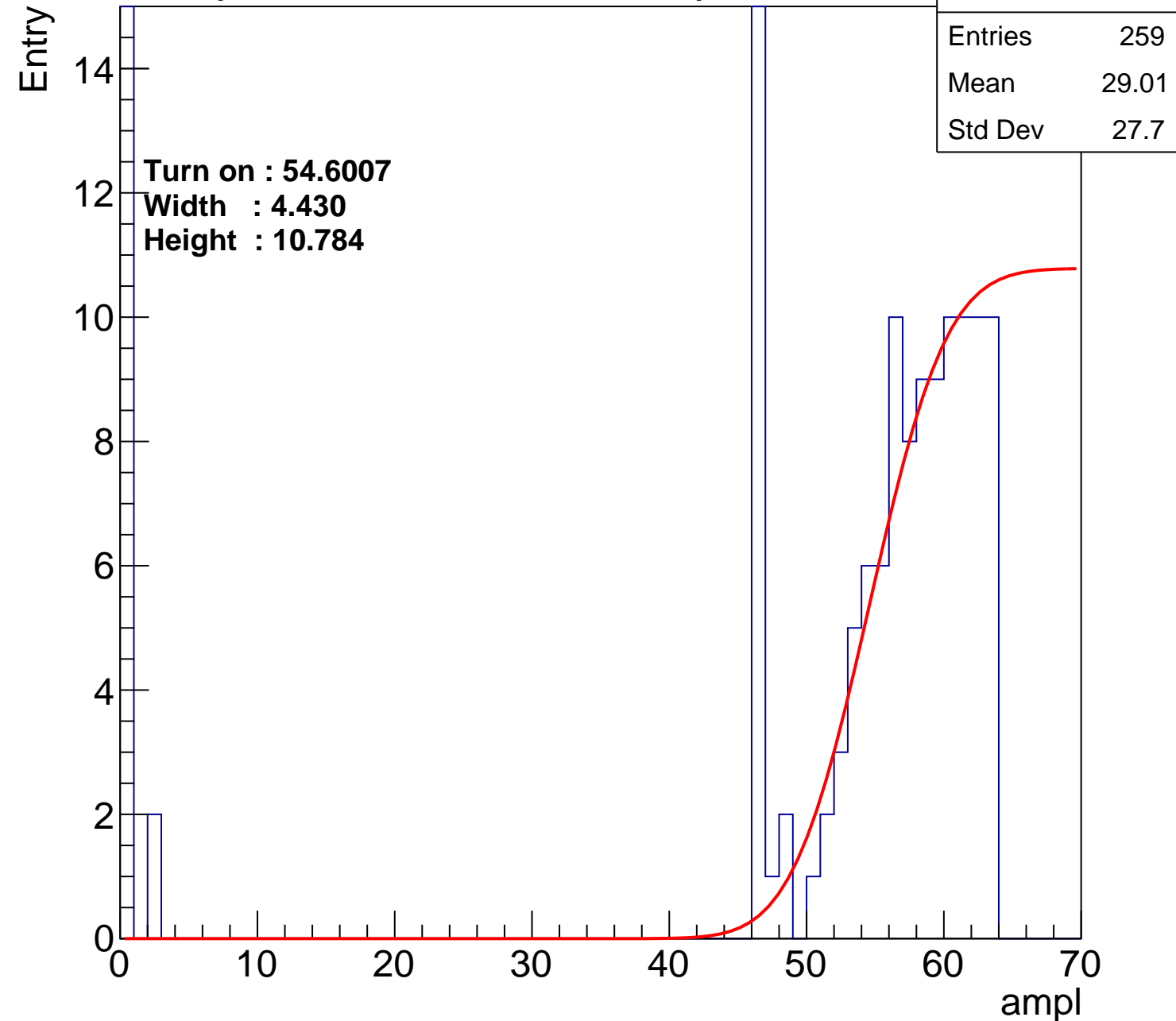
Width : 4.430

Height : 10.784

Entry

14
12
10
8
6
4
2
0

ampl



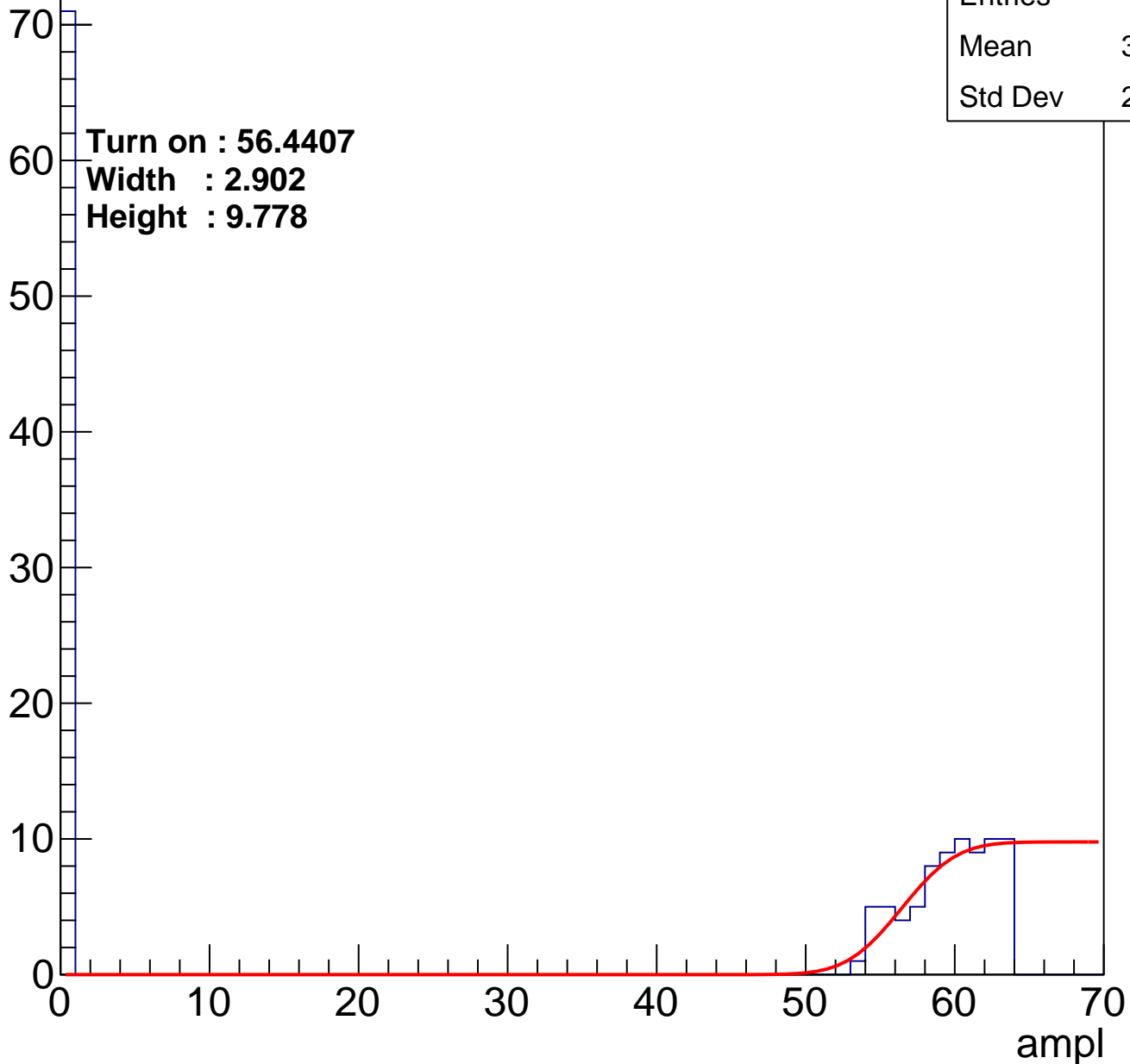
B1L104S, U10-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	147
Mean	30.62
Std Dev	29.66

Turn on : 56.4407
Width : 2.902
Height : 9.778

Entry



B1L104S, U10-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	37.6
Std Dev	27.39

Turn on : 53.5361

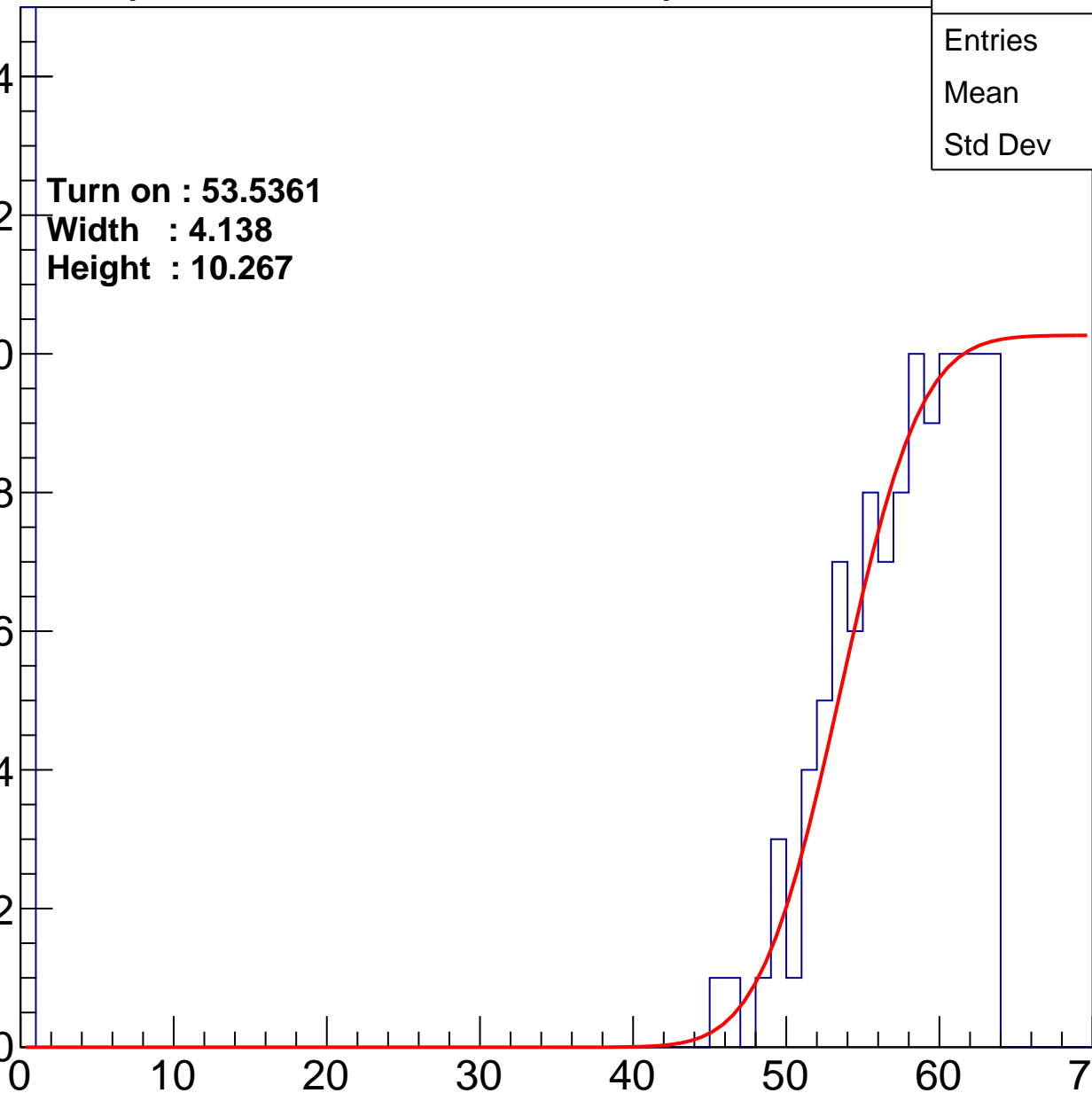
Width : 4.138

Height : 10.267

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch19

calib_packv5_033123_0516.root, FC#4, port A1

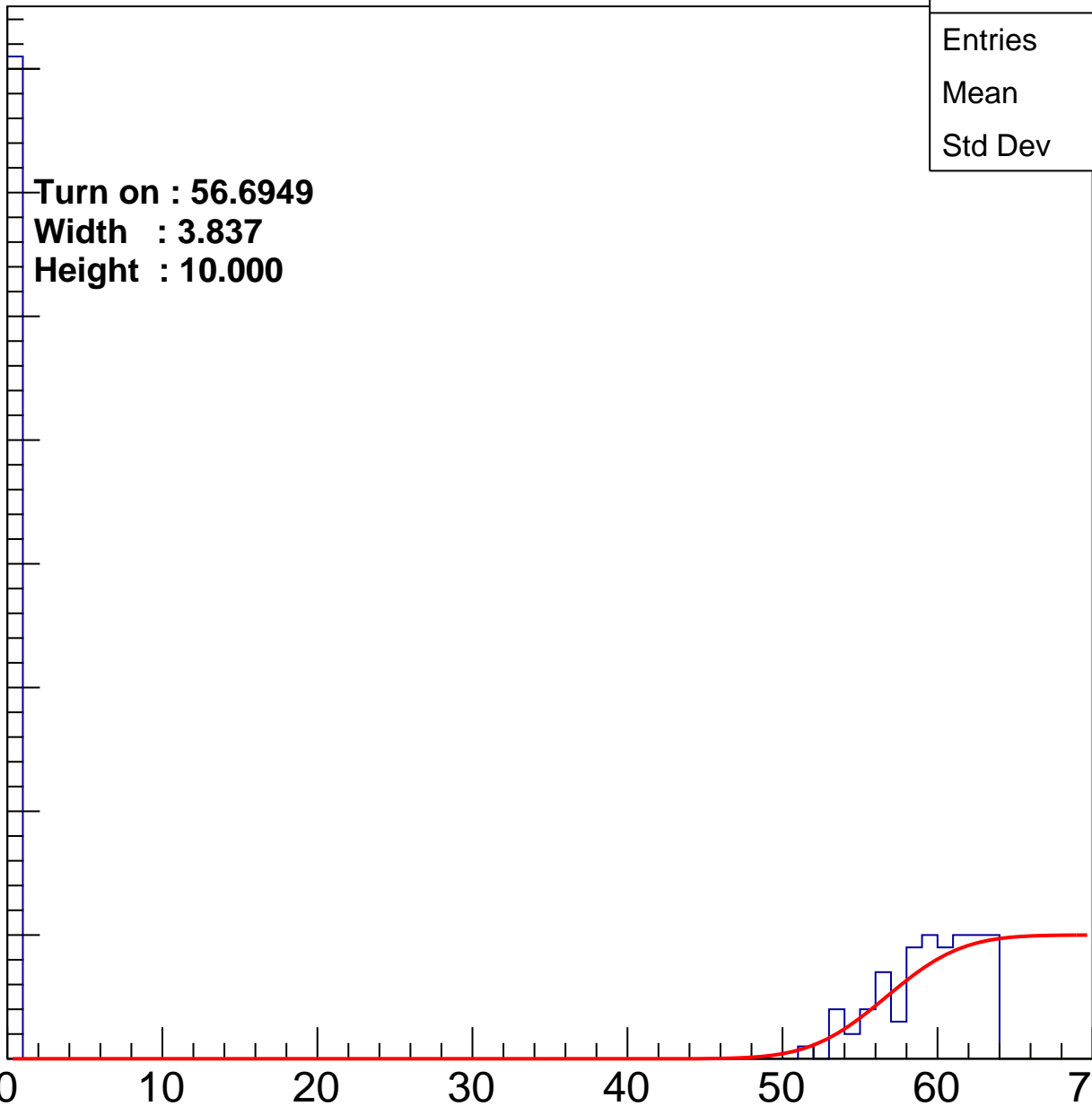
Entry

80
70
60
50
40
30
20
10
0

Turn on : 56.6949
Width : 3.837
Height : 10.000

Entries	160
Mean	29.16
Std Dev	29.6

ampl



B1L104S, U10-ch20

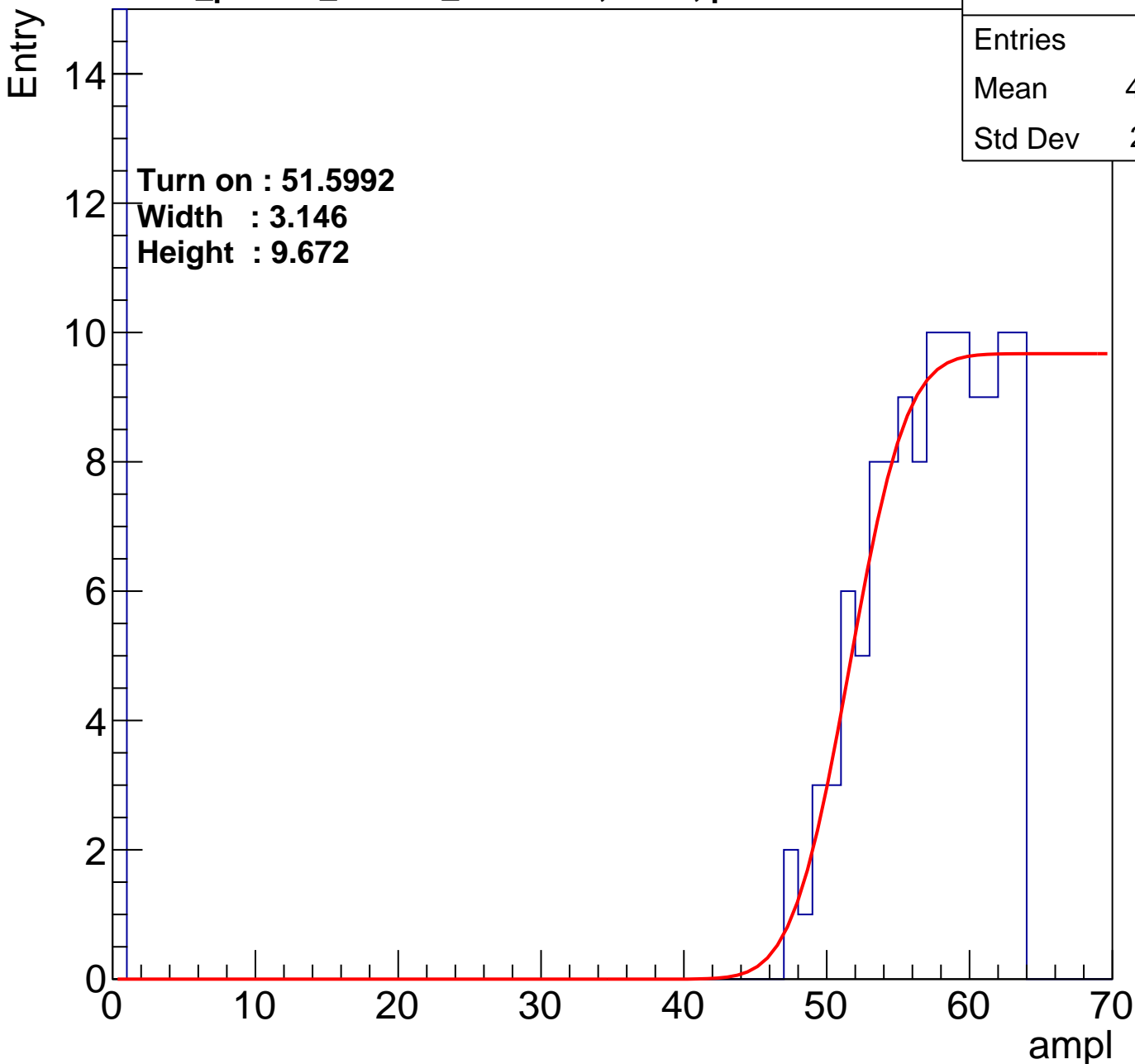
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	40.25
Std Dev	26.11

Turn on : 51.5992

Width : 3.146

Height : 9.672



B1L104S, U10-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	33.37
Std Dev	28.57

Turn on : 53.3695

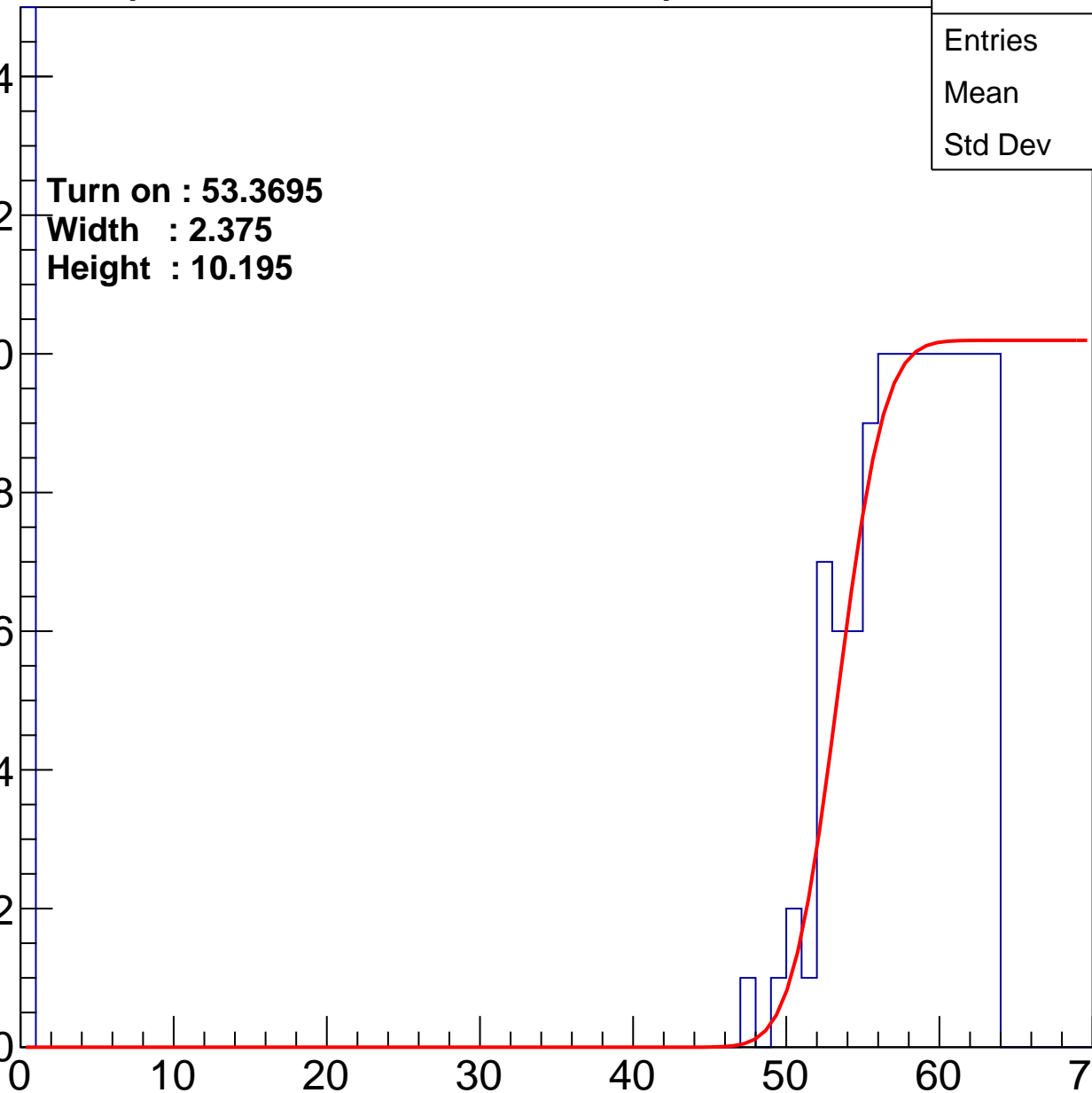
Width : 2.375

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	30.95
Std Dev	28.59

Turn on : 54.1272

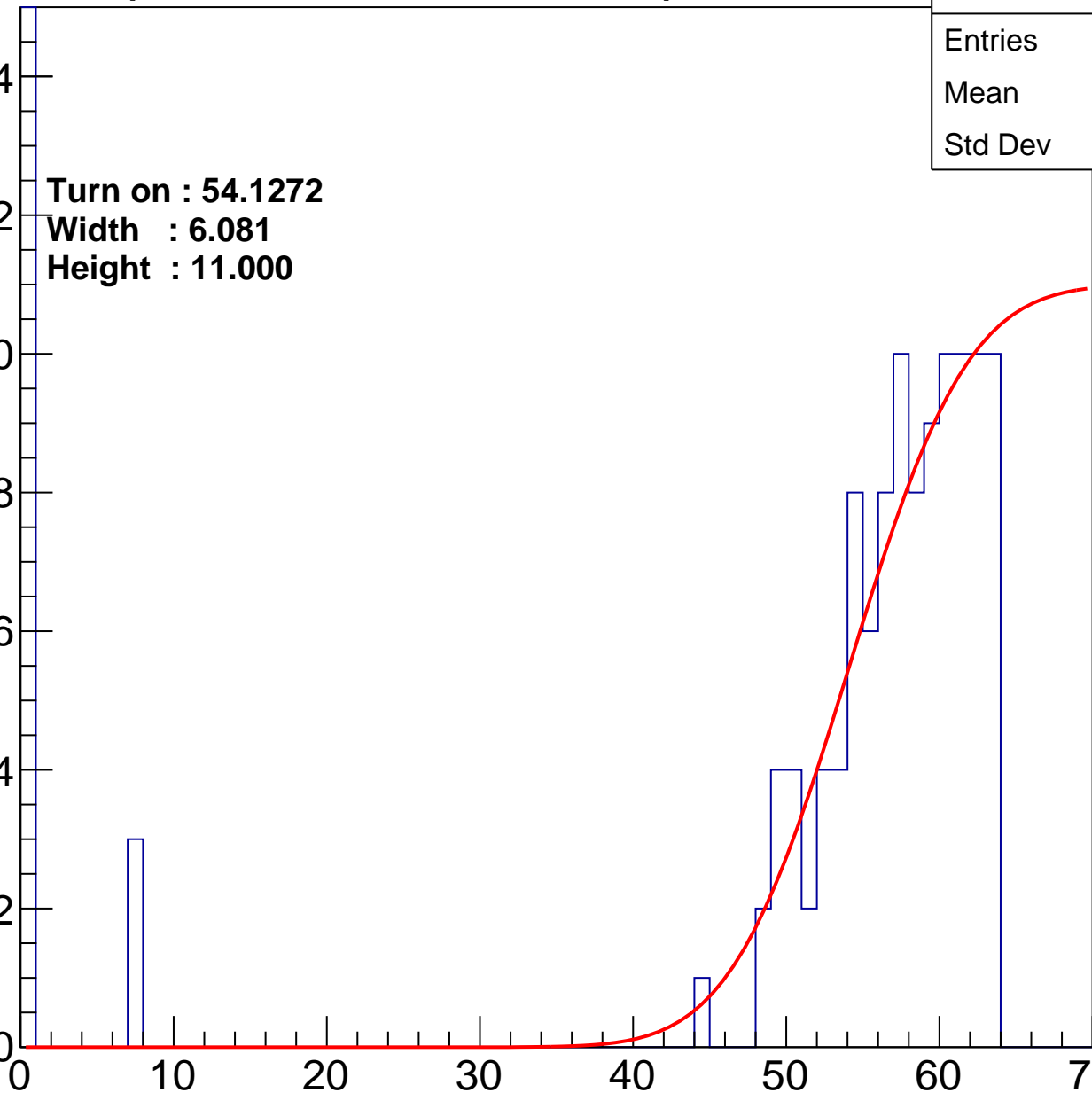
Width : 6.081

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	31.72
Std Dev	28.87

Turn on : 53.7461

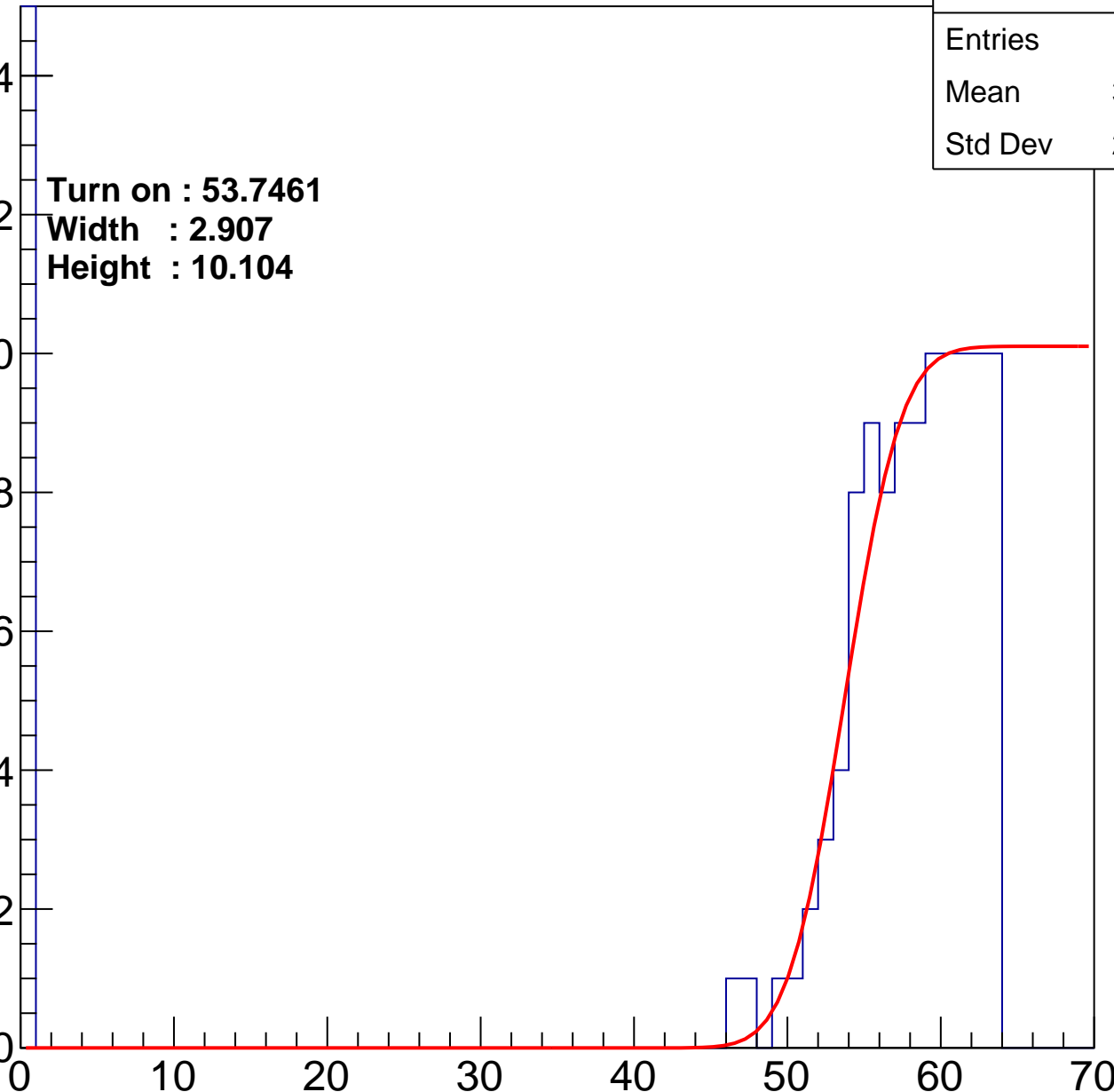
Width : 2.907

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	33.53
Std Dev	28.59

Turn on : 54.4793

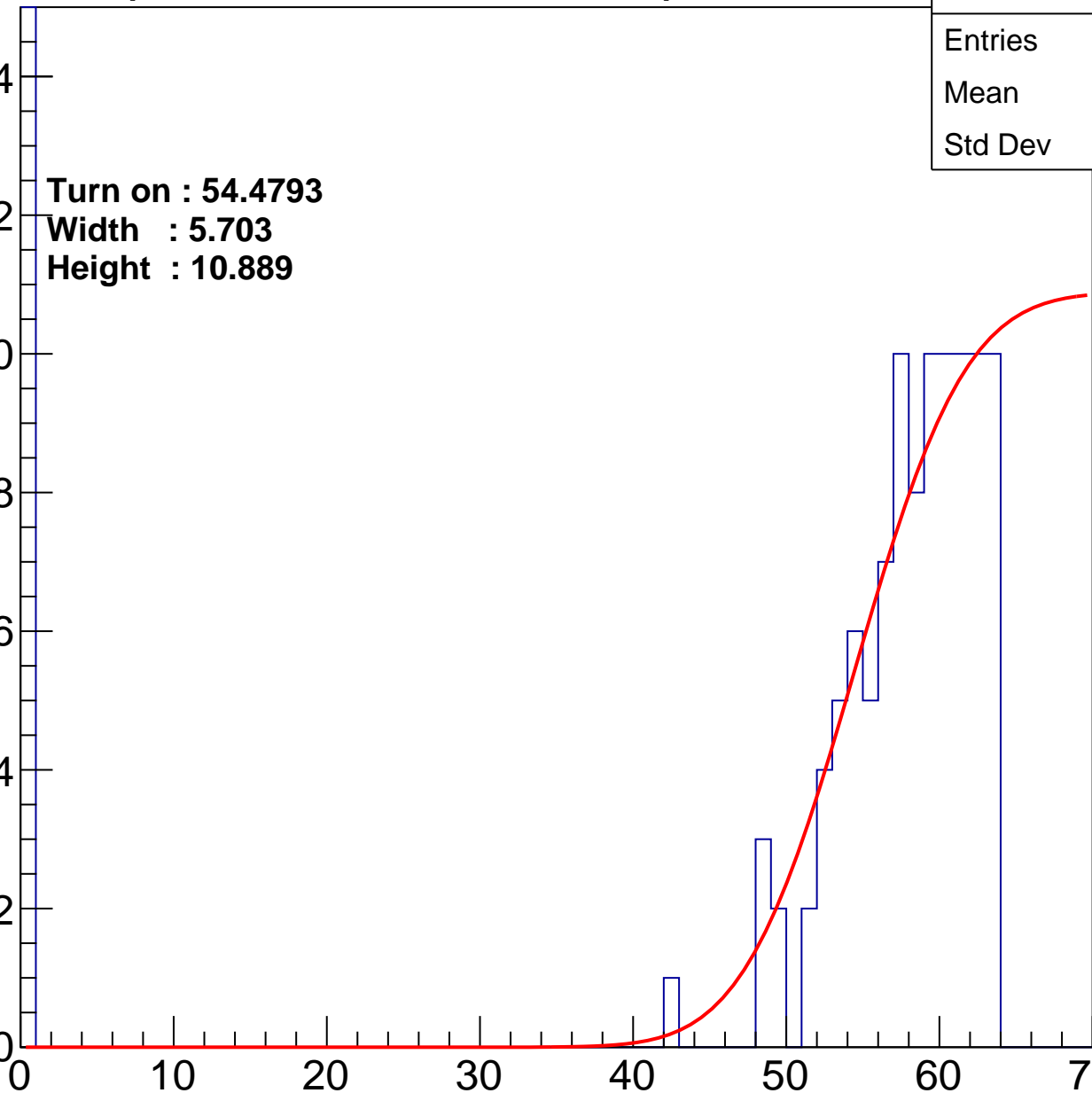
Width : 5.703

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	32.83
Std Dev	28.96

Turn on : 54.9026

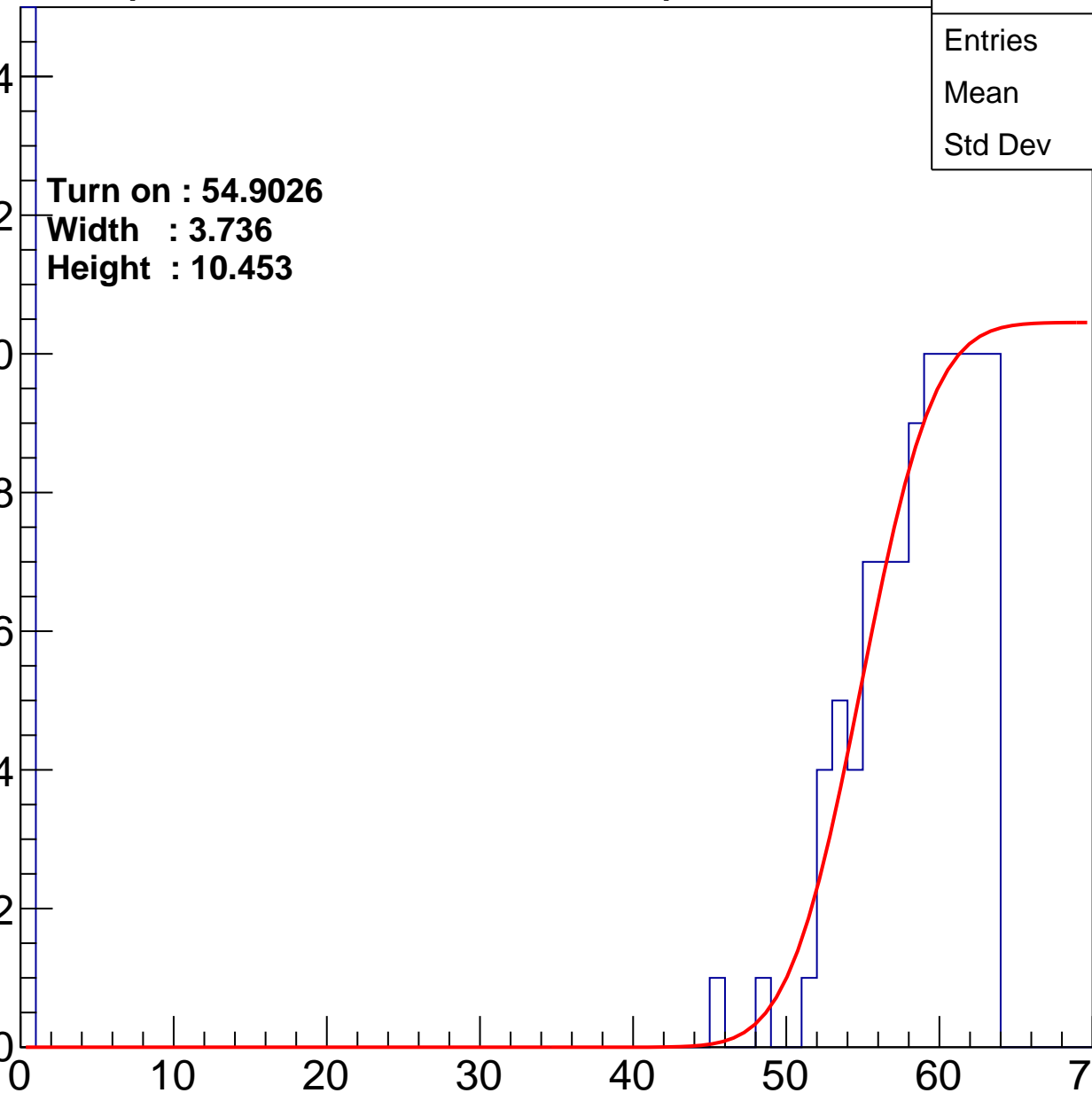
Width : 3.736

Height : 10.453

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	34.69
Std Dev	27.91

Turn on : 52.5480

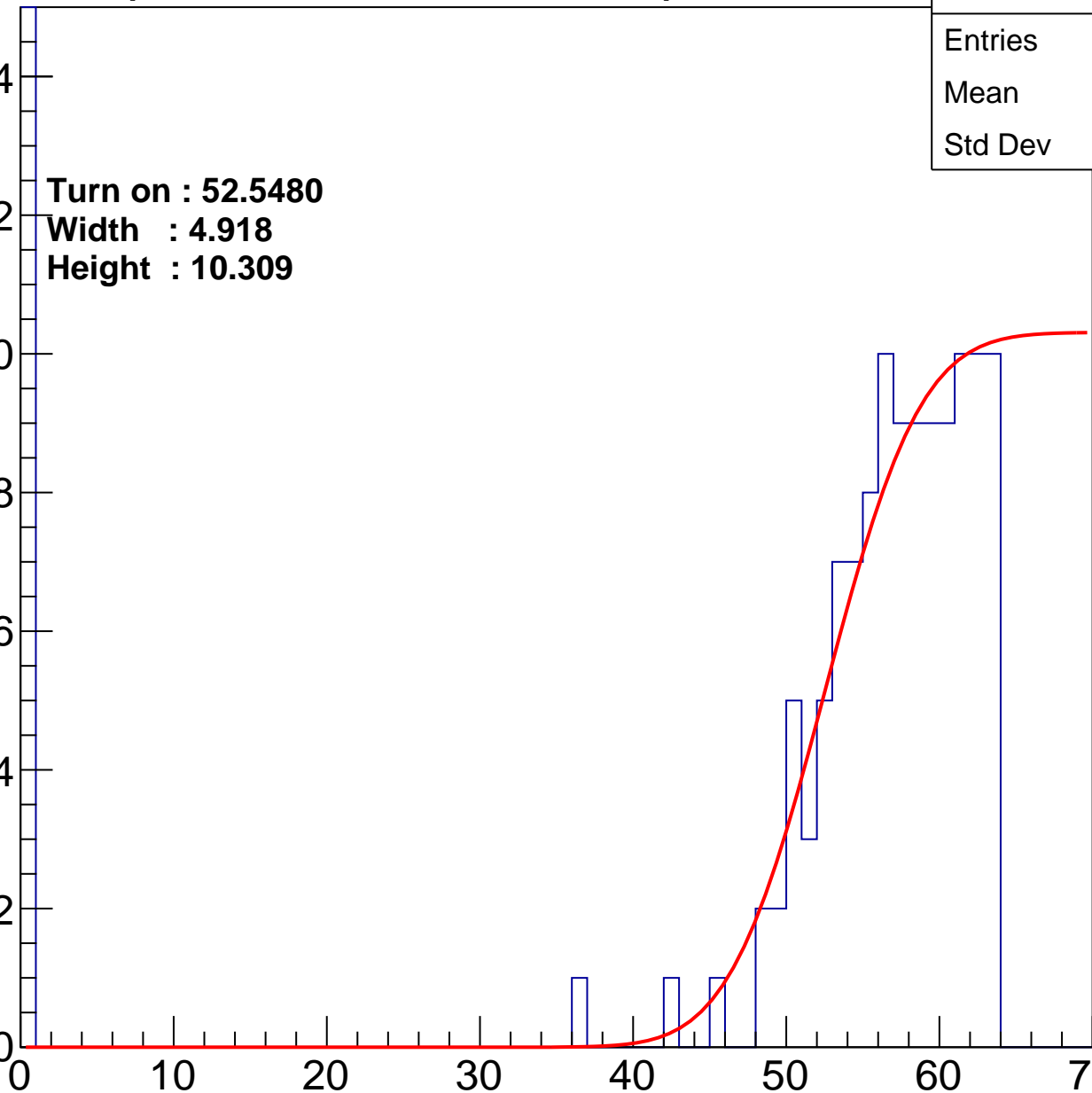
Width : 4.918

Height : 10.309

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch27

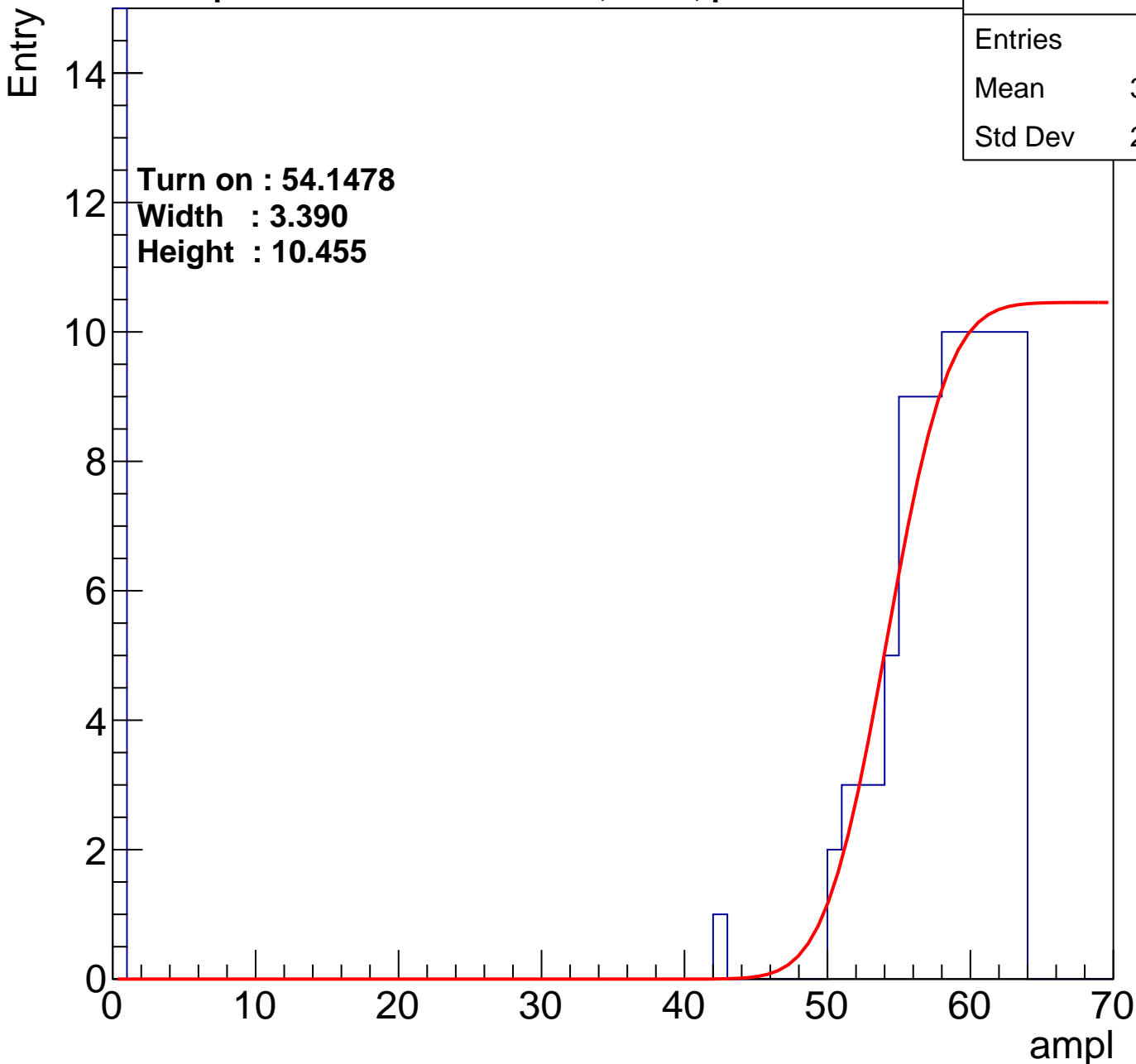
calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	36.94
Std Dev	27.99

Turn on : 54.1478

Width : 3.390

Height : 10.455



B1L104S, U10-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	34.49
Std Dev	27.97

Turn on : 52.3462

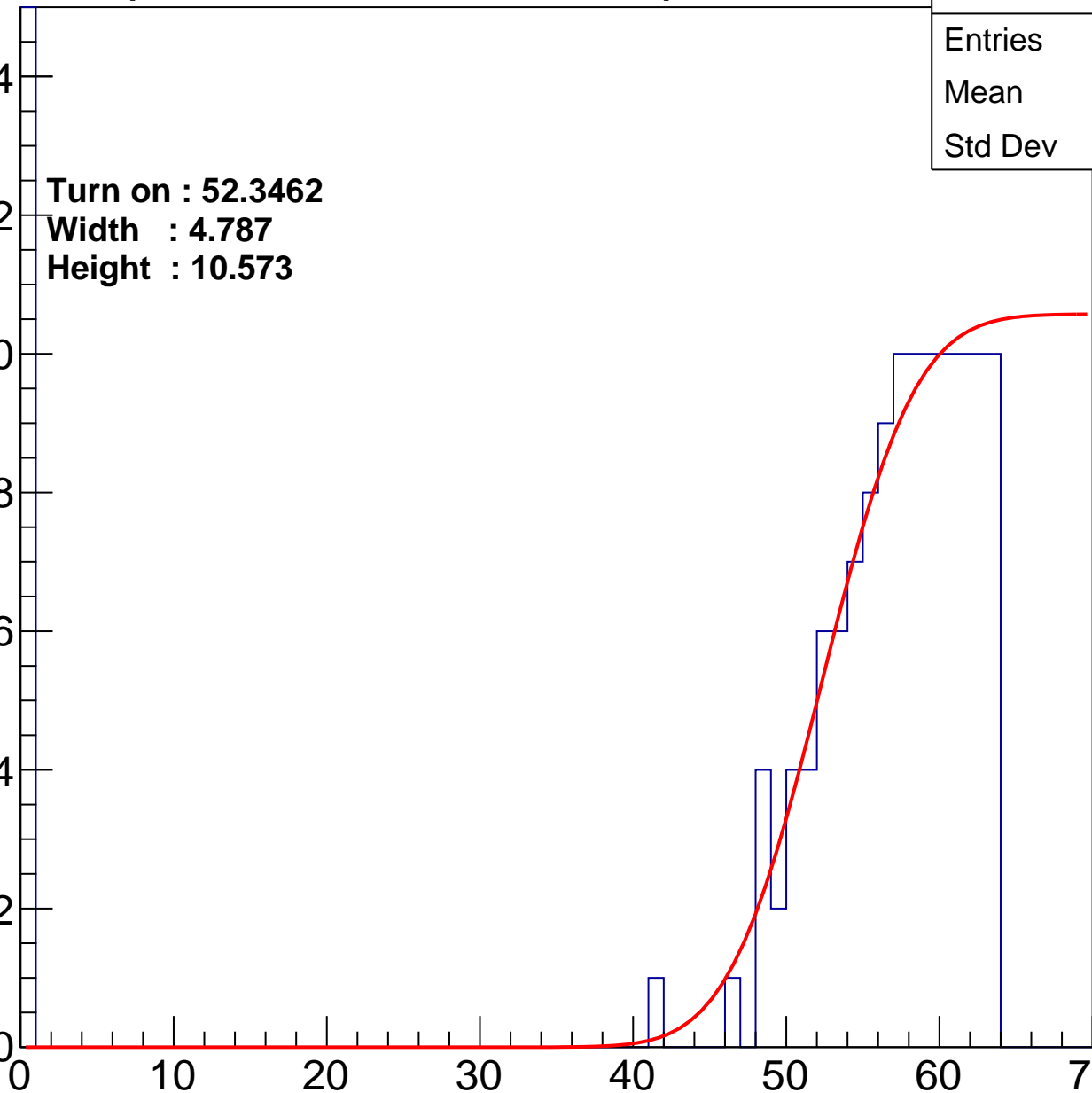
Width : 4.787

Height : 10.573

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	34.98
Std Dev	28.38

Turn on : 54.6110

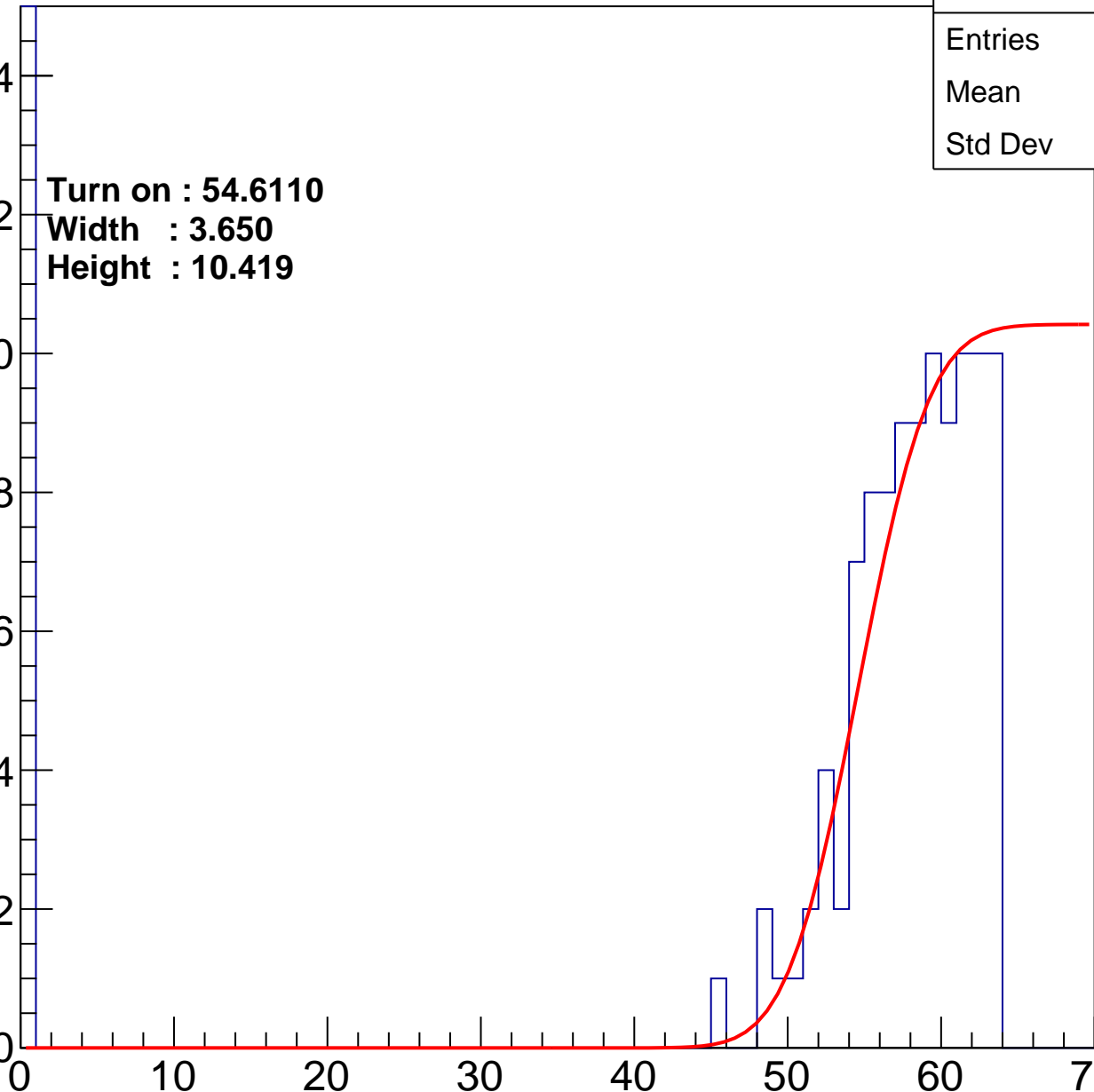
Width : 3.650

Height : 10.419

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch30

calib_packv5_033123_0516.root, FC#4, port A1

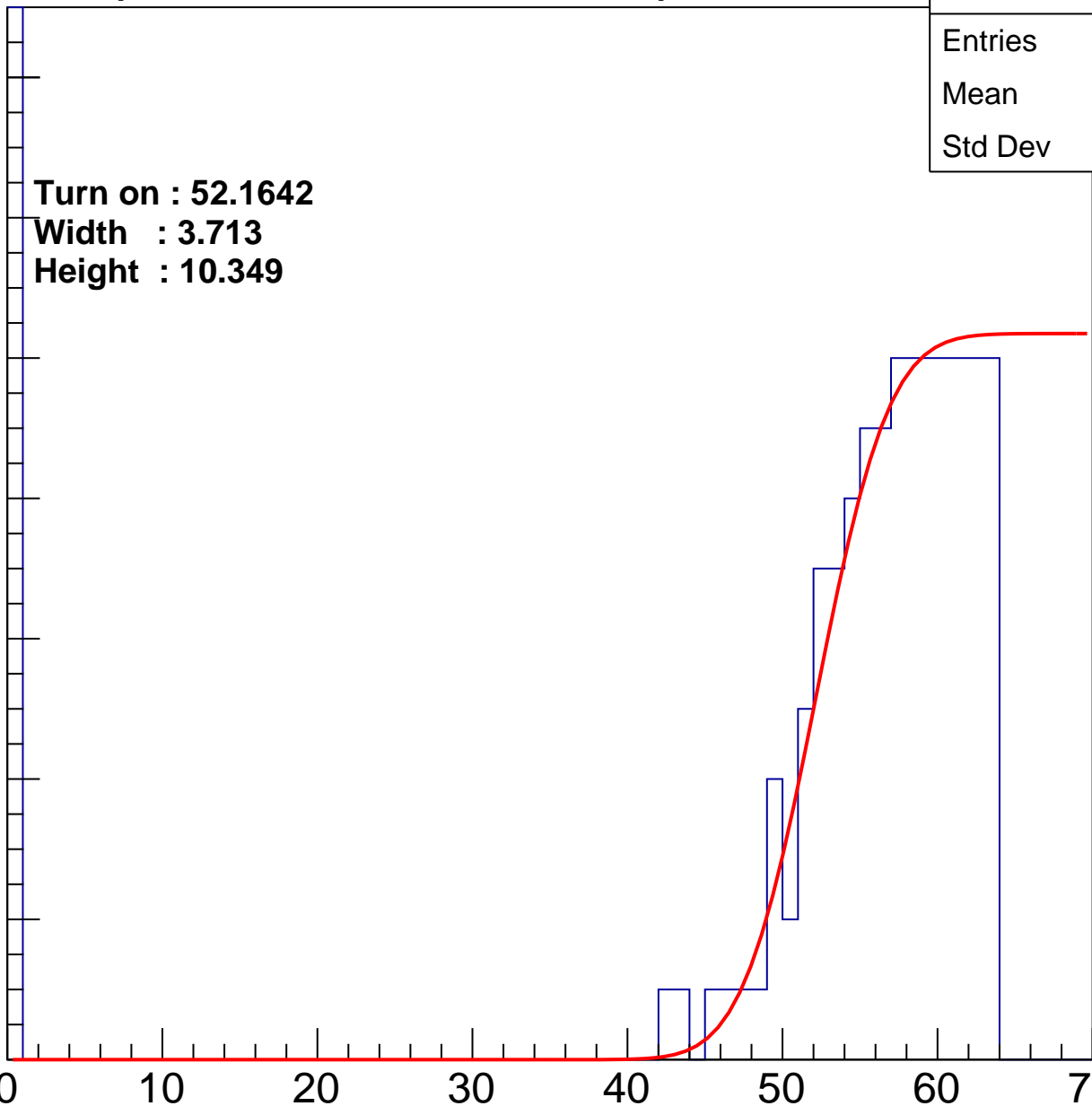
Entry

14
12
10
8
6
4
2
0

Turn on : 52.1642
Width : 3.713
Height : 10.349

Entries	208
Mean	34.56
Std Dev	27.83

ampl



B1L104S, U10-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	32.4
Std Dev	29.09

Turn on : 54.1947

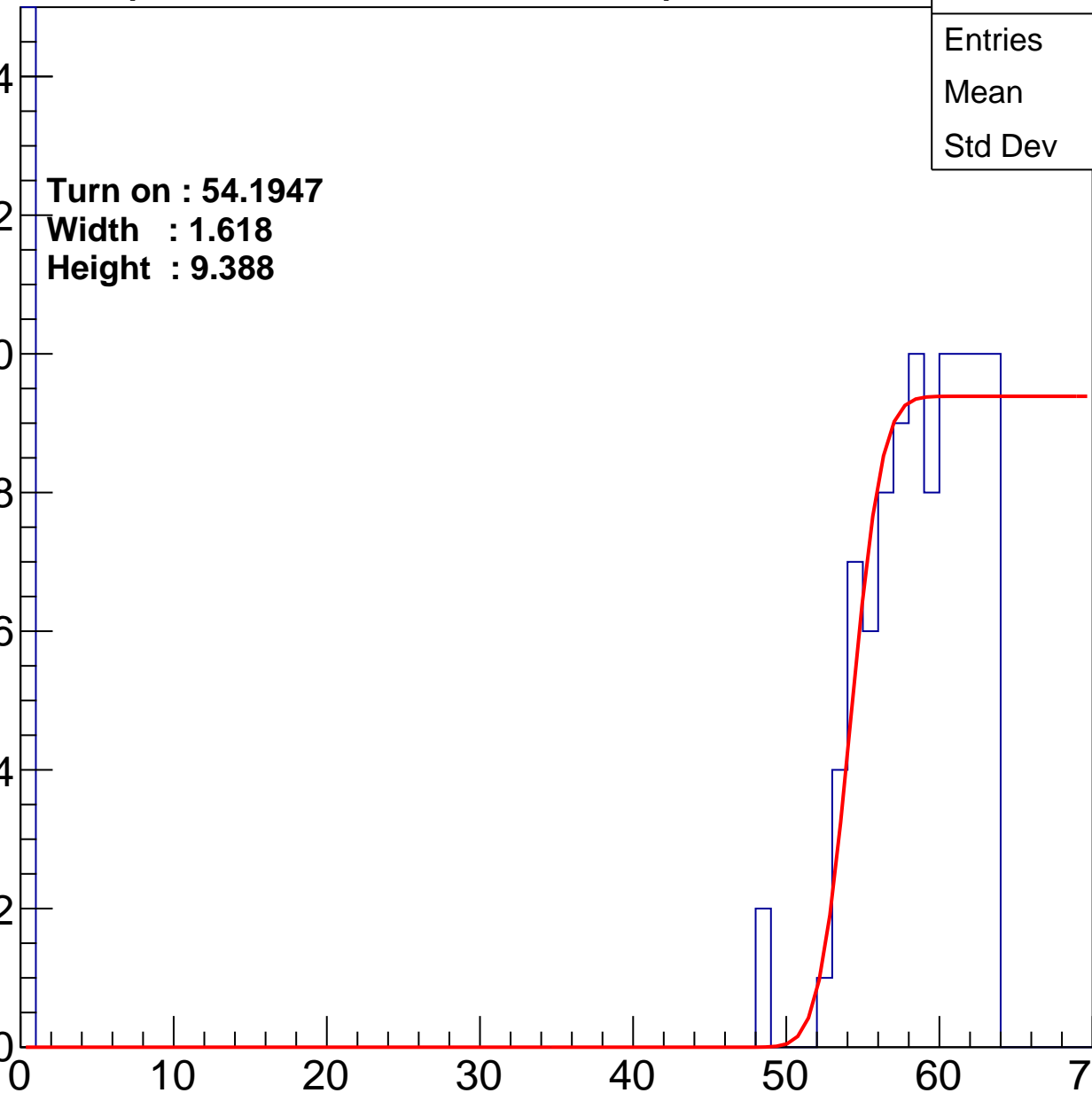
Width : 1.618

Height : 9.388

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	28.09
Std Dev	29.18

Turn on : 54.4720

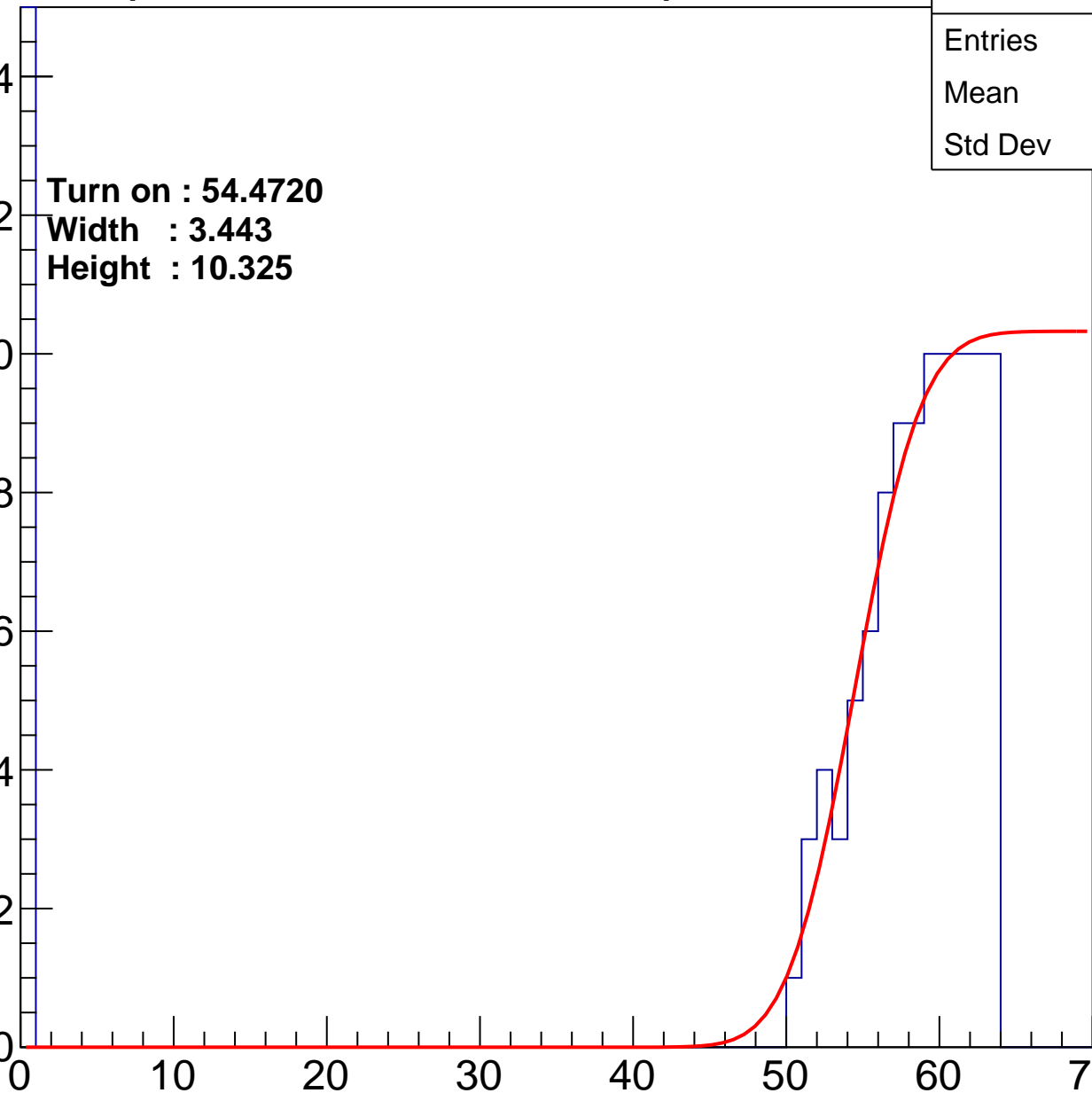
Width : 3.443

Height : 10.325

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	36.56
Std Dev	28.06

Turn on : 54.0934

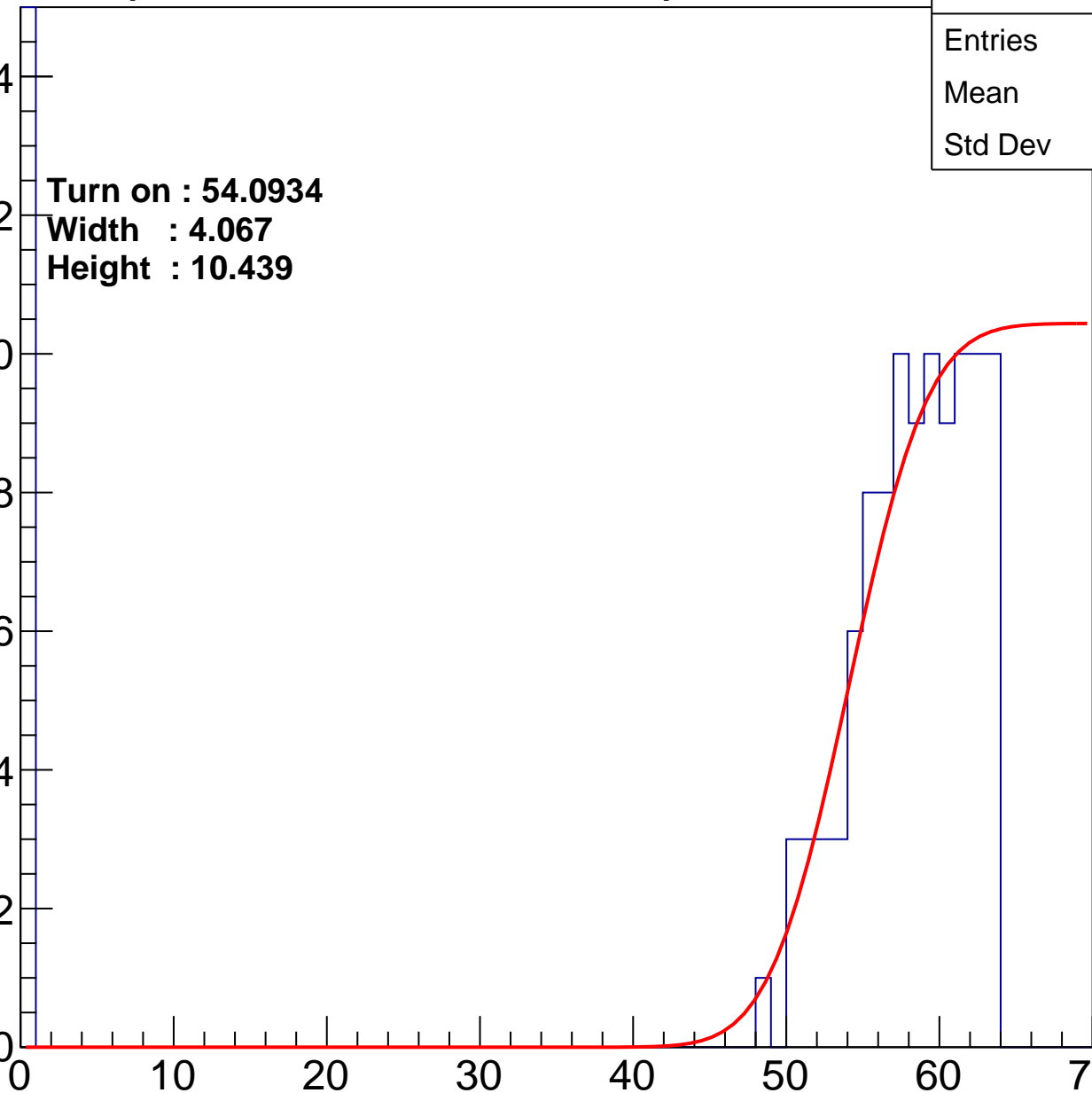
Width : 4.067

Height : 10.439

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch34

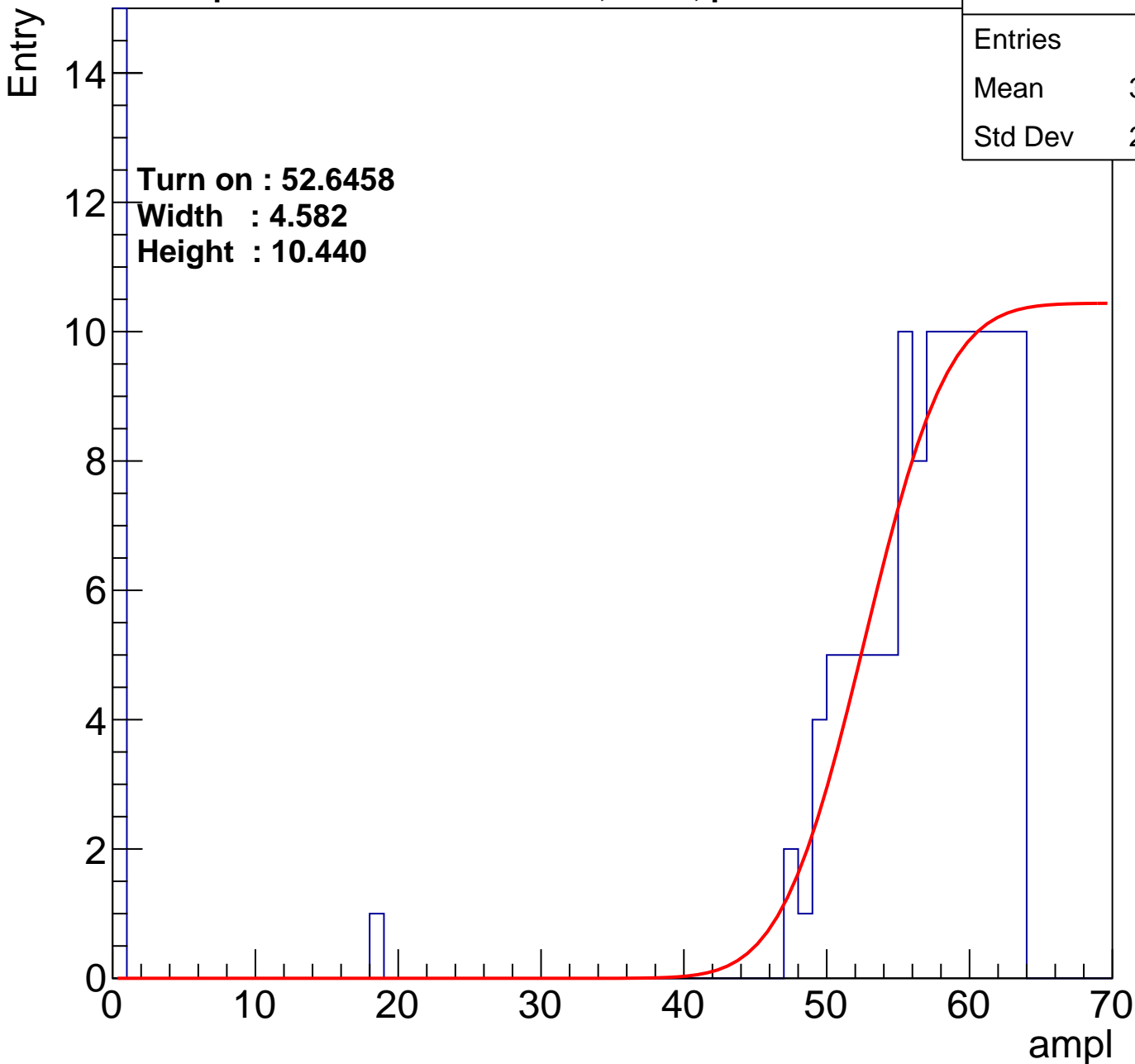
calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	31.44
Std Dev	28.45

Turn on : 52.6458

Width : 4.582

Height : 10.440



B1L104S, U10-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	32.65
Std Dev	28.9

Turn on : 53.8340

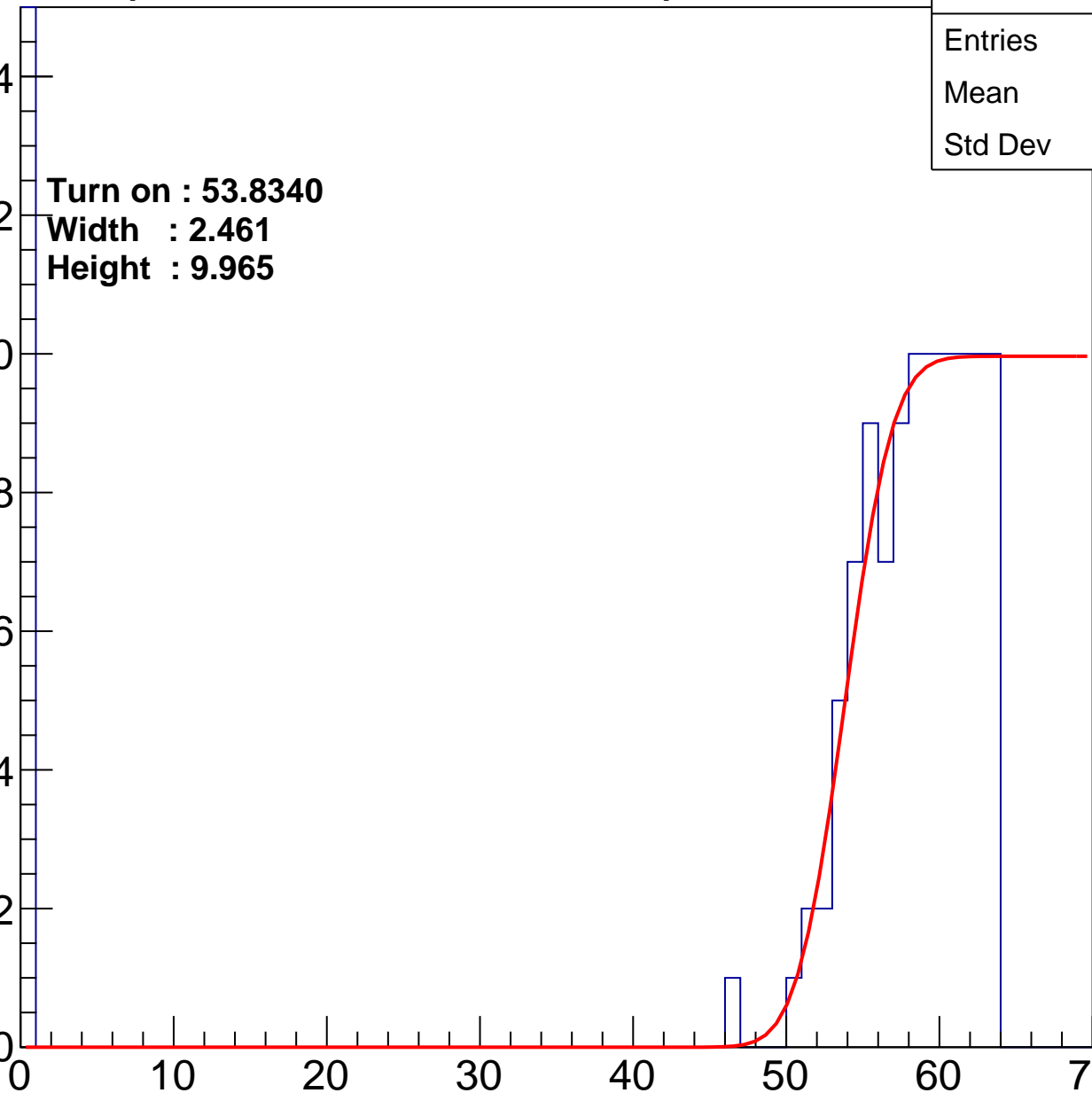
Width : 2.461

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch36

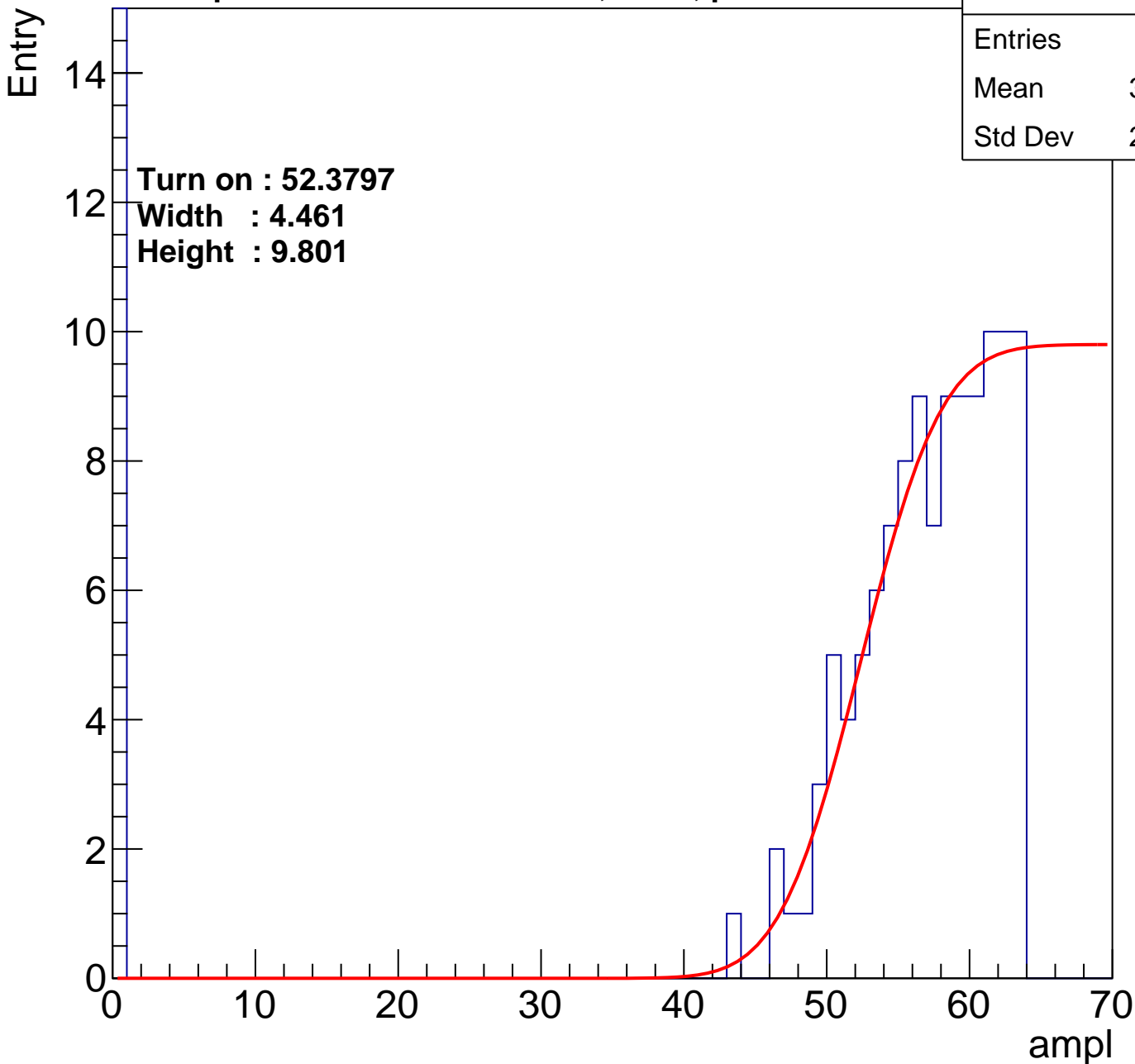
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.25
Std Dev	28.17

Turn on : 52.3797

Width : 4.461

Height : 9.801



B1L104S, U10-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	30.36
Std Dev	29.53

Turn on : 55.6283

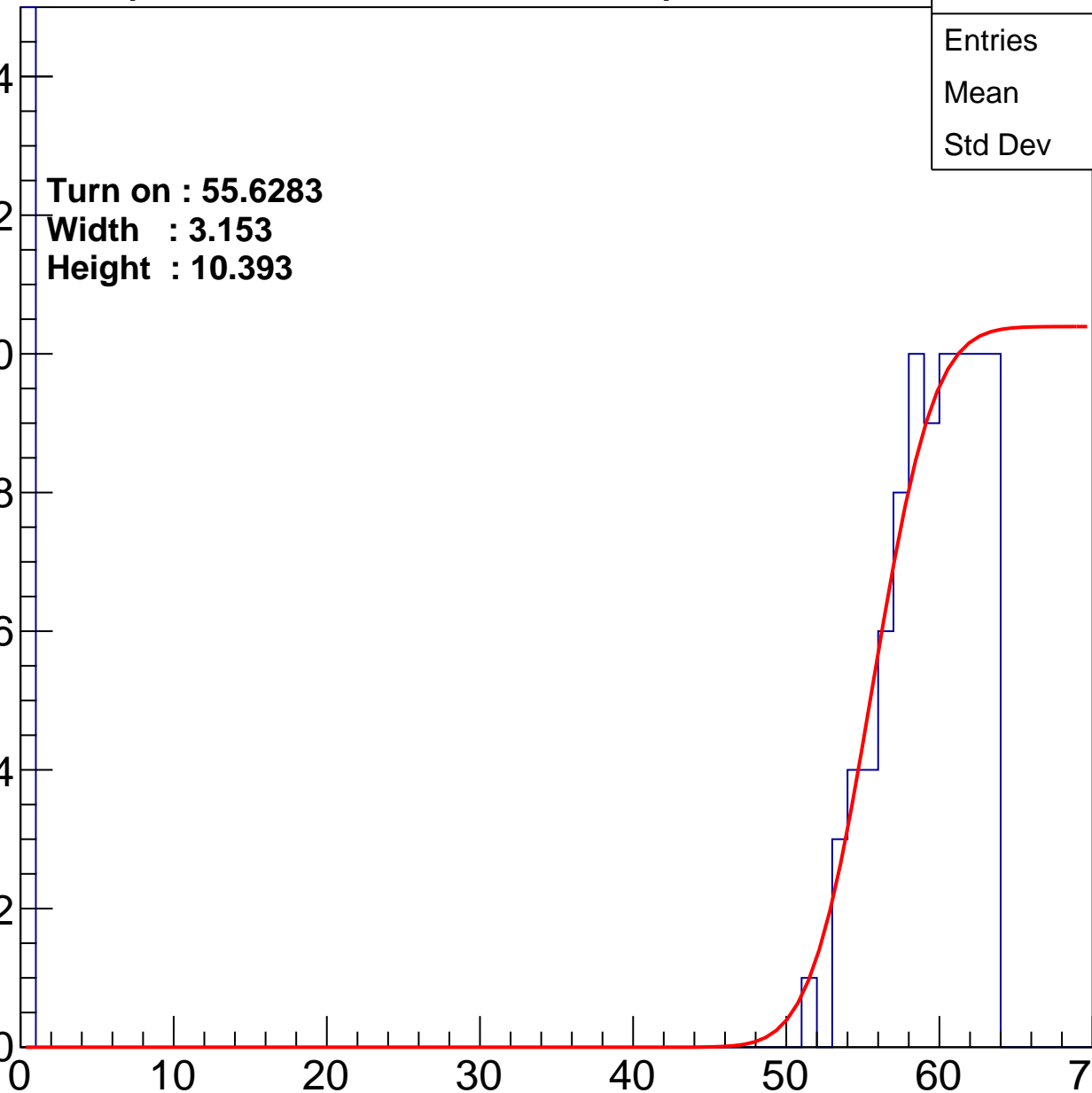
Width : 3.153

Height : 10.393

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	31.45
Std Dev	28.7

Turn on : 54.3267

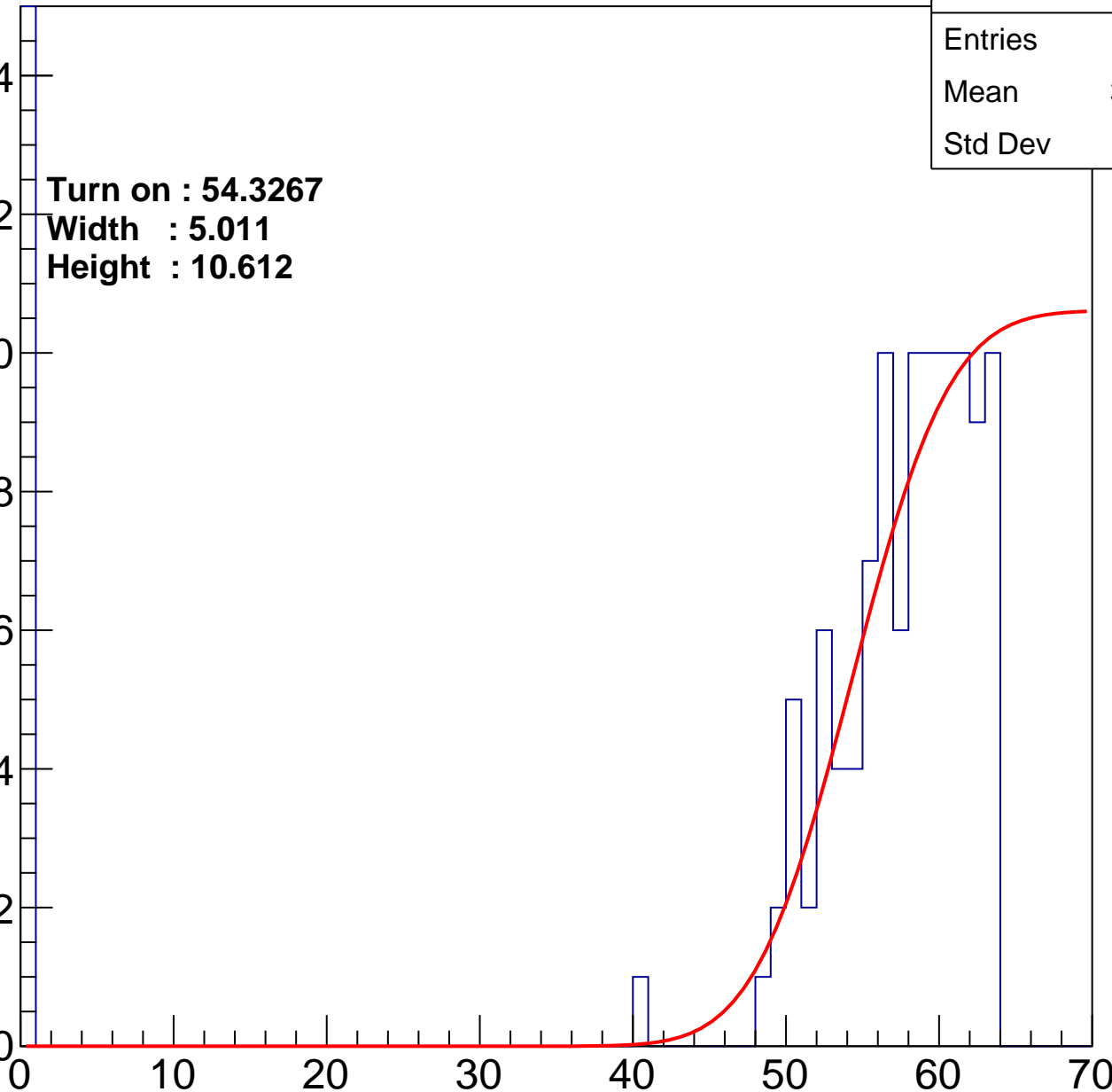
Width : 5.011

Height : 10.612

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entry

70

60

50

40

30

20

10

0

Turn on : 55.5333

Width : 2.104

Height : 9.144

Entries	157
Mean	30.34
Std Dev	29.47

ampl

0

10

20

30

40

50

60

70

B1L104S, U10-ch40

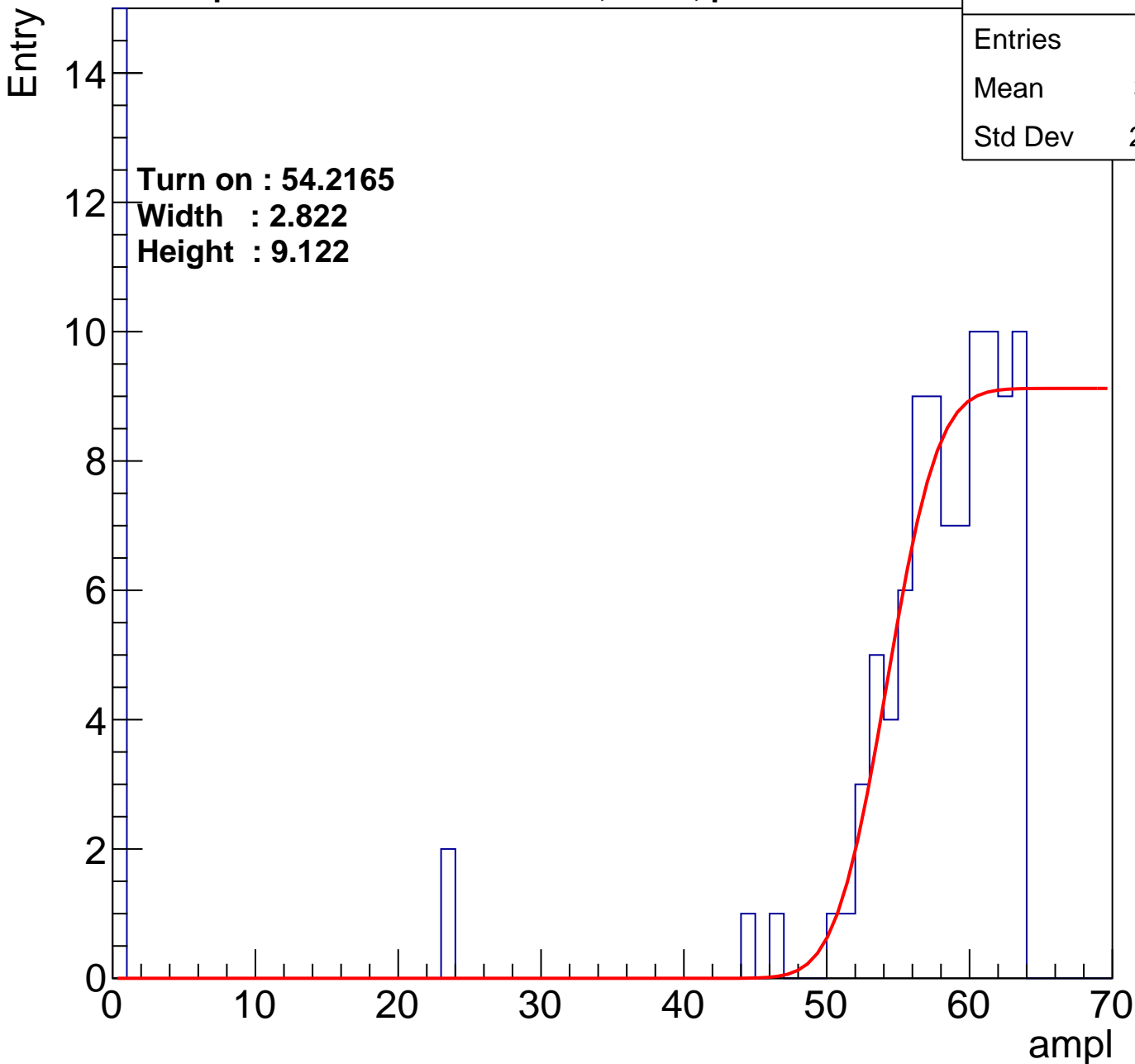
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	30.21
Std Dev	28.94

Turn on : 54.2165

Width : 2.822

Height : 9.122



B1L104S, U10-ch41

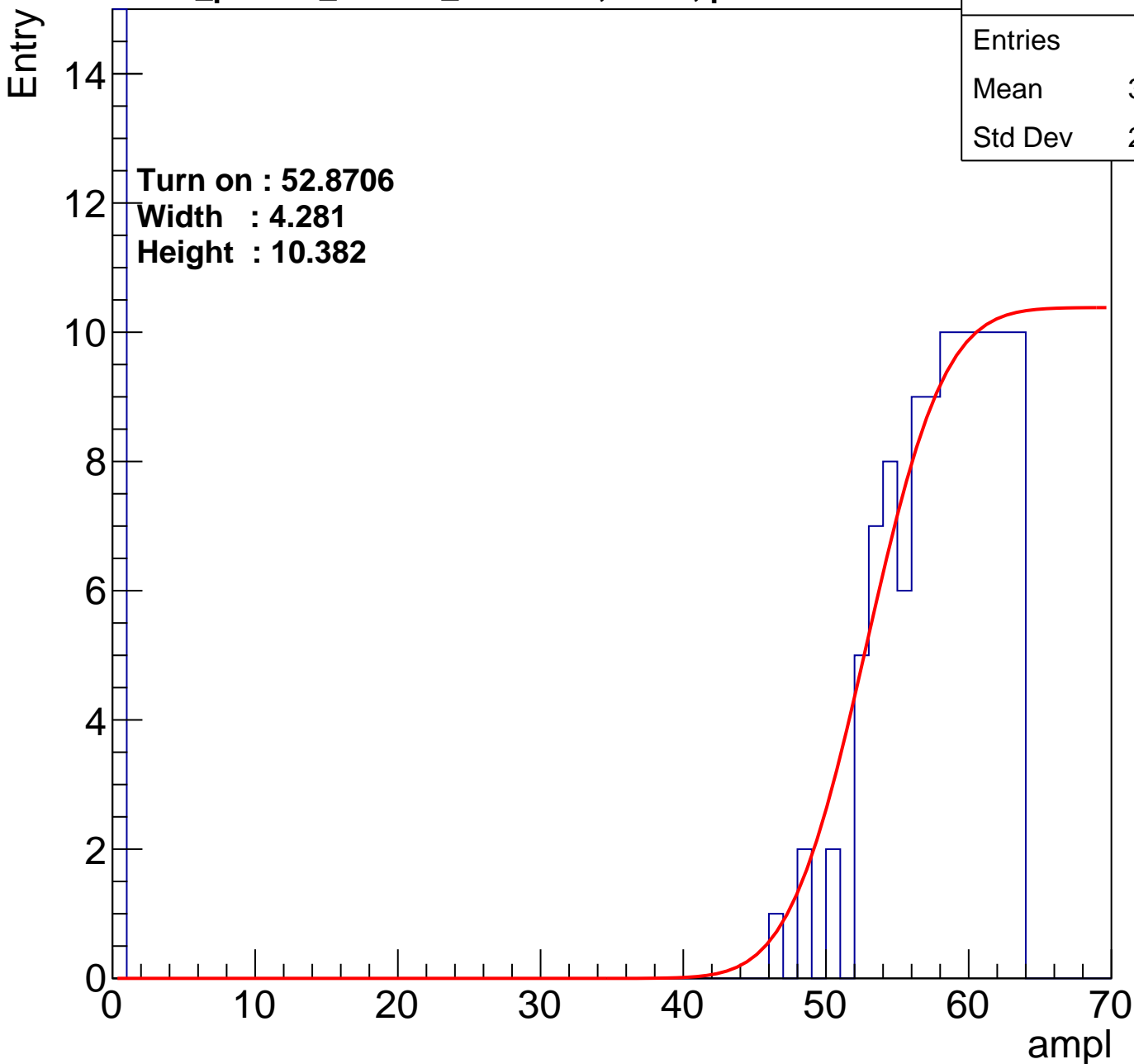
calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	38.78
Std Dev	27.22

Turn on : 52.8706

Width : 4.281

Height : 10.382



B1L104S, U10-ch42

calib_packv5_033123_0516.root, FC#4, port A1

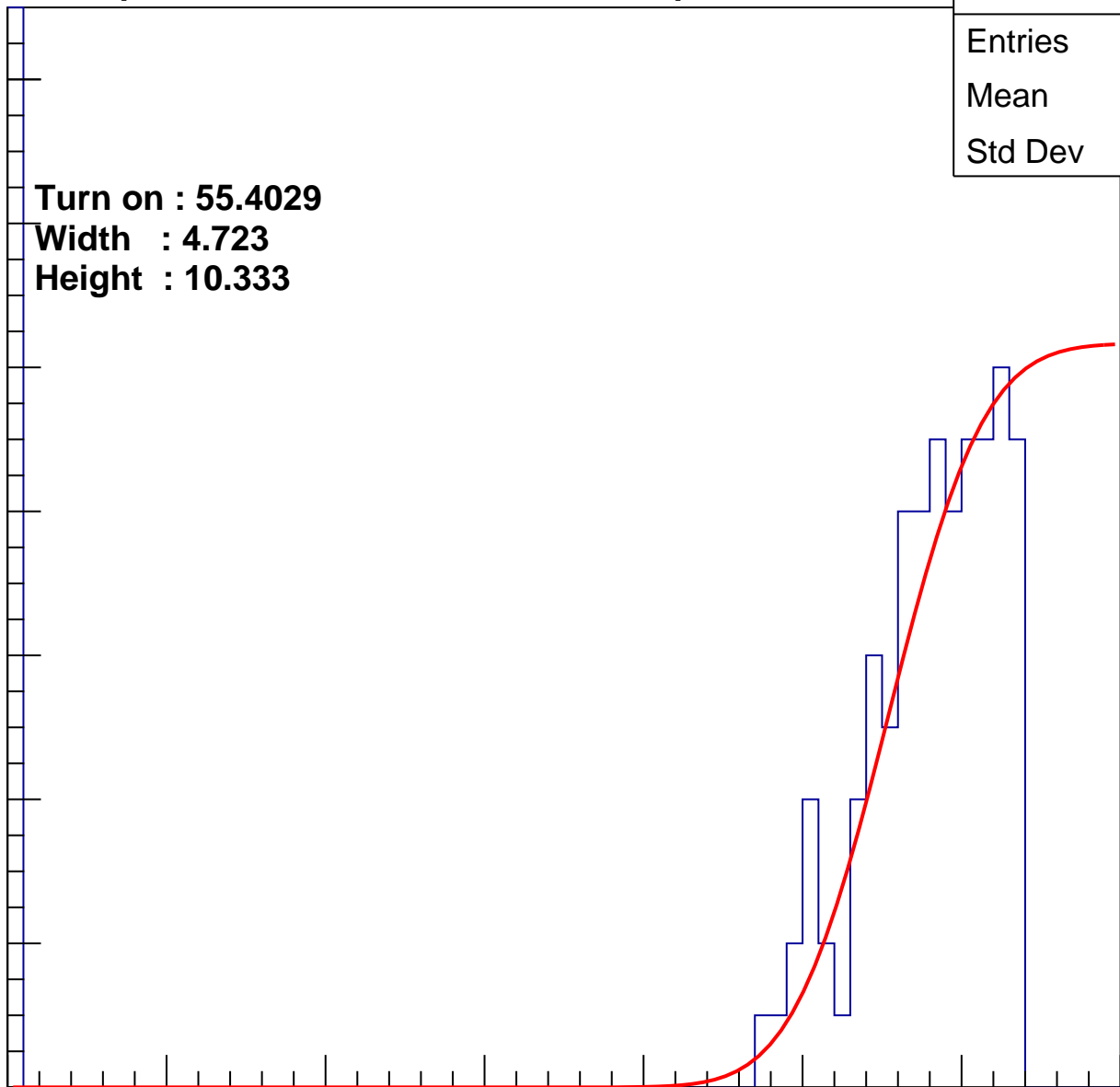
Entry

14
12
10
8
6
4
2
0

Turn on : 55.4029
Width : 4.723
Height : 10.333

Entries	181
Mean	30.56
Std Dev	28.91

ampl



B1L104S, U10-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	32.69
Std Dev	29.2

Turn on : 56.4728

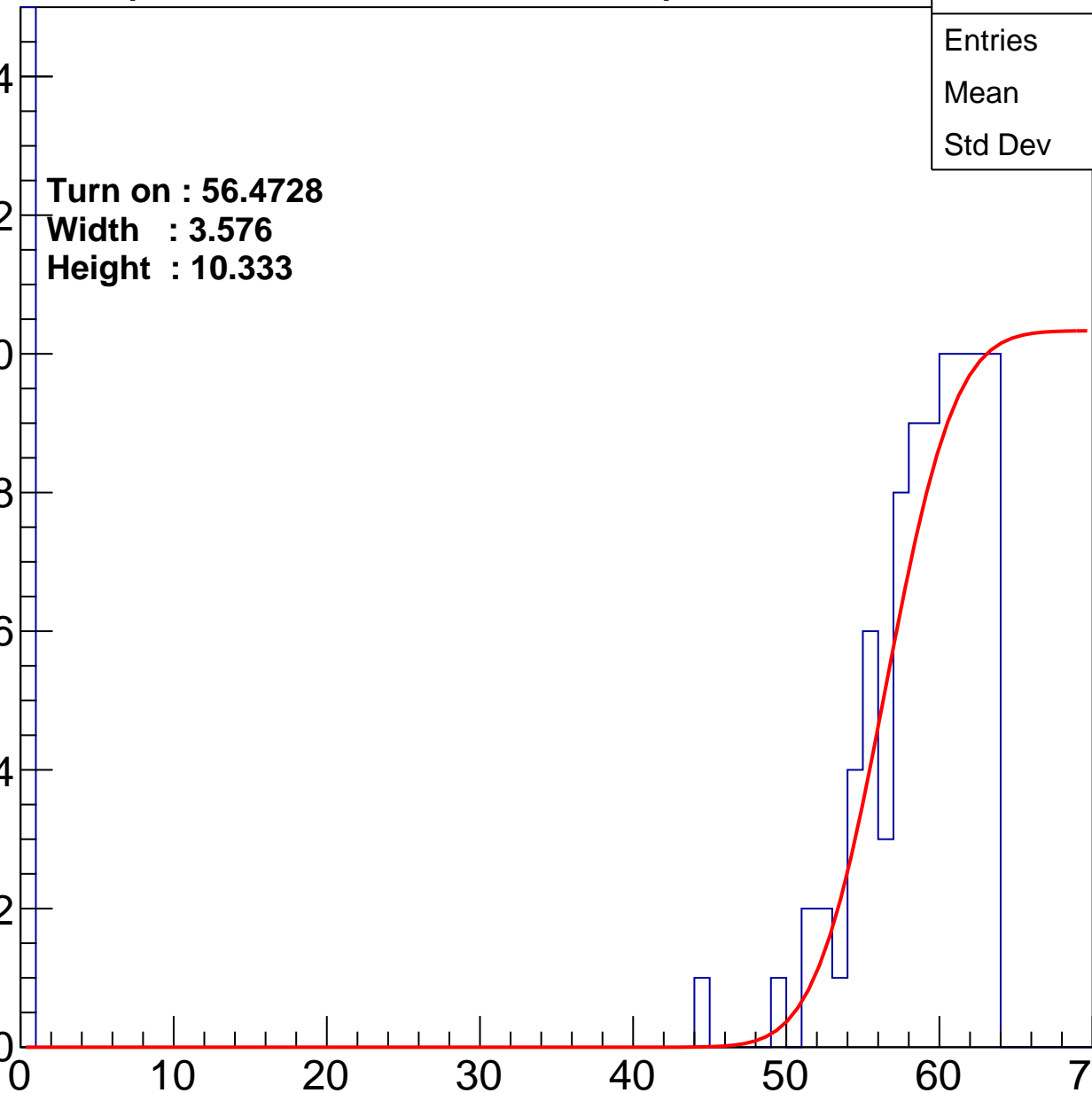
Width : 3.576

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	27.94
Std Dev	28.89

Turn on : 54.3331

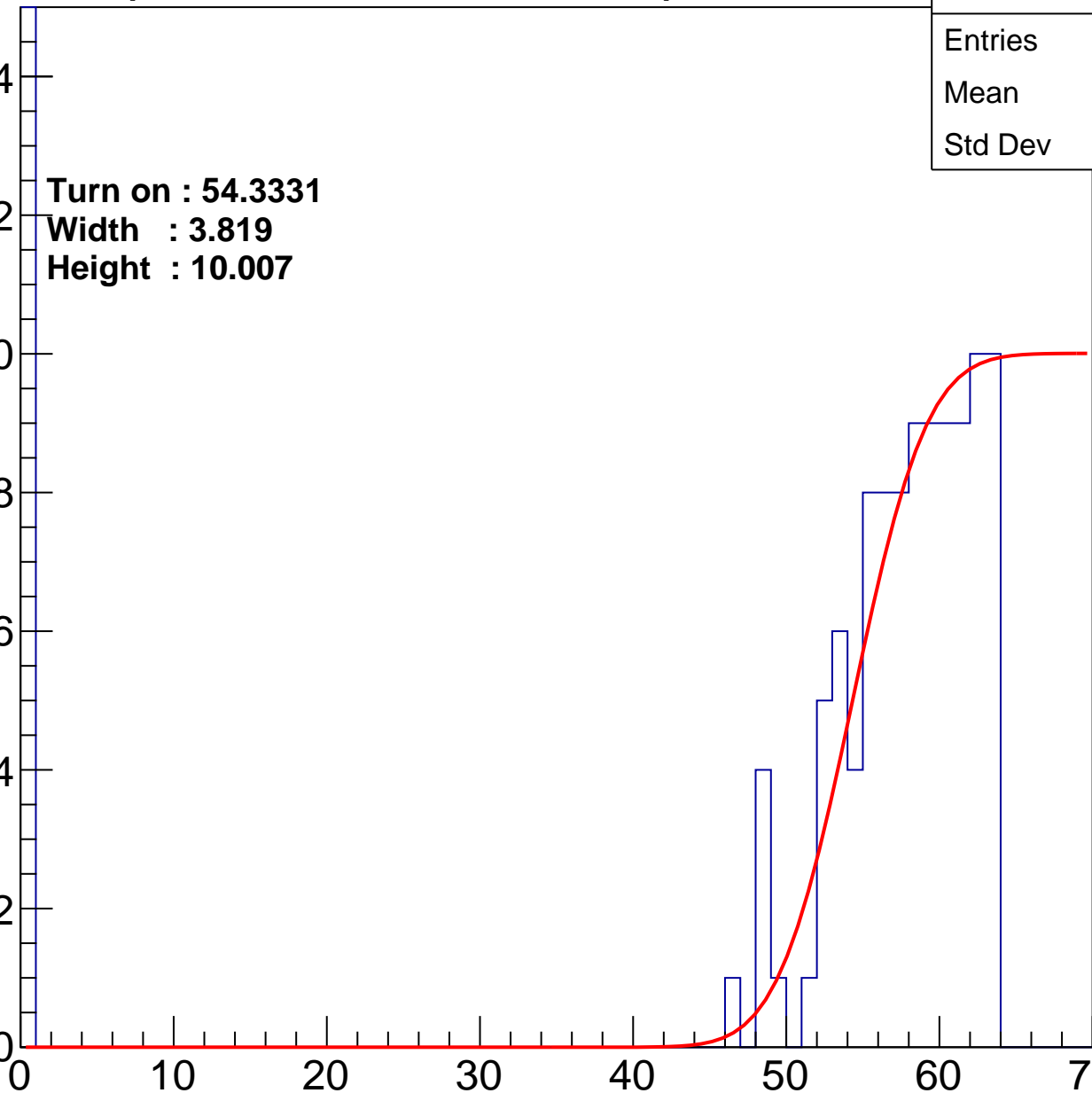
Width : 3.819

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch45

calib_packv5_033123_0516.root, FC#4, port A1

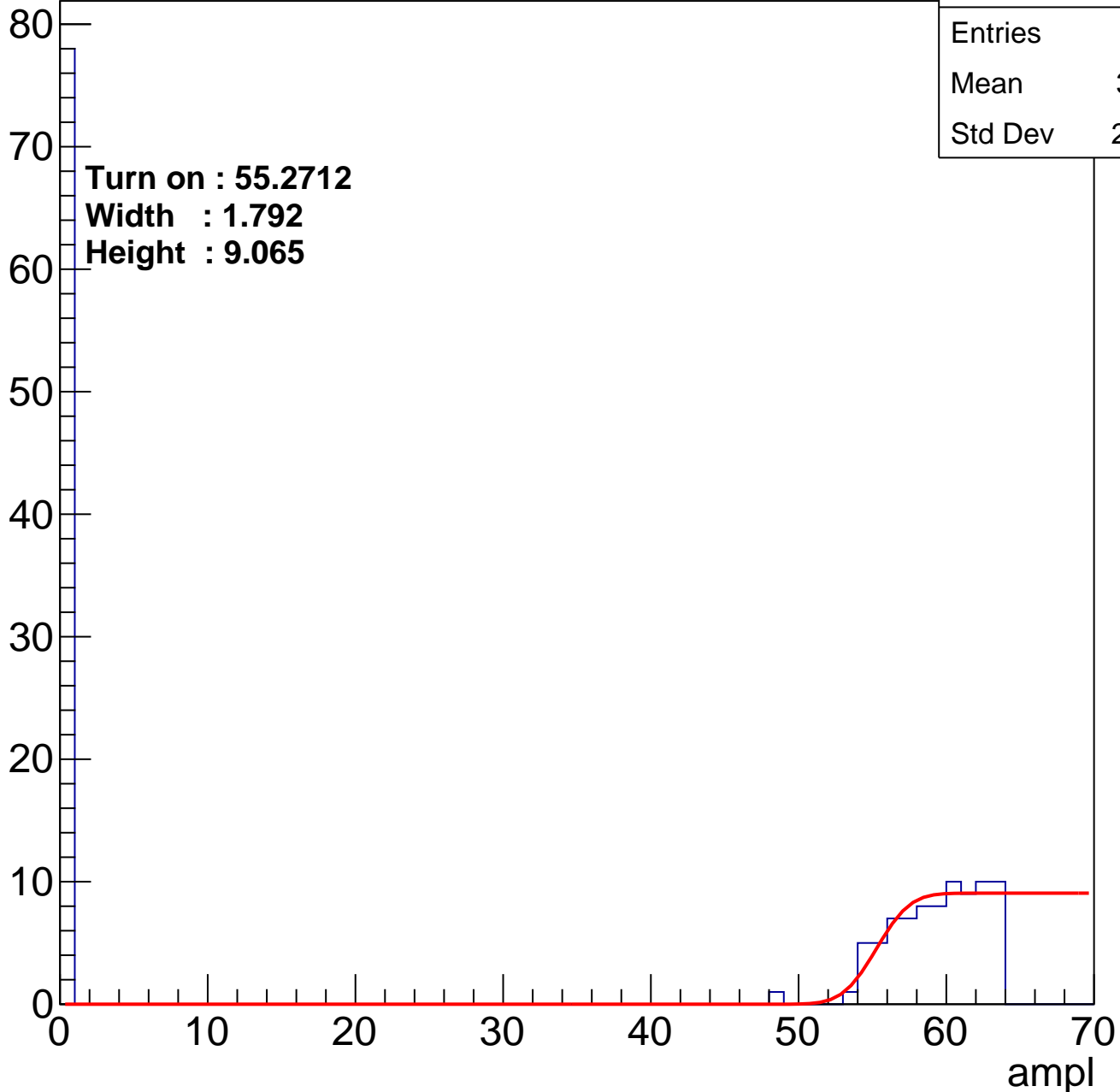
Entries	159
Mean	30.01
Std Dev	29.53

Turn on : 55.2712

Width : 1.792

Height : 9.065

Entry



B1L104S, U10-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	35.8
Std Dev	27.54

Turn on : 54.8352

Width : 5.942

Height : 11.530

Entry

14

12

10

8

6

4

2

0

0

10

20

30

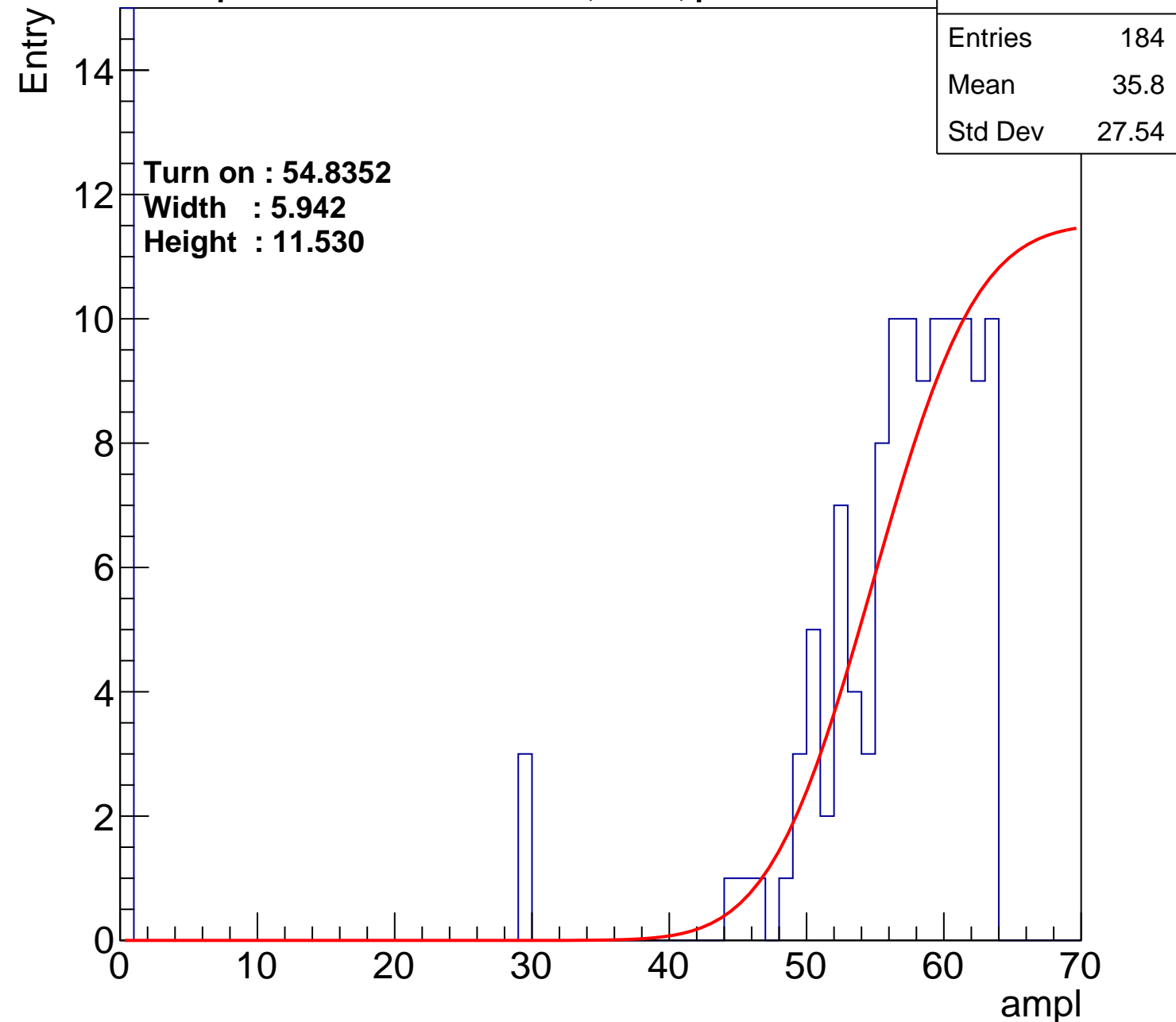
40

50

60

70

ampl



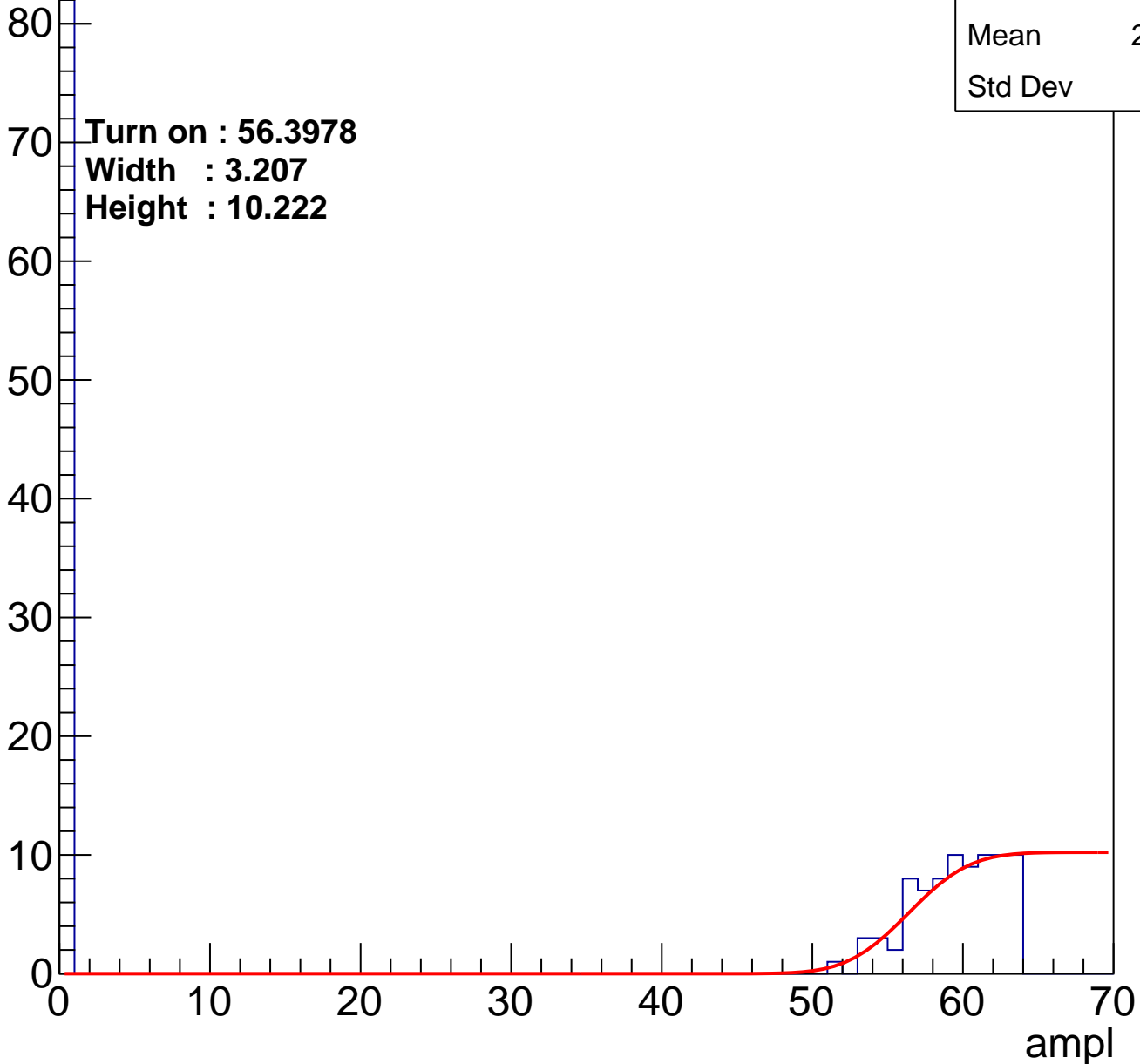
B1L104S, U10-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	29.34
Std Dev	29.6

Turn on : 56.3978
Width : 3.207
Height : 10.222

Entry



B1L104S, U10-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	34.24
Std Dev	27.94

Turn on : 51.3832

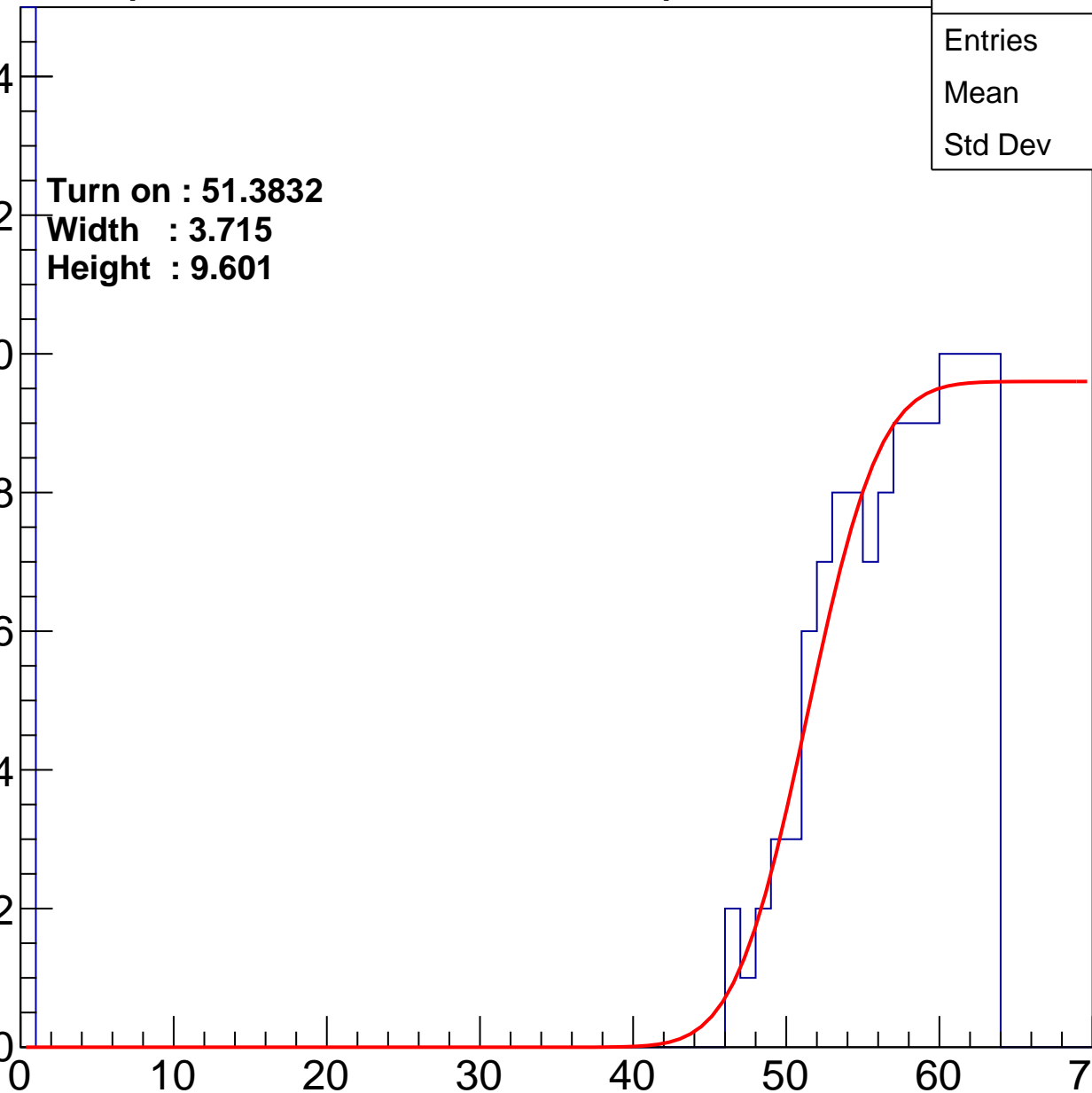
Width : 3.715

Height : 9.601

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	35.47
Std Dev	28.04

Turn on : 52.9026

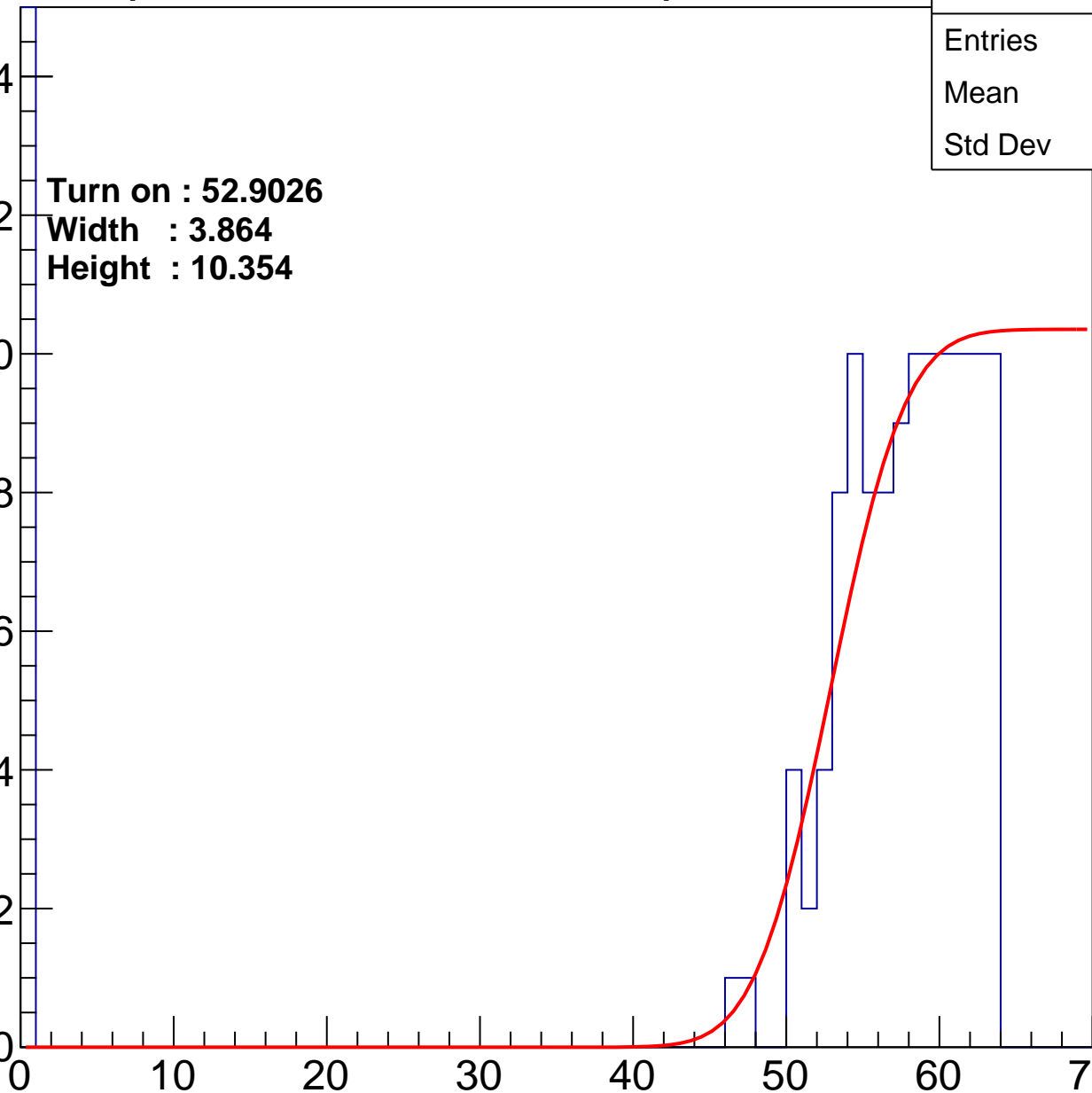
Width : 3.864

Height : 10.354

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch50

calib_packv5_033123_0516.root, FC#4, port A1

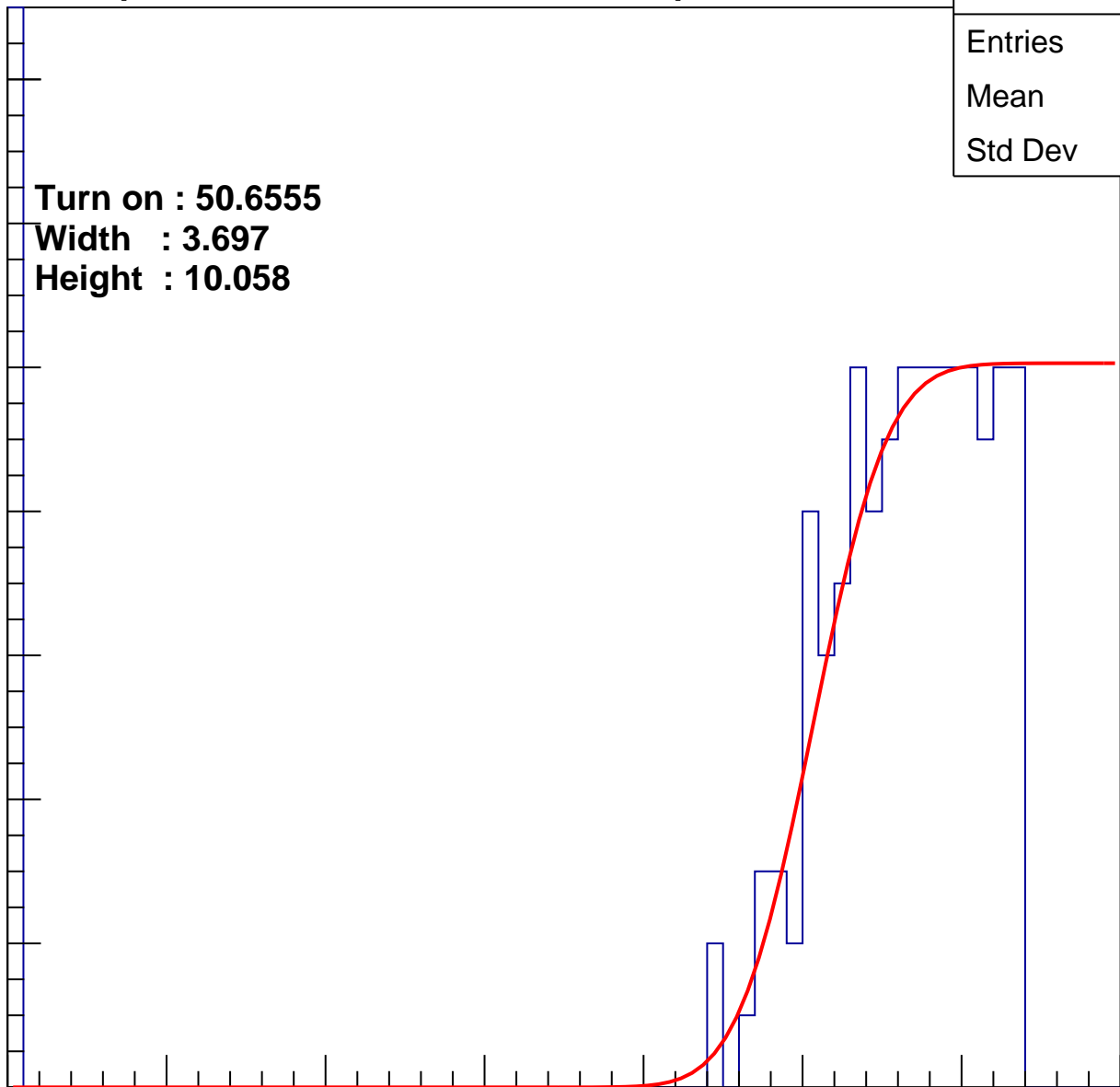
Entry

14
12
10
8
6
4
2
0

Turn on : 50.6555
Width : 3.697
Height : 10.058

Entries	206
Mean	37.59
Std Dev	26.66

ampl



B1L104S, U10-ch51

calib_packv5_033123_0516.root, FC#4, port A1

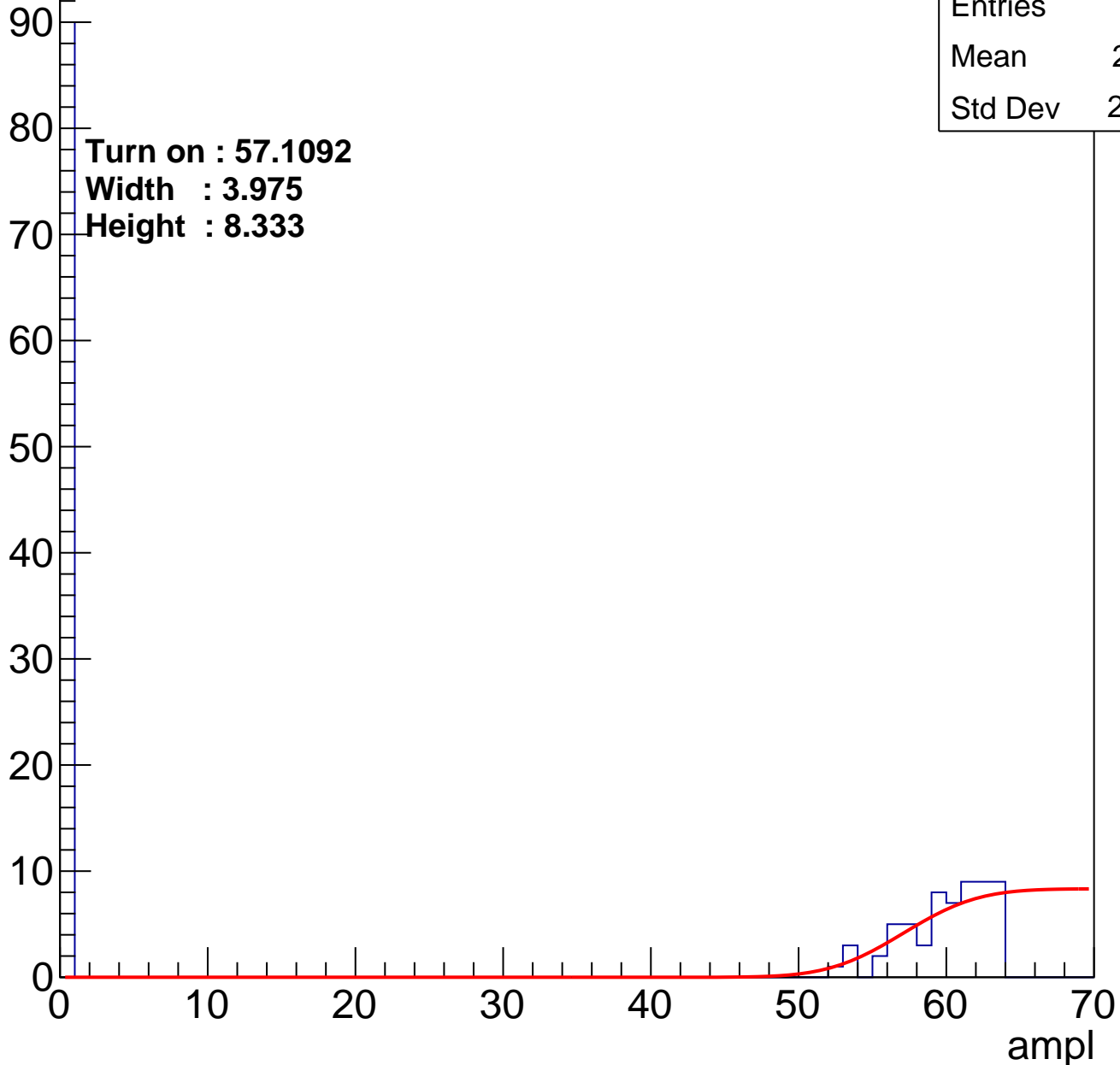
Entries	151
Mean	24.01
Std Dev	29.23

Turn on : 57.1092

Width : 3.975

Height : 8.333

Entry



B1L104S, U10-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.34
Std Dev	28.71

Turn on : 53.4560

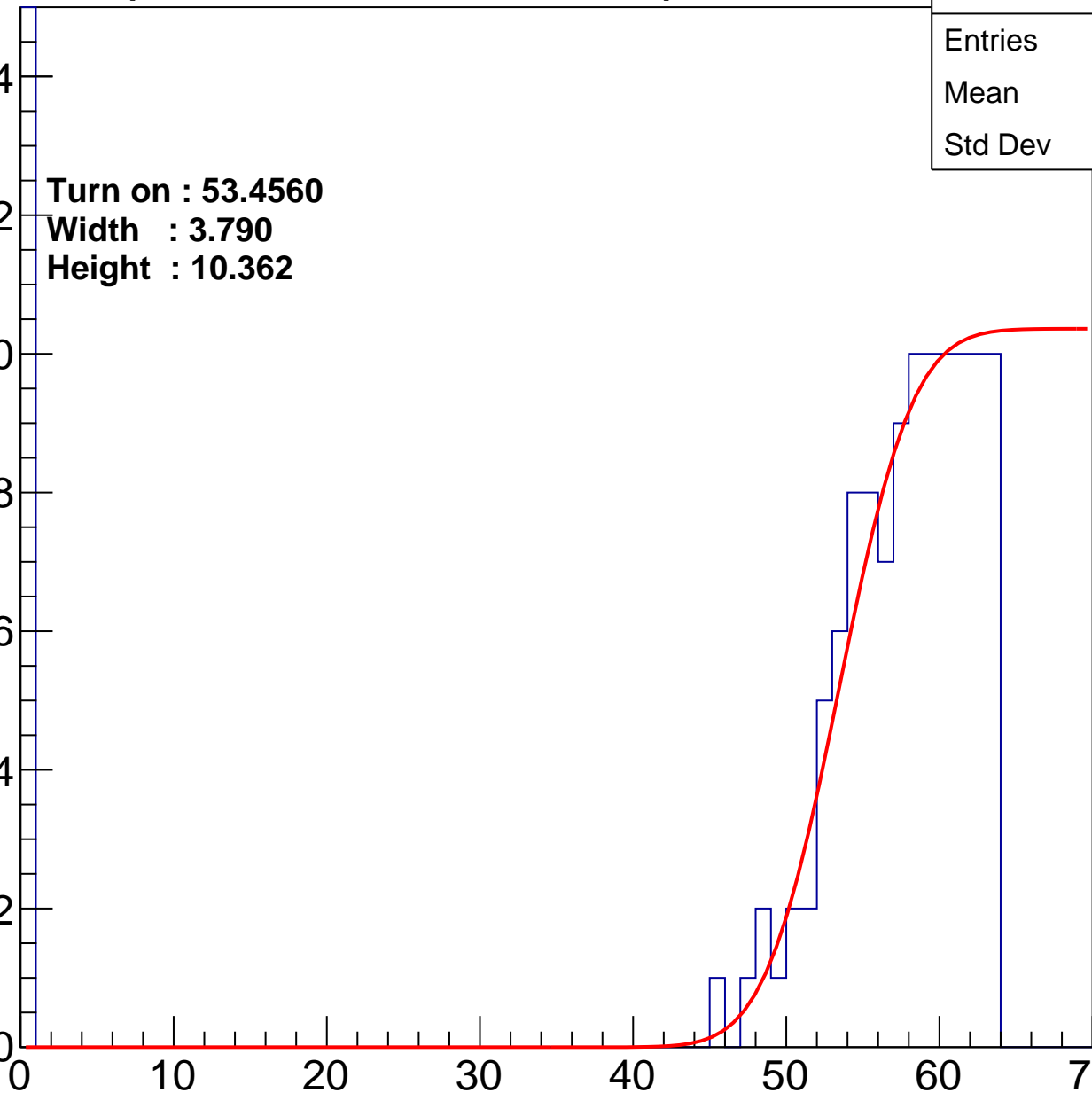
Width : 3.790

Height : 10.362

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch53

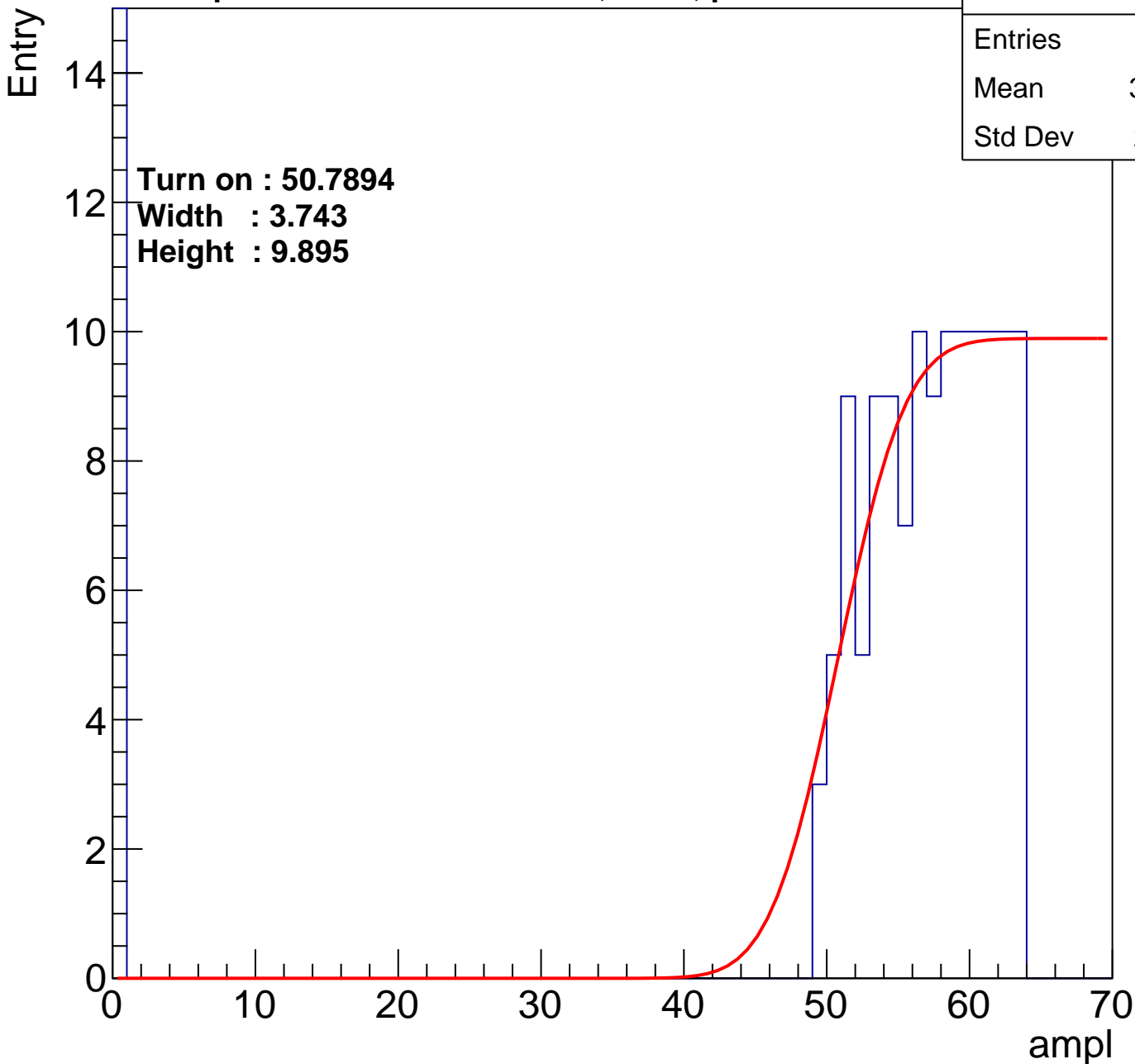
calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	34.29
Std Dev	28.01

Turn on : 50.7894

Width : 3.743

Height : 9.895



B1L104S, U10-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	229
Mean	34.17
Std Dev	27.94

Turn on : 52.6707

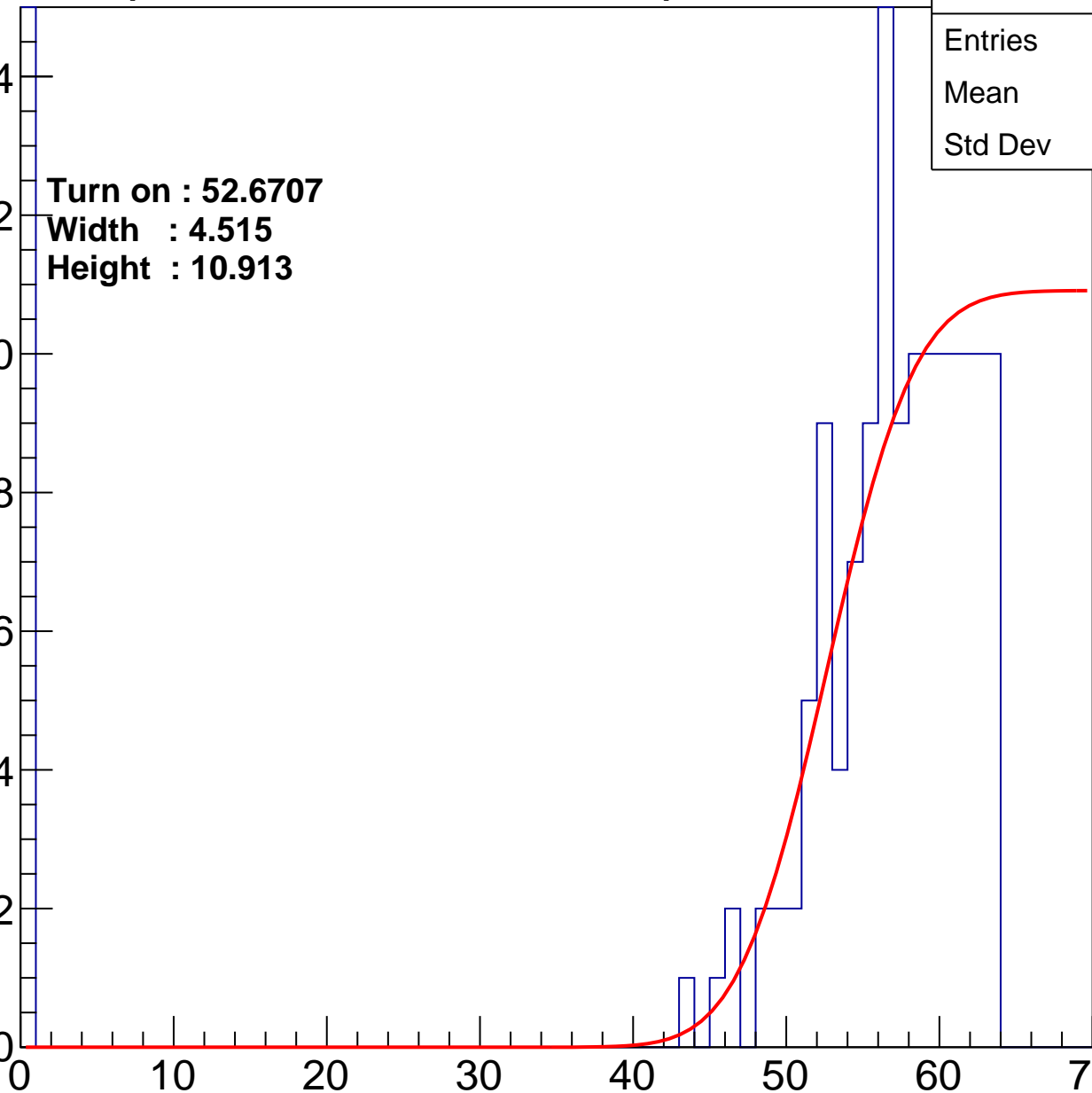
Width : 4.515

Height : 10.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch55

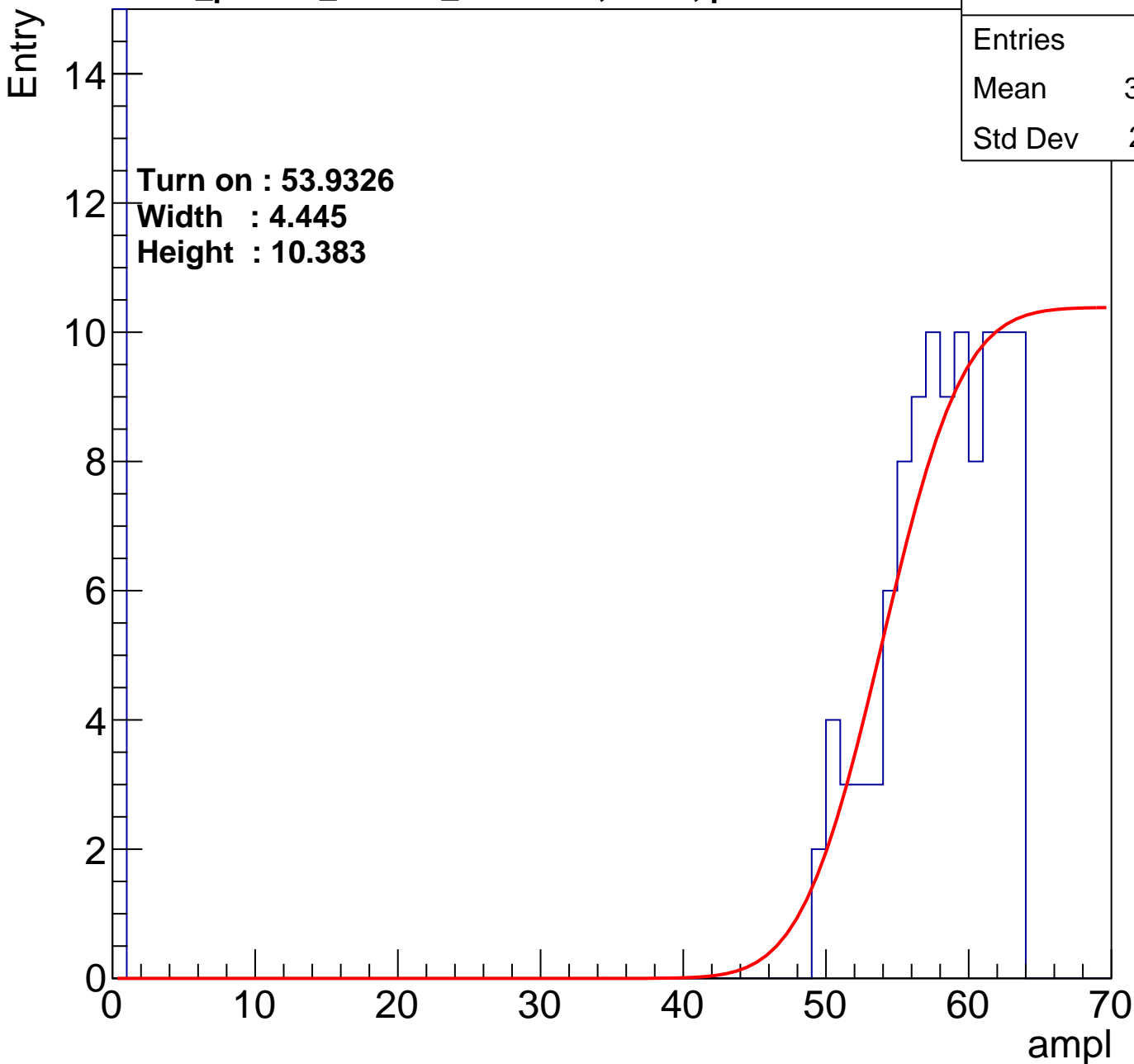
calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	33.46
Std Dev	28.61

Turn on : 53.9326

Width : 4.445

Height : 10.383



B1L104S, U10-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	32.57
Std Dev	28.13

Turn on : 51.1829

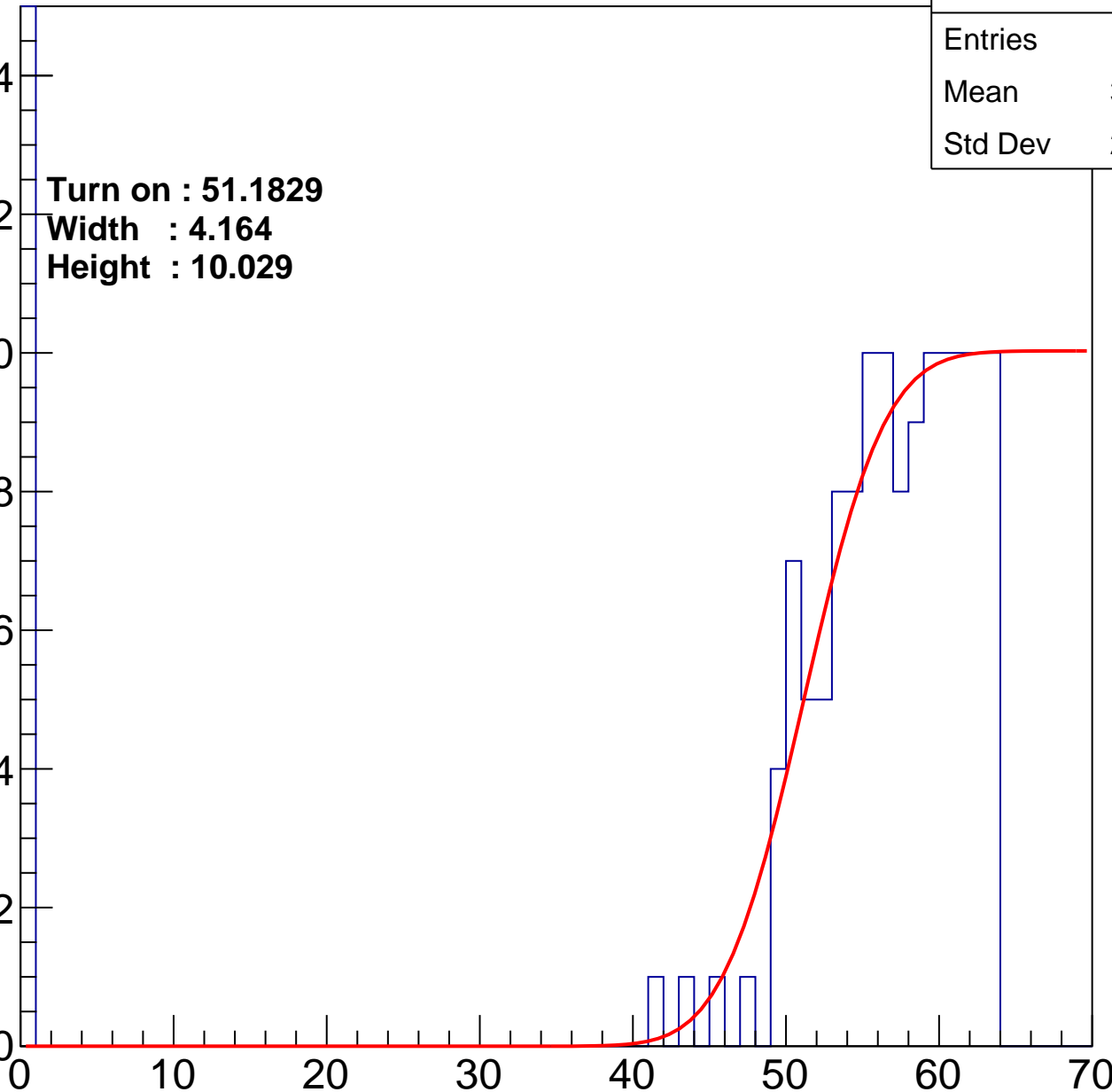
Width : 4.164

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch57

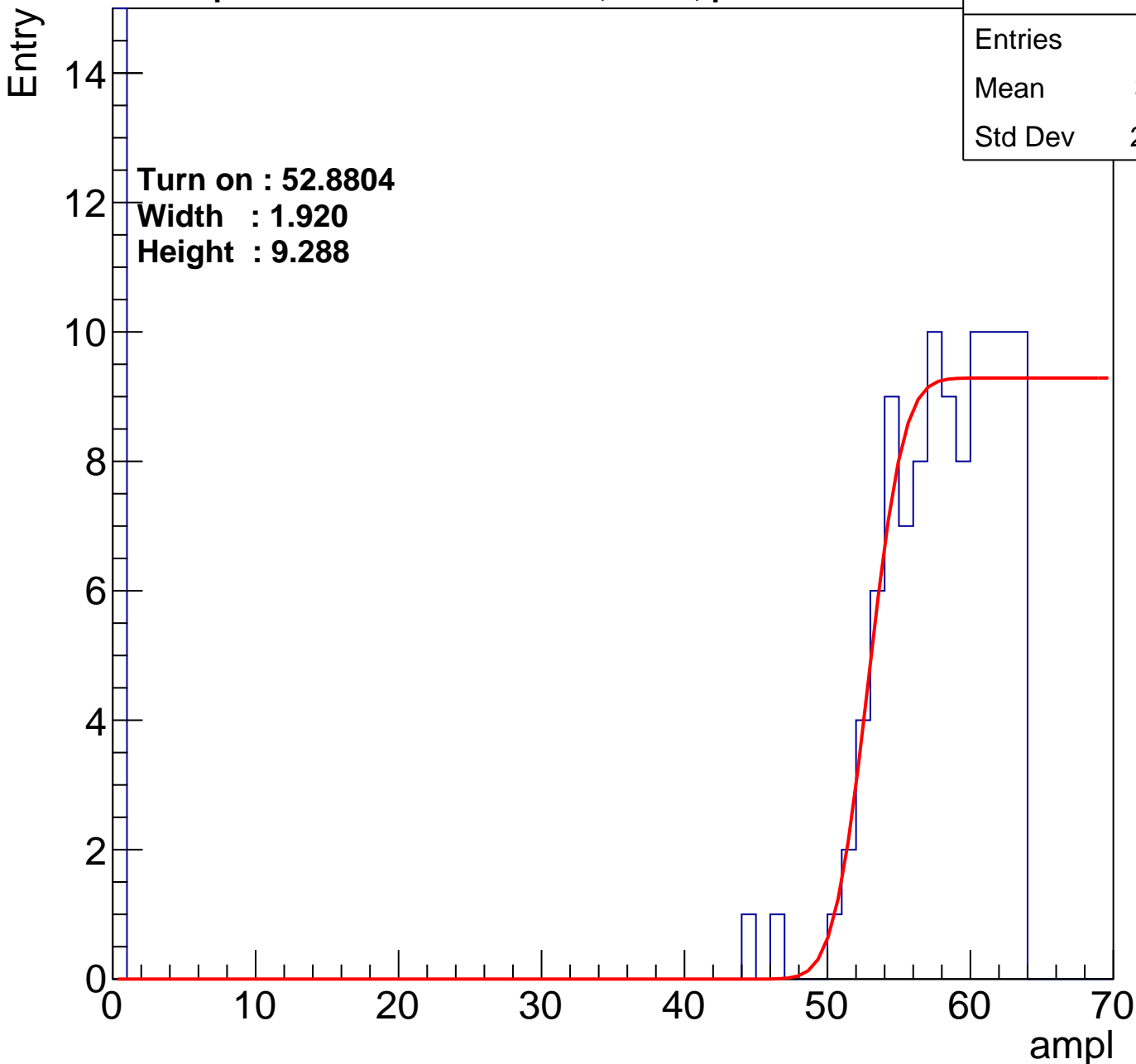
calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.21
Std Dev	28.64

Turn on : 52.8804

Width : 1.920

Height : 9.288



B1L104S, U10-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	34.57
Std Dev	27.74

Turn on : 52.1450

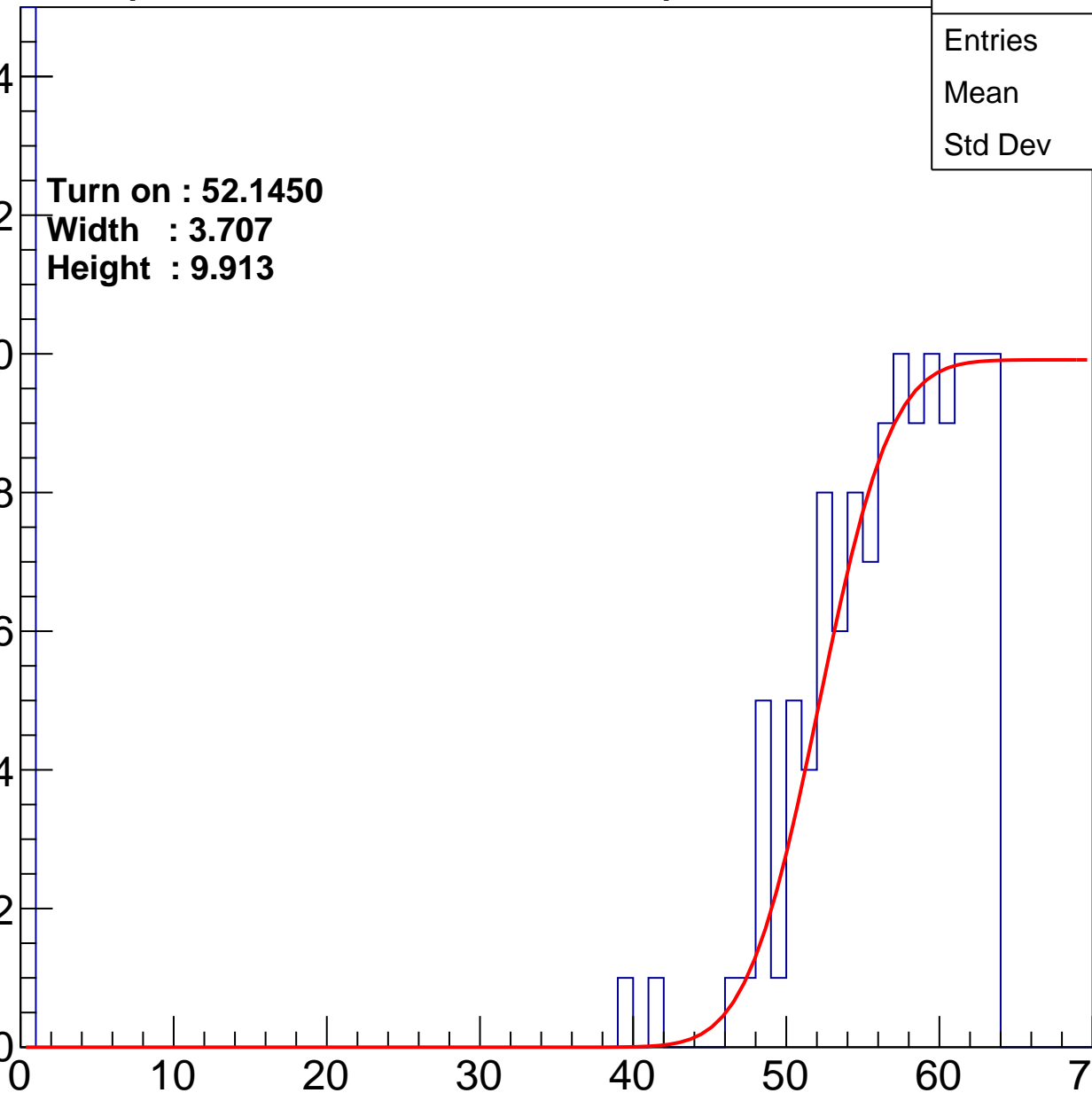
Width : 3.707

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl

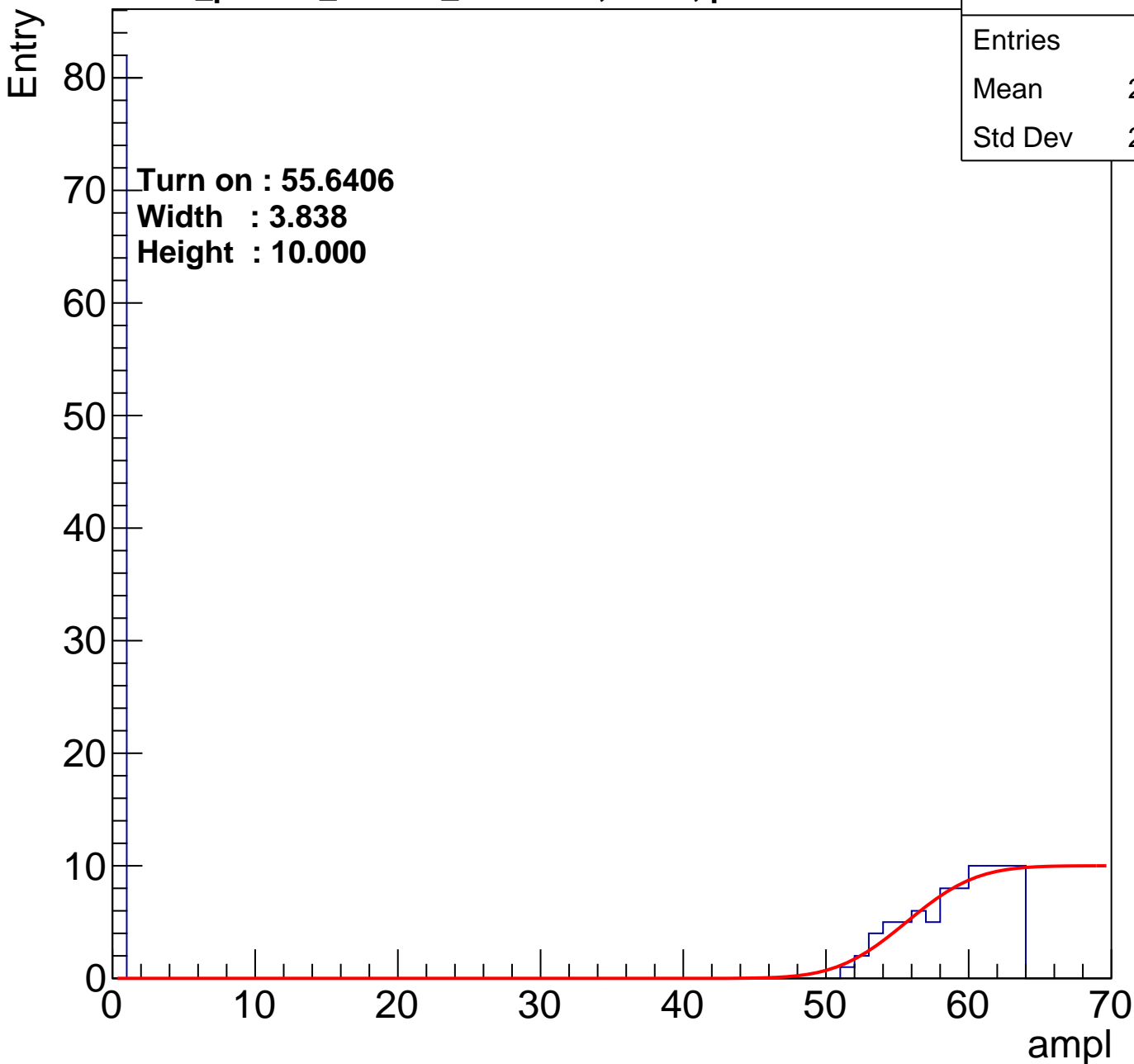


B1L104S, U10-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	29.69
Std Dev	29.43

Turn on : 55.6406
Width : 3.838
Height : 10.000



B1L104S, U10-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	36.73
Std Dev	27.5

Turn on : 52.8492

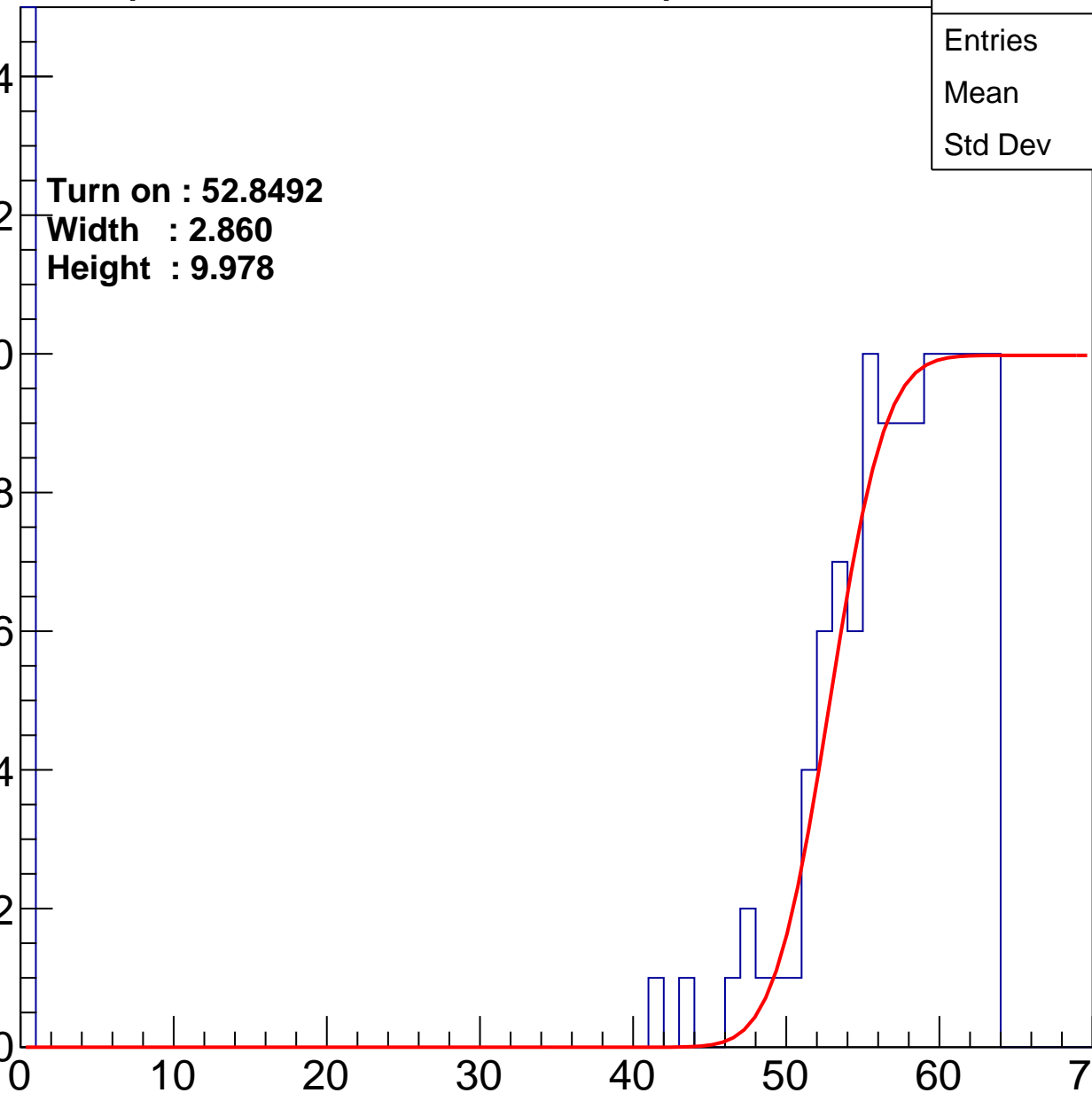
Width : 2.860

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	36.83
Std Dev	27.44

Turn on : 51.2825

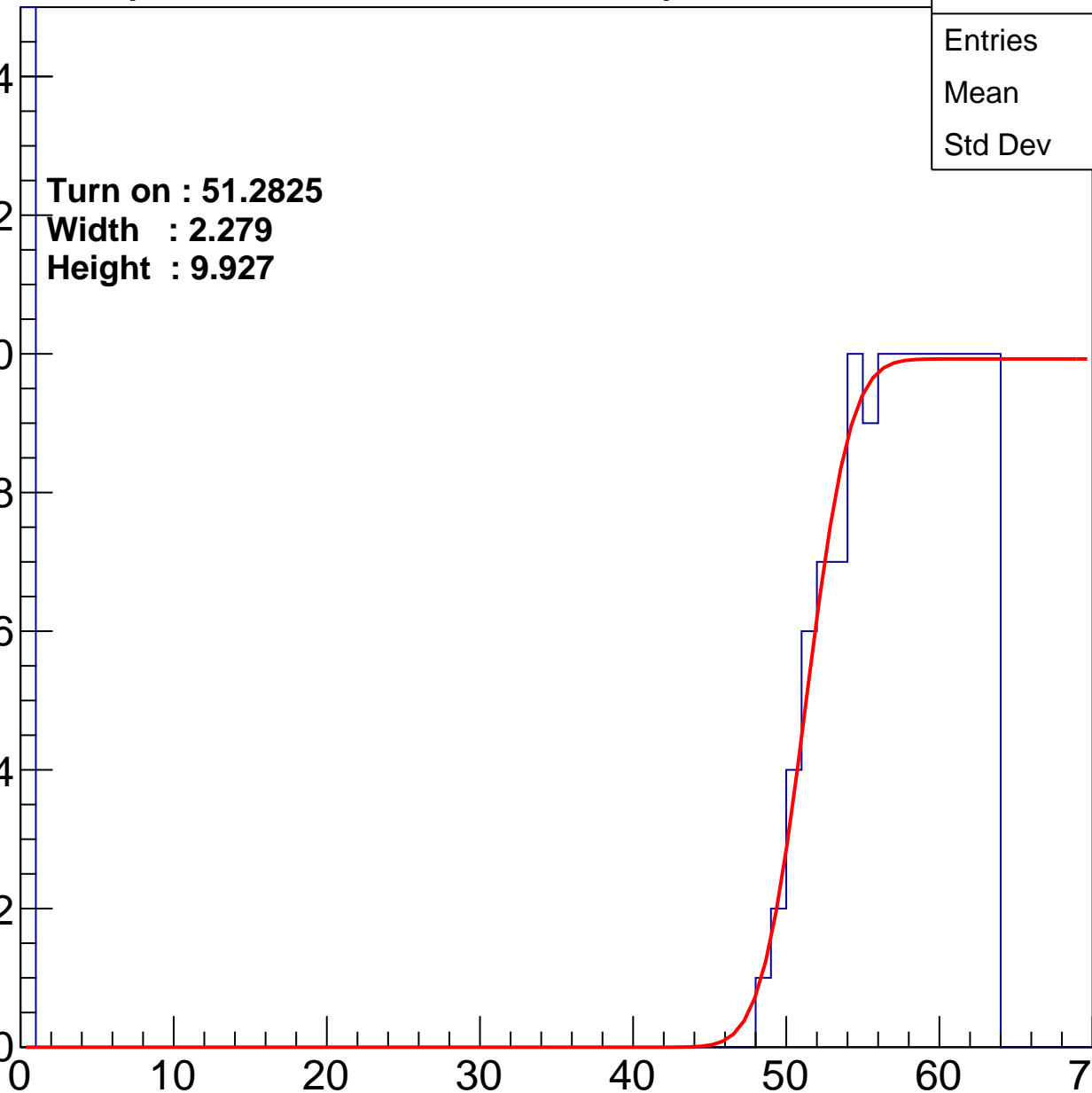
Width : 2.279

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch62

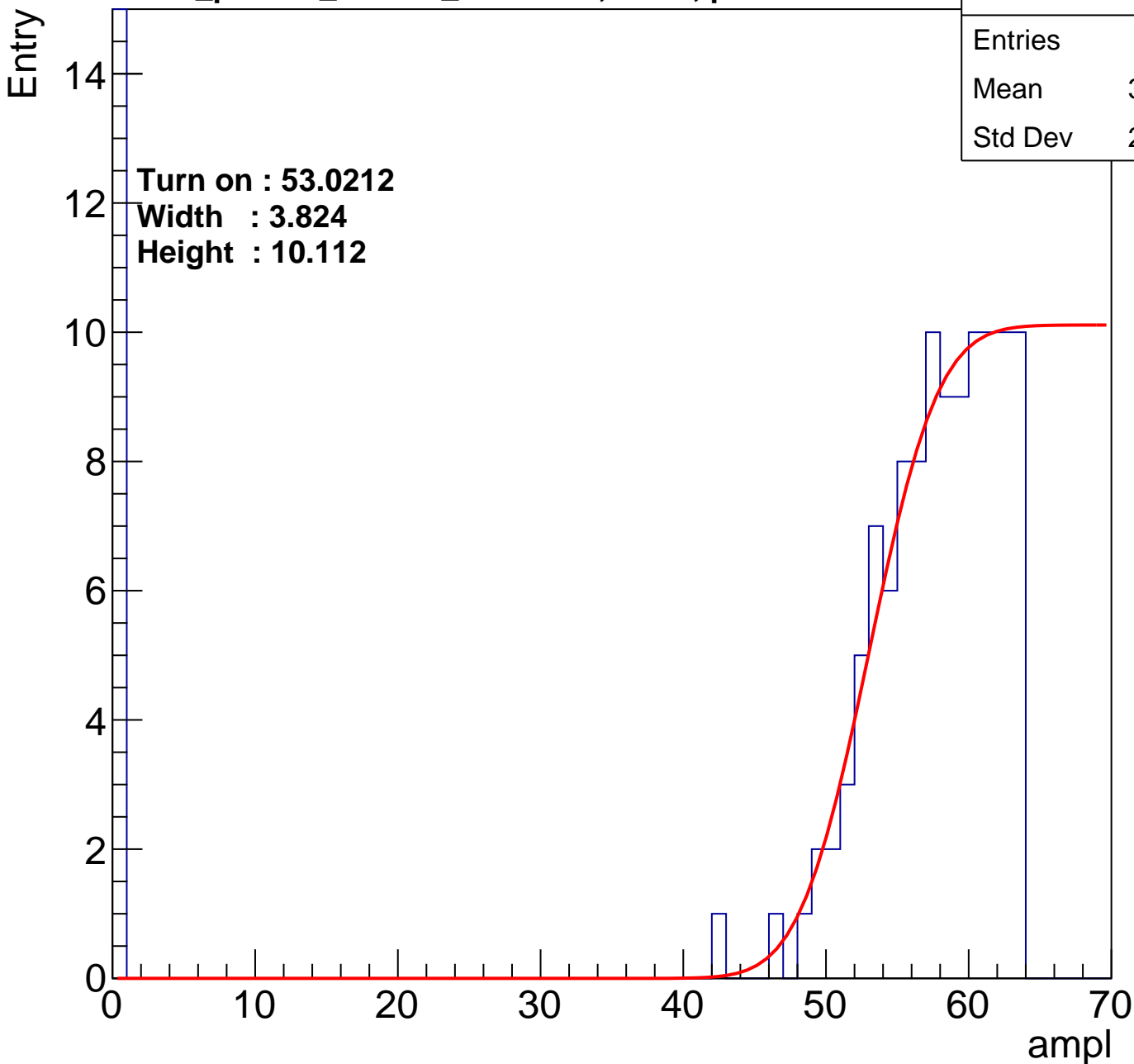
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.23
Std Dev	28.44

Turn on : 53.0212

Width : 3.824

Height : 10.112



B1L104S, U10-ch63

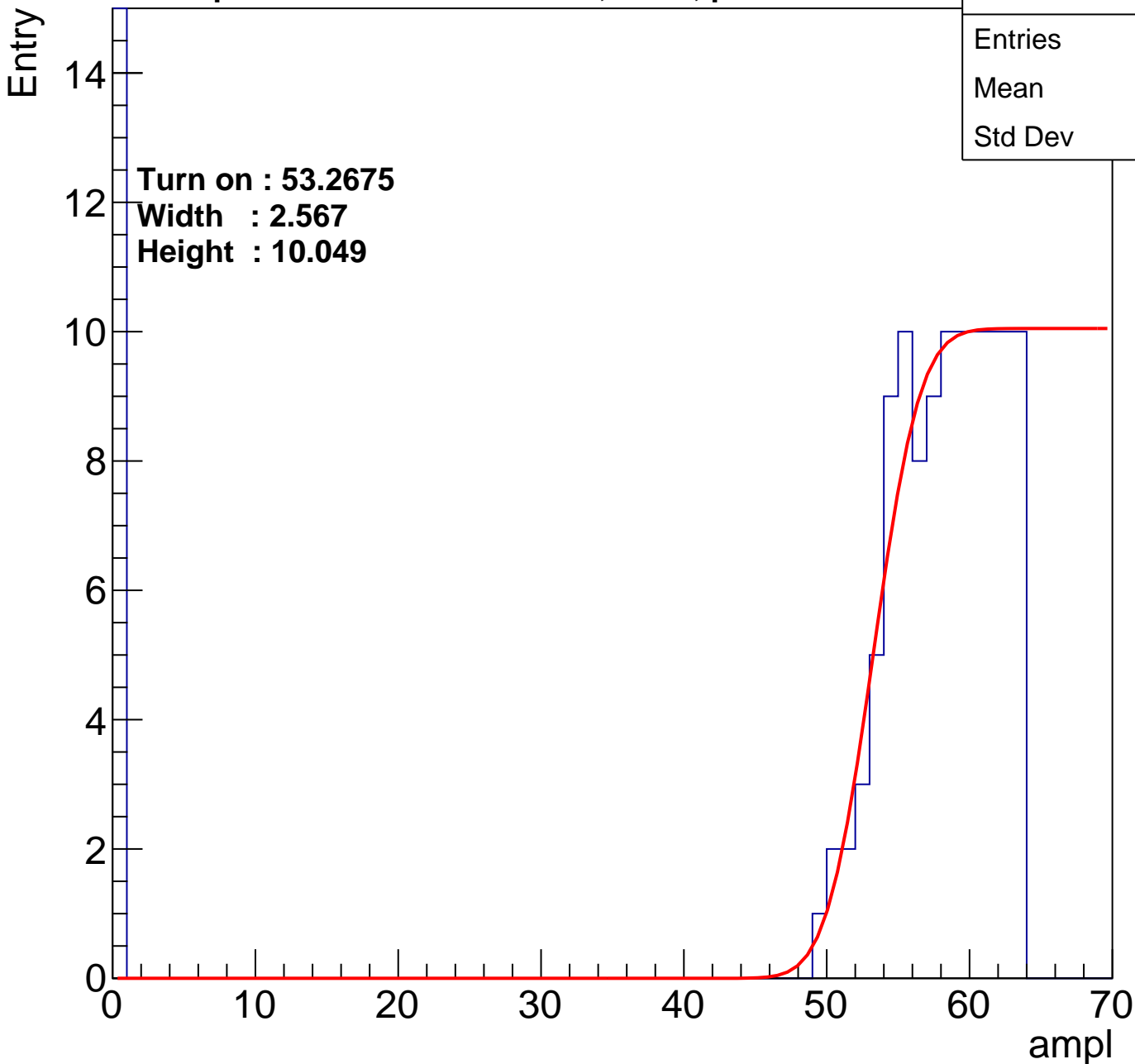
calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	31.5
Std Dev	28.9

Turn on : 53.2675

Width : 2.567

Height : 10.049



B1L104S, U10-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	32.72
Std Dev	28.12

Turn on : 53.8257

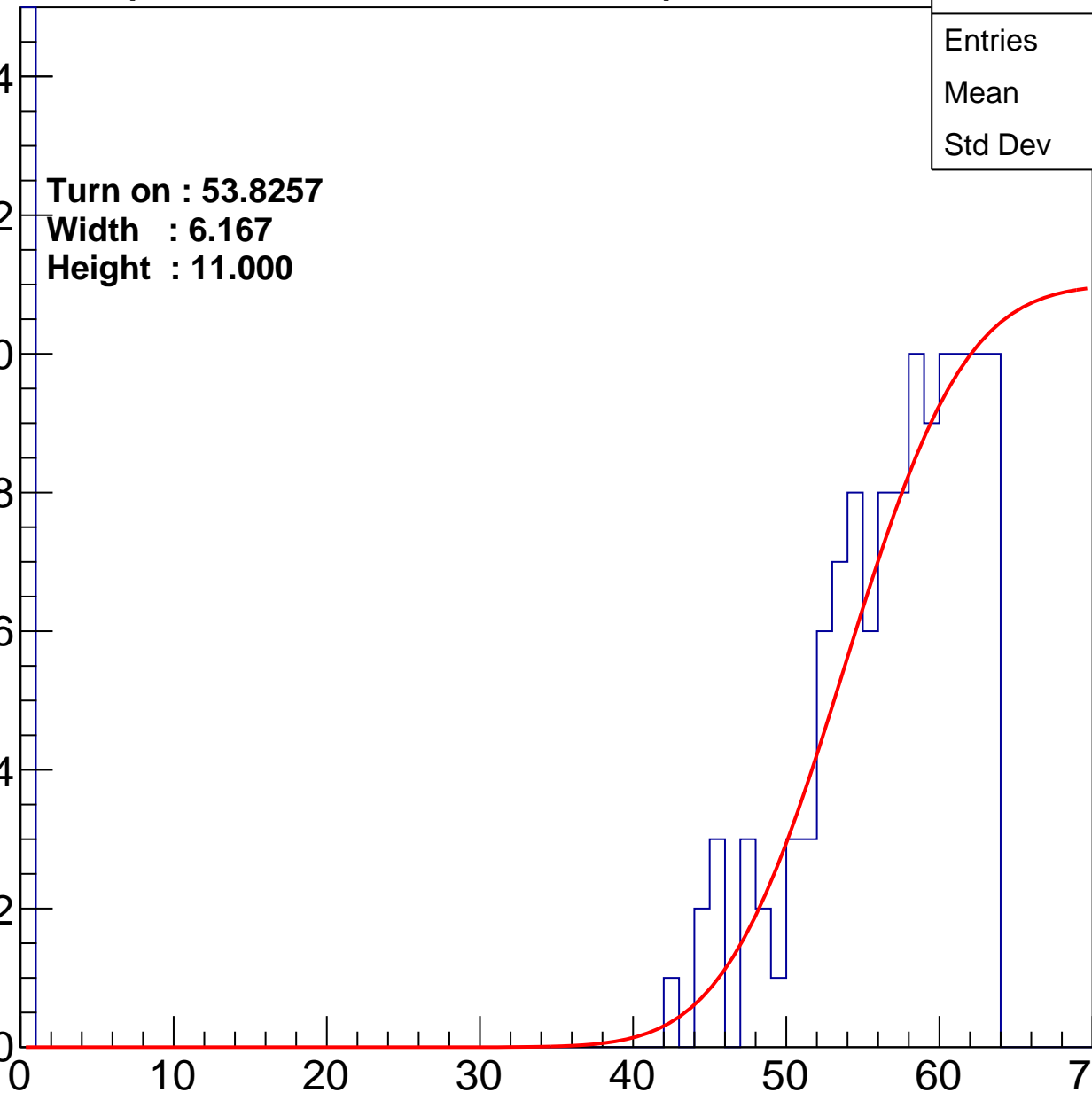
Width : 6.167

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	35.2
Std Dev	28.34

Turn on : 53.6839

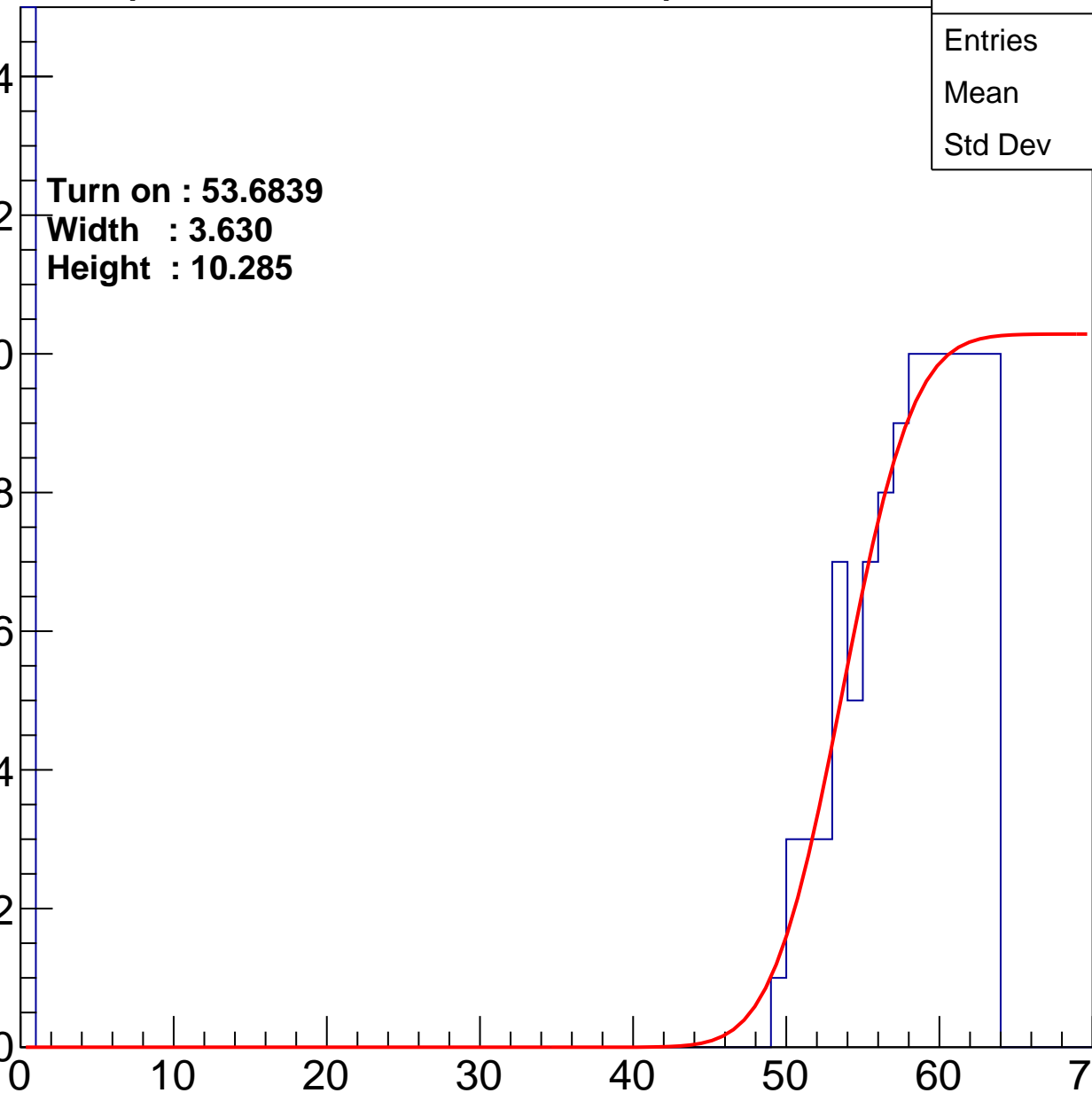
Width : 3.630

Height : 10.285

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	28.52
Std Dev	28.95

Turn on : 54.8335

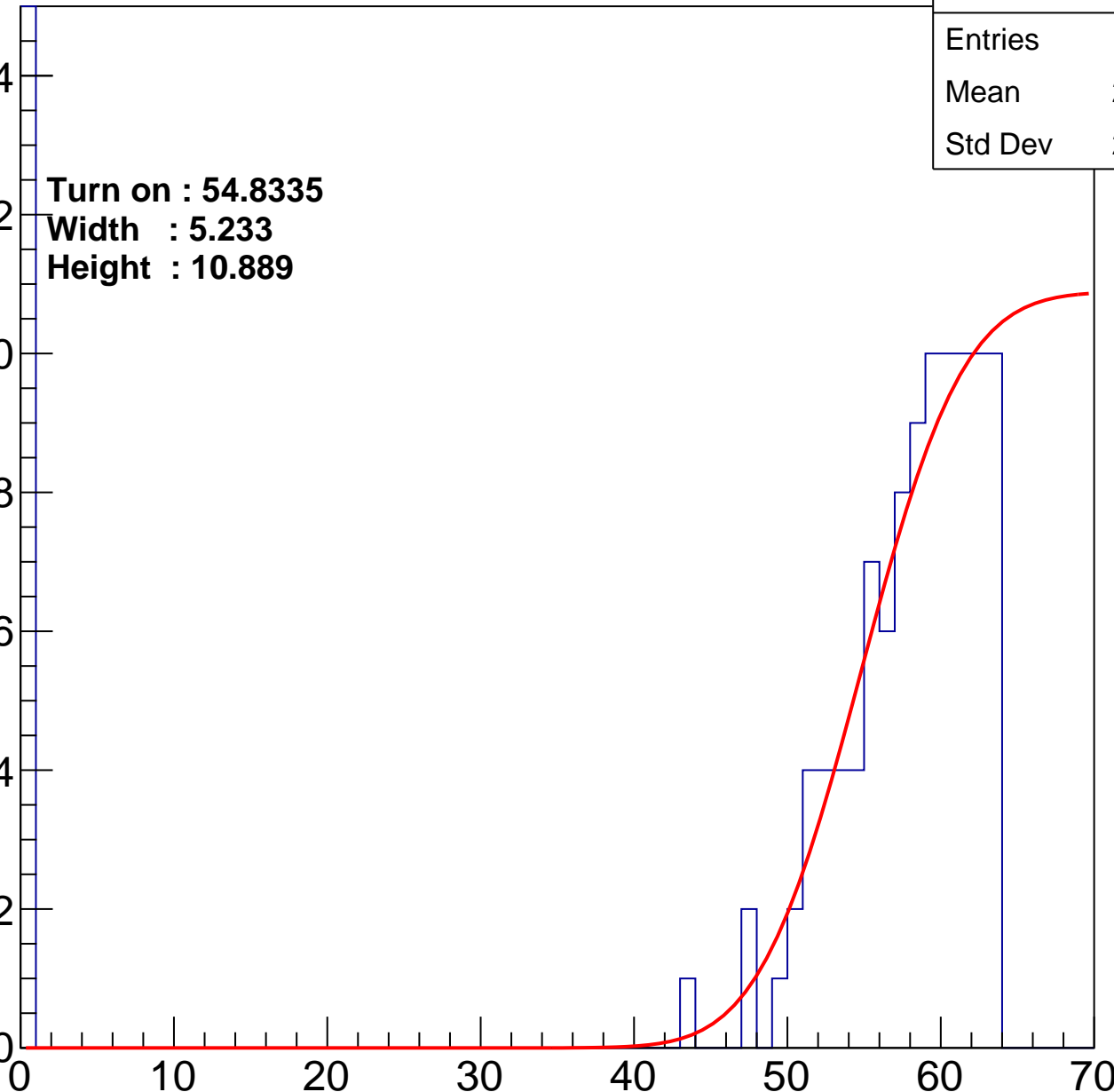
Width : 5.233

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch67

calib_packv5_033123_0516.root, FC#4, port A1

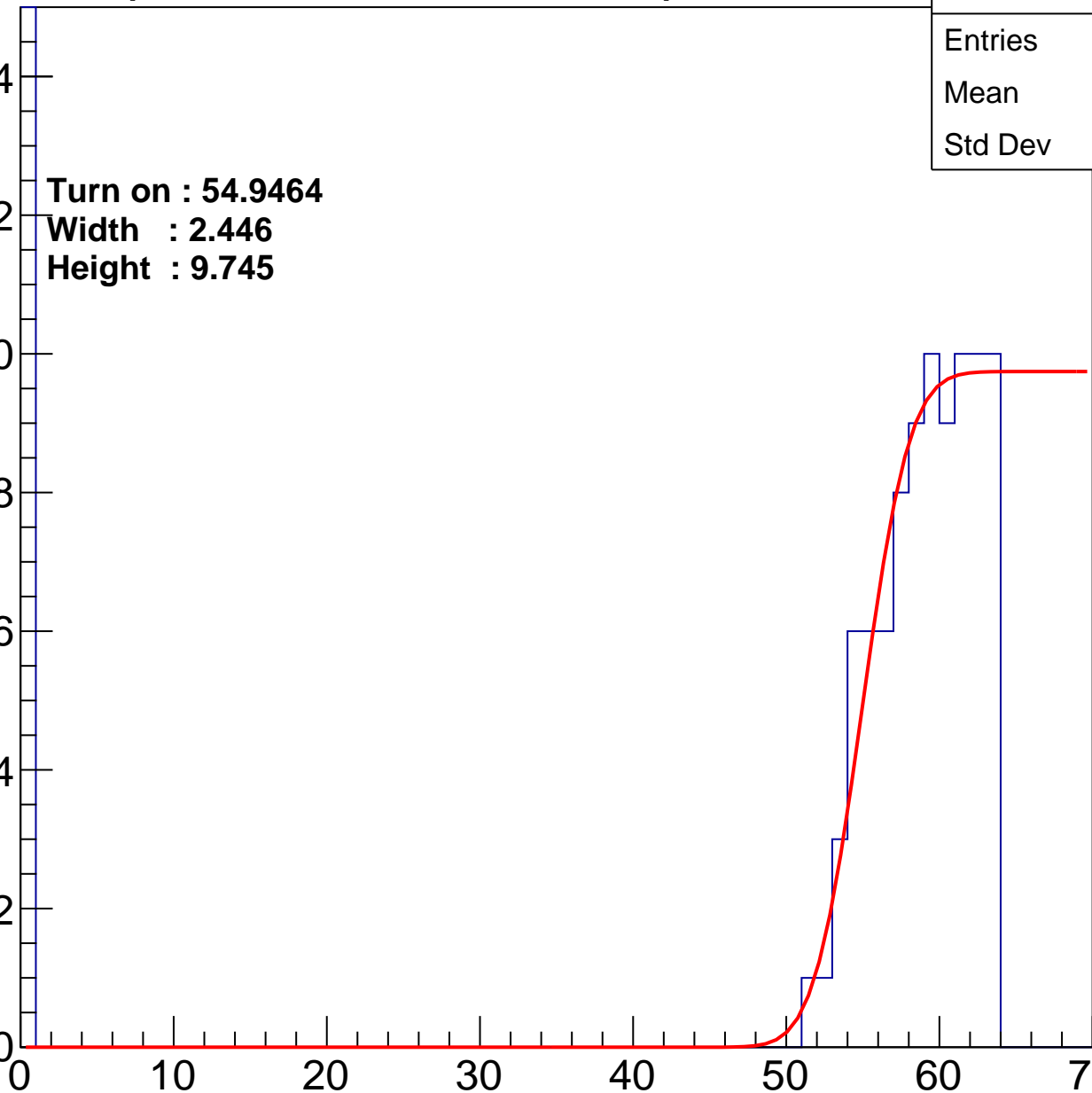
Entry

14
12
10
8
6
4
2
0

Turn on : 54.9464
Width : 2.446
Height : 9.745

Entries	176
Mean	29.66
Std Dev	29.41

ampl



B1L104S, U10-ch68

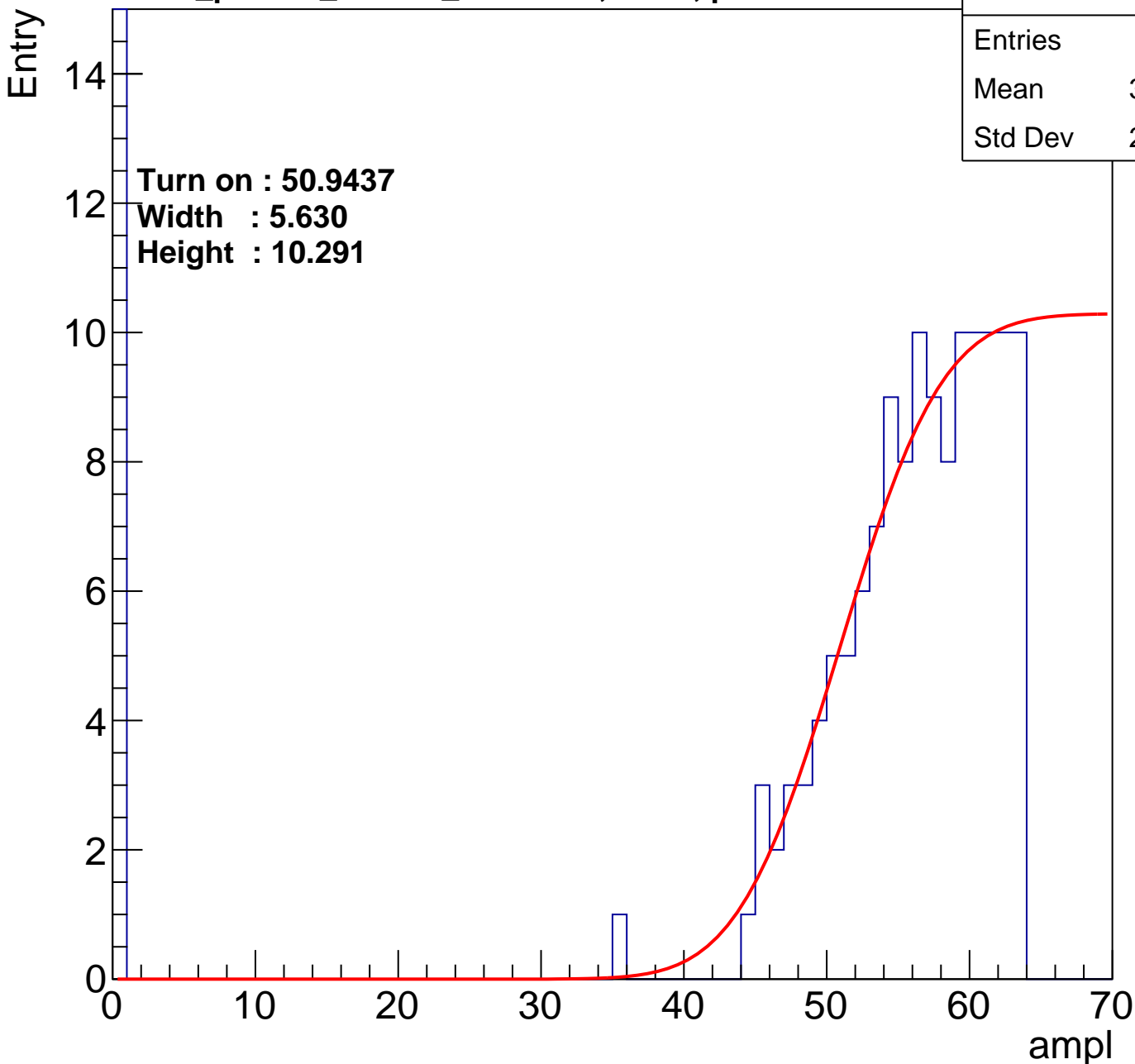
calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	34.19
Std Dev	27.54

Turn on : 50.9437

Width : 5.630

Height : 10.291



B1L104S, U10-ch69

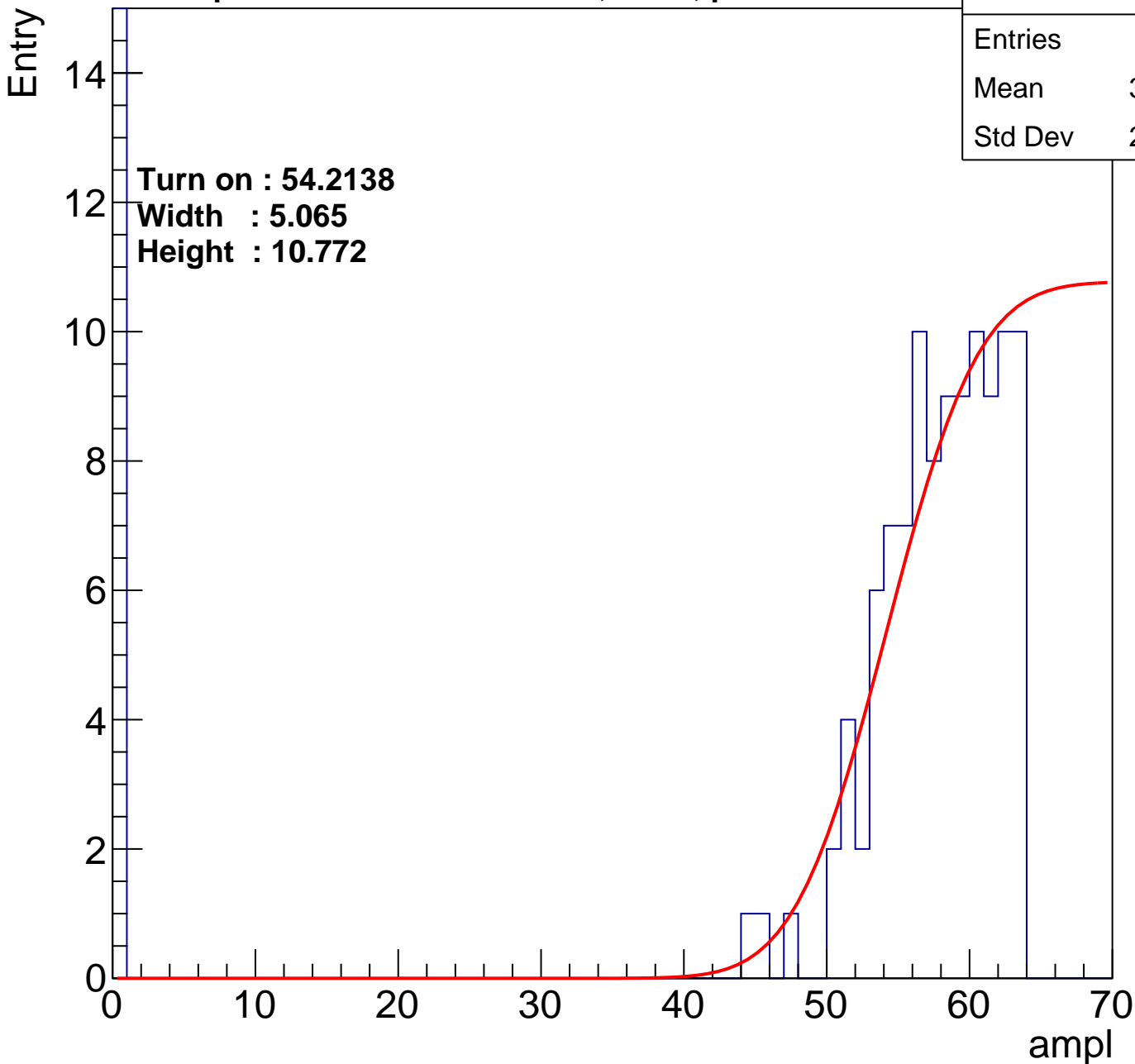
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	30.62
Std Dev	28.83

Turn on : 54.2138

Width : 5.065

Height : 10.772



B1L104S, U10-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	244
Mean	32.39
Std Dev	27.91

Turn on : 50.1940

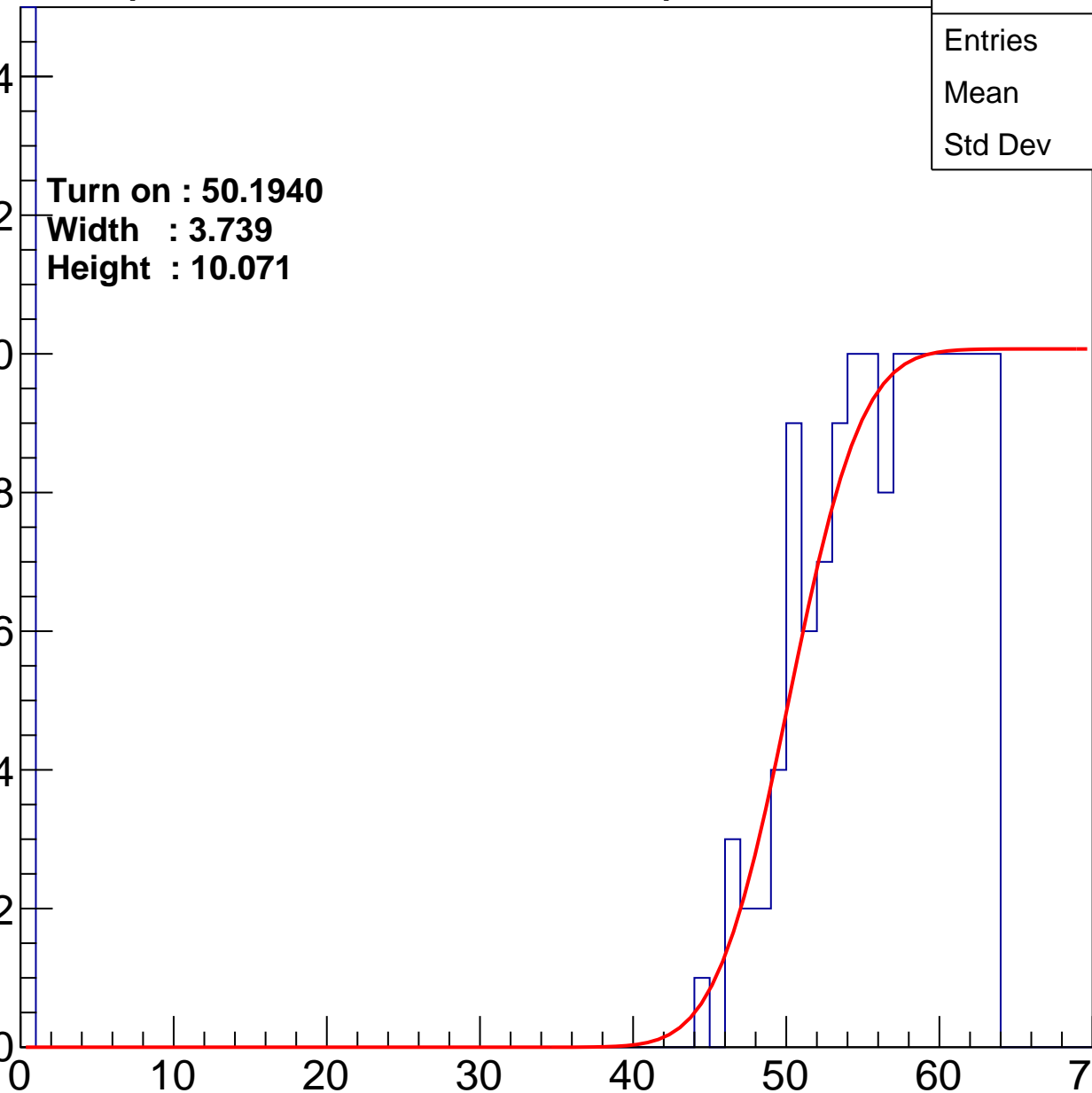
Width : 3.739

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	28.41
Std Dev	29.23

Turn on : 54.1046

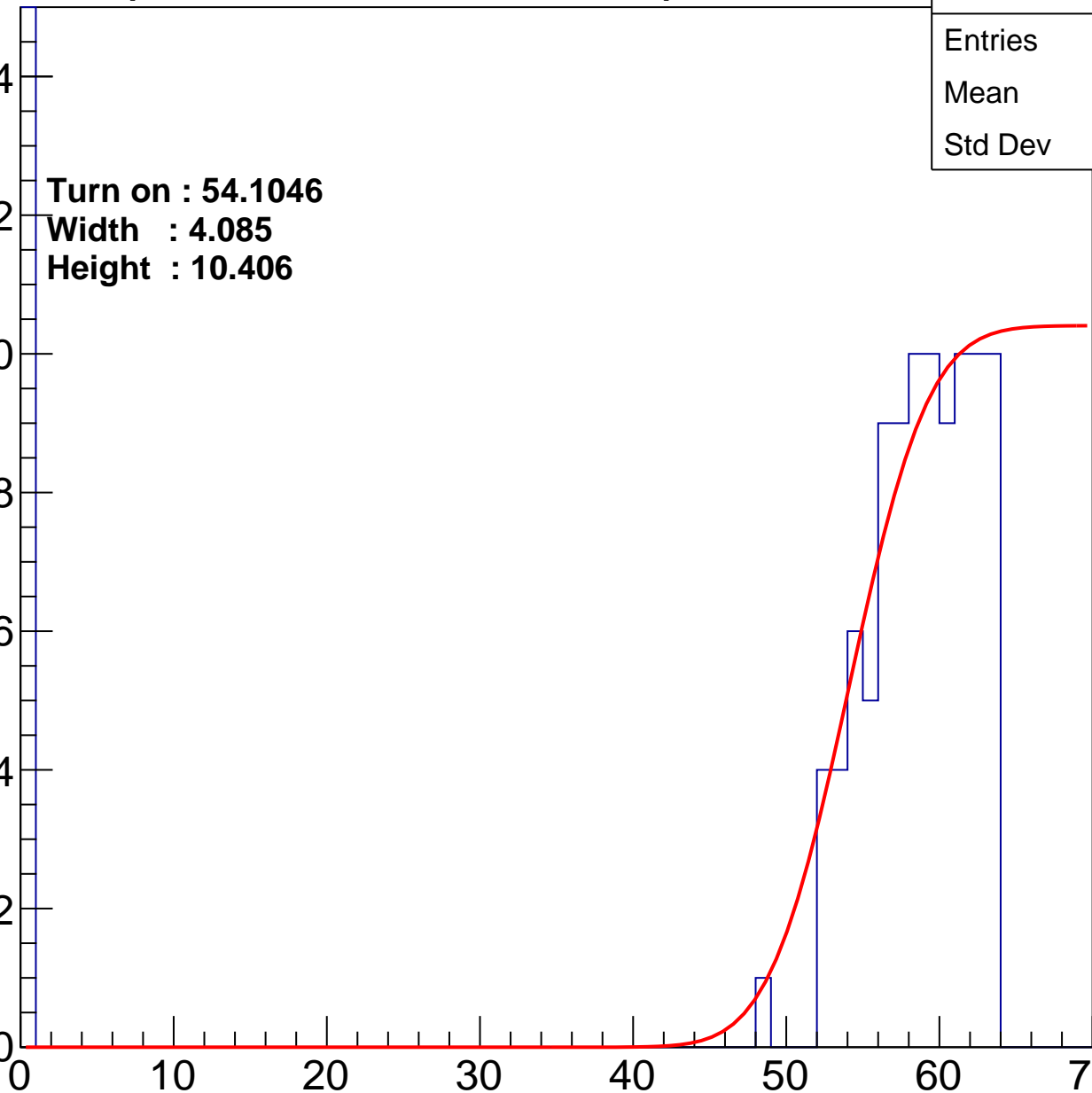
Width : 4.085

Height : 10.406

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch72

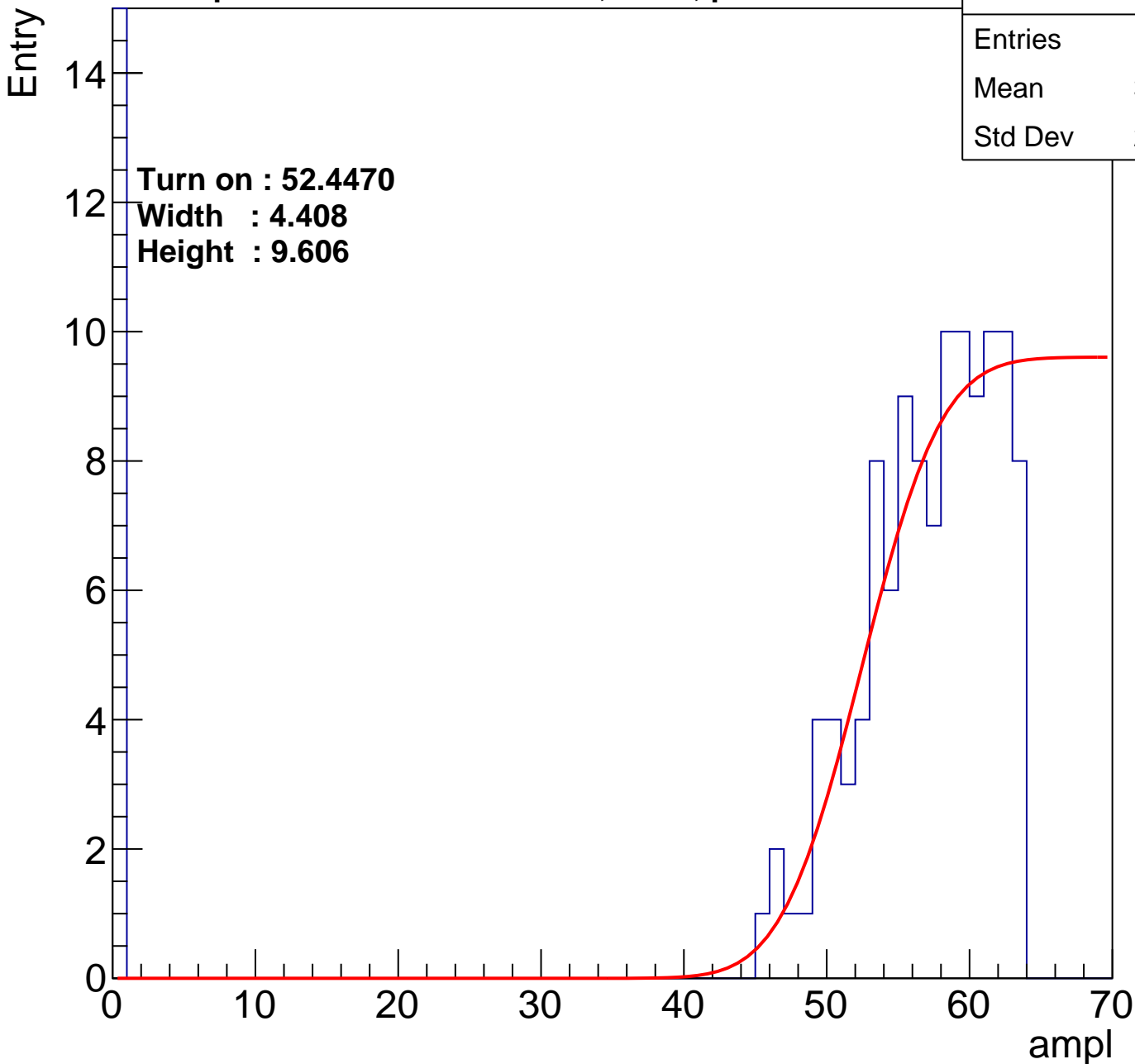
calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	31.21
Std Dev	28.41

Turn on : 52.4470

Width : 4.408

Height : 9.606



B1L104S, U10-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	35.37
Std Dev	28.12

Turn on : 52.6976

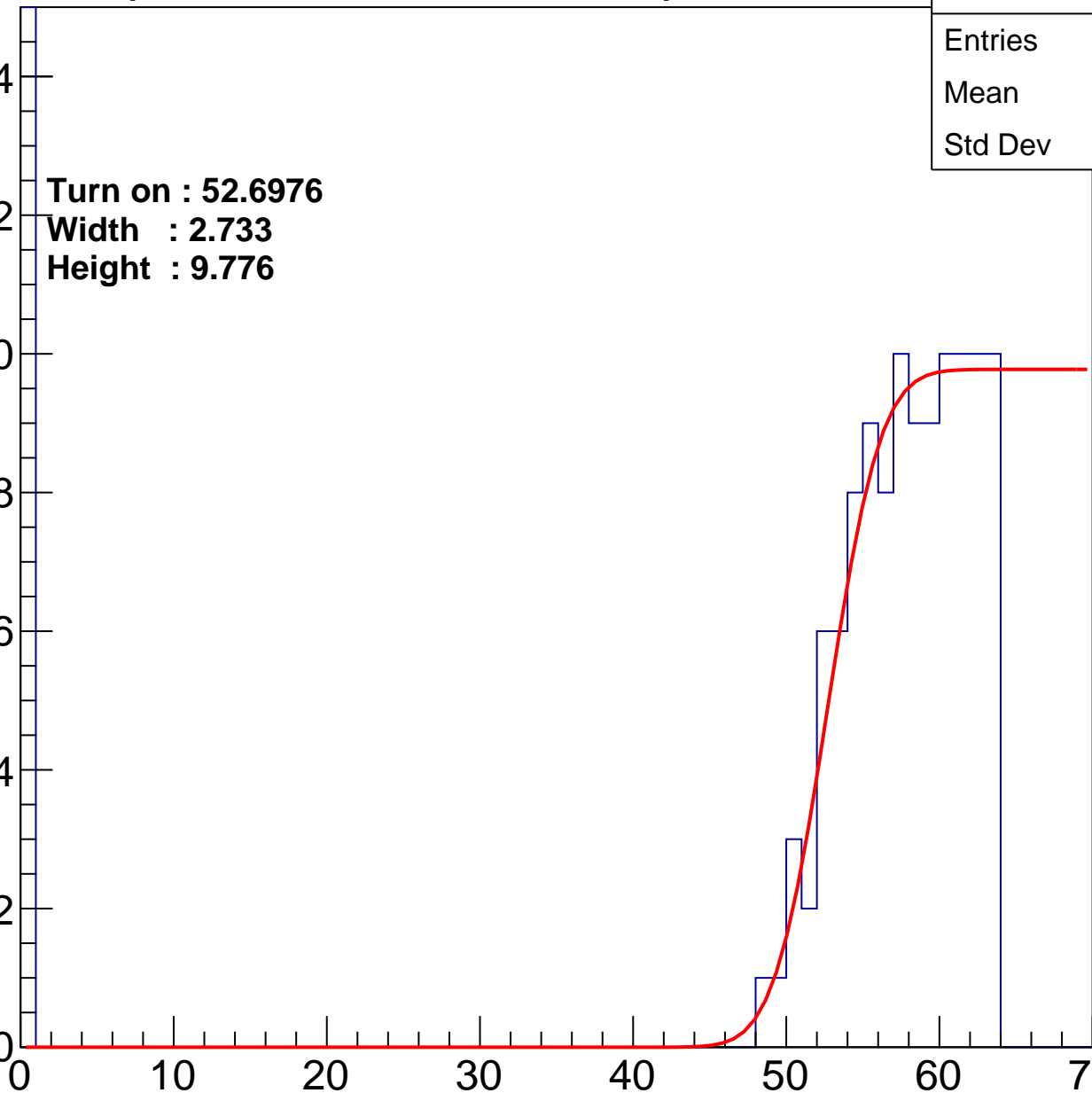
Width : 2.733

Height : 9.776

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	39.37
Std Dev	26.69

Turn on : 52.8200

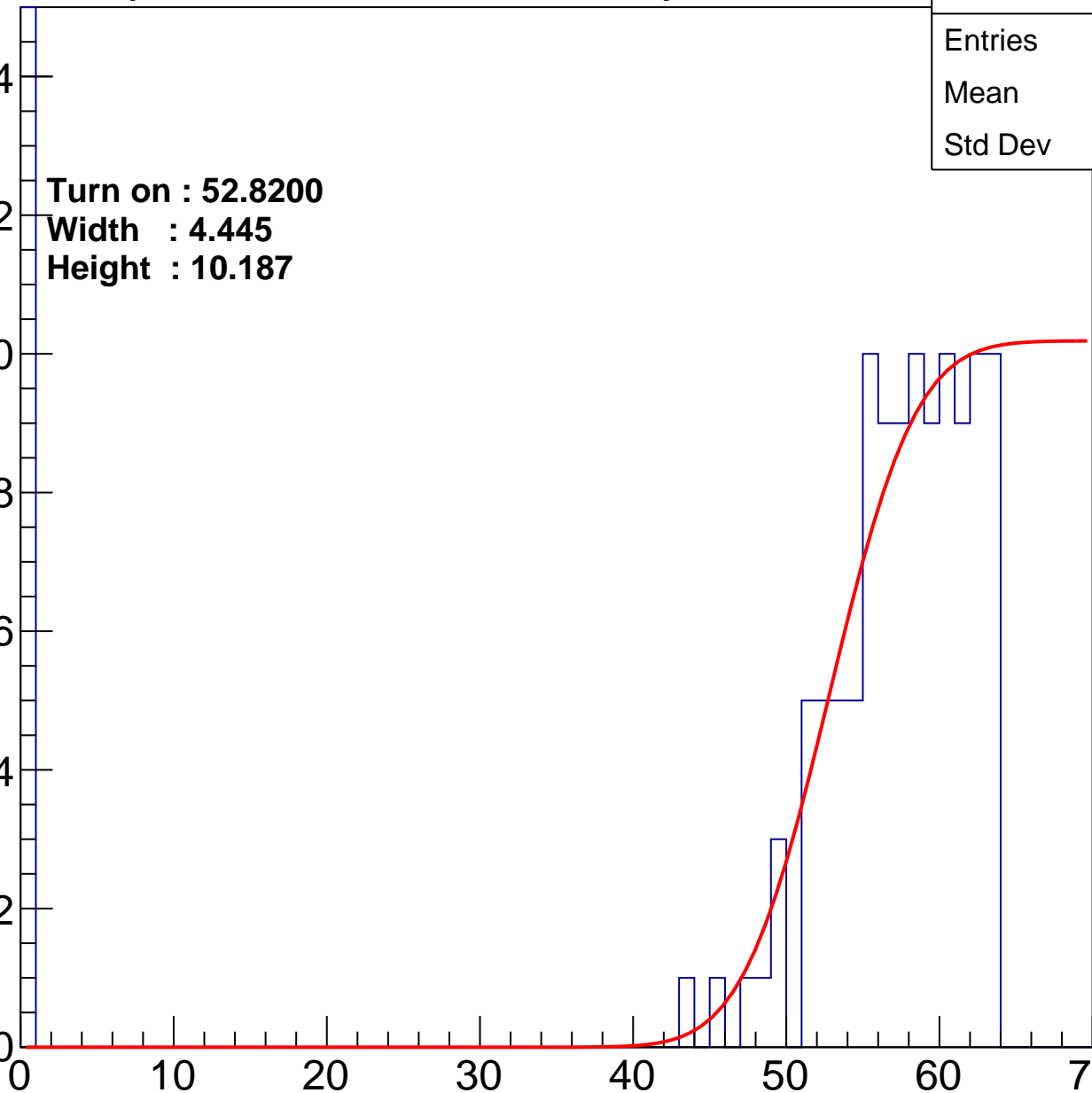
Width : 4.445

Height : 10.187

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	29.64
Std Dev	29.4

Turn on : 55.8754

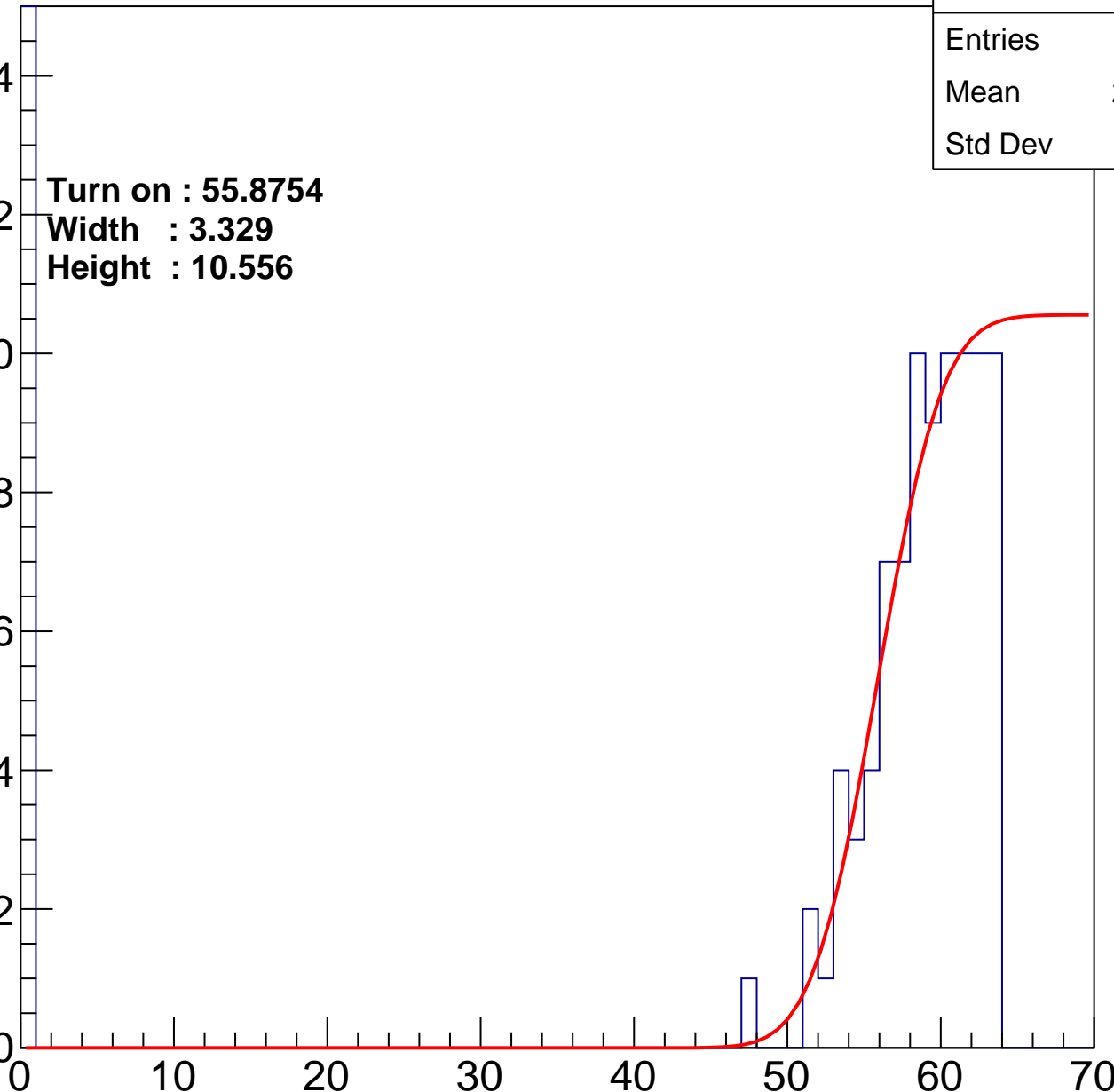
Width : 3.329

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	31.79
Std Dev	28.56

Turn on : 52.8082

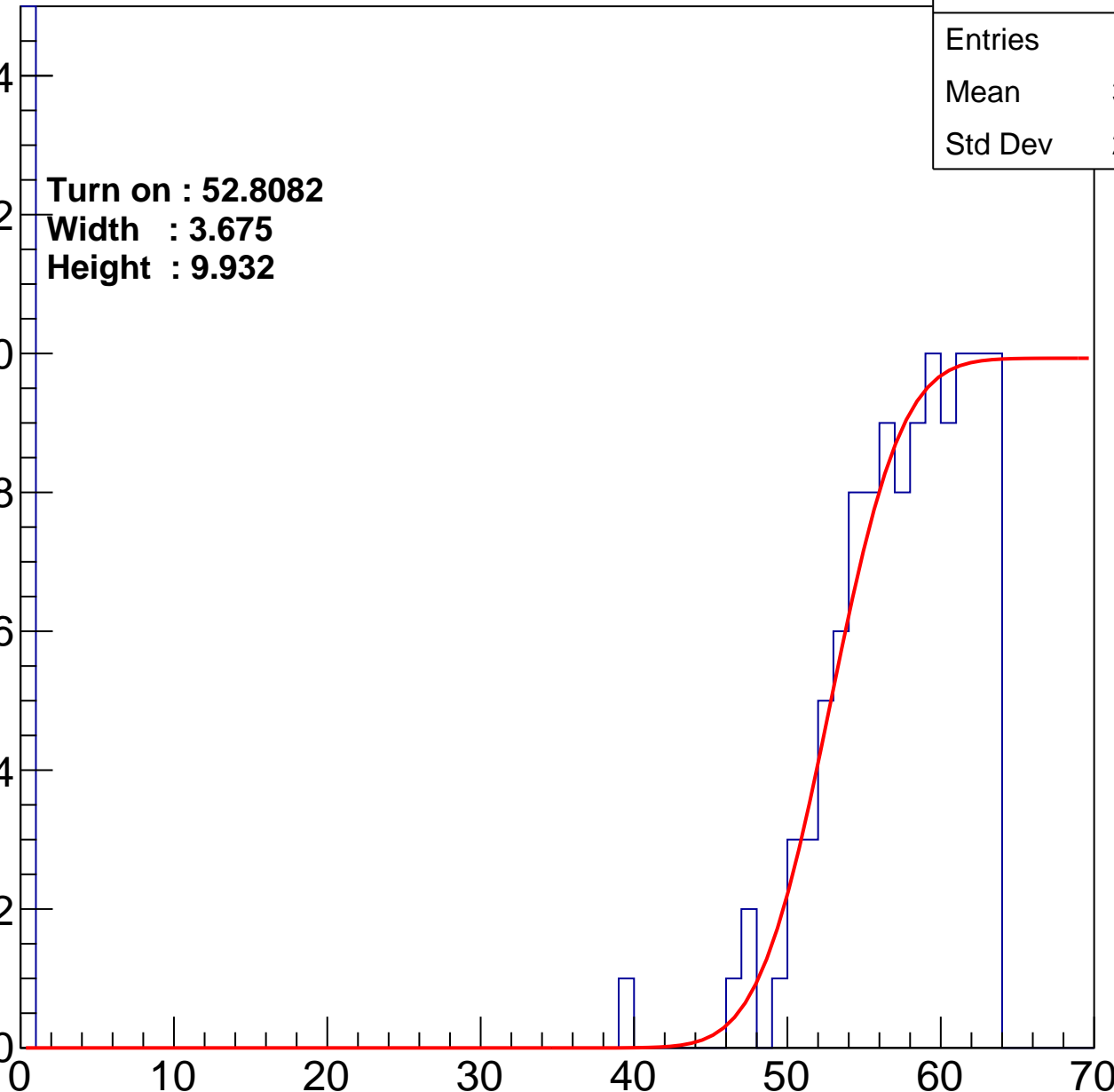
Width : 3.675

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.61
Std Dev	28.44

Turn on : 53.4128

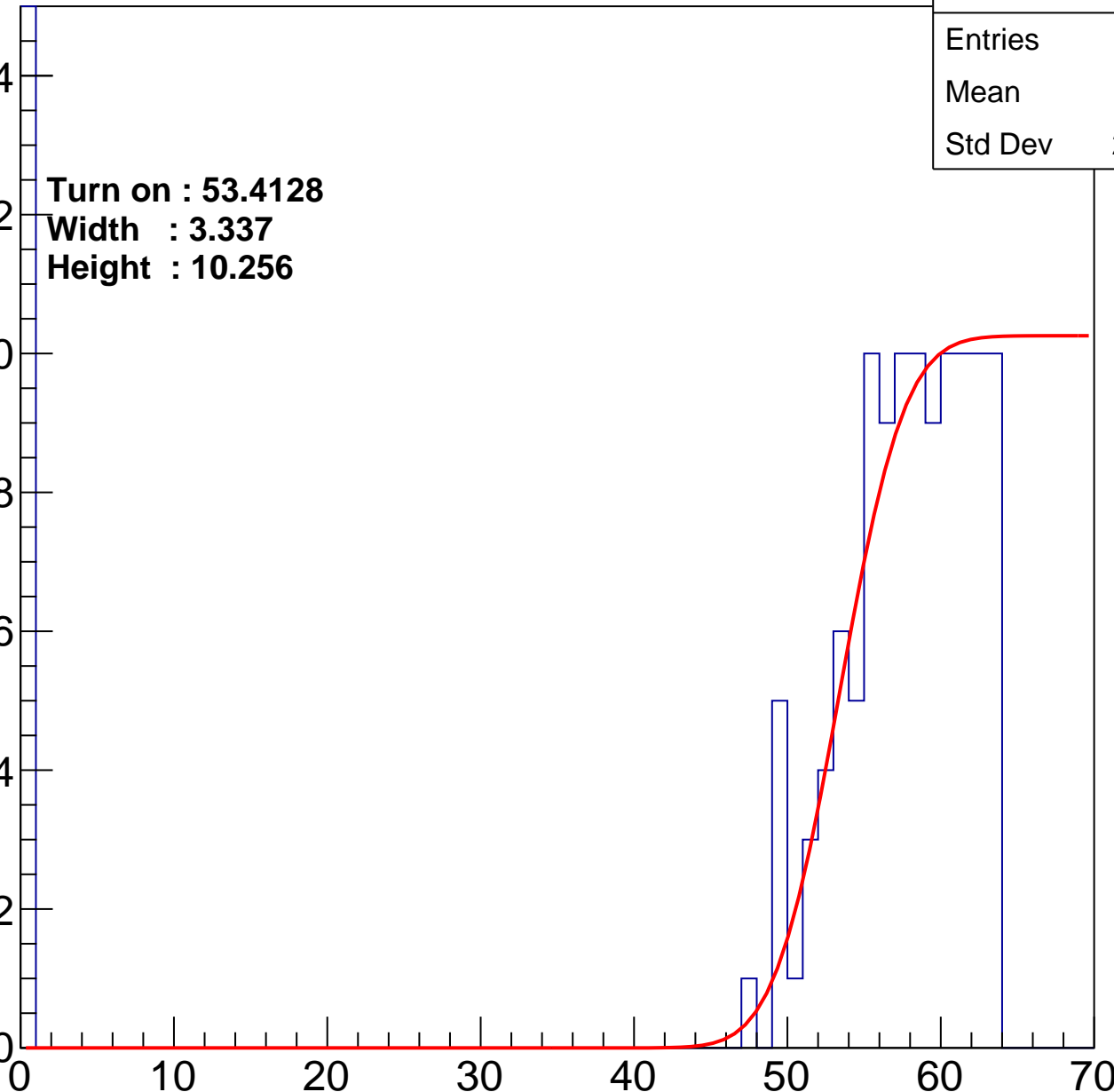
Width : 3.337

Height : 10.256

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	34.92
Std Dev	27.68

Turn on : 52.5272

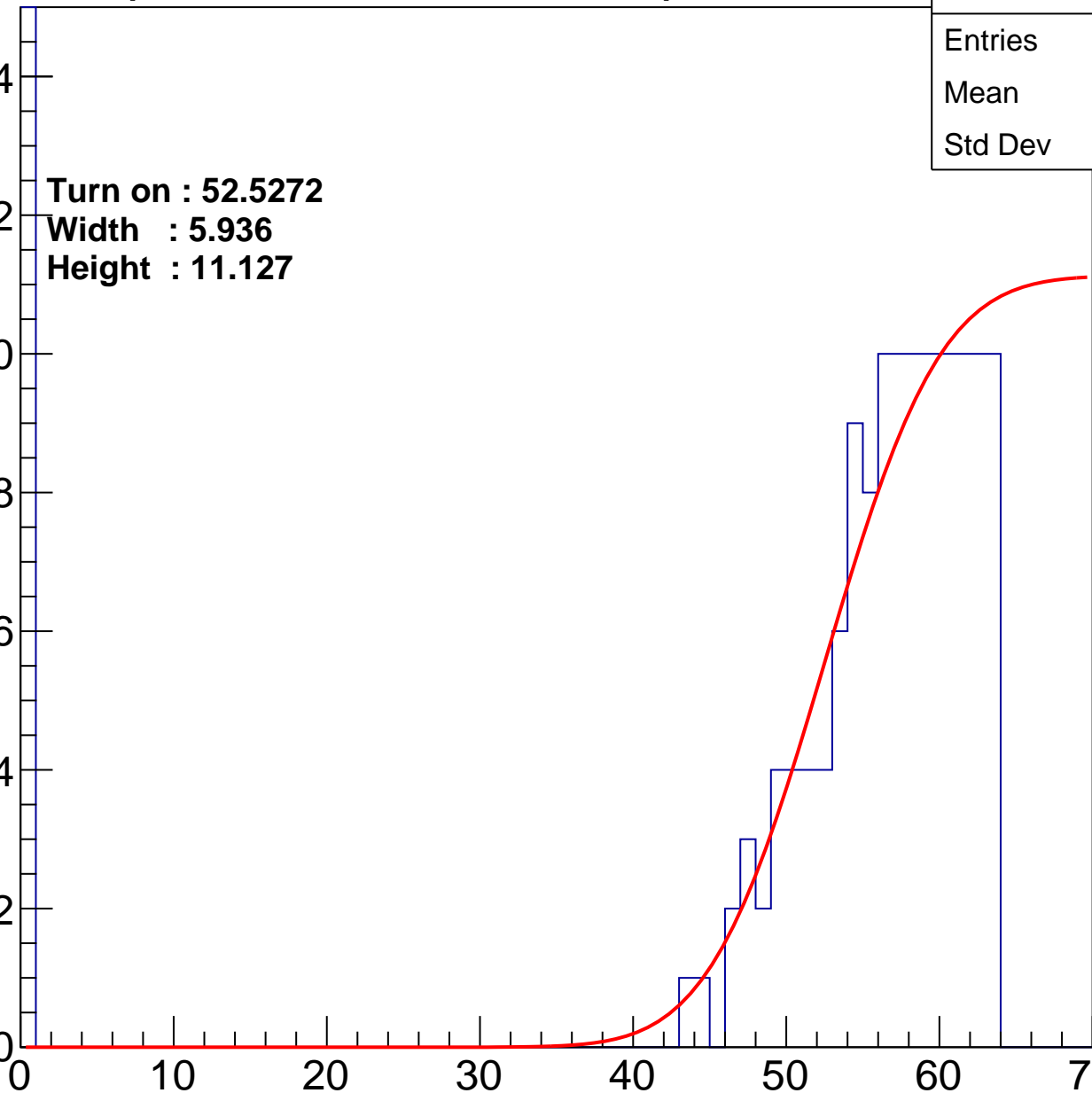
Width : 5.936

Height : 11.127

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch79

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.33
Std Dev	28.12

Turn on : 53.7356

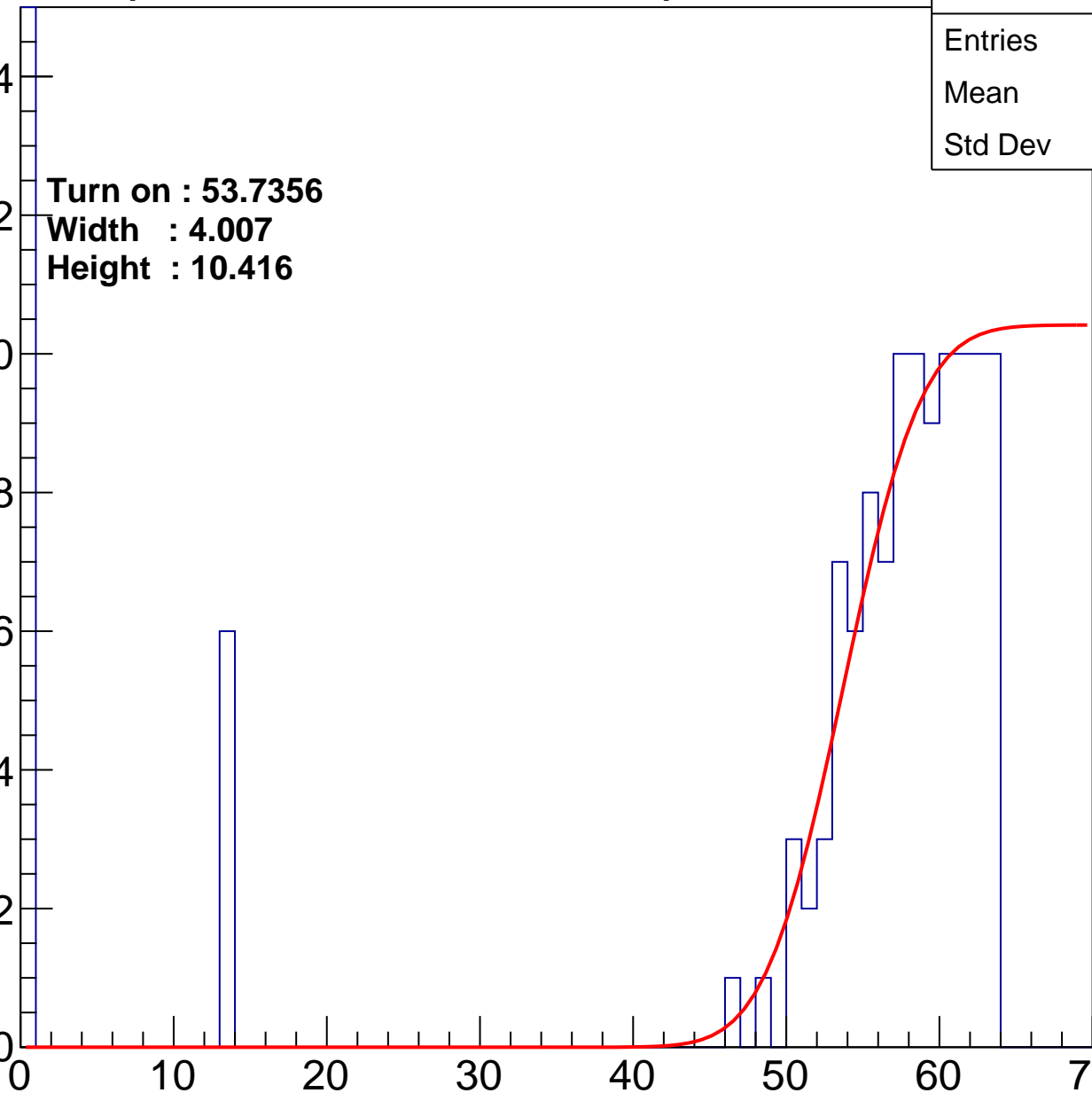
Width : 4.007

Height : 10.416

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	32.07
Std Dev	28.44

Turn on : 53.9785

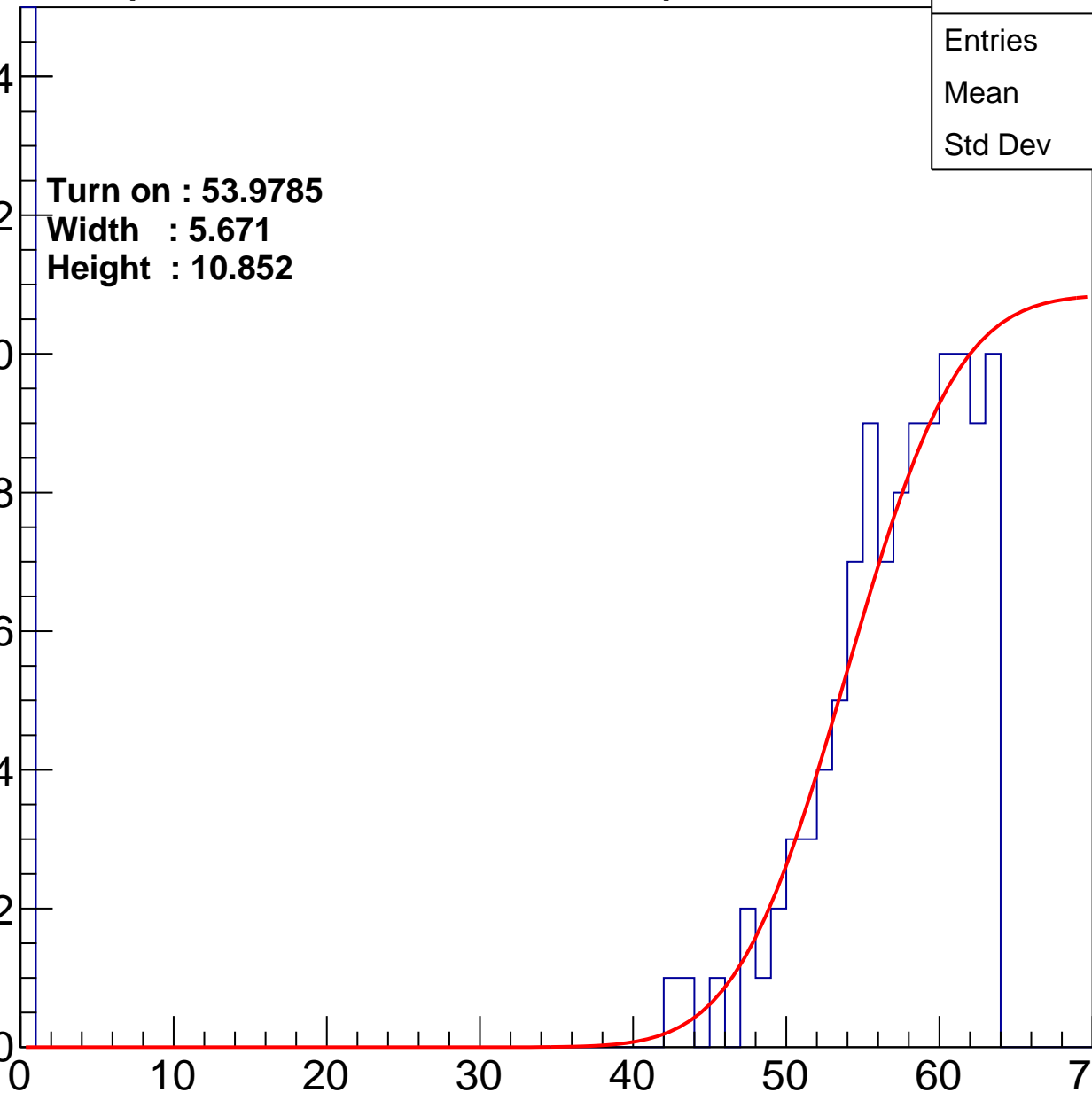
Width : 5.671

Height : 10.852

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch81

calib_packv5_033123_0516.root, FC#4, port A1

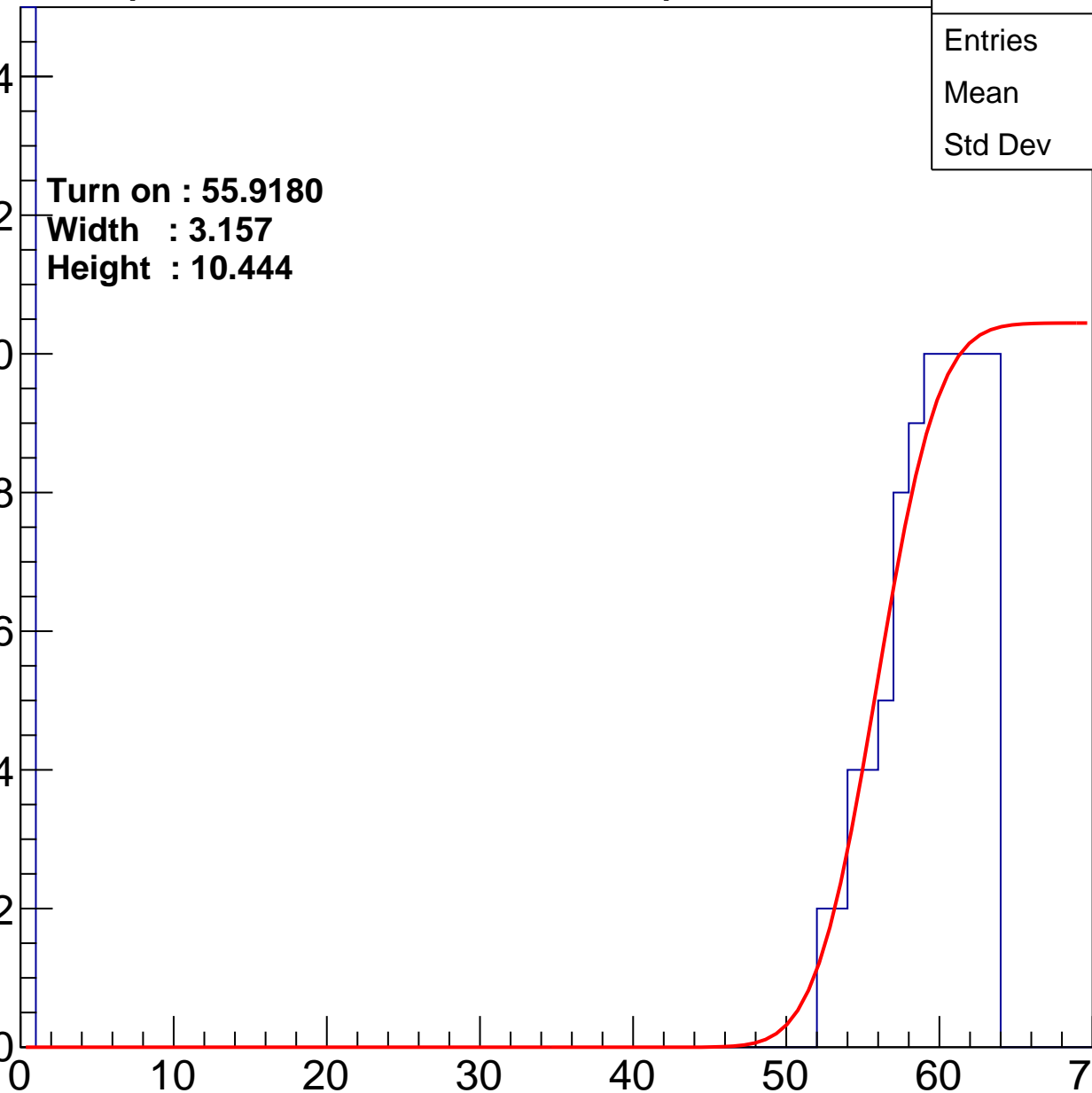
Entry

14
12
10
8
6
4
2
0

Turn on : 55.9180
Width : 3.157
Height : 10.444

Entries	168
Mean	29.49
Std Dev	29.56

ampl



B1L104S, U10-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	36.42
Std Dev	27.43

Turn on : 54.6883

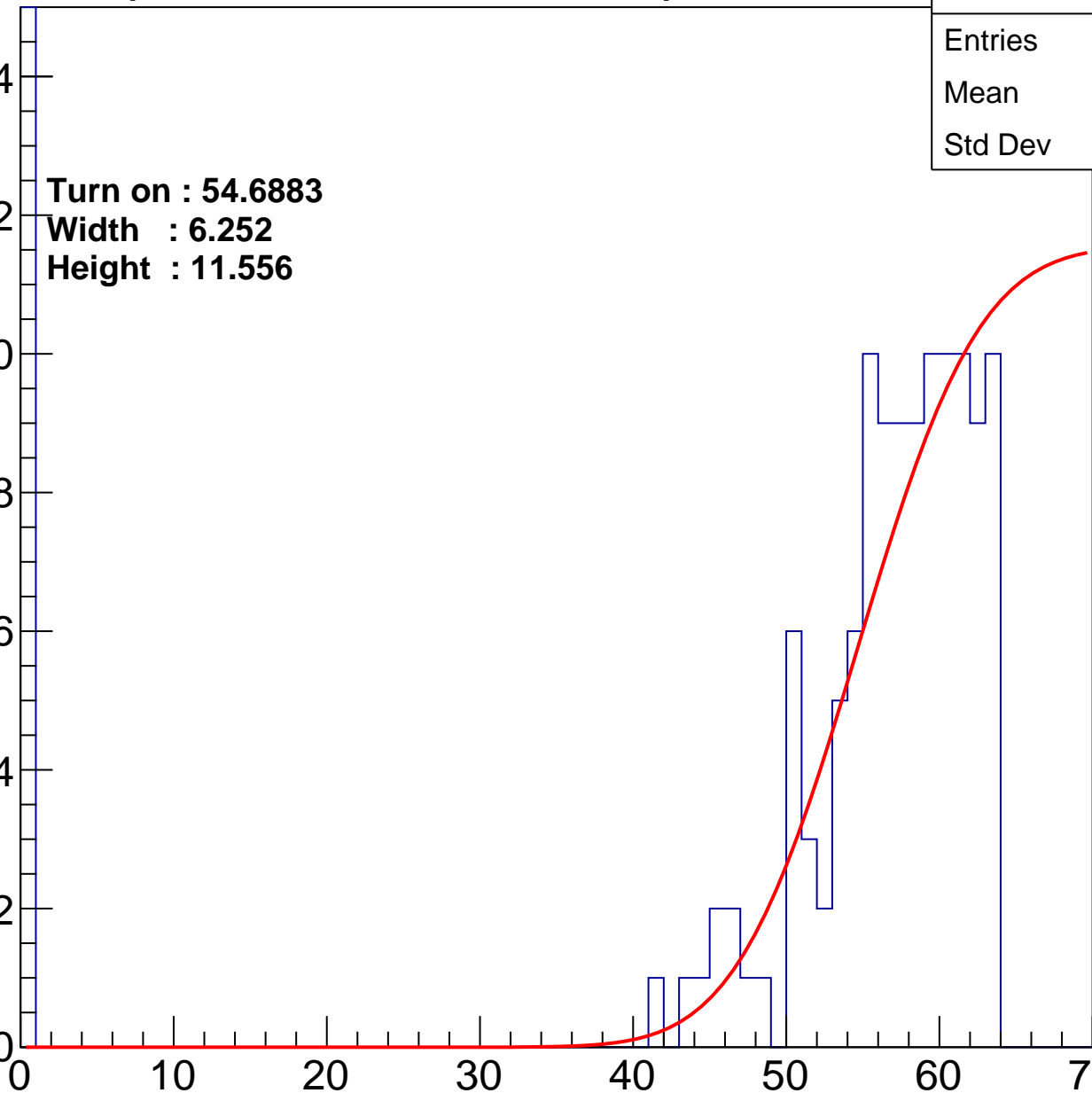
Width : 6.252

Height : 11.556

Entry

14
12
10
8
6
4
2
0

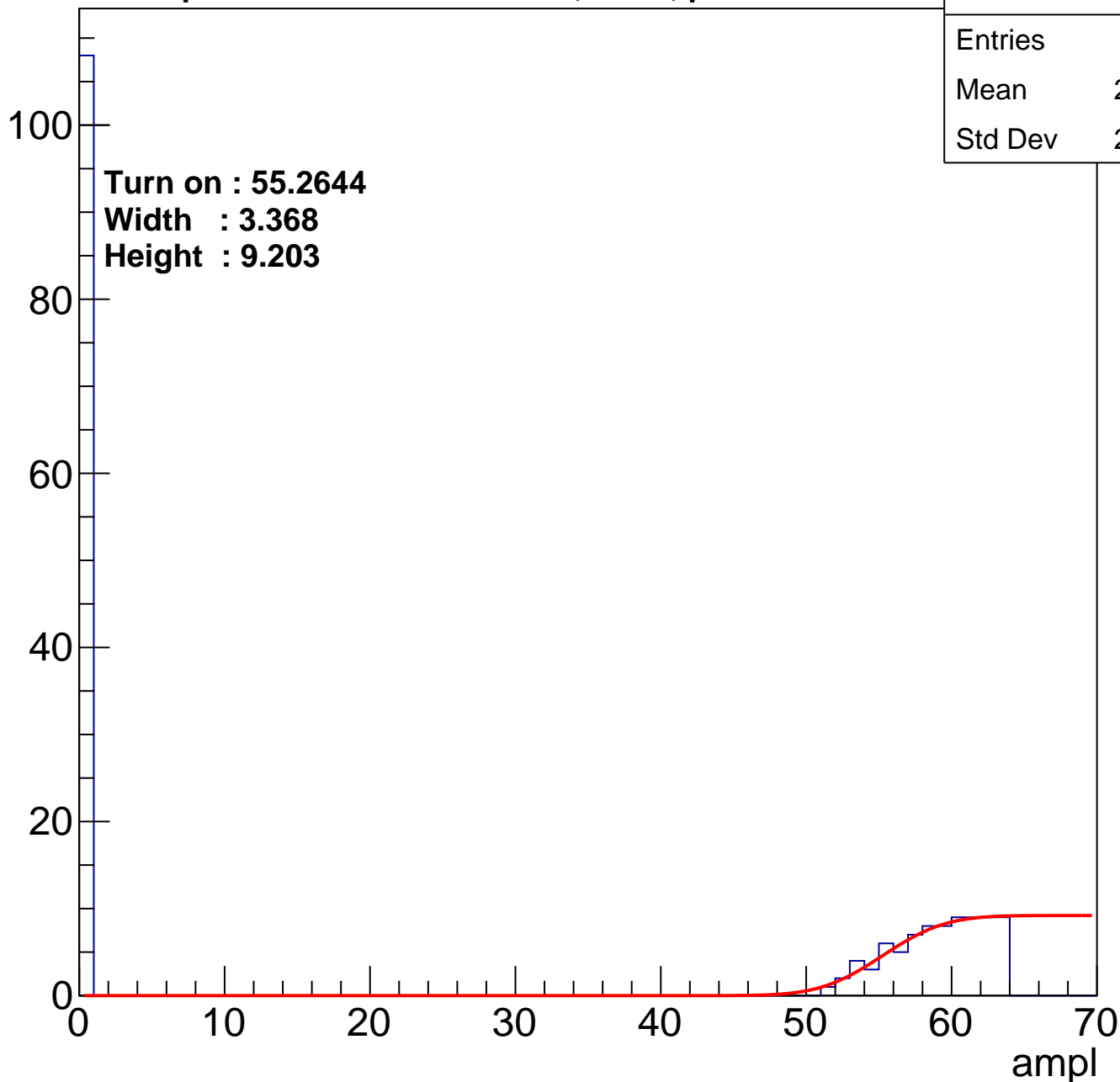
ampl



B1L104S, U10-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entry



B1L104S, U10-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	29.41
Std Dev	28.67

Turn on : 53.0173

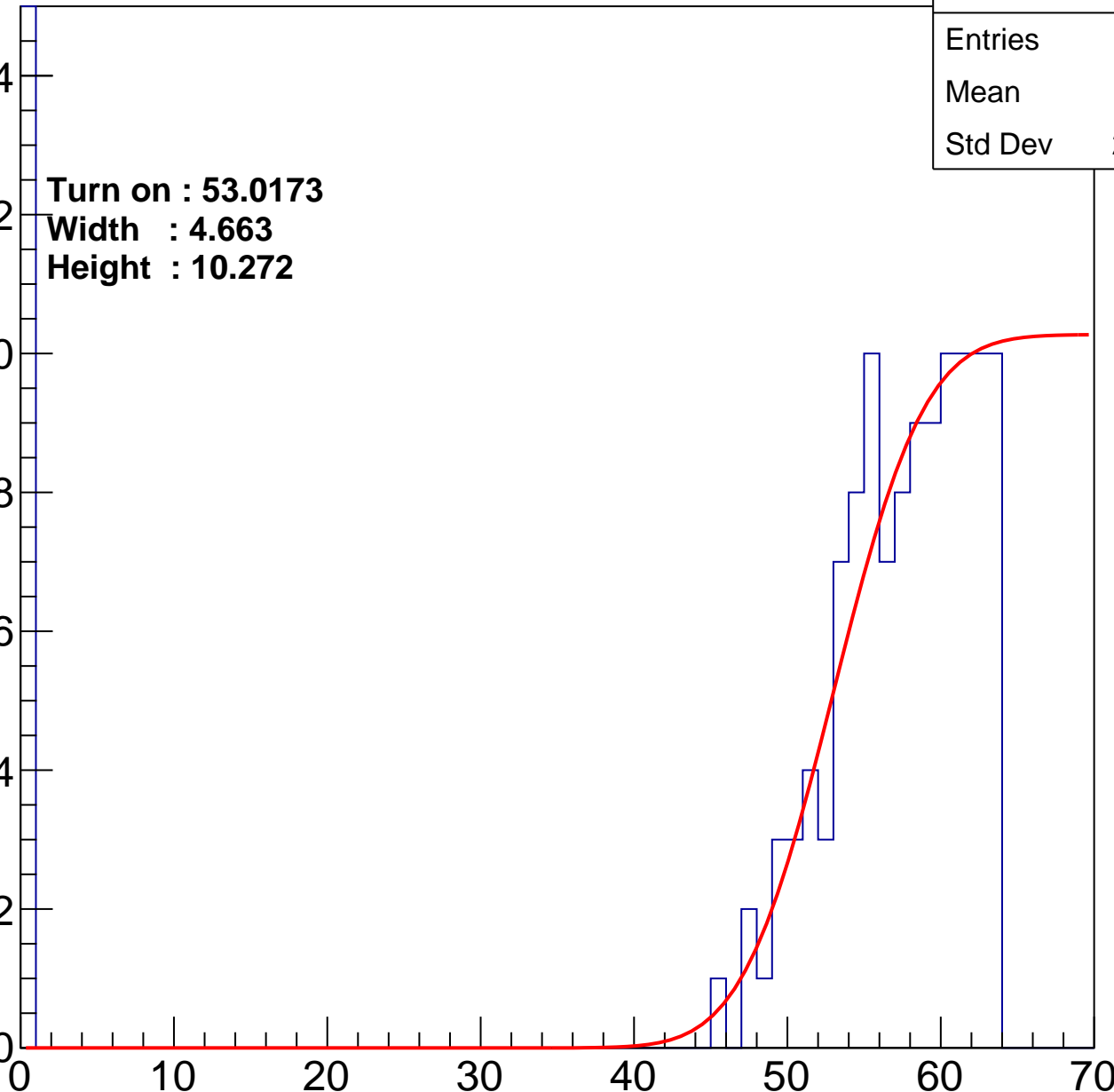
Width : 4.663

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	30.85
Std Dev	28.9

Turn on : 53.8949

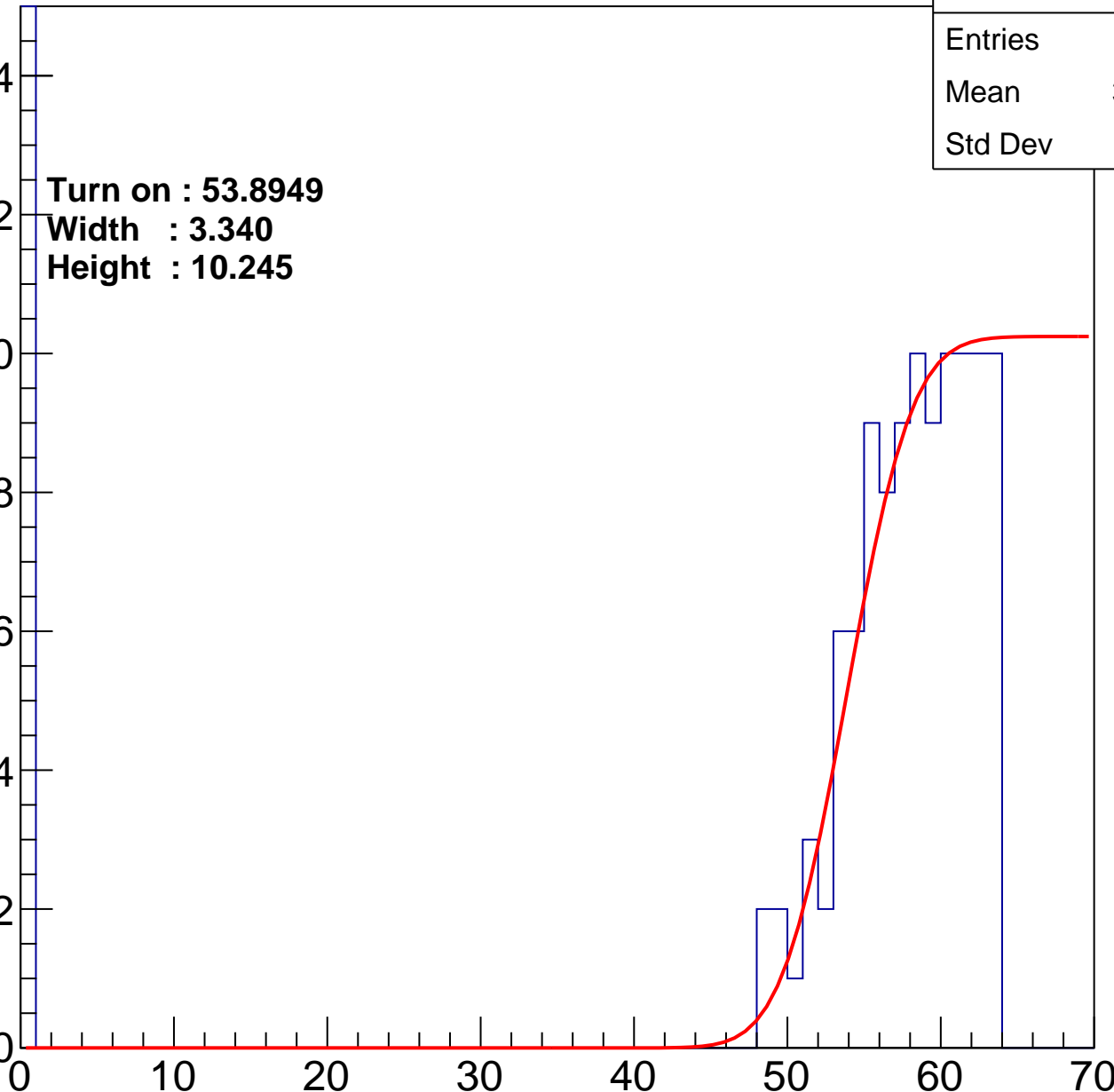
Width : 3.340

Height : 10.245

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	35.62
Std Dev	27.81

Turn on : 53.6842

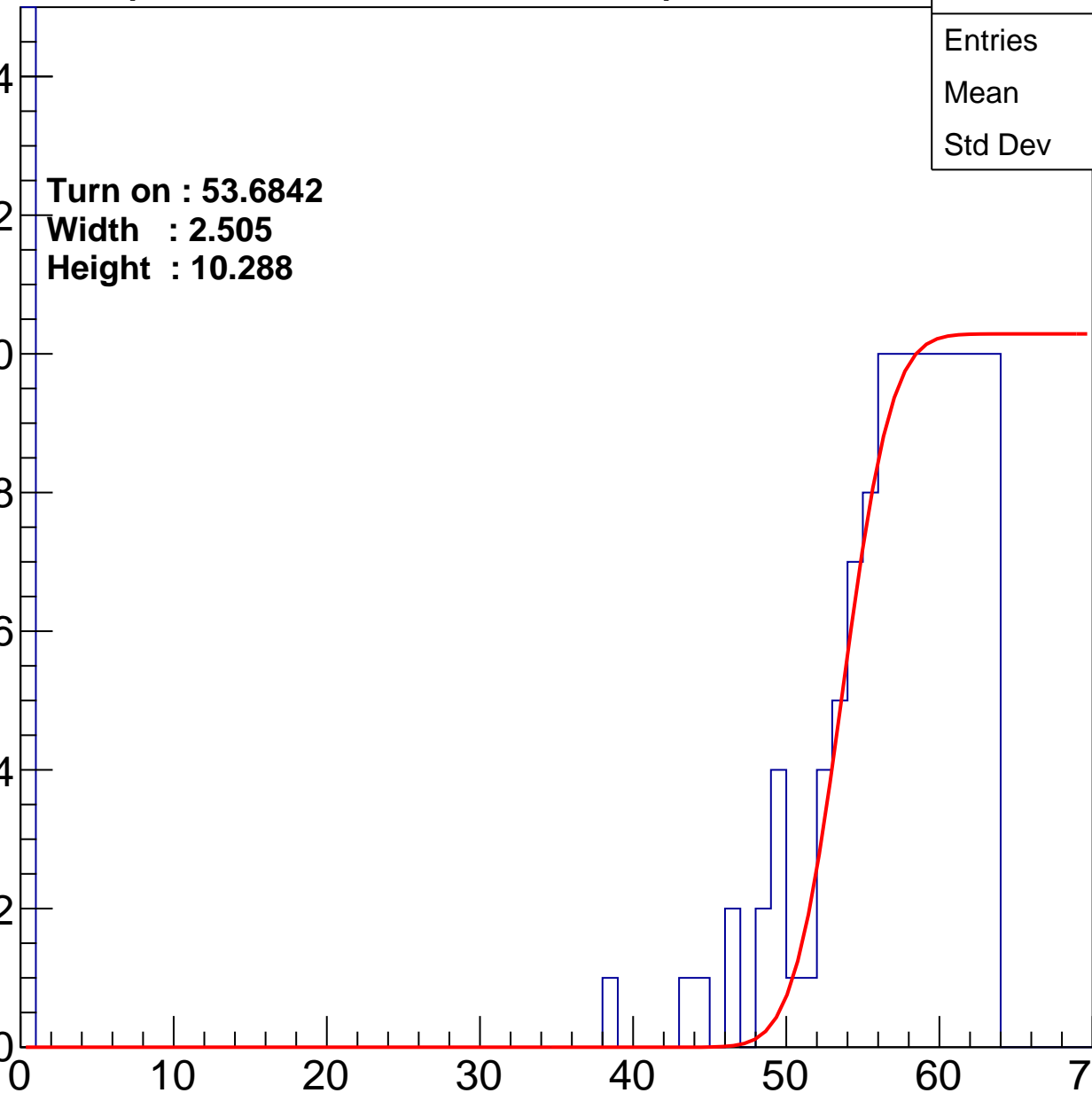
Width : 2.505

Height : 10.288

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	31.76
Std Dev	28.7

Turn on : 55.4054

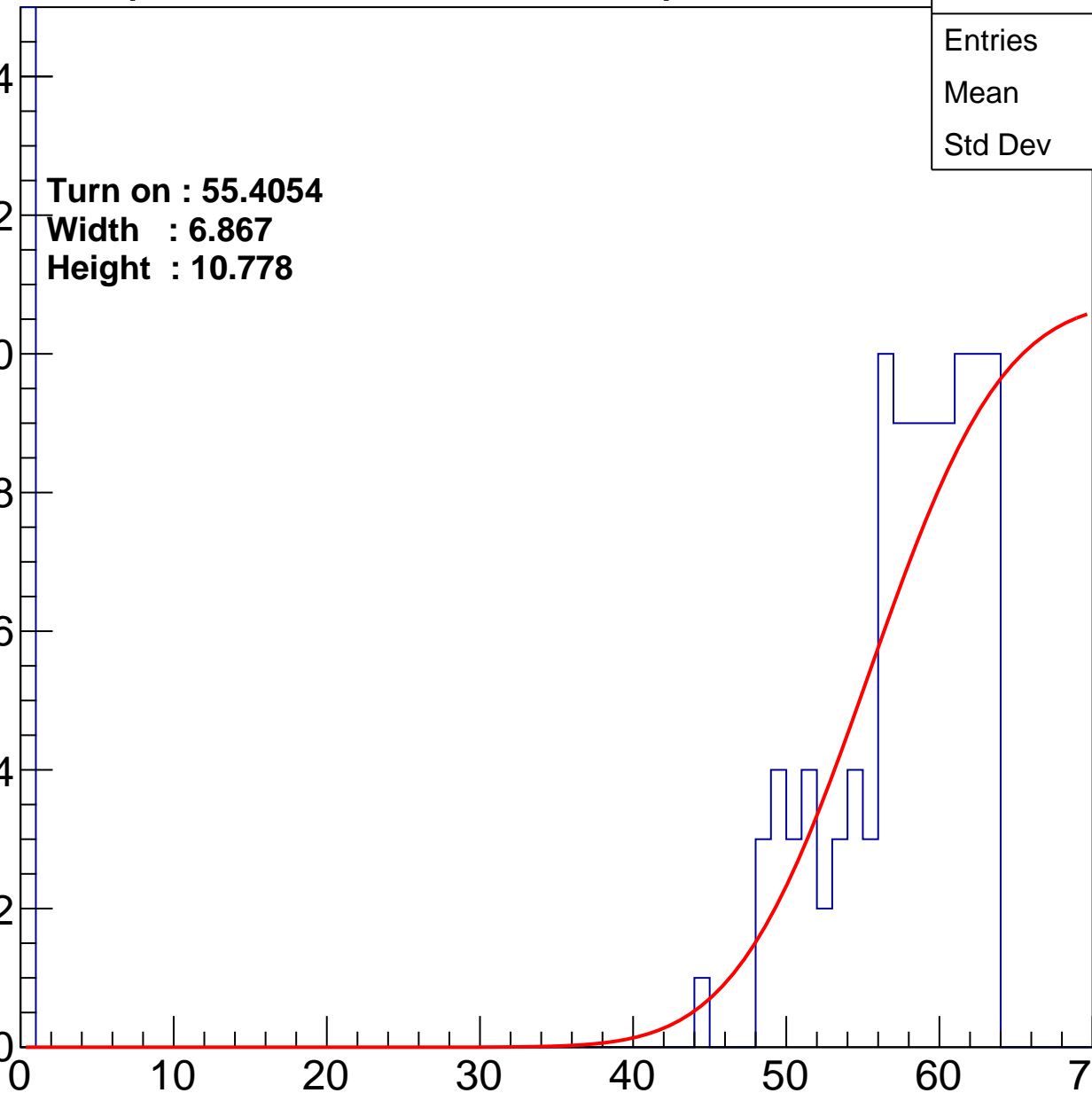
Width : 6.867

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch88

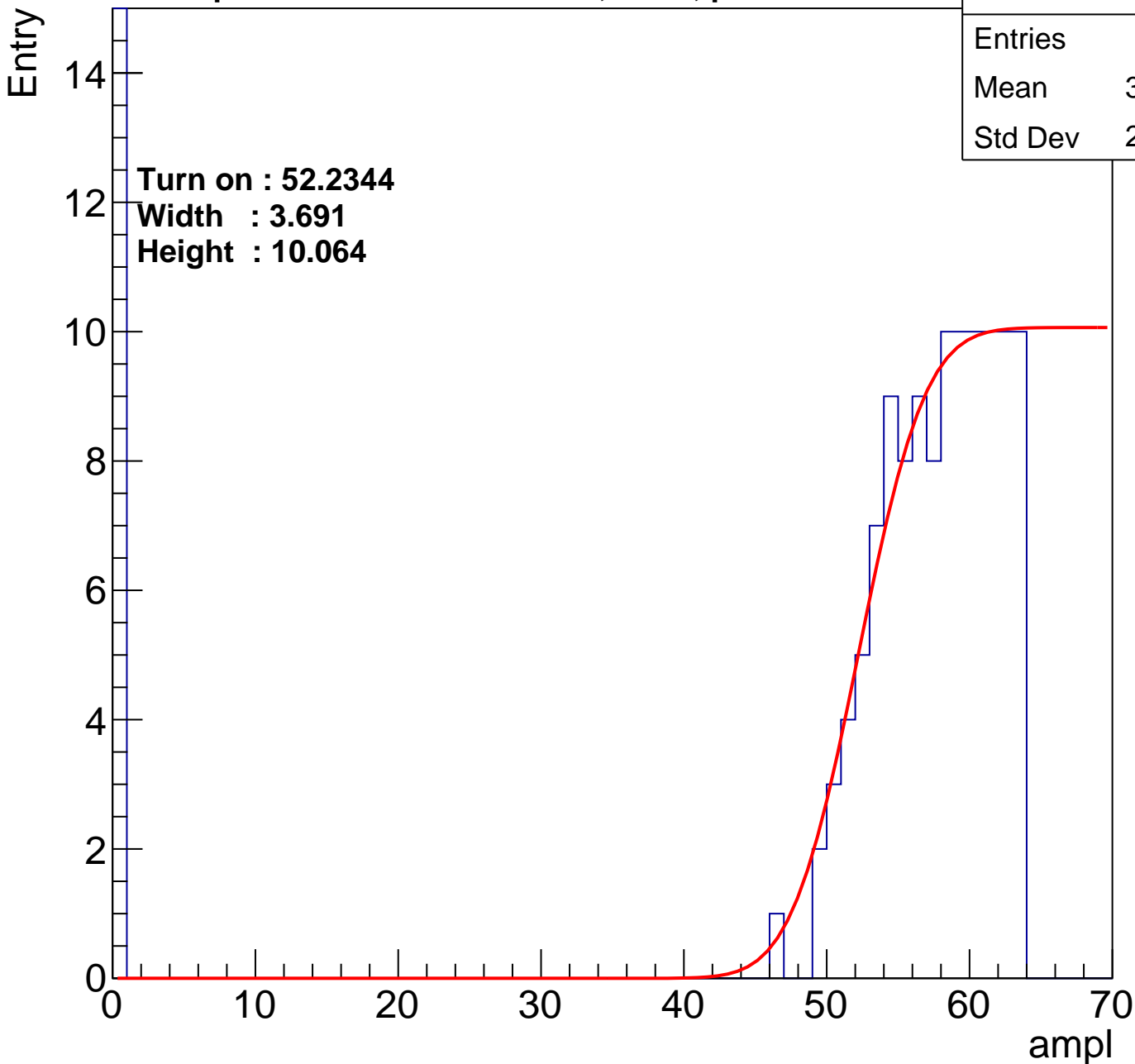
calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	33.06
Std Dev	28.46

Turn on : 52.2344

Width : 3.691

Height : 10.064



B1L104S, U10-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	29.03
Std Dev	29.15

Turn on : 54.7553

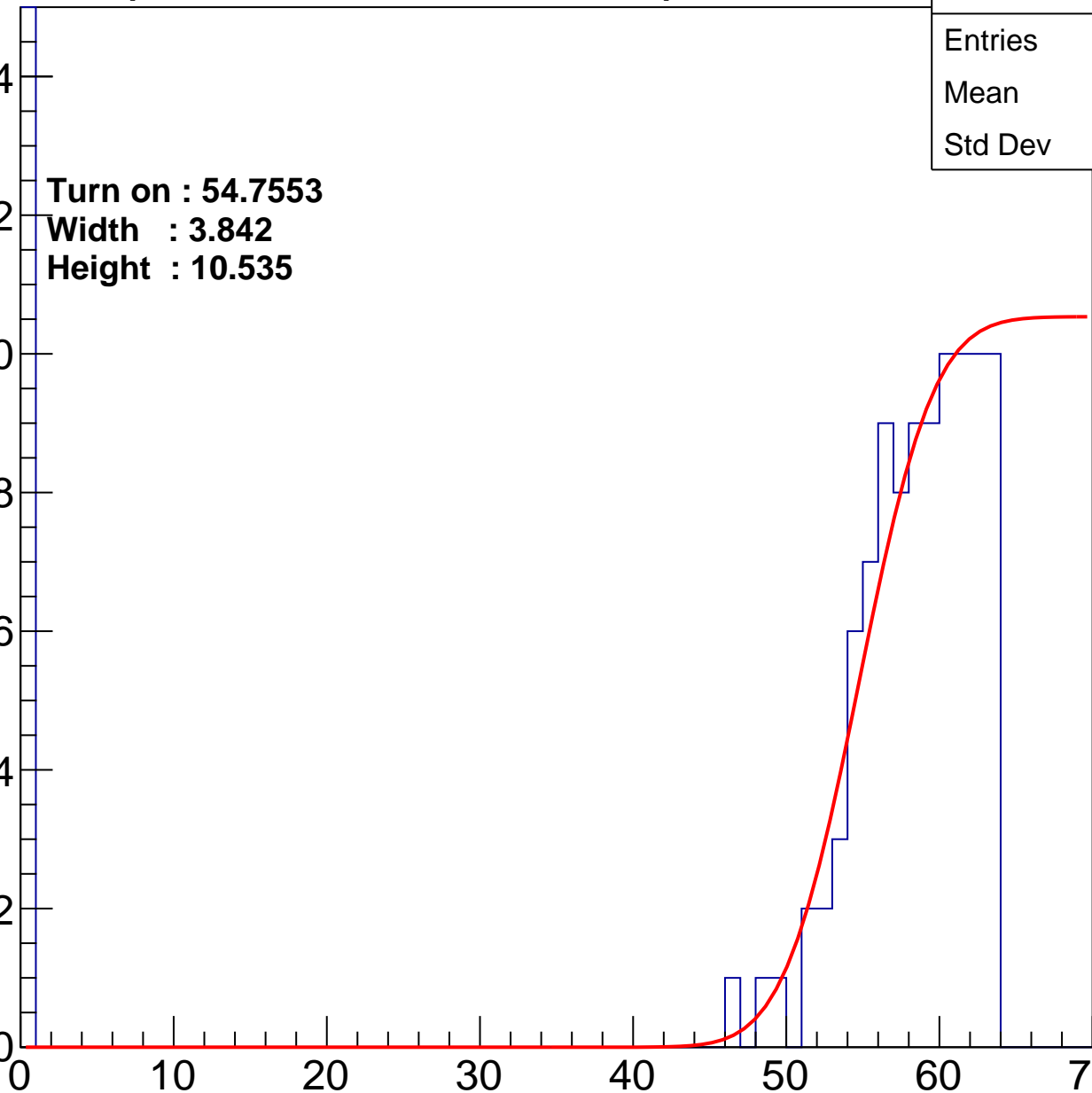
Width : 3.842

Height : 10.535

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	36.6
Std Dev	27.57

Turn on : 53.0359

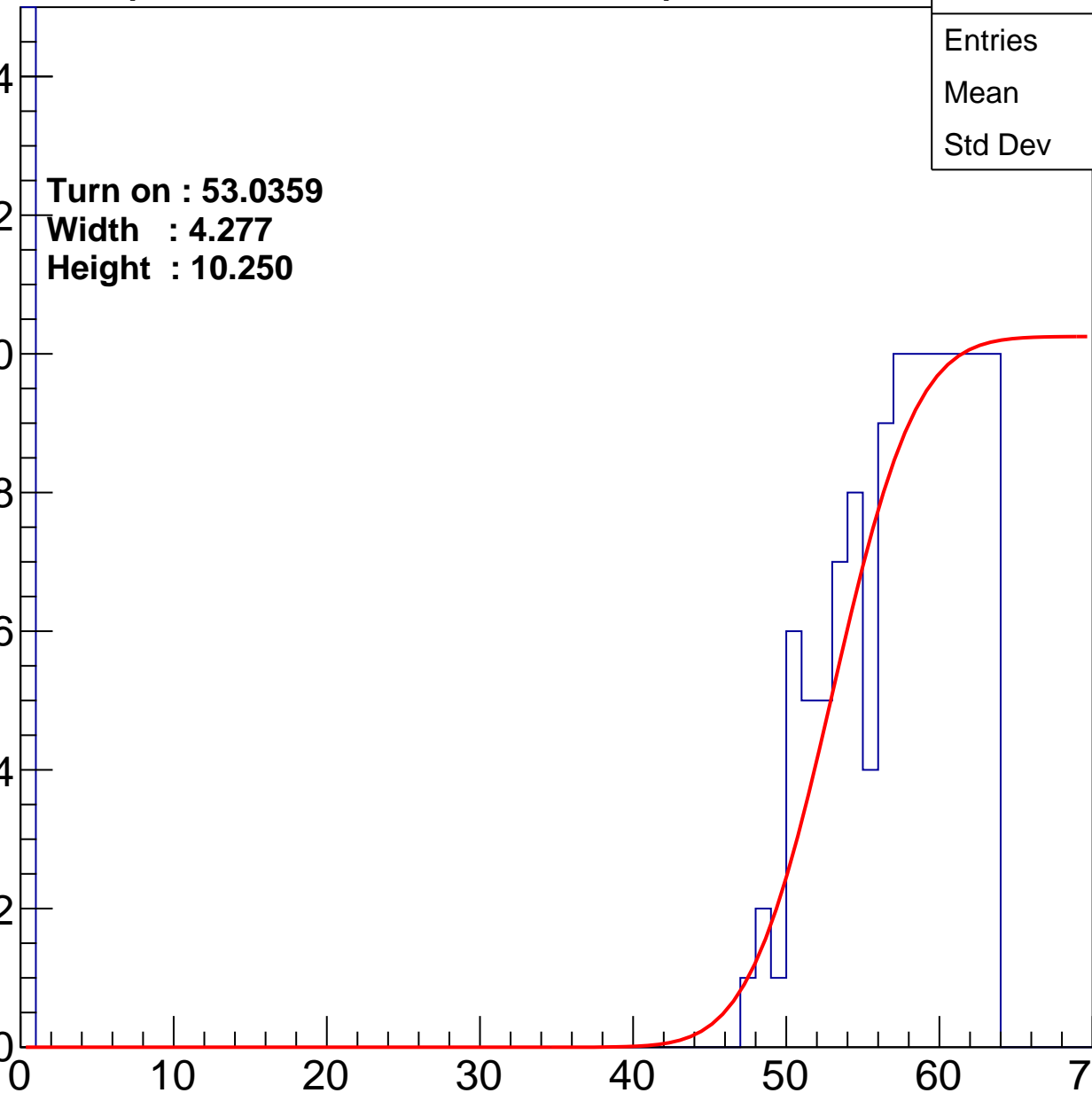
Width : 4.277

Height : 10.250

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	25.18
Std Dev	29.04

Turn on : 55.7544

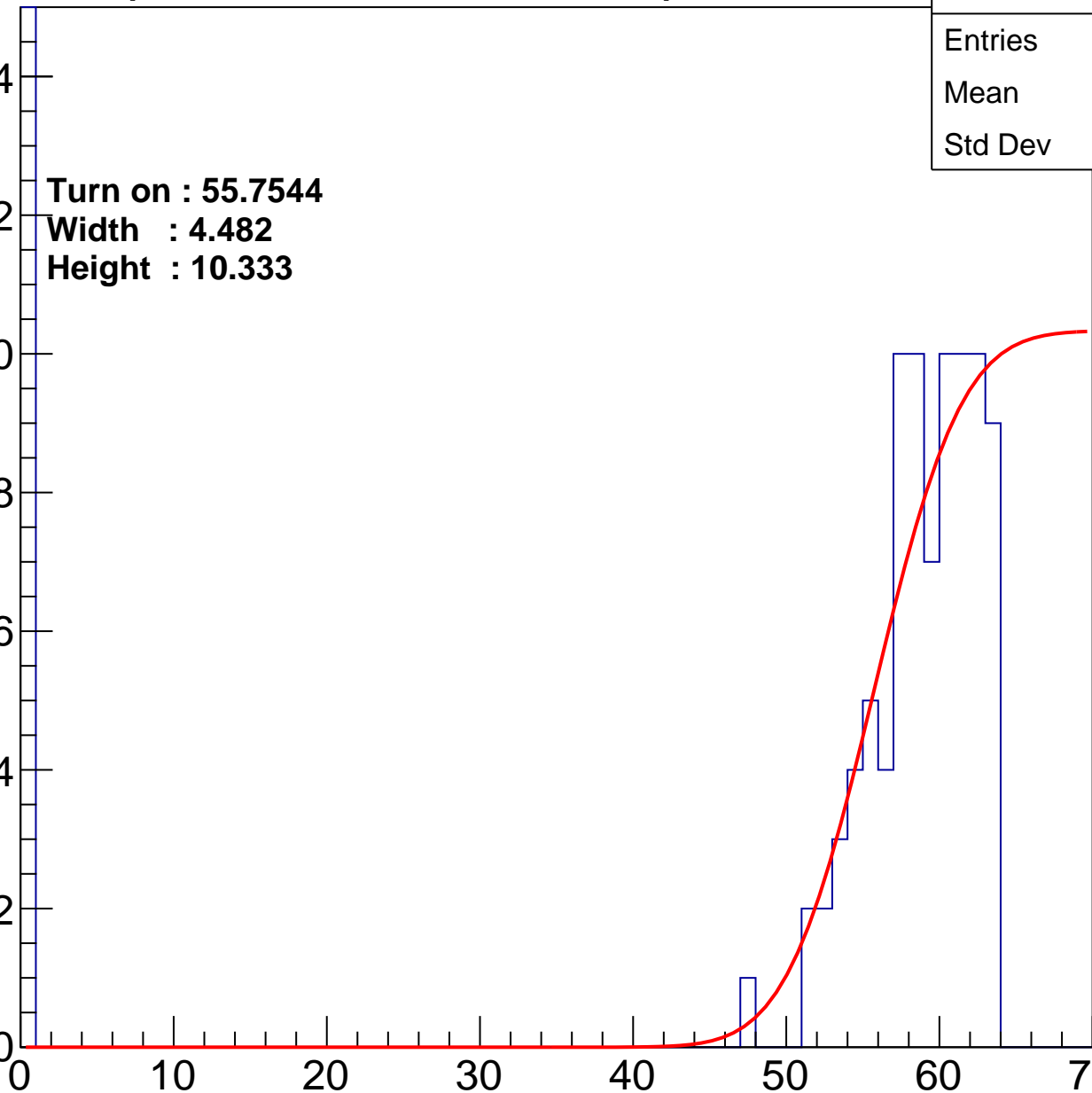
Width : 4.482

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	33.42
Std Dev	28.17

Turn on : 52.1091

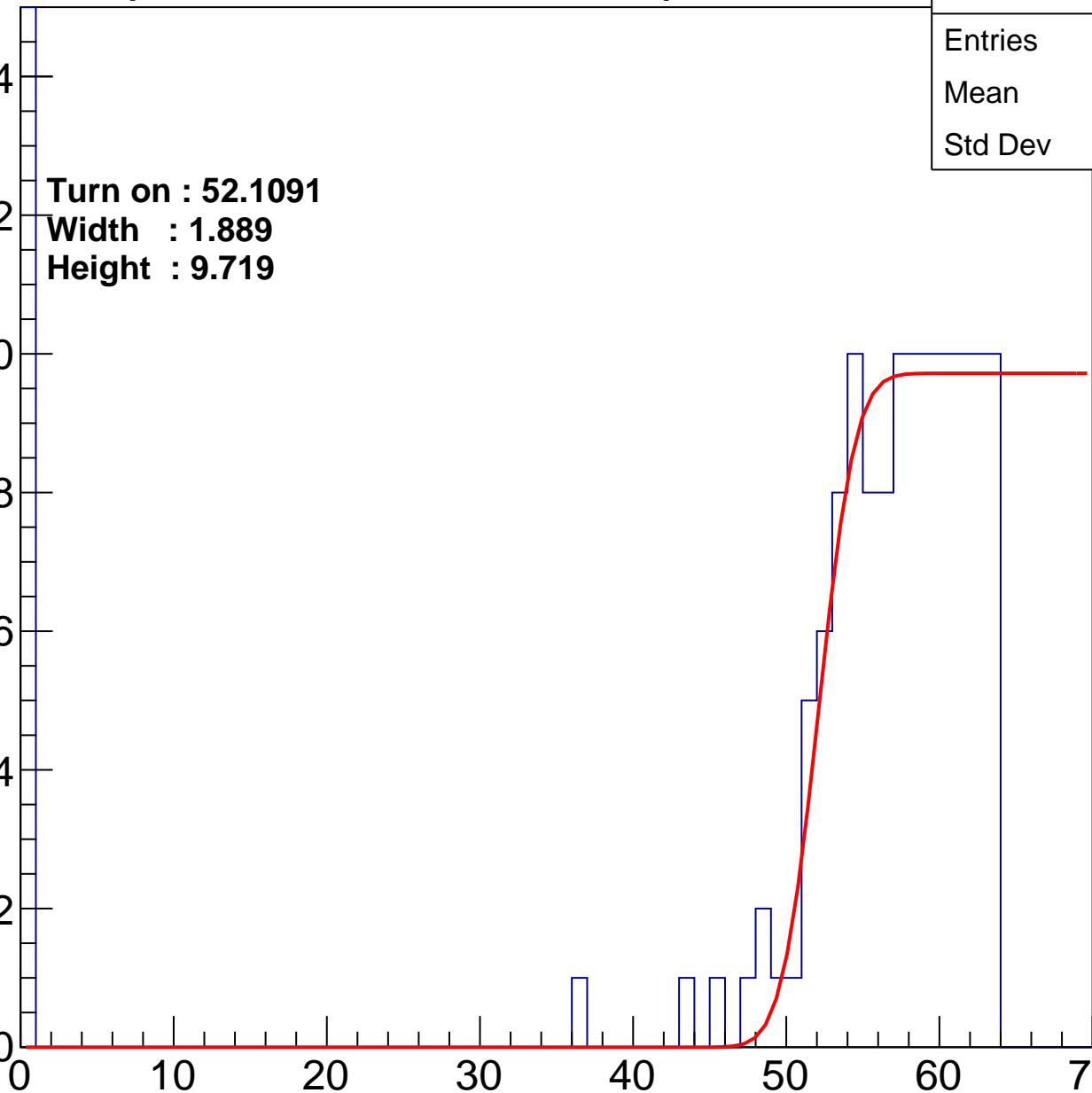
Width : 1.889

Height : 9.719

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	32.1
Std Dev	28.82

Turn on : 54.8337

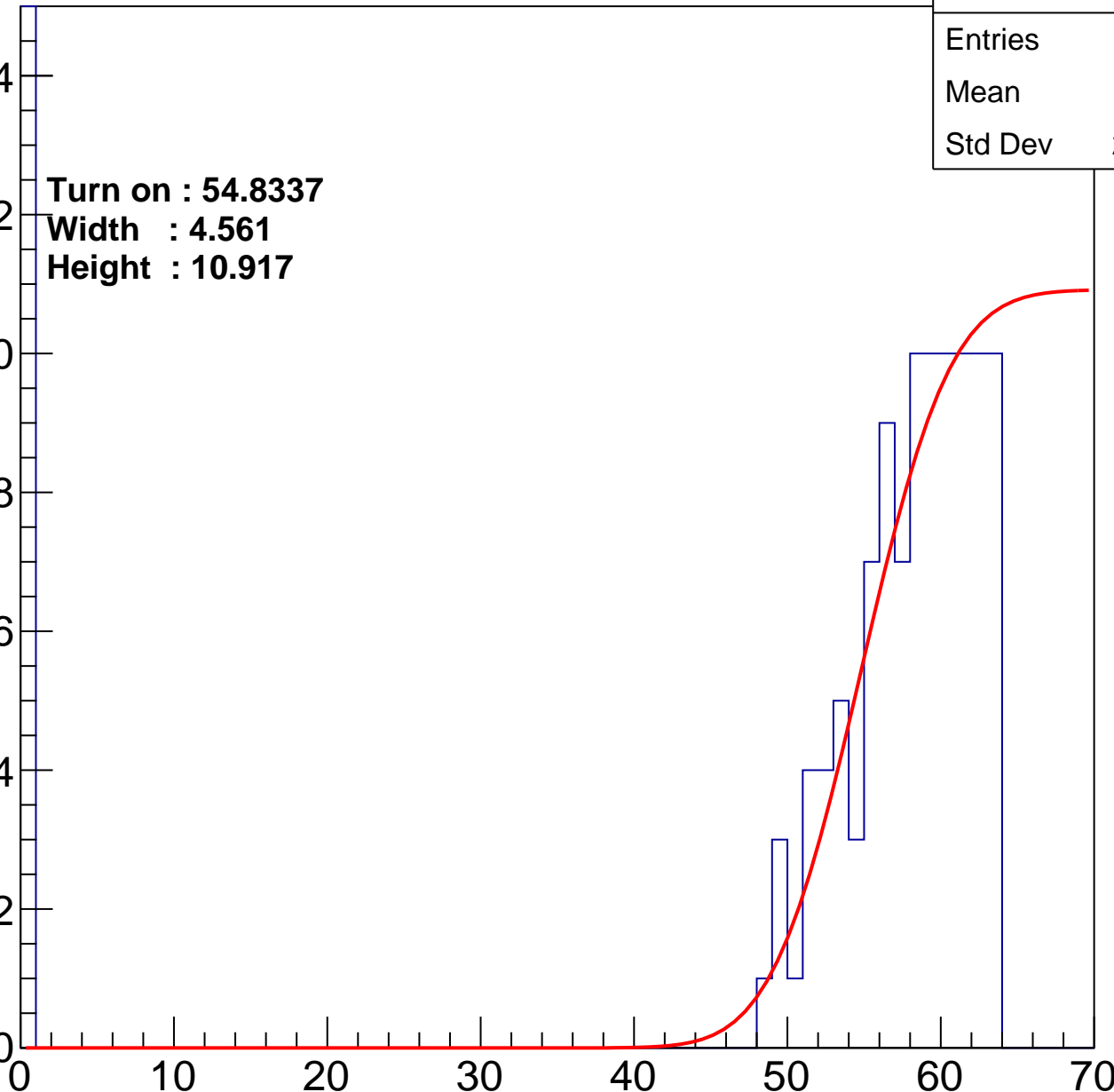
Width : 4.561

Height : 10.917

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch94

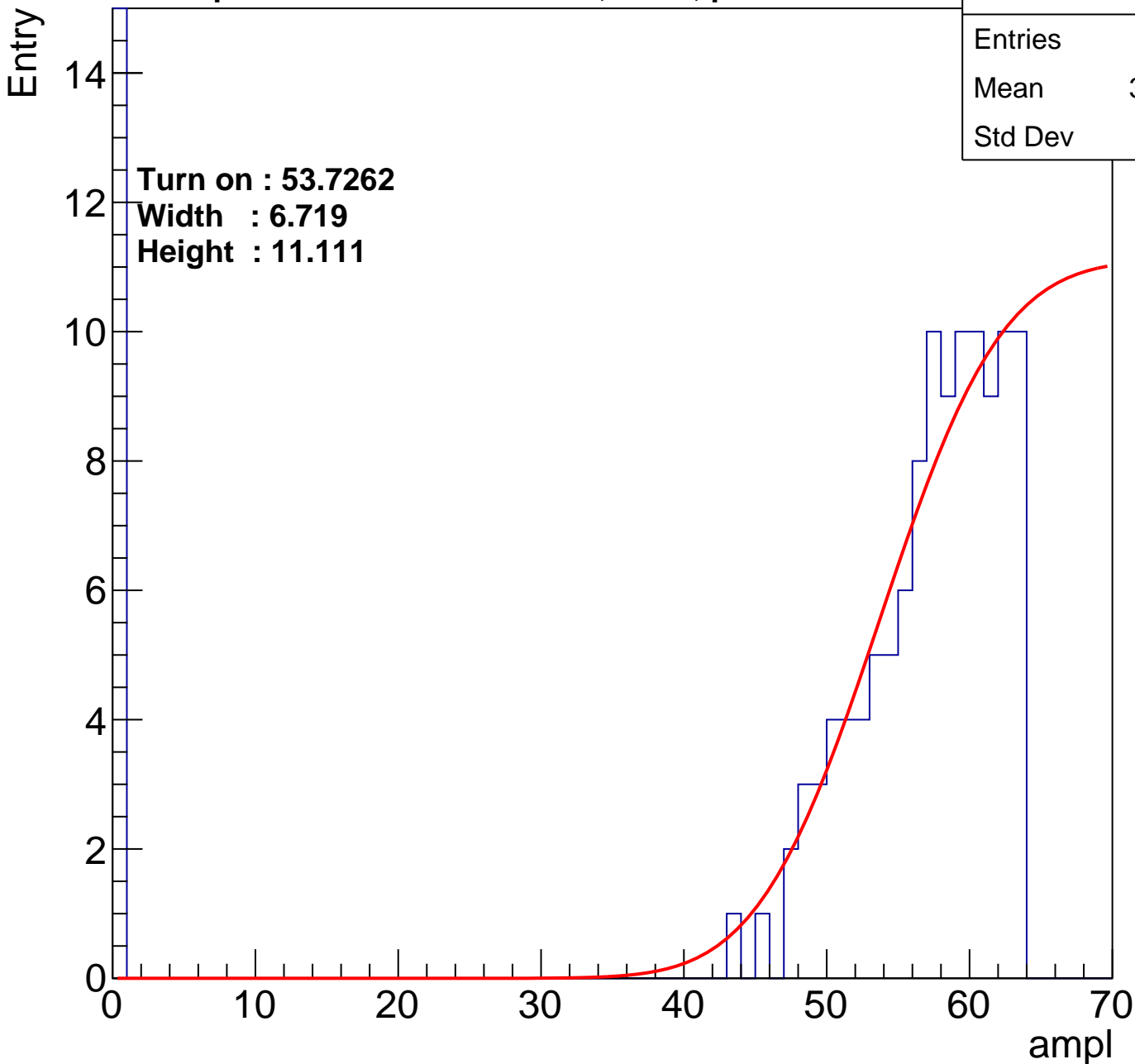
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	34.46
Std Dev	28

Turn on : 53.7262

Width : 6.719

Height : 11.111



B1L104S, U10-ch95

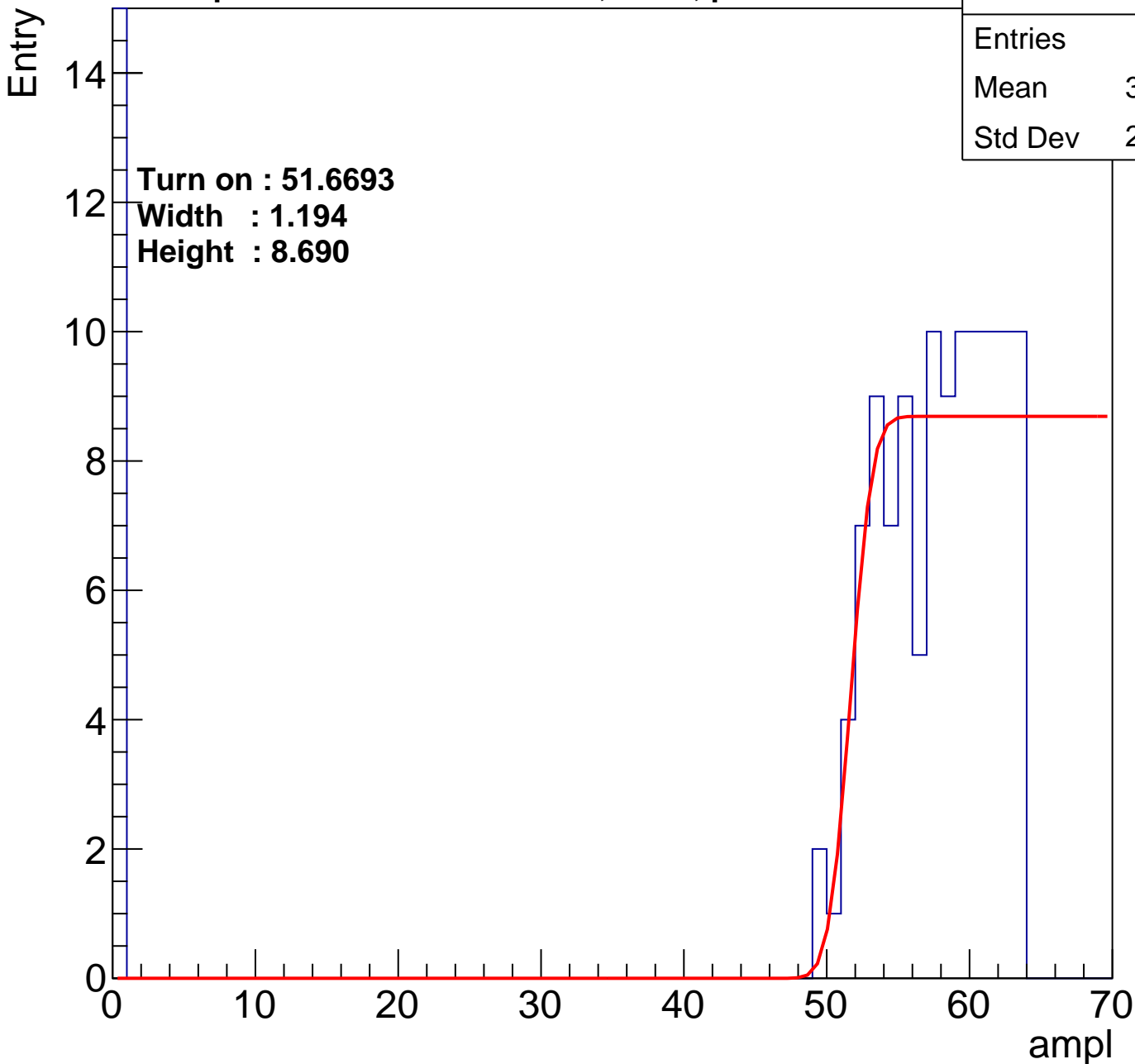
calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	33.97
Std Dev	28.37

Turn on : 51.6693

Width : 1.194

Height : 8.690



B1L104S, U10-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.89
Std Dev	28.8

Turn on : 54.3301

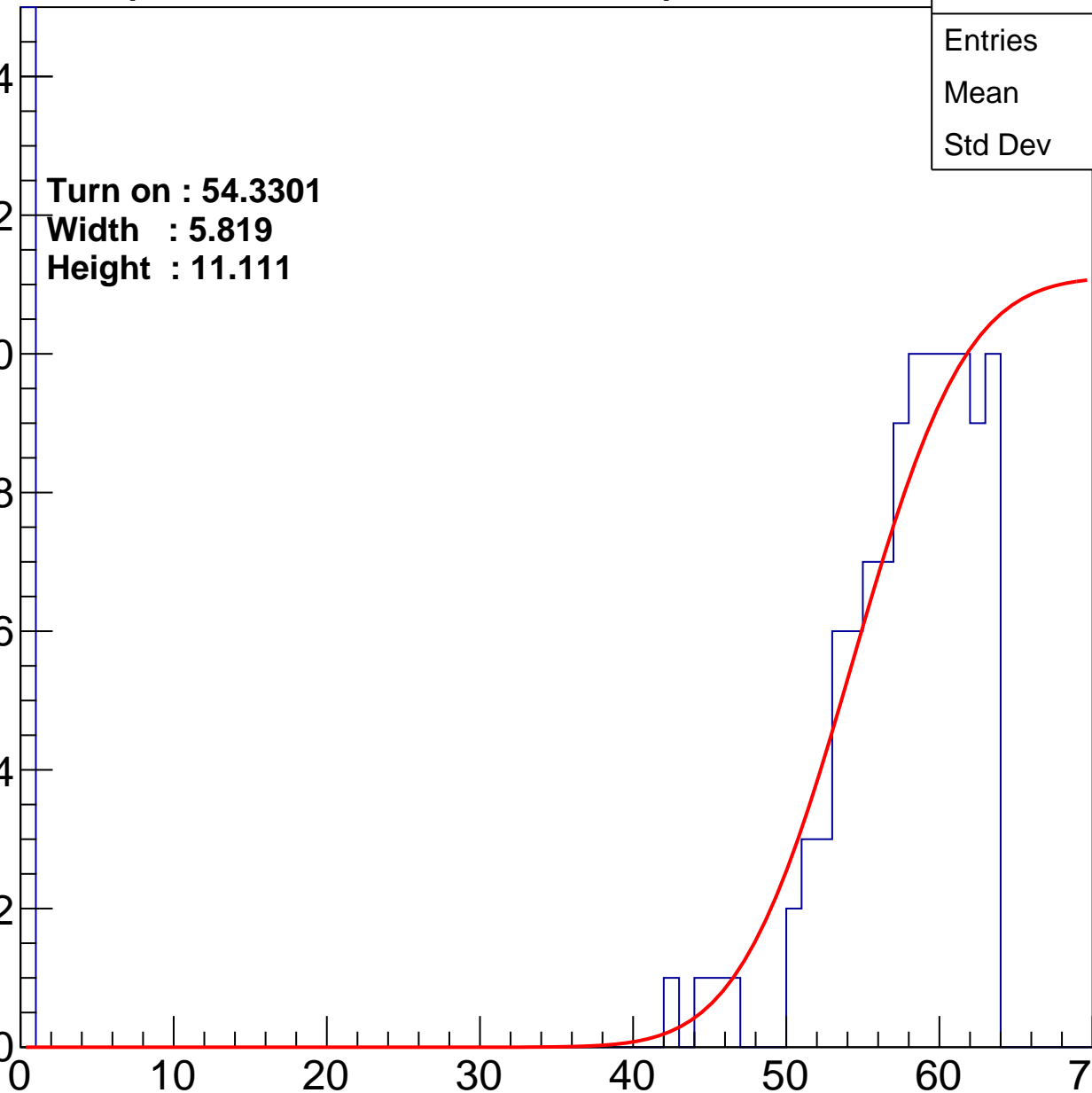
Width : 5.819

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	30.64
Std Dev	28.98

Turn on : 53.7866

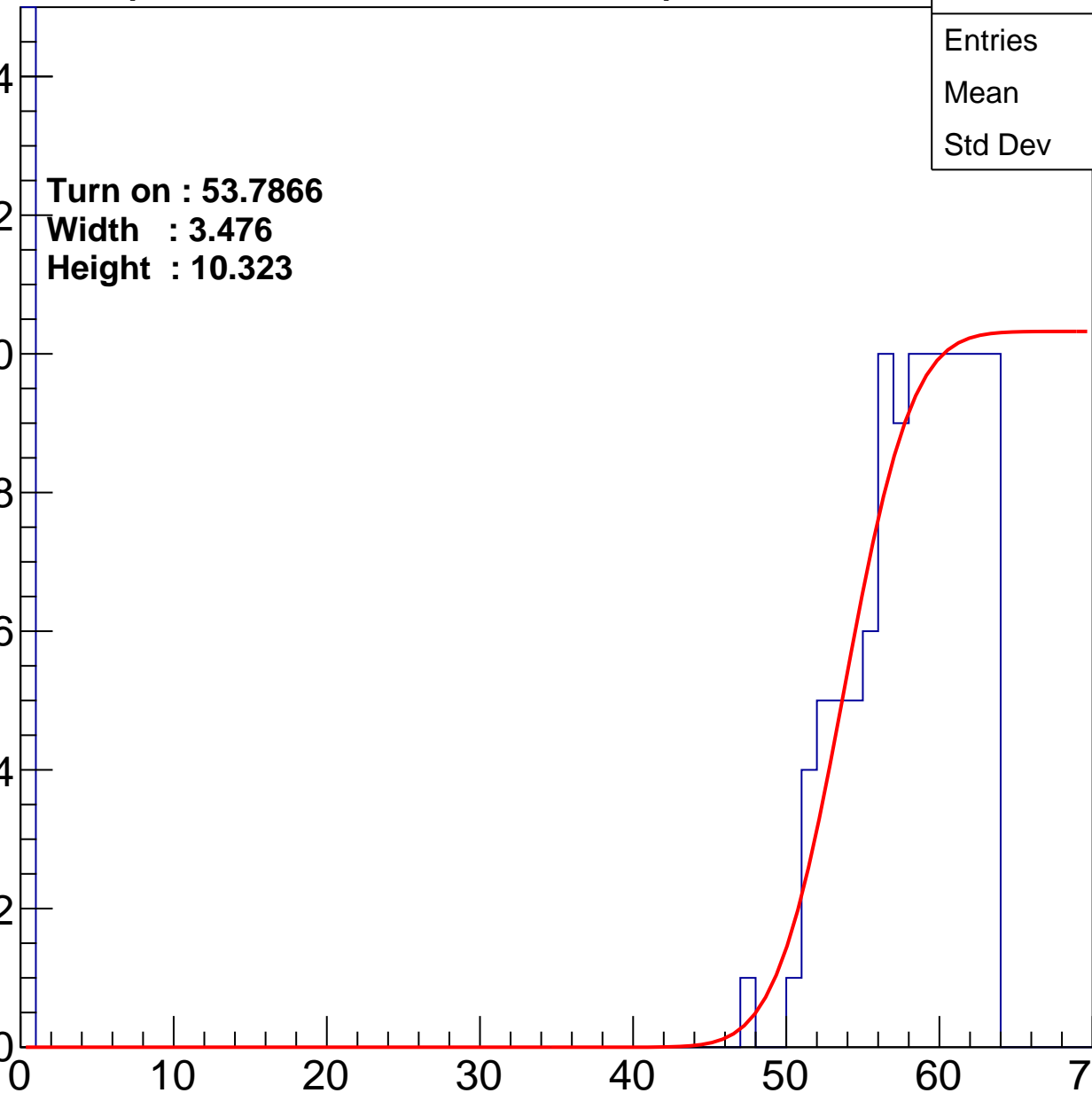
Width : 3.476

Height : 10.323

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	228
Mean	32.15
Std Dev	28.13

Turn on : 51.6870

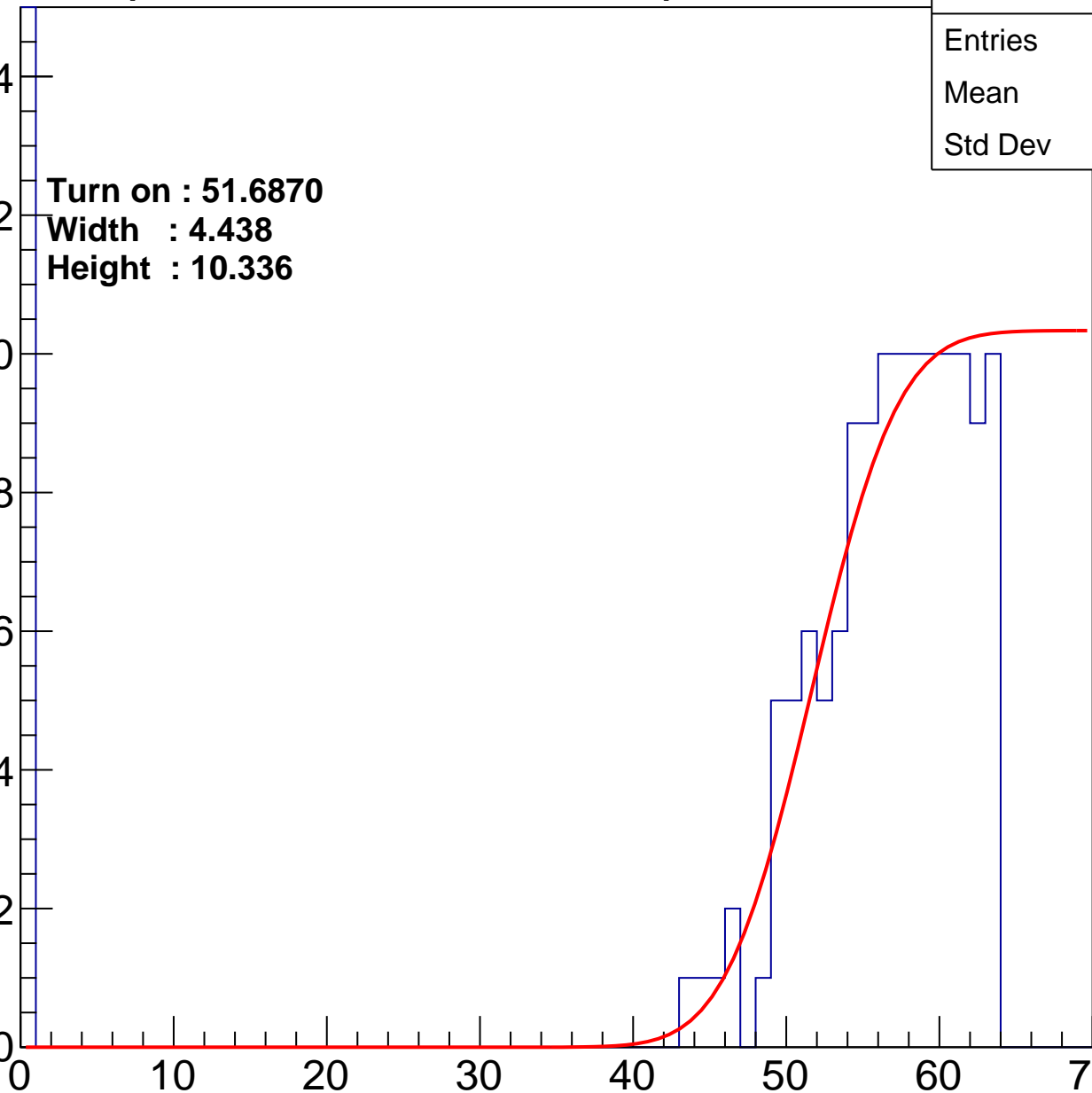
Width : 4.438

Height : 10.336

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	26.69
Std Dev	29.17

Turn on : 55.6697

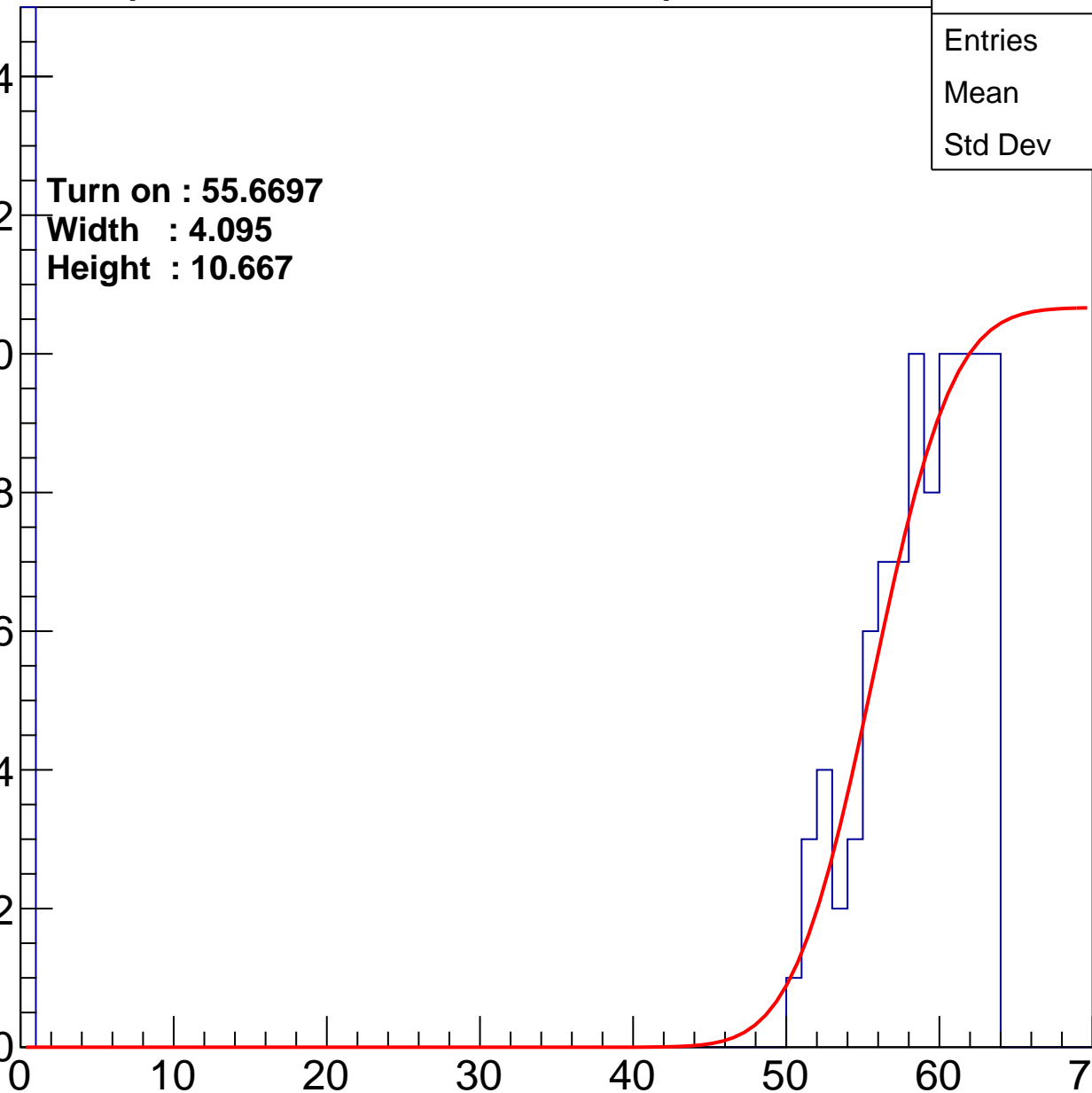
Width : 4.095

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	35.95
Std Dev	28.13

Turn on : 55.3351

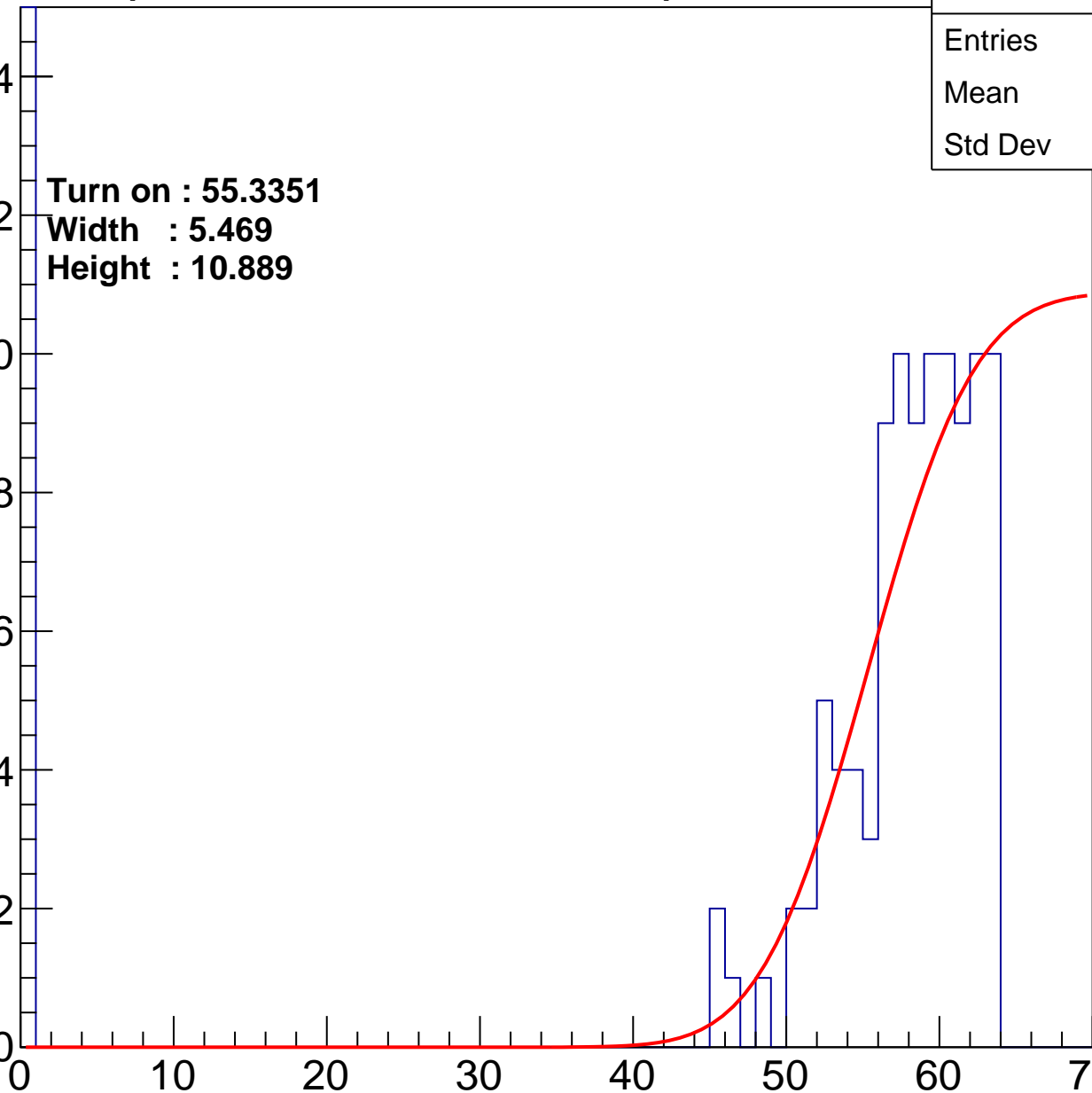
Width : 5.469

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	29.32
Std Dev	29.02

Turn on : 55.0274

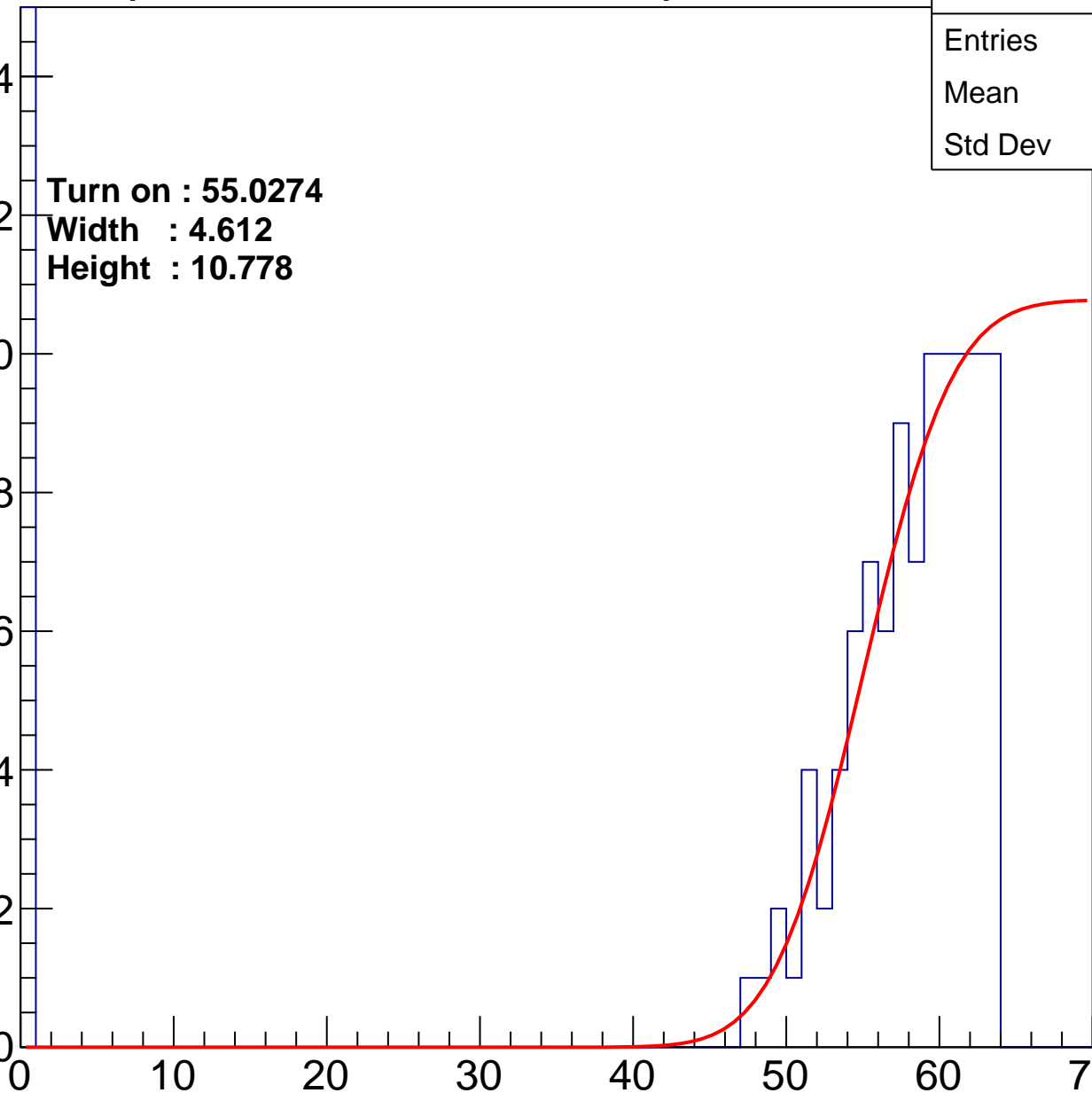
Width : 4.612

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	30.05
Std Dev	28.98

Turn on : 54.8397

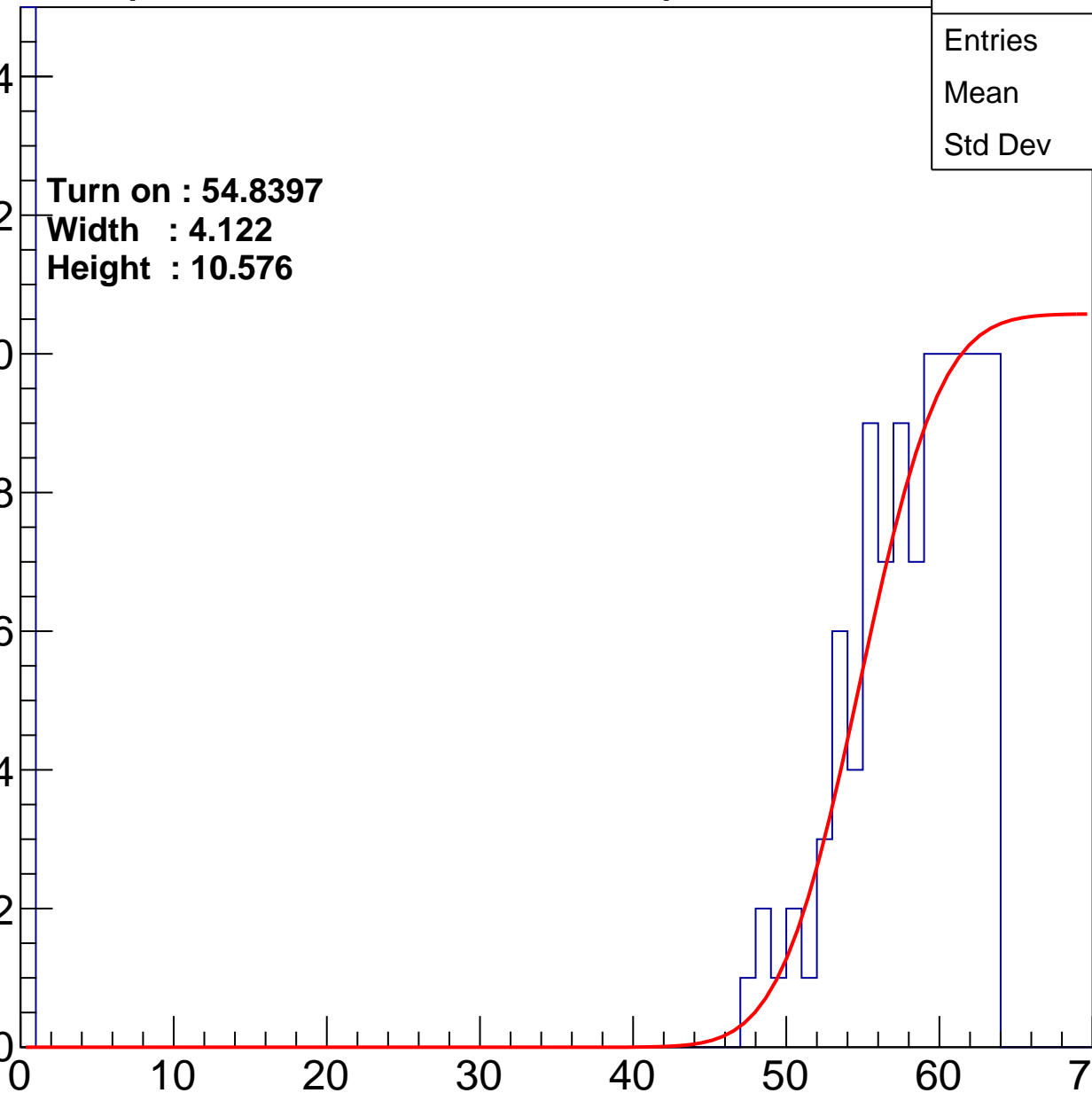
Width : 4.122

Height : 10.576

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	28.03
Std Dev	28.54

Turn on : 53.6794

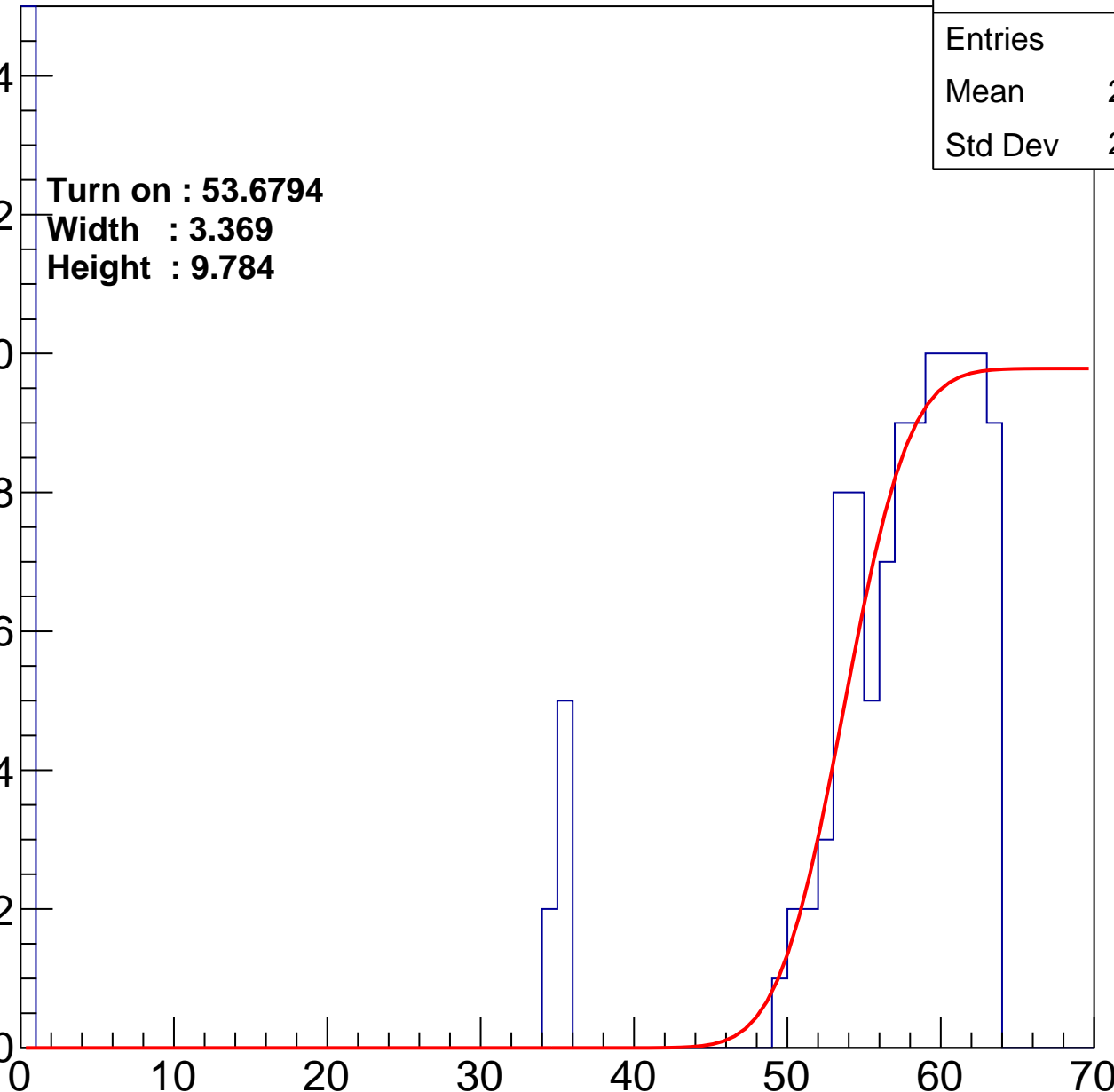
Width : 3.369

Height : 9.784

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	32.15
Std Dev	28.8

Turn on : 54.7541

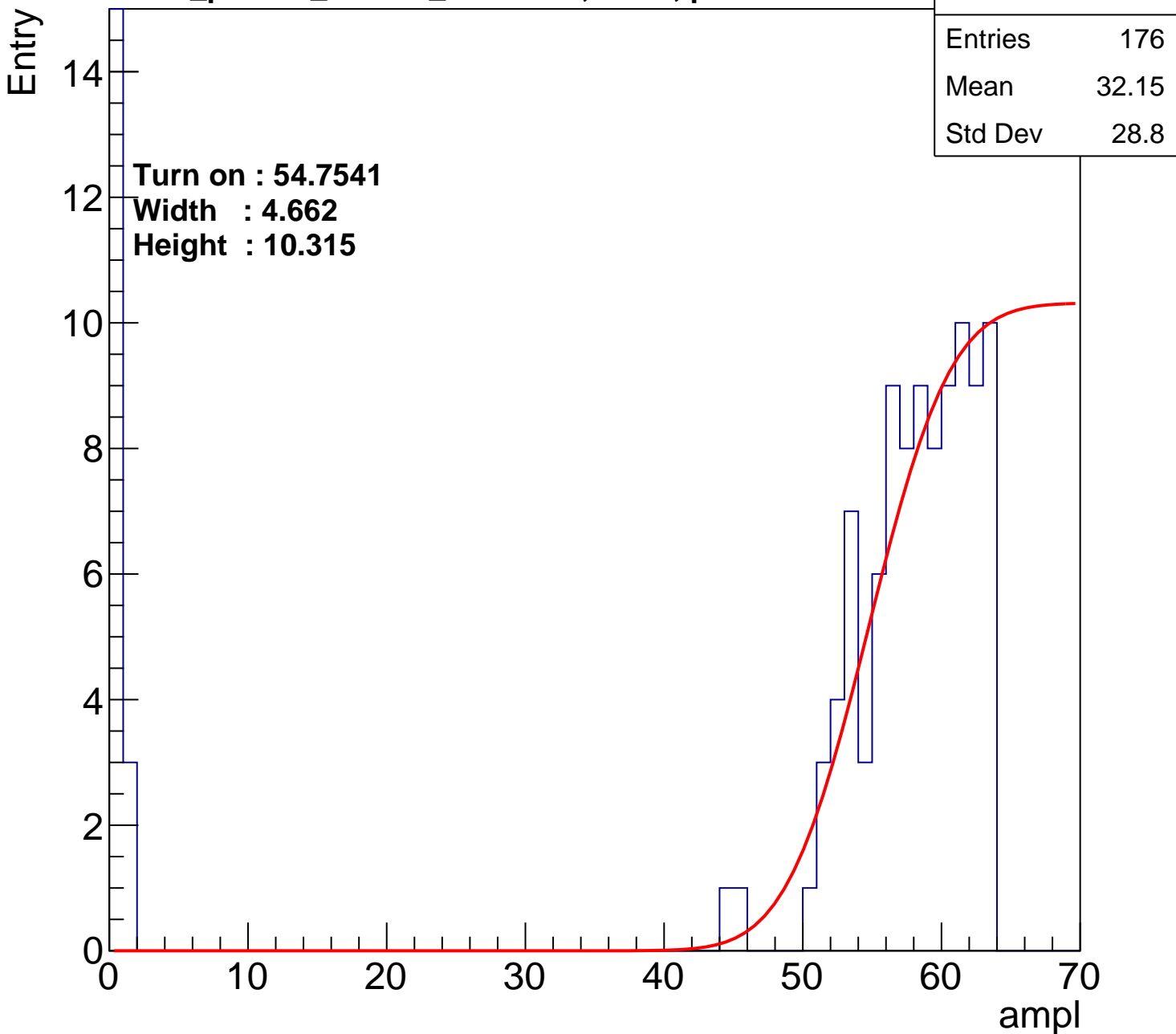
Width : 4.662

Height : 10.315

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	35.18
Std Dev	28.56

Turn on : 54.6648

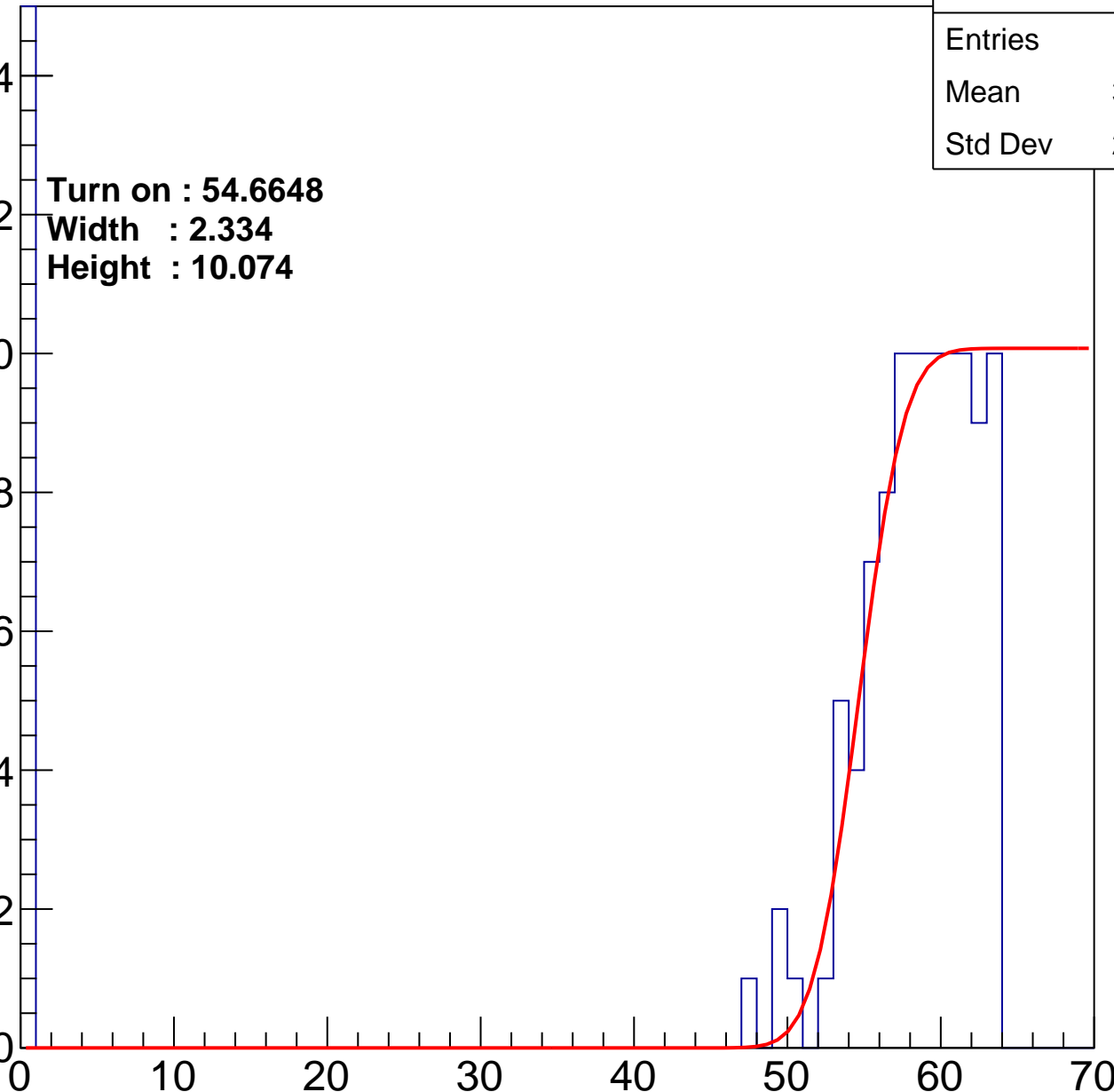
Width : 2.334

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	29.58
Std Dev	28.7

Turn on : 52.9736

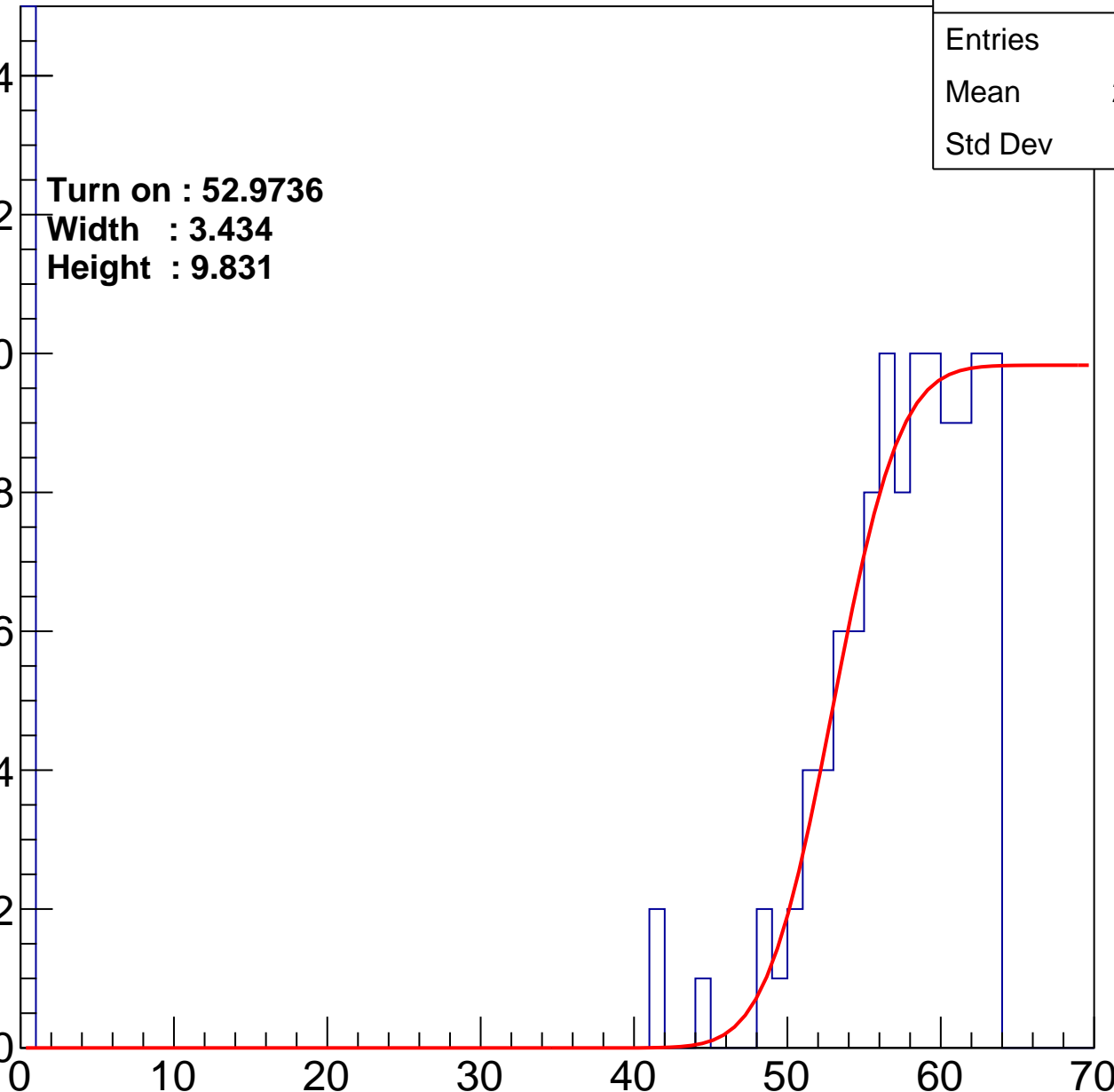
Width : 3.434

Height : 9.831

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	32.03
Std Dev	29

Turn on : 54.9470

Width : 4.699

Height : 10.667

Entry

14

12

10

8

6

4

2

0

0

10

20

30

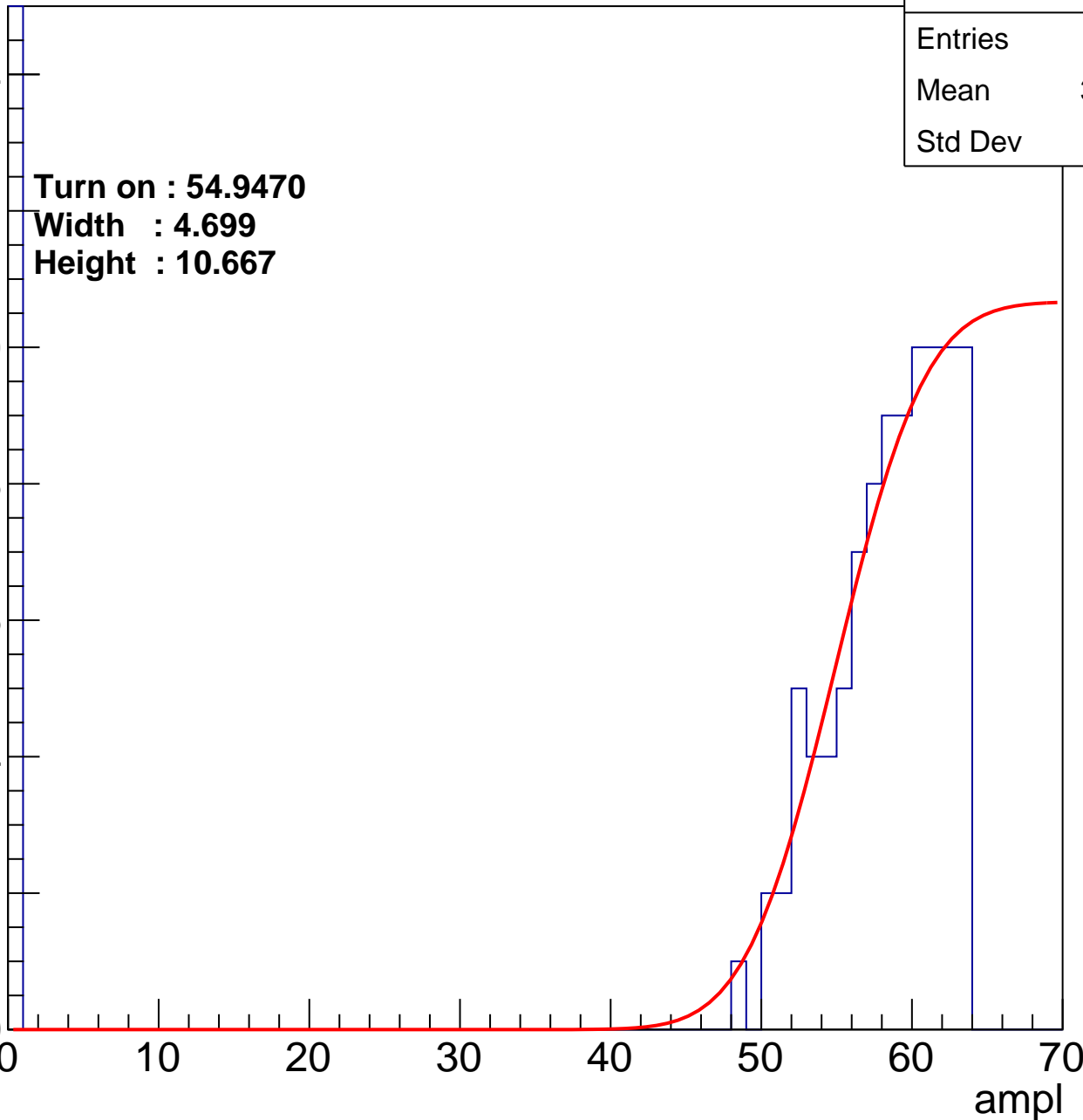
40

50

60

70

ampl



B1L104S, U10-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	226
Mean	26.39
Std Dev	28.75

Turn on : 54.7627

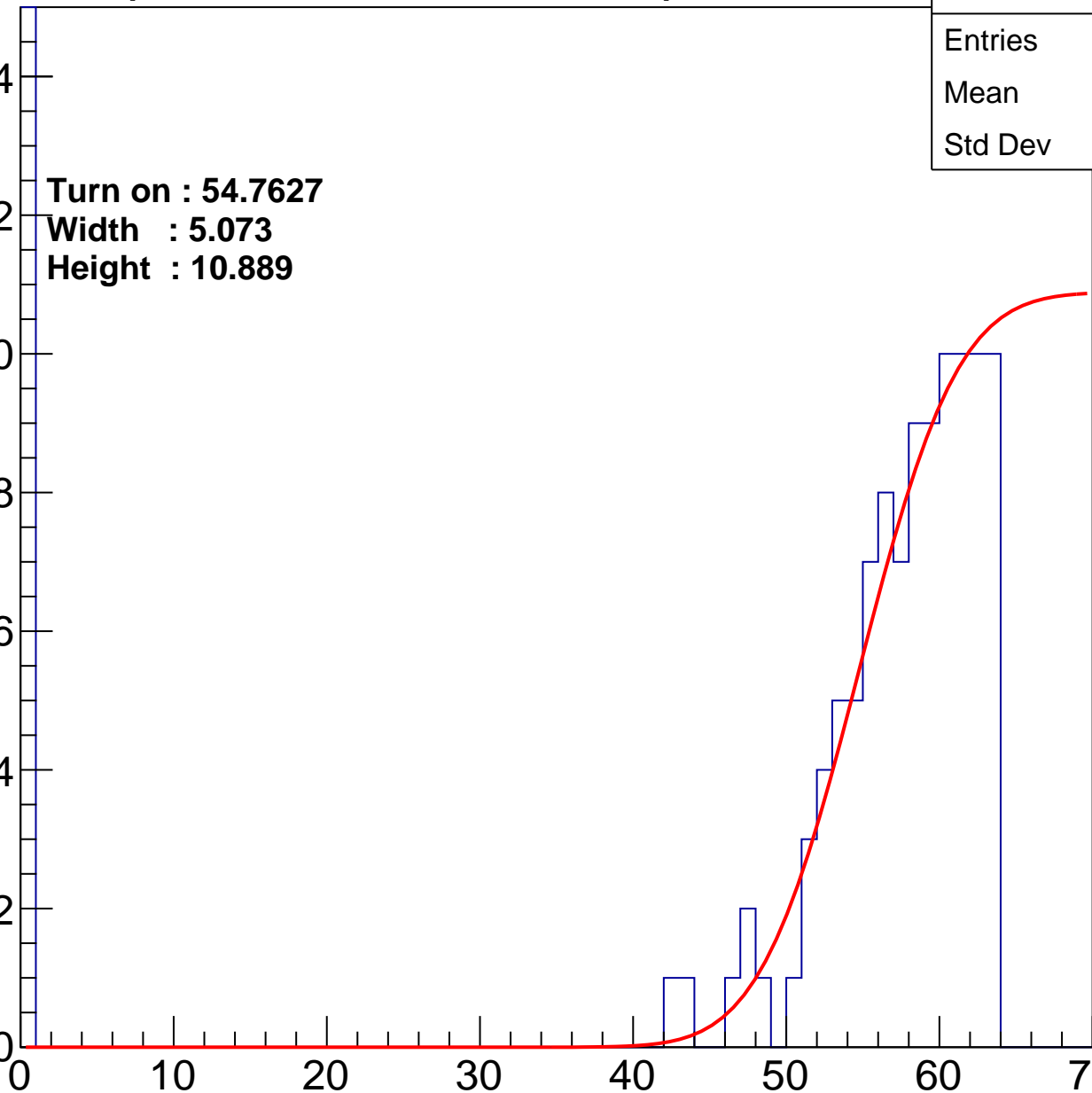
Width : 5.073

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	30.76
Std Dev	29.1

Turn on : 55.5253

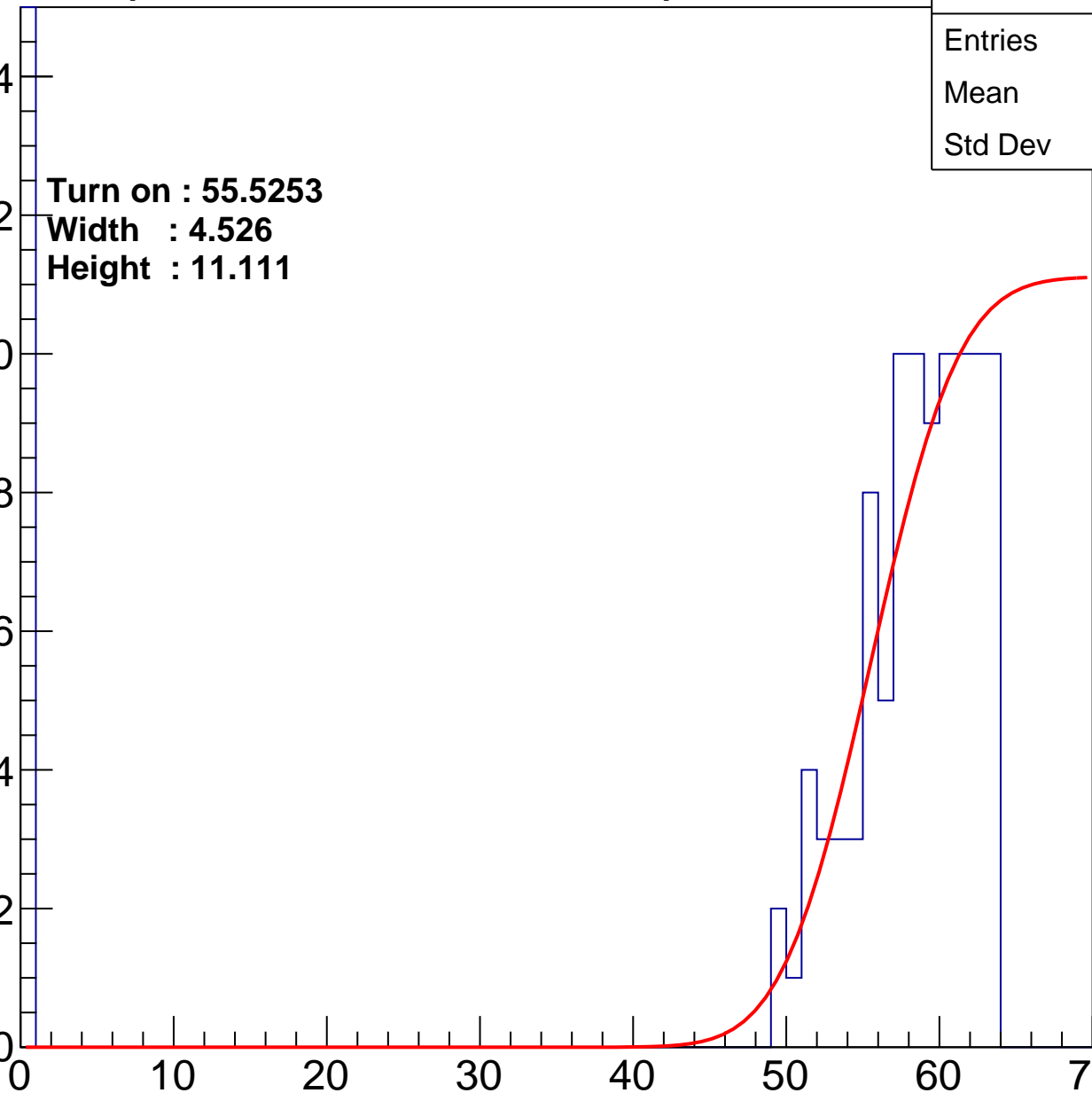
Width : 4.526

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	232
Mean	26.12
Std Dev	28.84

Turn on : 53.9131

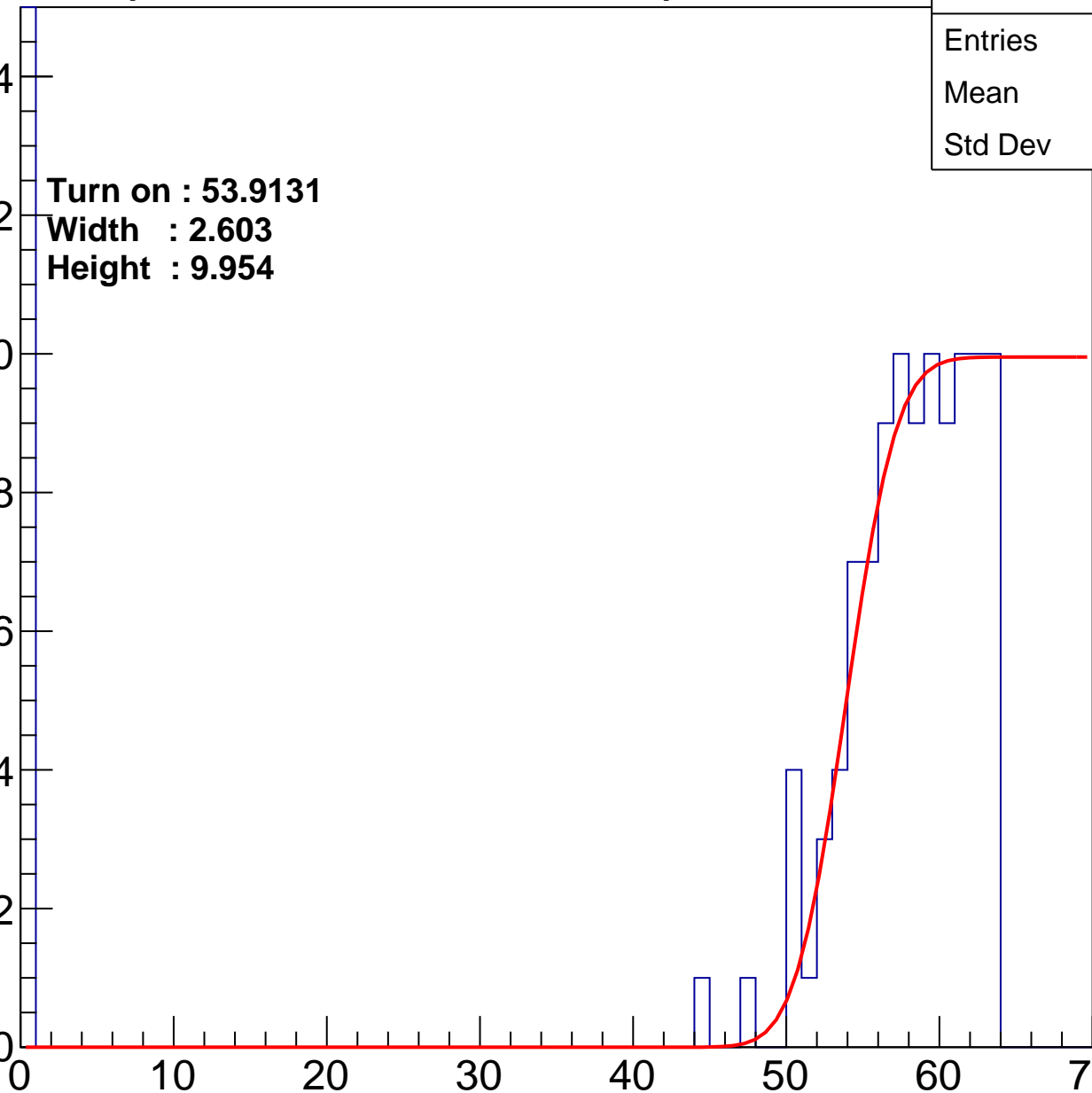
Width : 2.603

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	226
Mean	27.29
Std Dev	28.91

Turn on : 53.9769

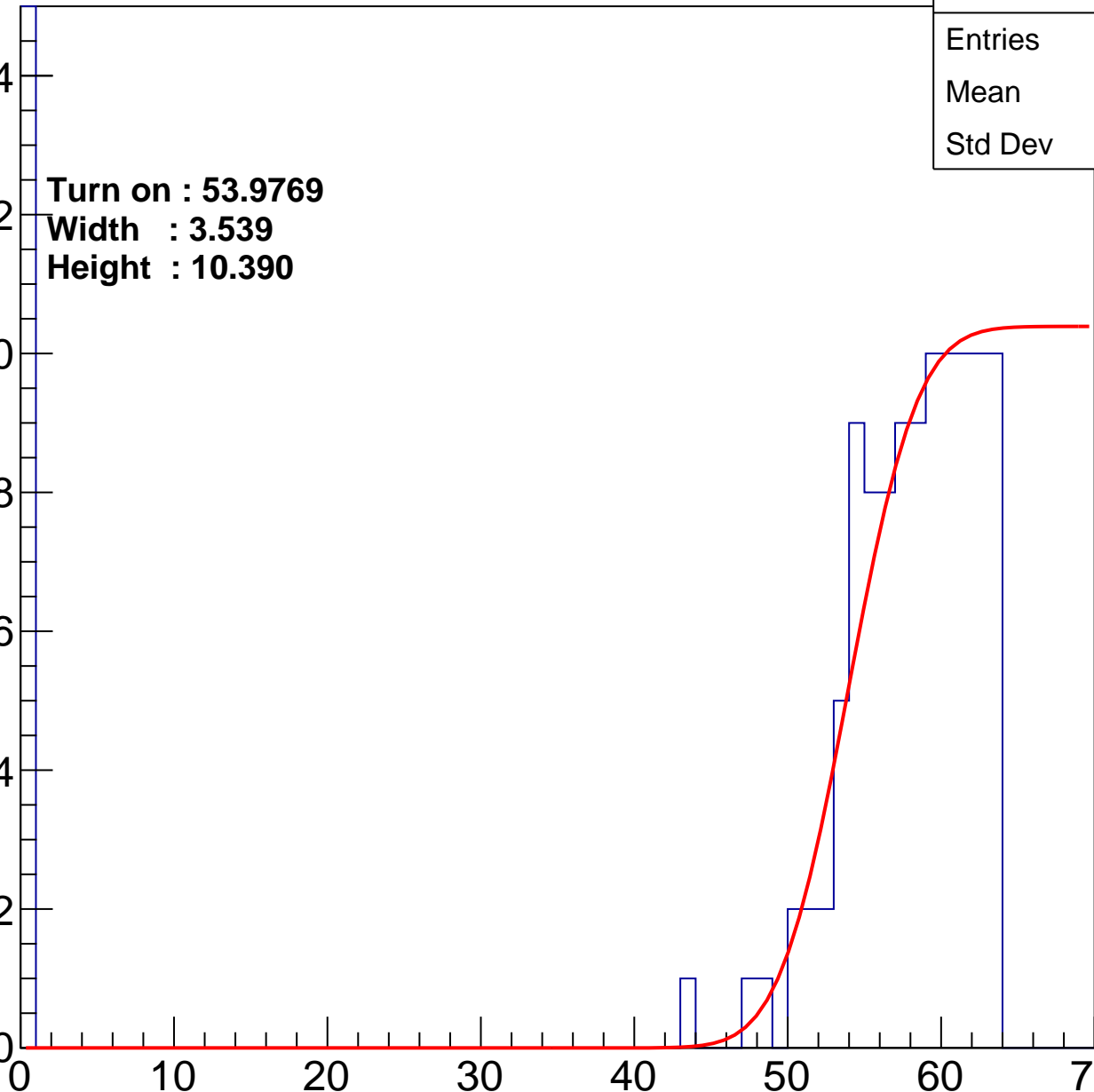
Width : 3.539

Height : 10.390

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	27.14
Std Dev	28.88

Turn on : 54.8788

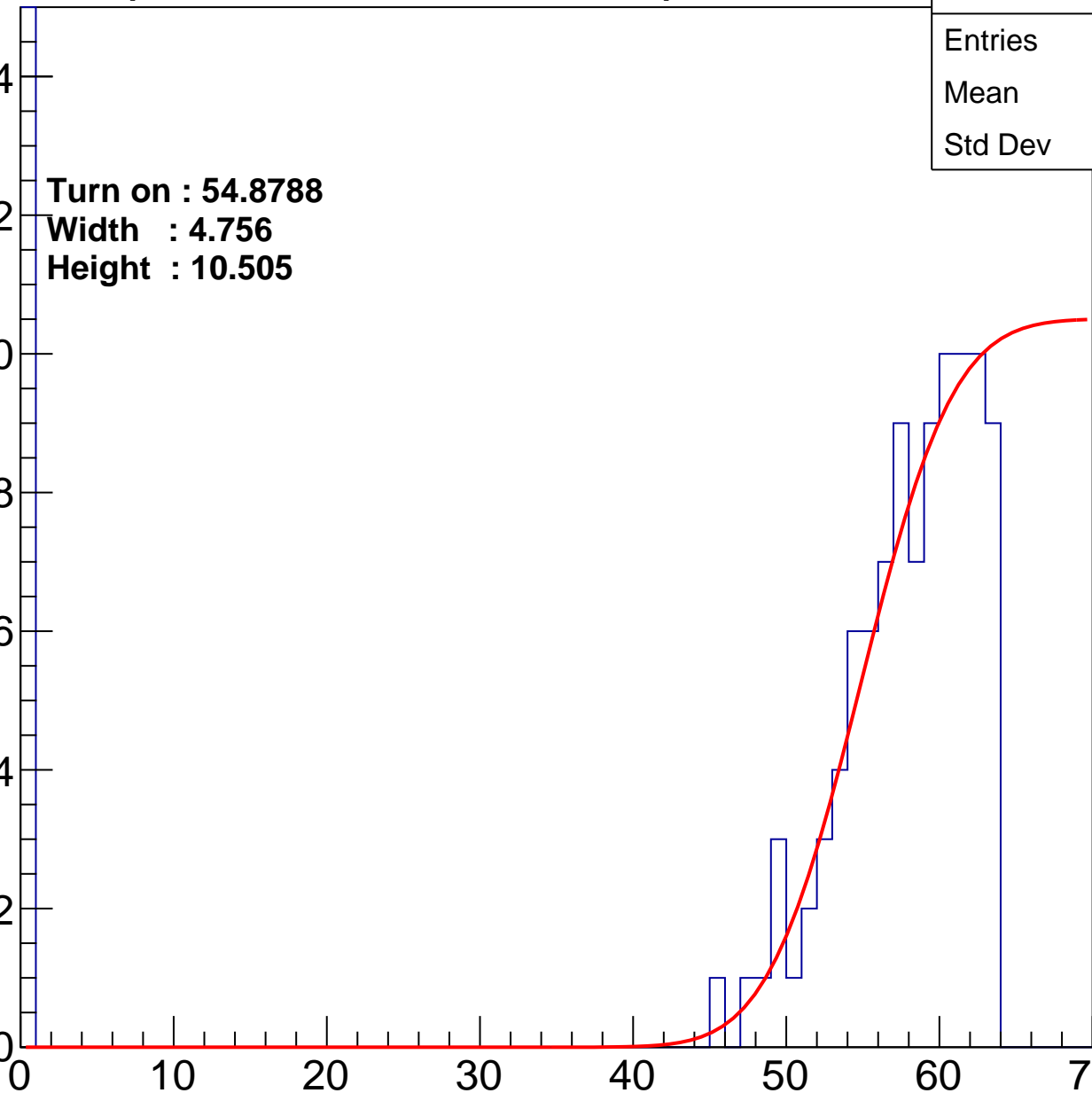
Width : 4.756

Height : 10.505

Entry

14
12
10
8
6
4
2
0

ampl



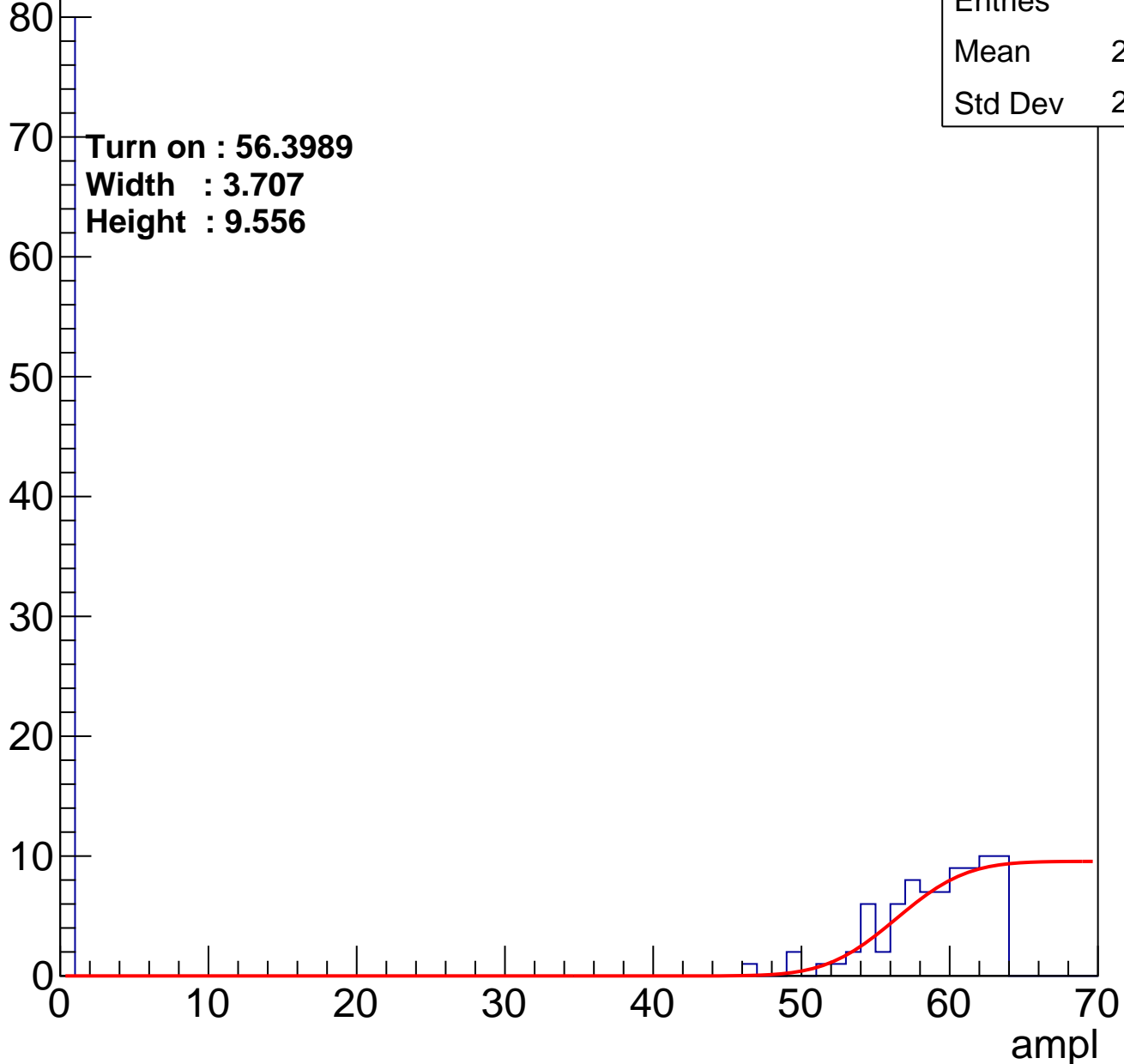
B1L104S, U10-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	29.42
Std Dev	29.36

Turn on : 56.3989
Width : 3.707
Height : 9.556

Entry



B1L104S, U10-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	30.81
Std Dev	28.52

Turn on : 52.4987

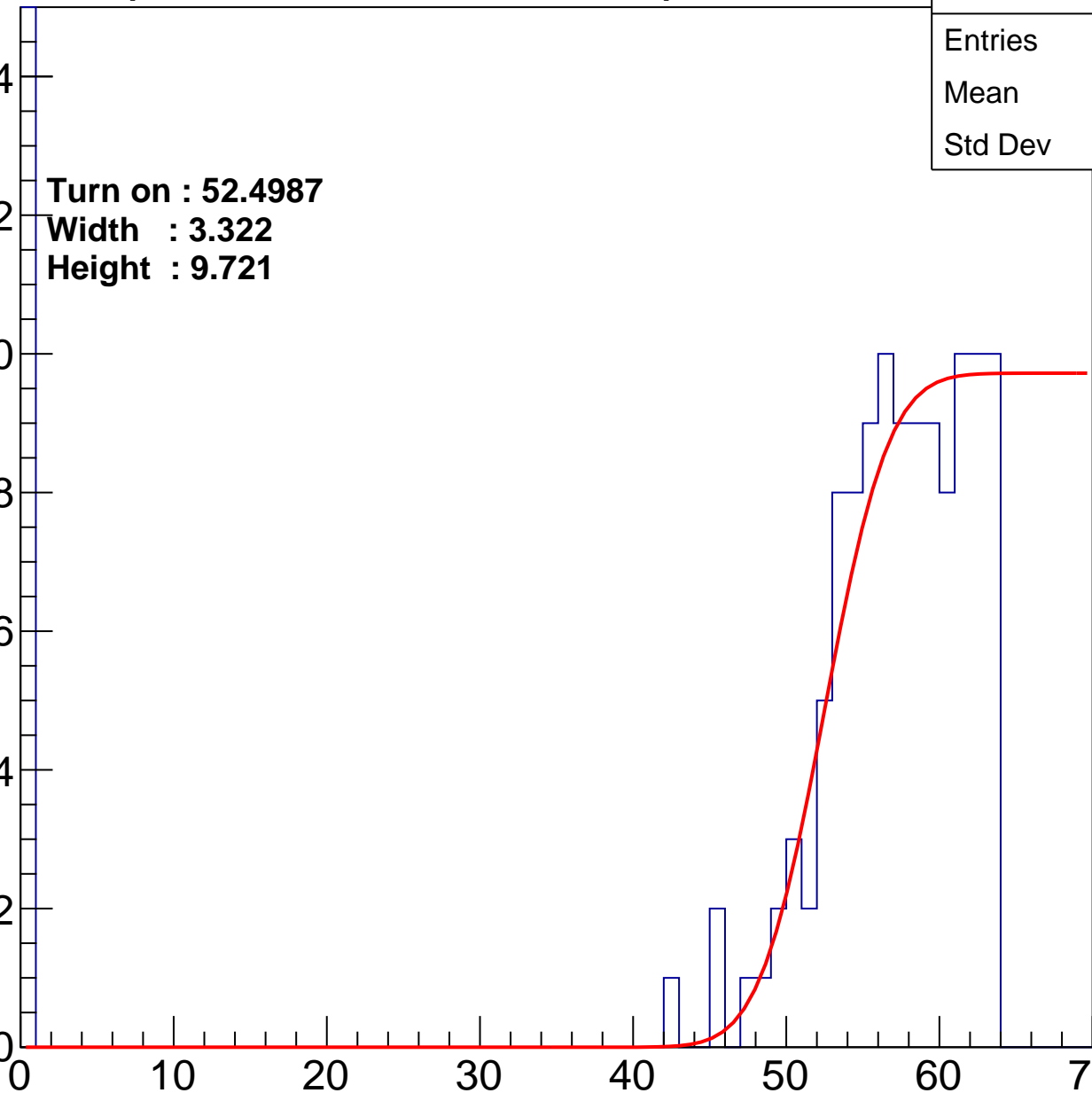
Width : 3.322

Height : 9.721

Entry

14
12
10
8
6
4
2
0

ampl



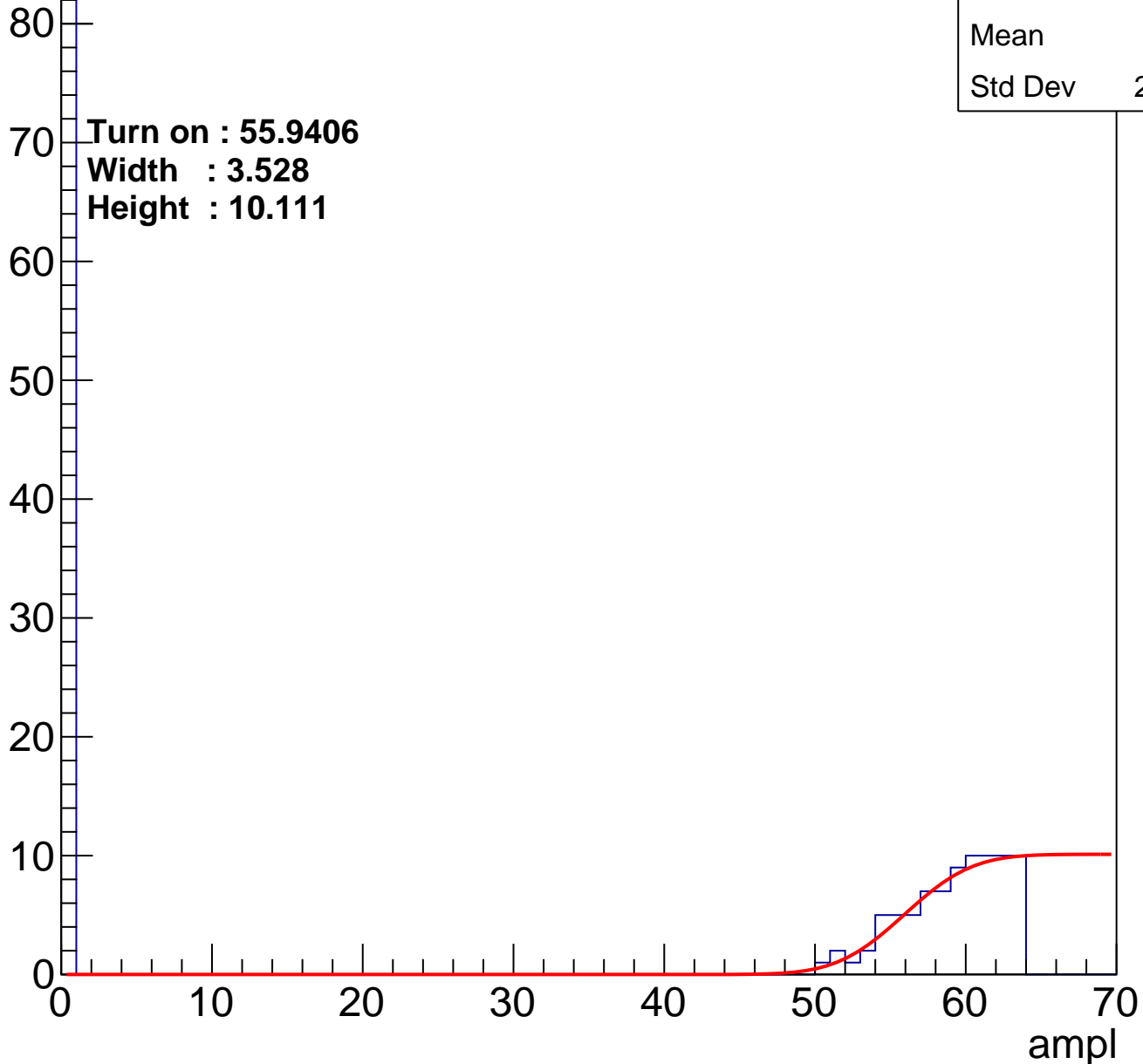
B1L104S, U10-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	29.7
Std Dev	29.44

Turn on : 55.9406
Width : 3.528
Height : 10.111

Entry



B1L104S, U10-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	30.81
Std Dev	28.53

Turn on : 52.6337

Width : 3.976

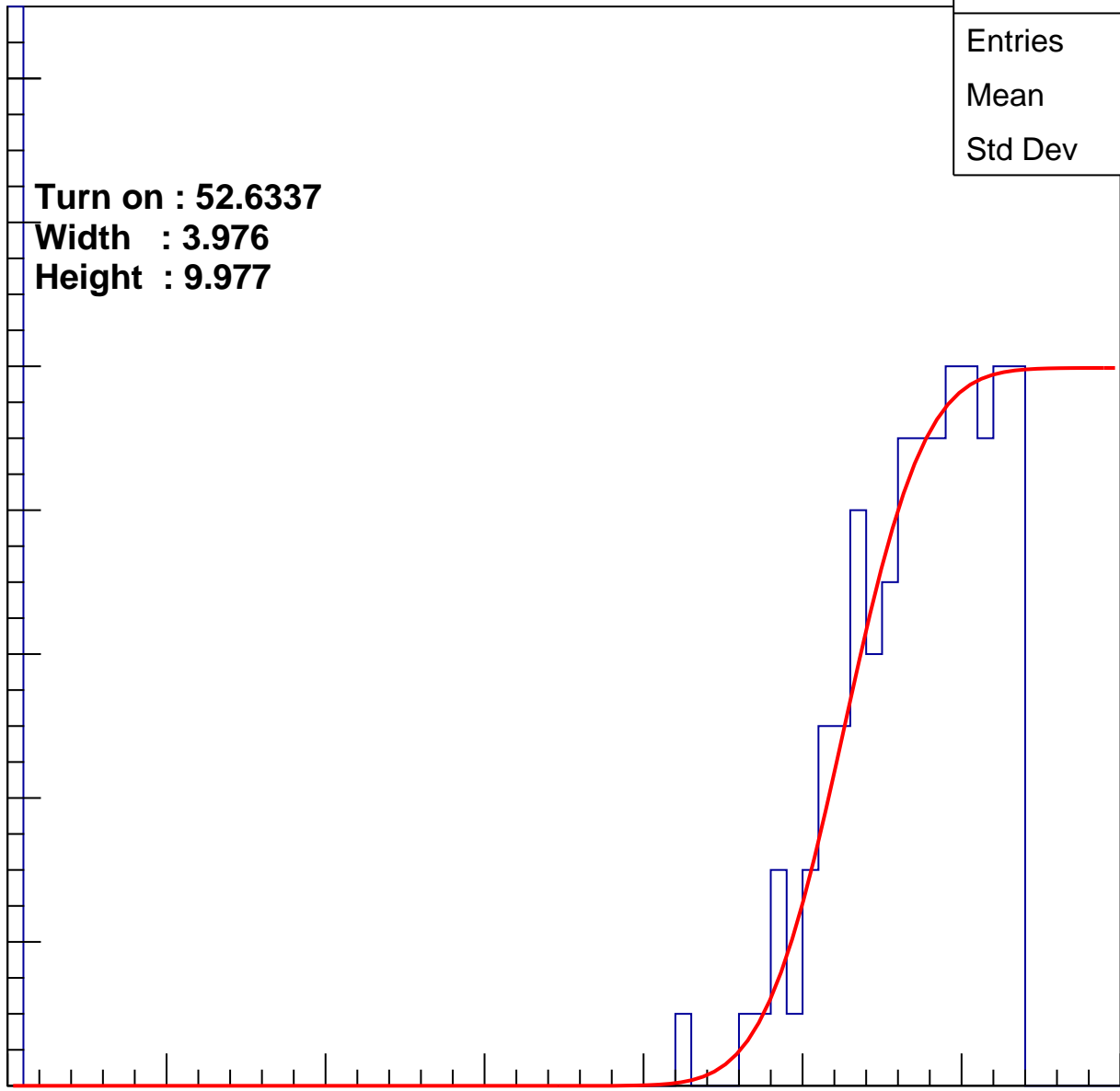
Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U10-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	35.26
Std Dev	28.49

Turn on : 55.1496

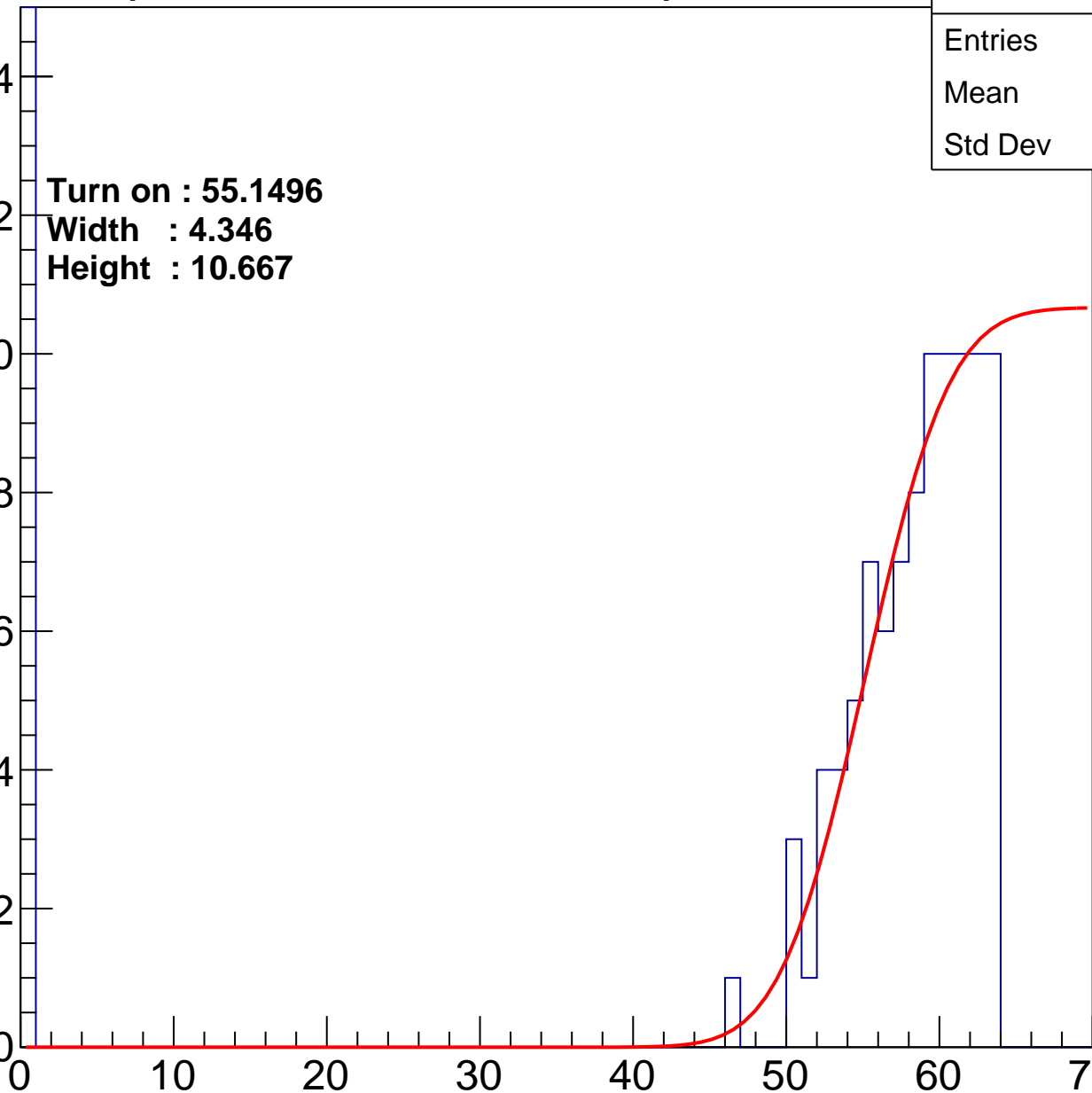
Width : 4.346

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	26.55
Std Dev	28.66

Turn on : 55.1601

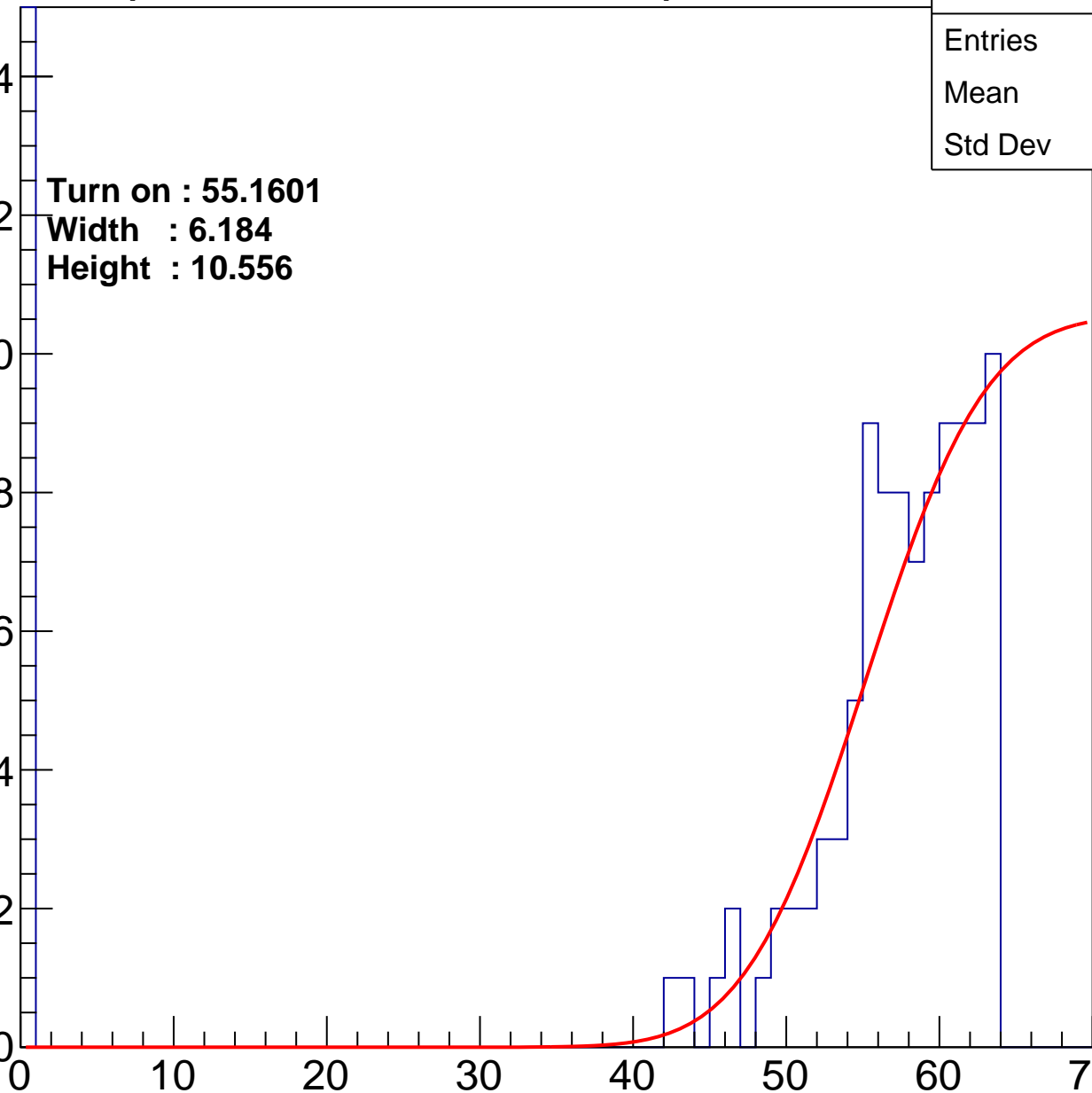
Width : 6.184

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	35.07
Std Dev	28.48

Turn on : 53.9591

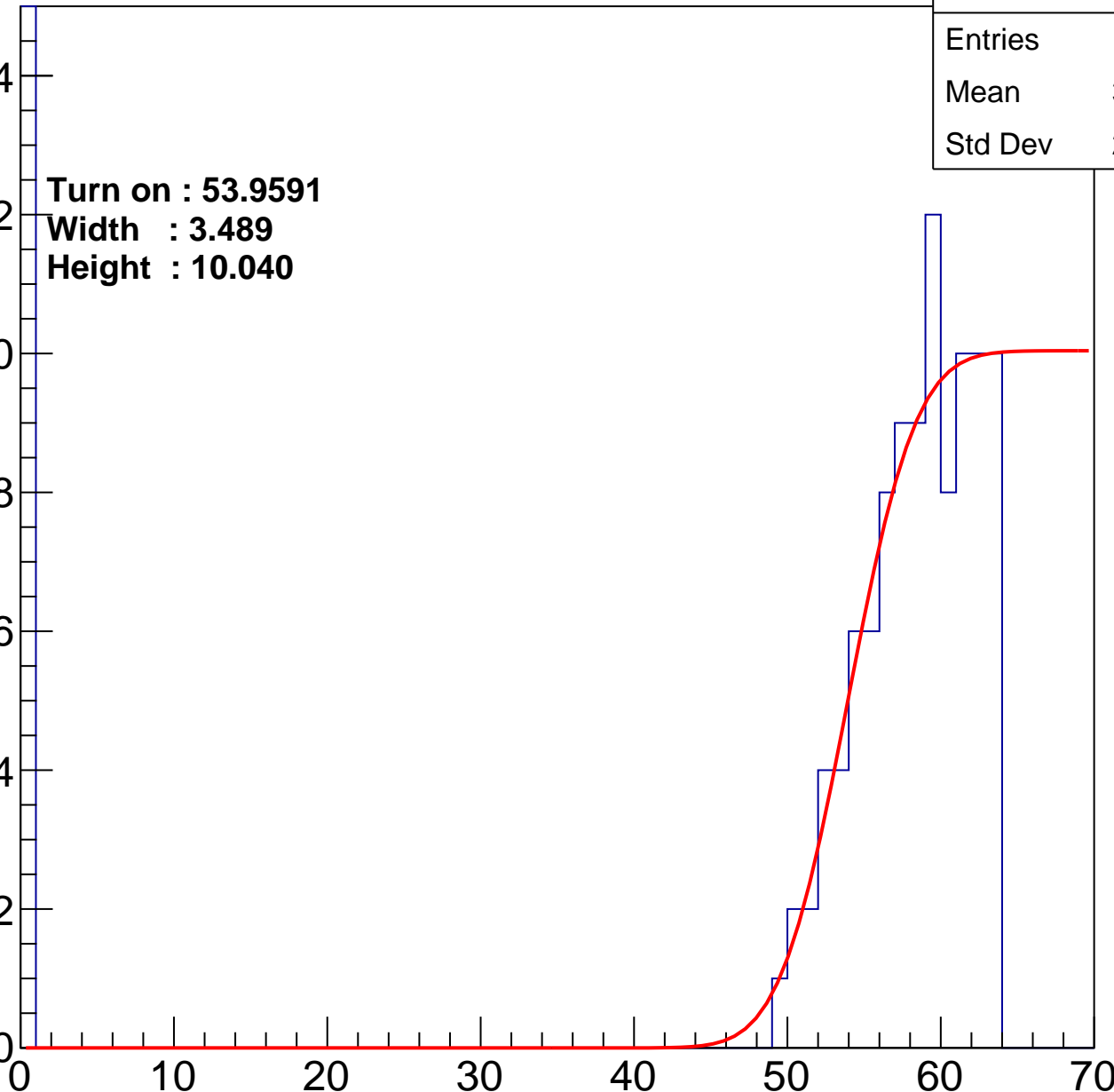
Width : 3.489

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	270
Mean	30.19
Std Dev	26.2

Turn on : 51.8097

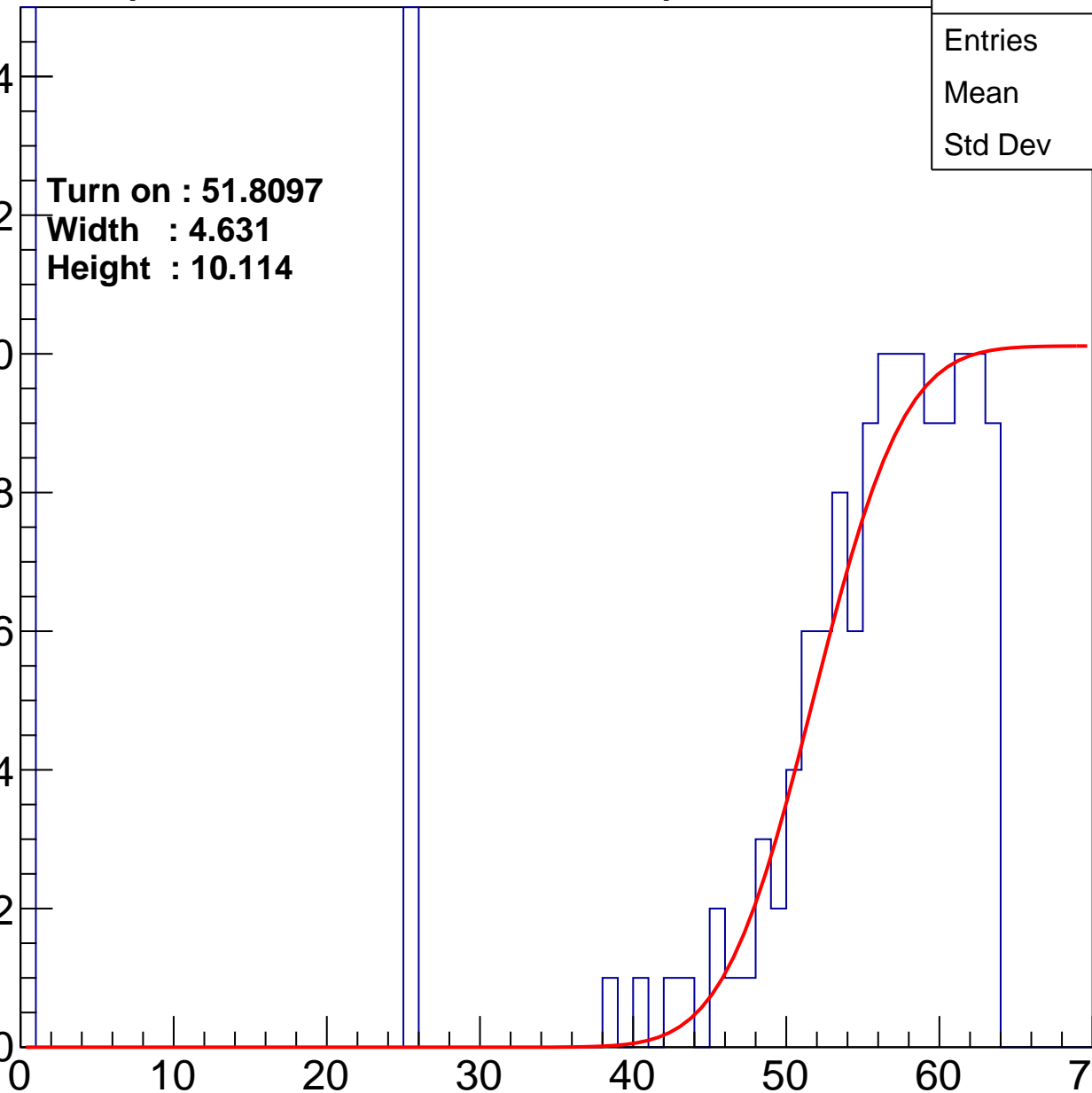
Width : 4.631

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl

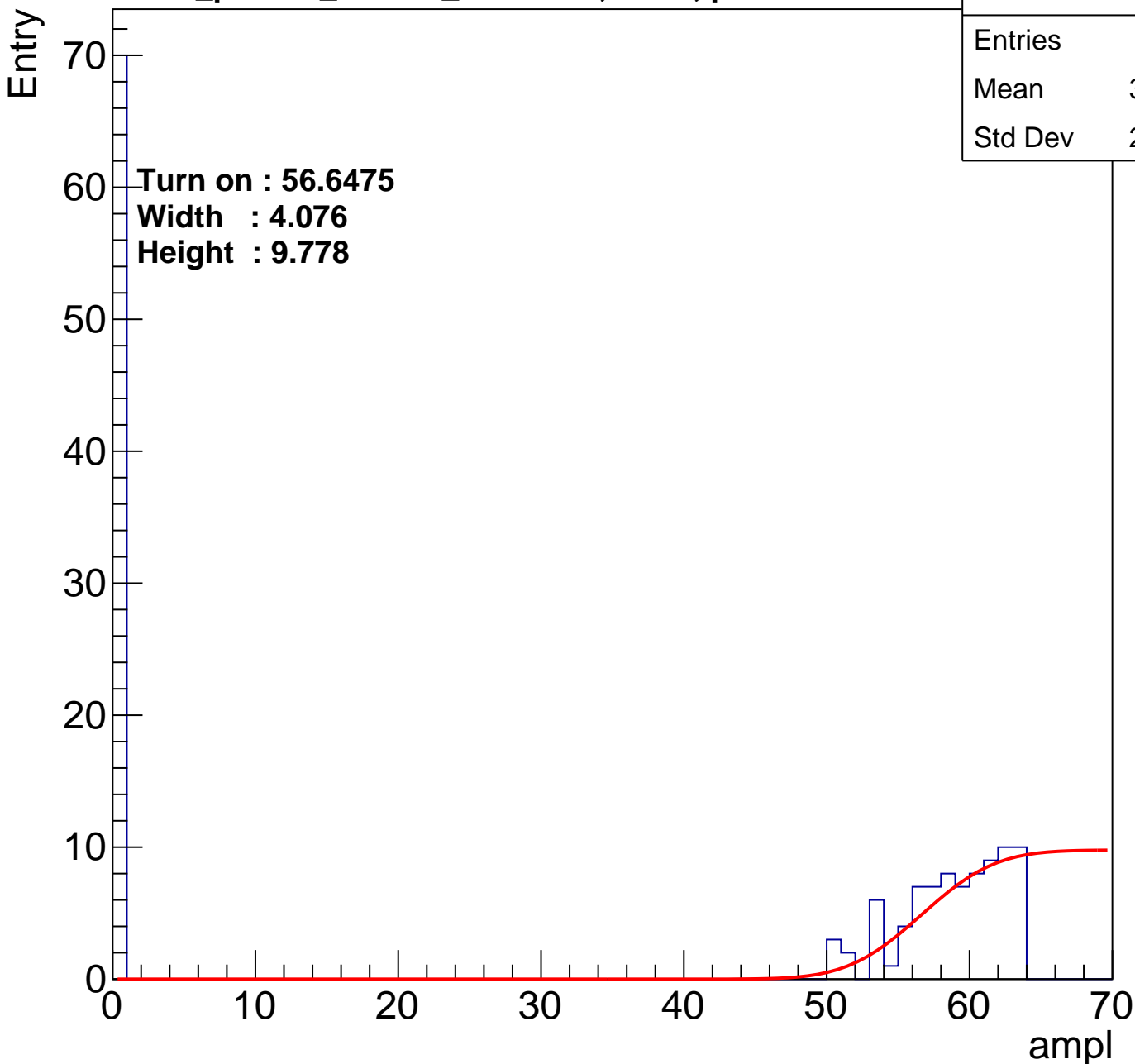


B1L104S, U10-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	31.52
Std Dev	29.24

Turn on : 56.6475
Width : 4.076
Height : 9.778



B1L104S, U10-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	231
Mean	26.84
Std Dev	28.59

Turn on : 54.0106

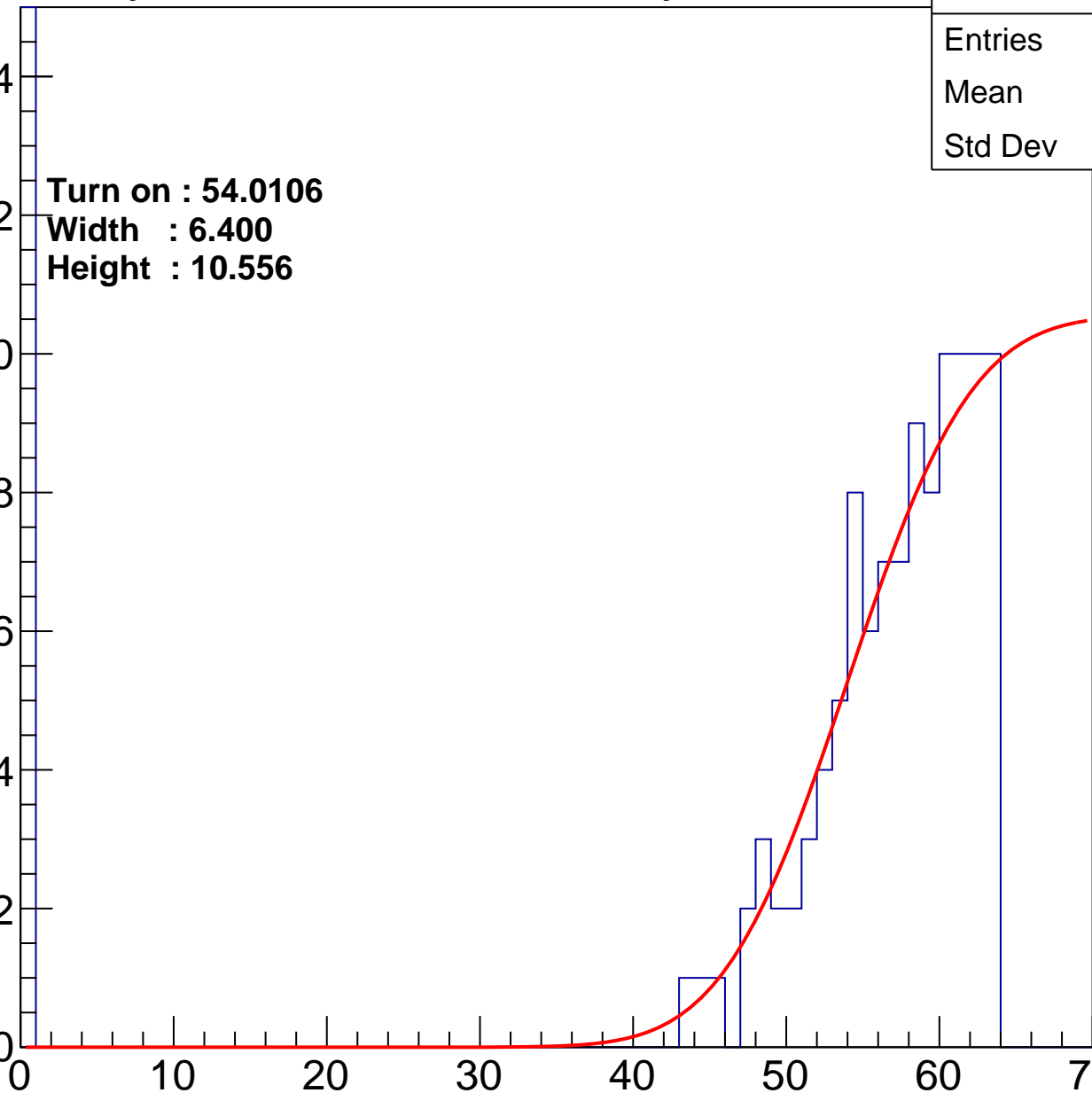
Width : 6.400

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	34.23
Std Dev	28.66

Turn on : 54.8903

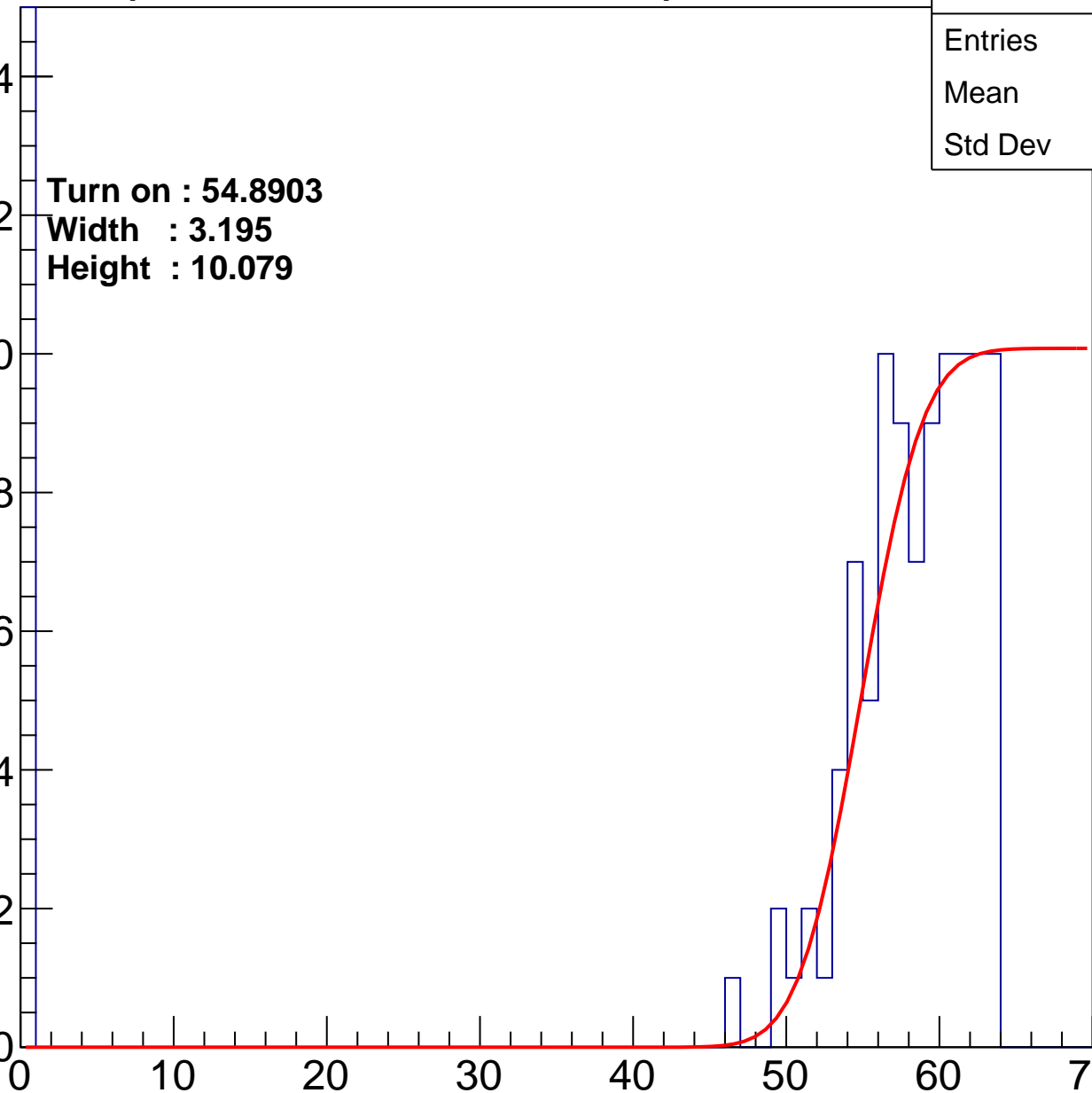
Width : 3.195

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch124

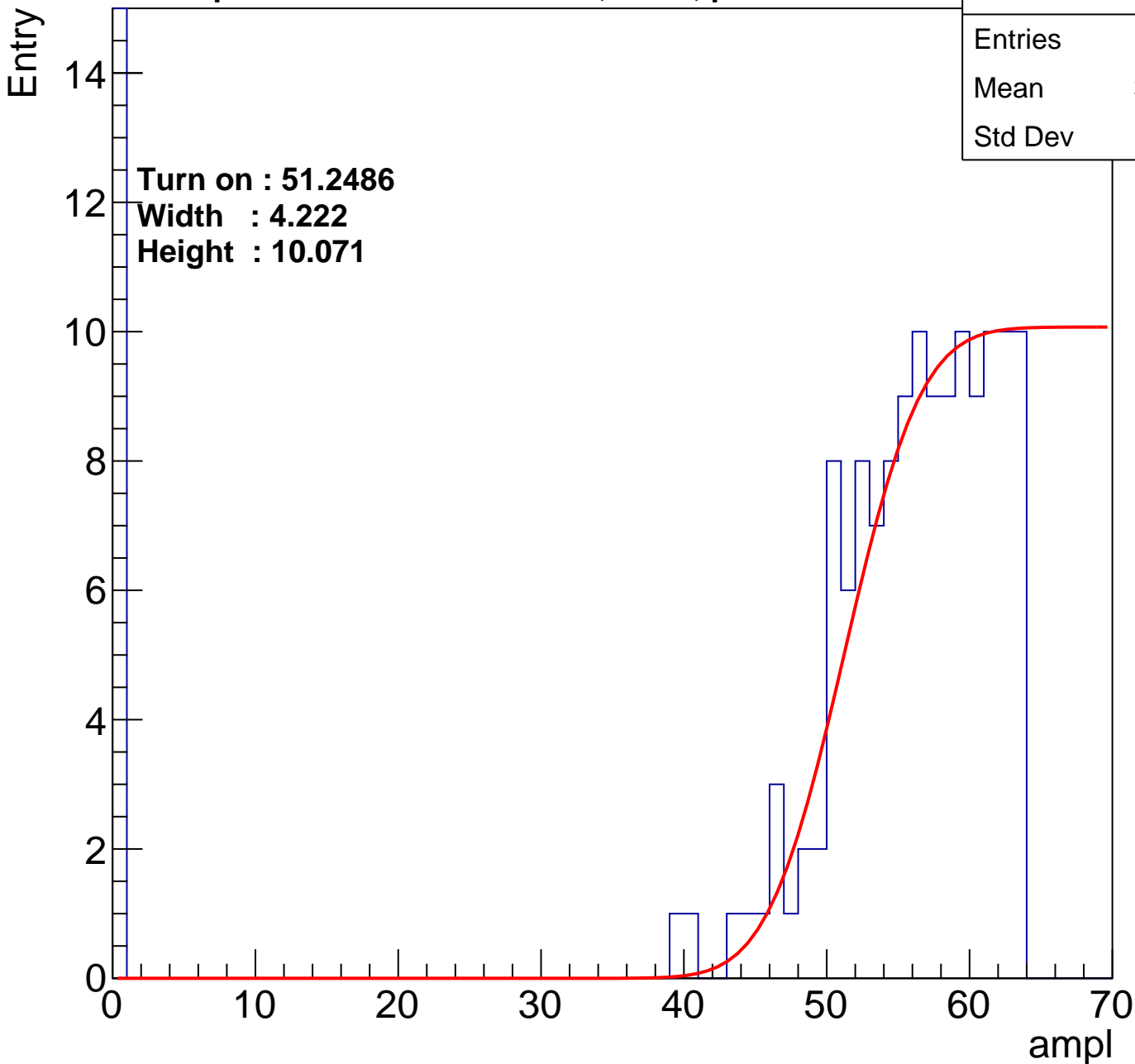
calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	34.21
Std Dev	27.5

Turn on : 51.2486

Width : 4.222

Height : 10.071



B1L104S, U10-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	29.29
Std Dev	29.1

Turn on : 55.5290

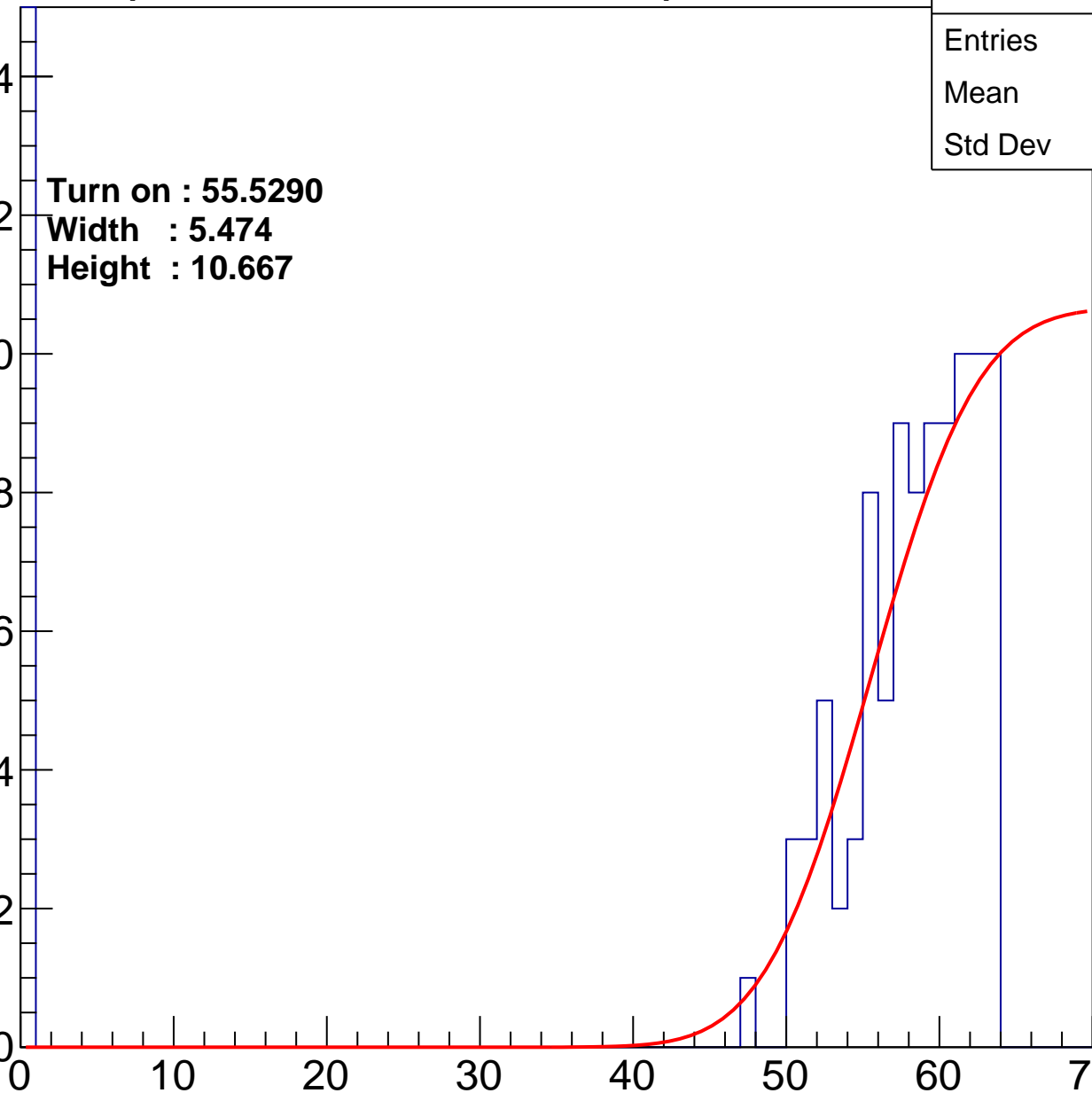
Width : 5.474

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	31.13
Std Dev	28.37

Turn on : 52.7880

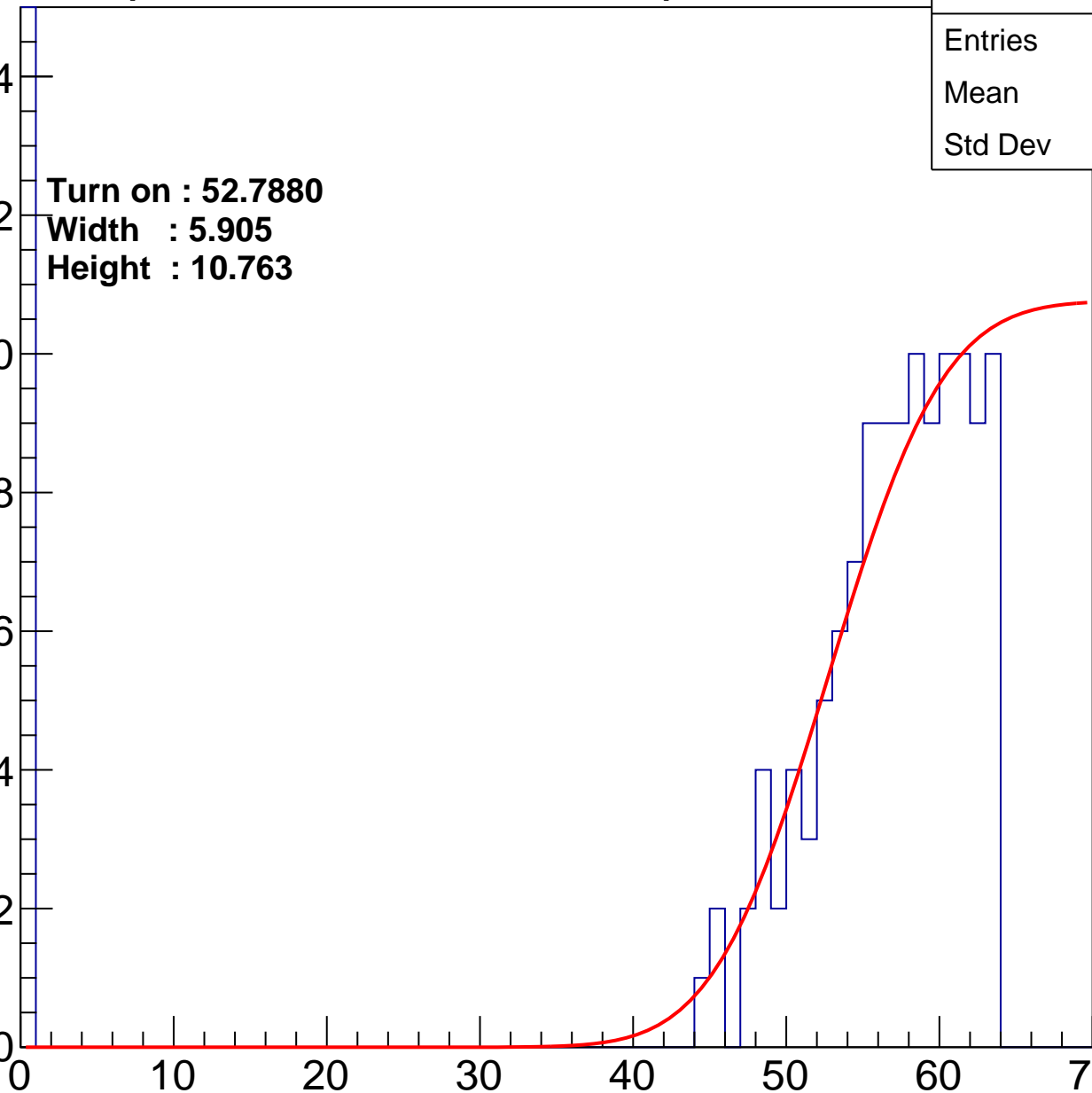
Width : 5.905

Height : 10.763

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U10-ch127

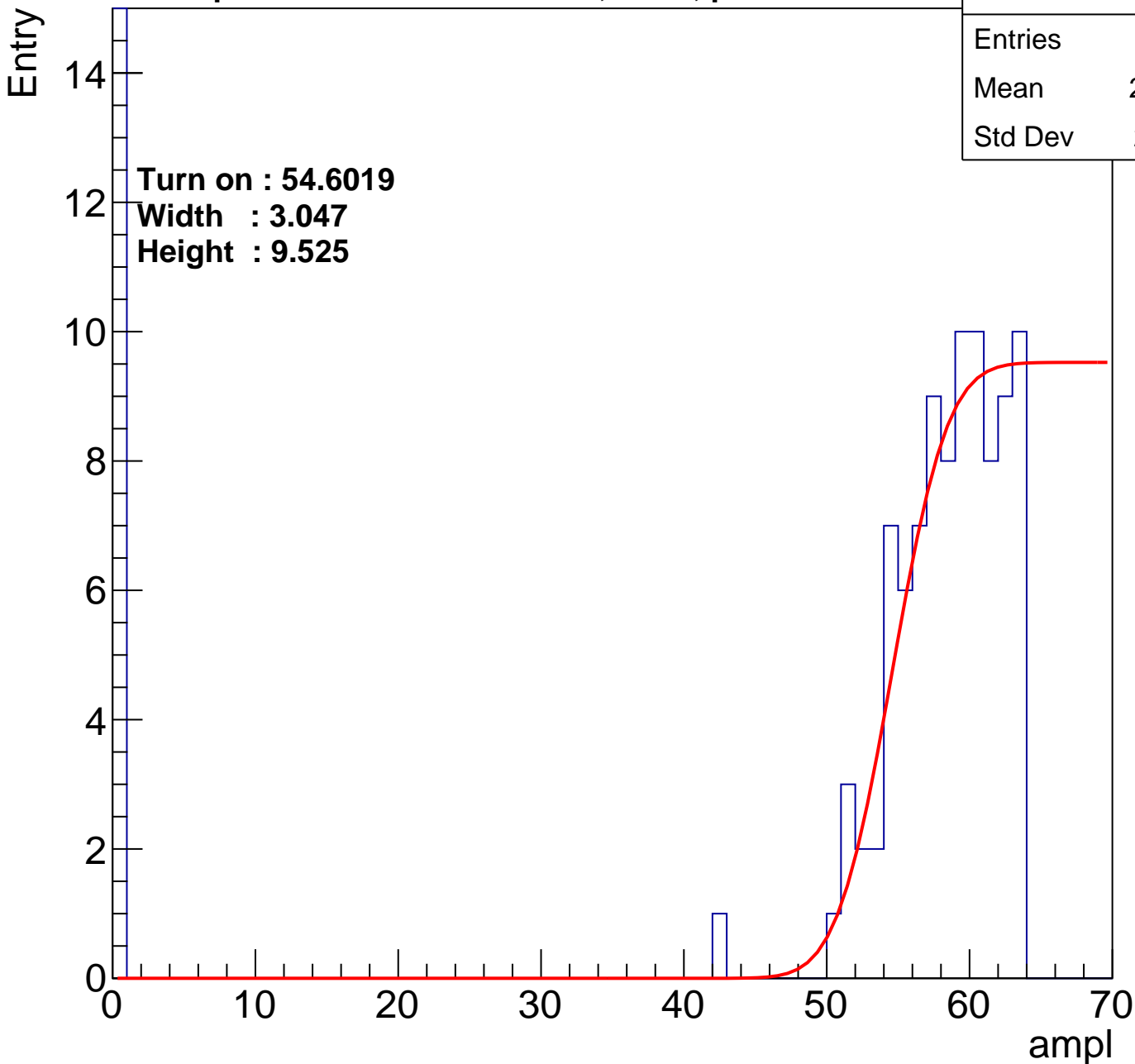
calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	27.82
Std Dev	29.11

Turn on : 54.6019

Width : 3.047

Height : 9.525



B1L104S, U10-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	27.82
Std Dev	29.11

Turn on : 54.6019

Width : 3.047

Height : 9.525

