

B0L002S, U12-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.38
Std Dev	11.48

Turn on : 26.8331

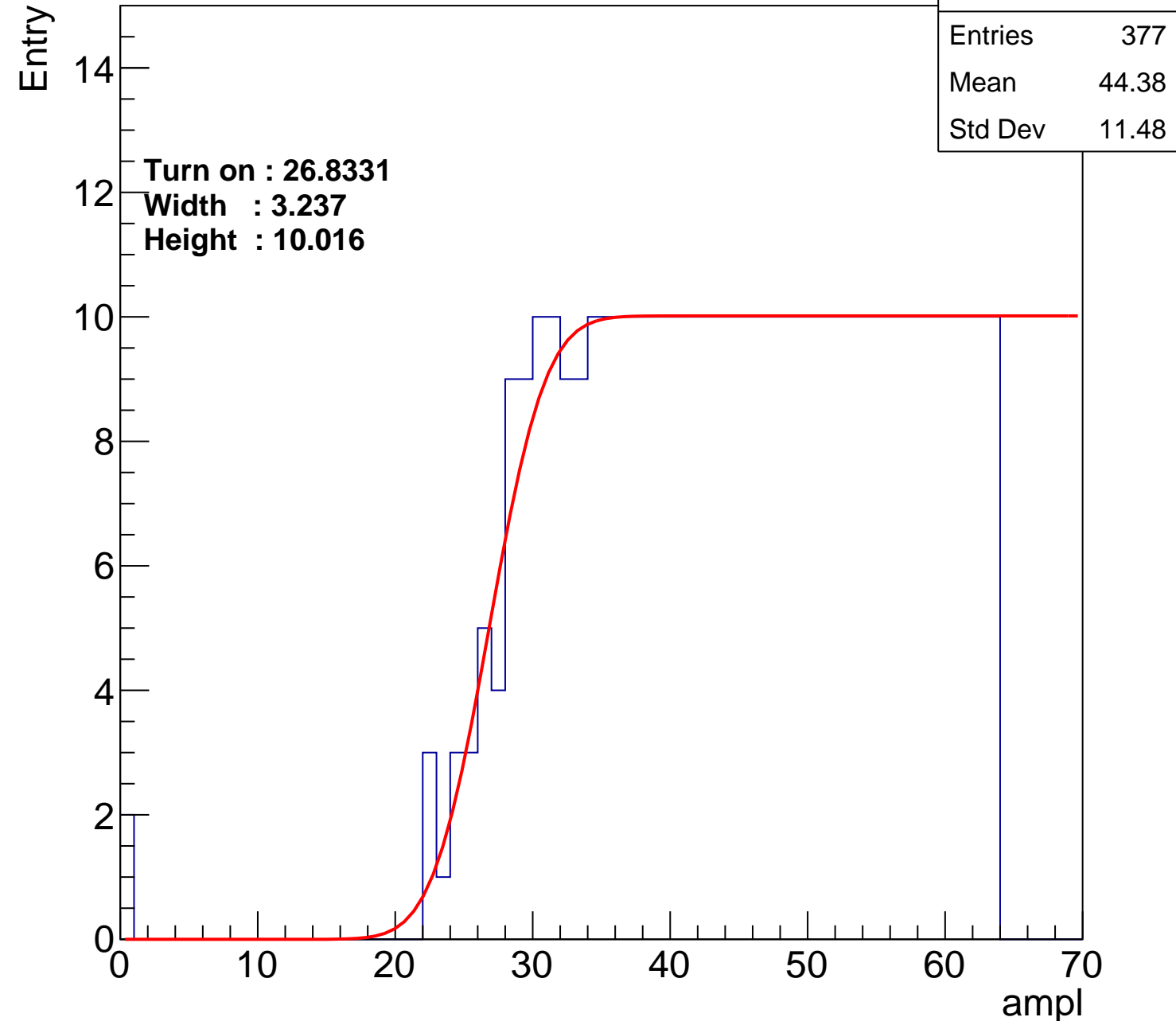
Width : 3.237

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.76
Std Dev	11.45

Turn on : 27.6717

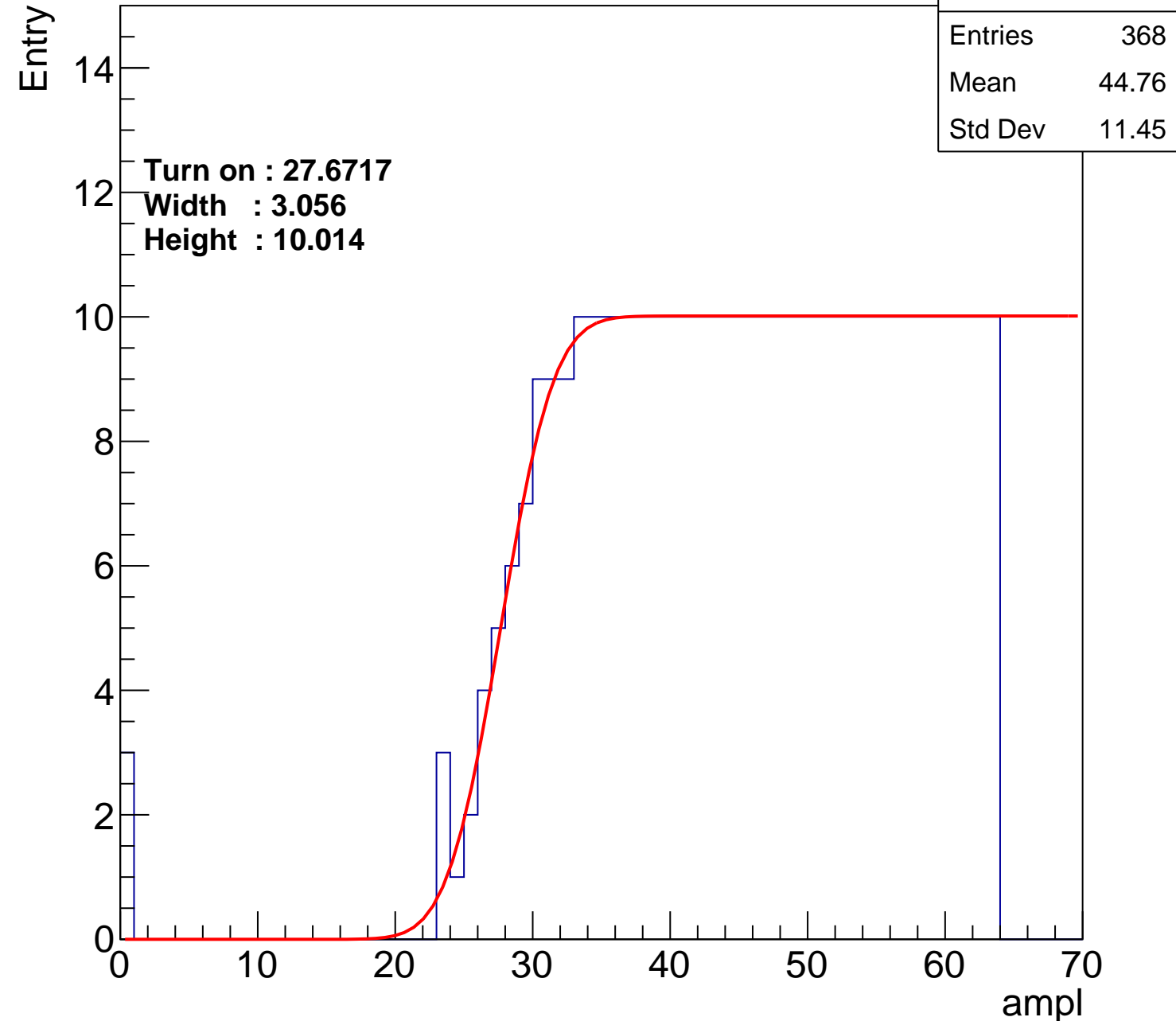
Width : 3.056

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.0476

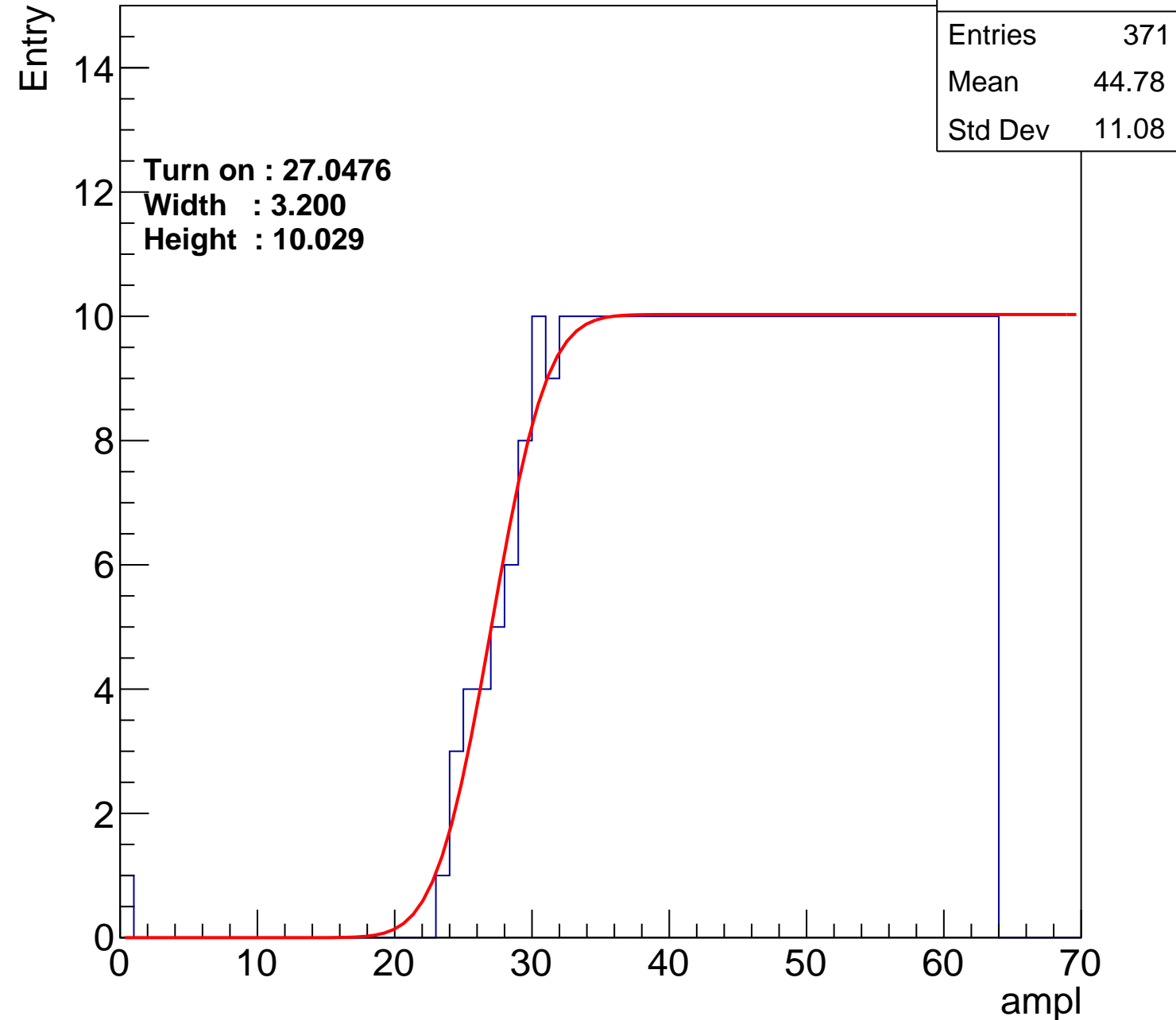
Width : 3.200

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 27.1254

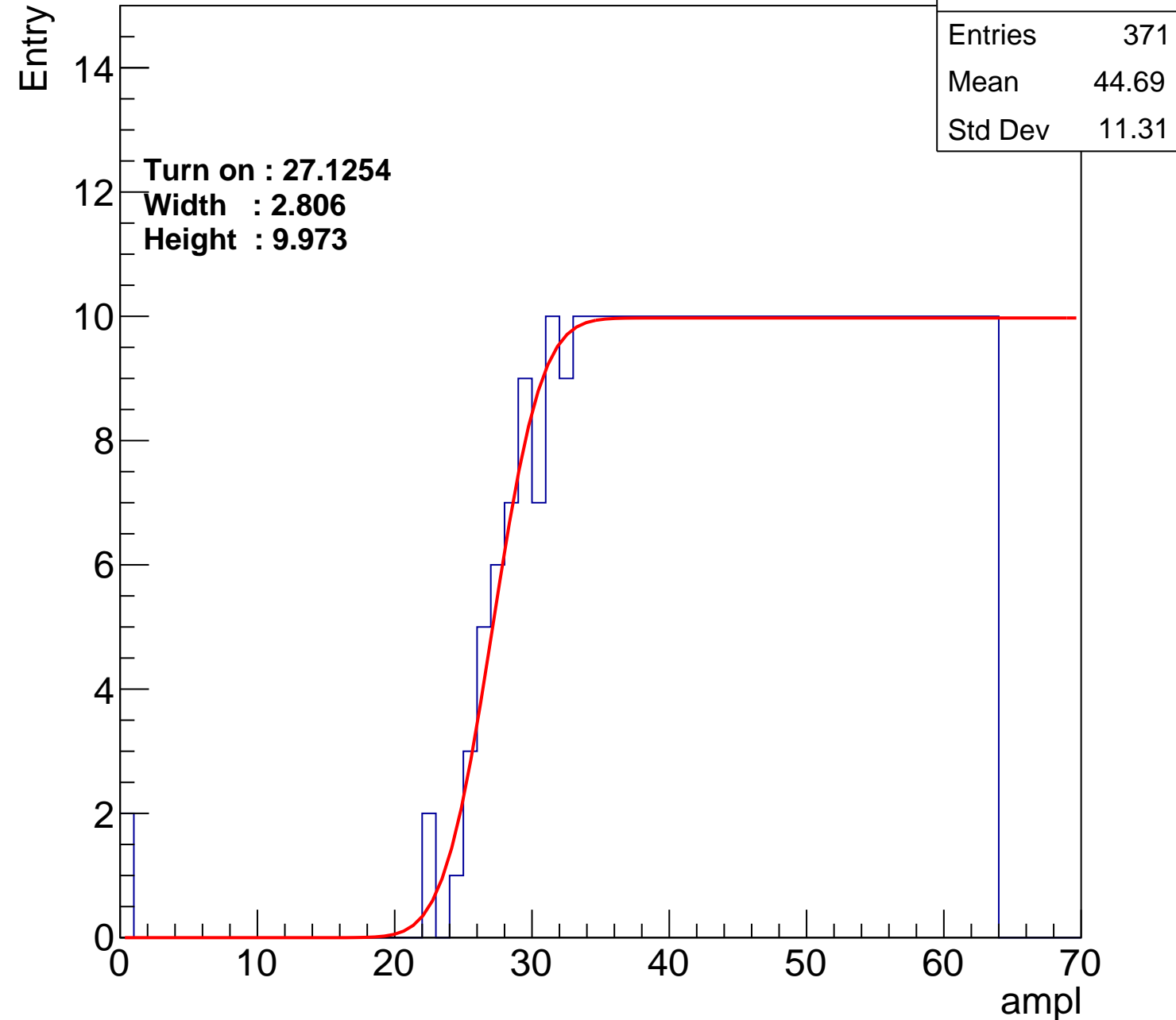
Width : 2.806

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch4

calib_packv5_042523_0143.root, FC#8, port C1

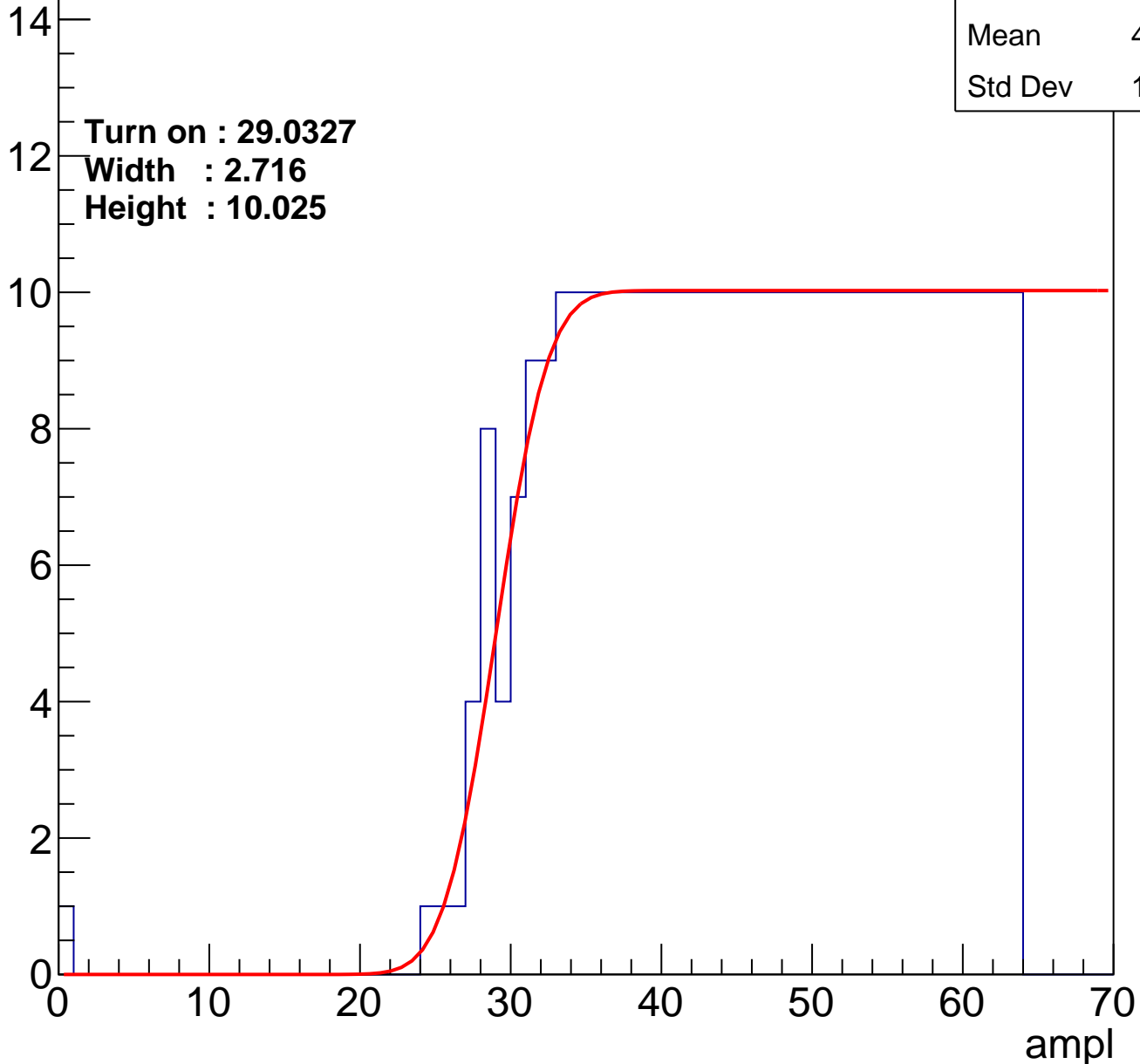
Entries	355
Mean	45.58
Std Dev	10.64

Turn on : 29.0327

Width : 2.716

Height : 10.025

Entry



B0L002S, U12-ch5

calib_packv5_042523_0143.root, FC#8, port C1

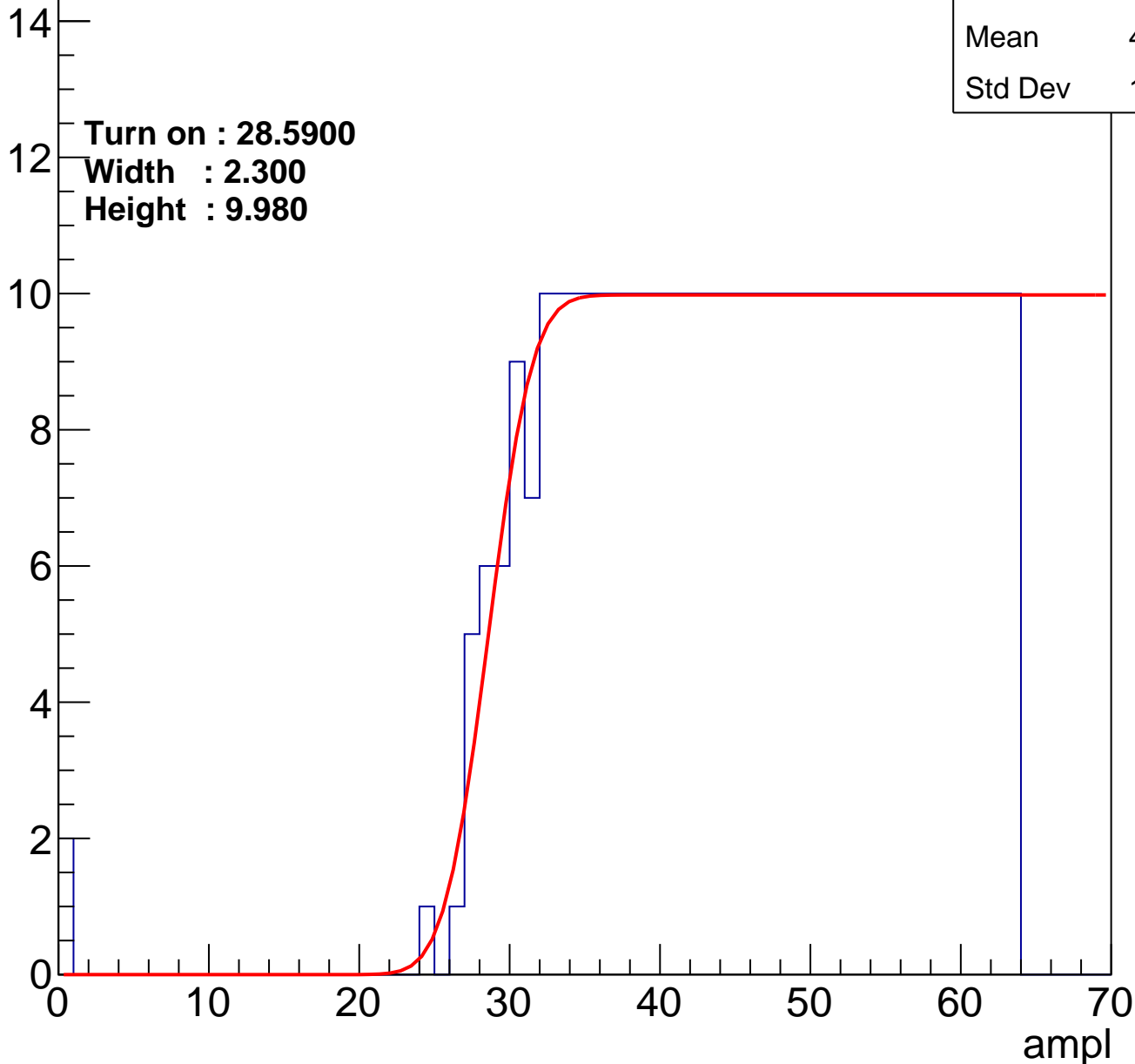
Entry

Entries	357
Mean	45.42
Std Dev	10.89

Turn on : 28.5900

Width : 2.300

Height : 9.980



B0L002S, U12-ch6

calib_packv5_042523_0143.root, FC#8, port C1

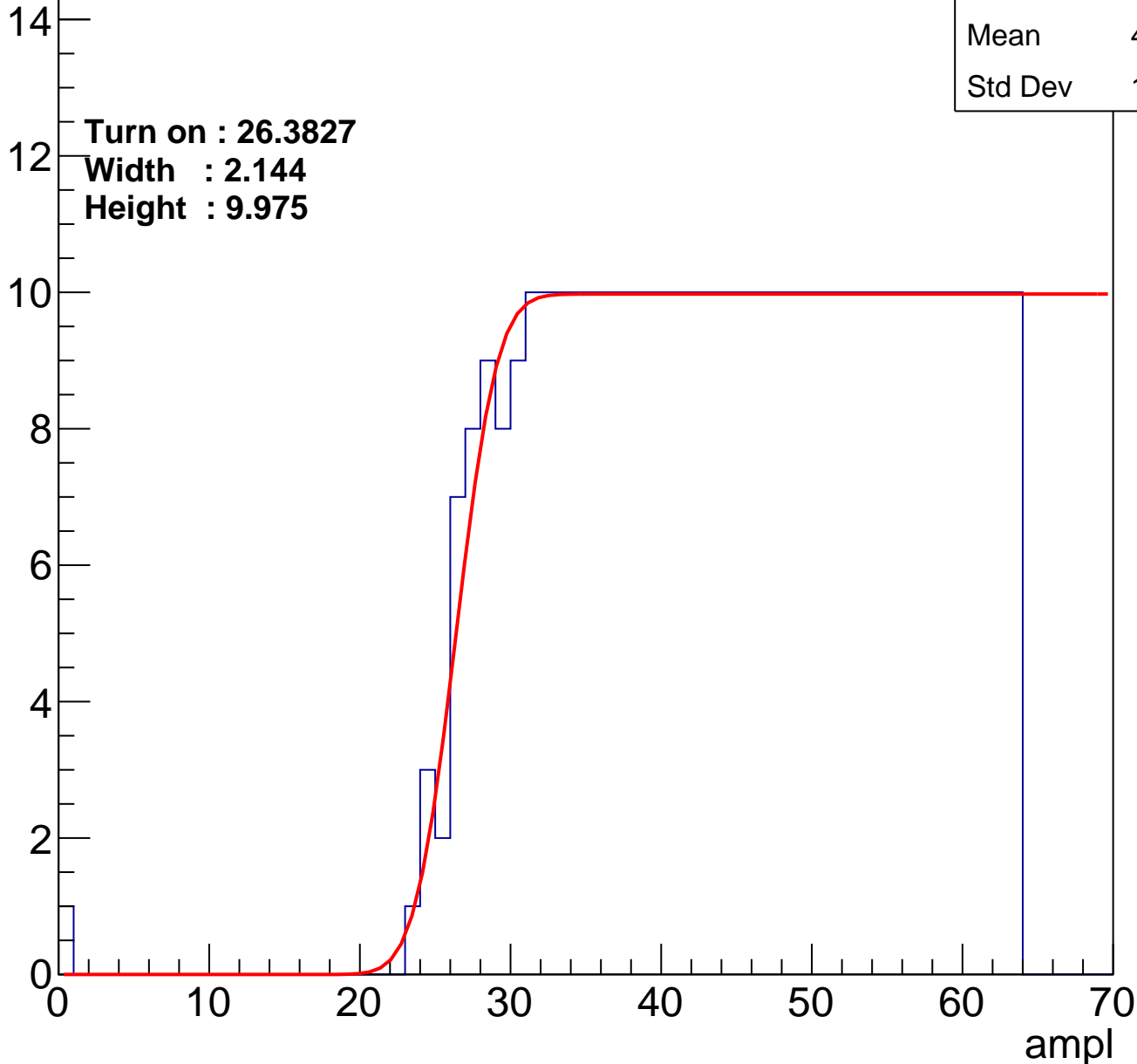
Entry

Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 26.3827

Width : 2.144

Height : 9.975



B0L002S, U12-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 26.8906

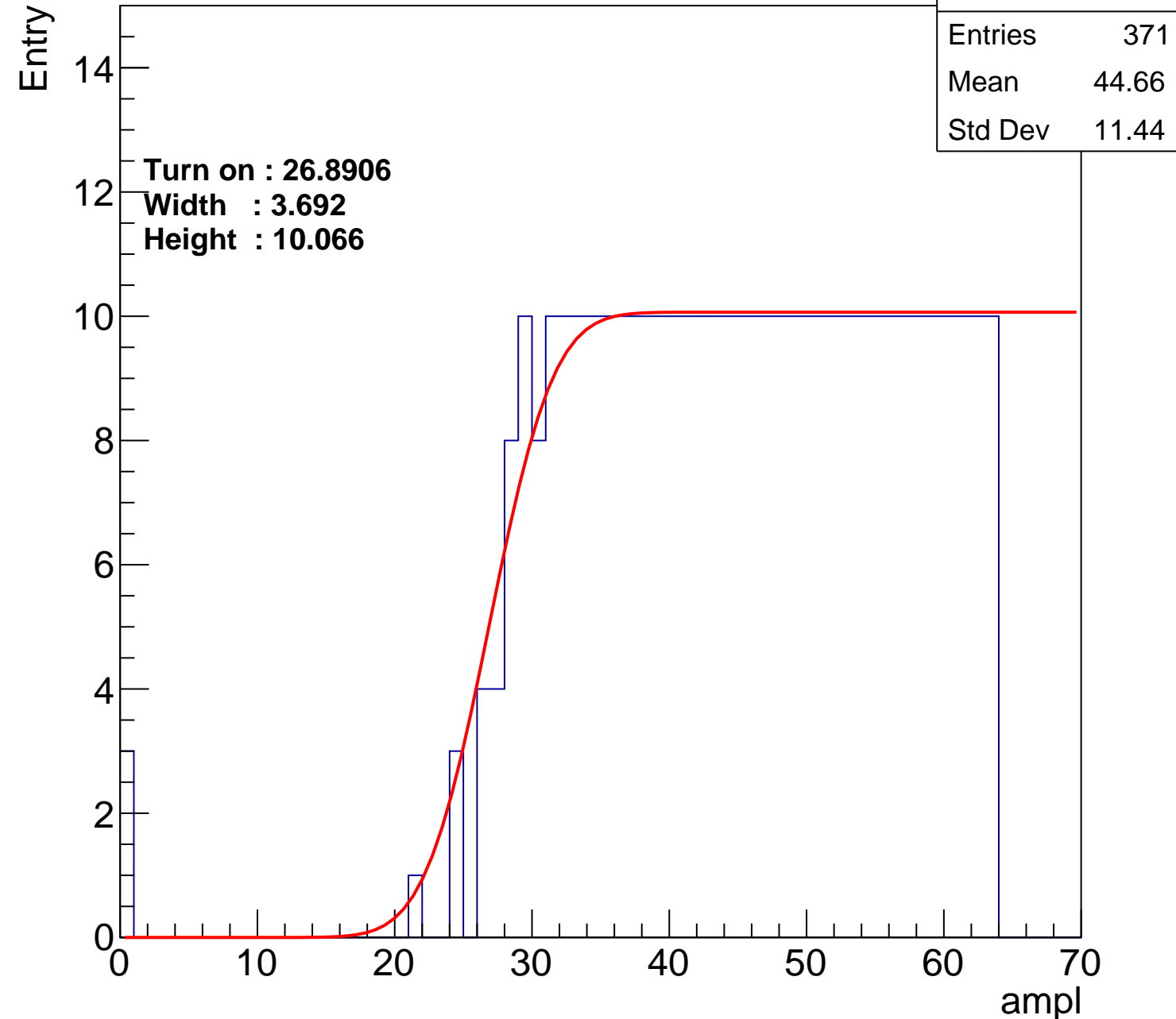
Width : 3.692

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.08
Std Dev	11.25

Turn on : 28.4657

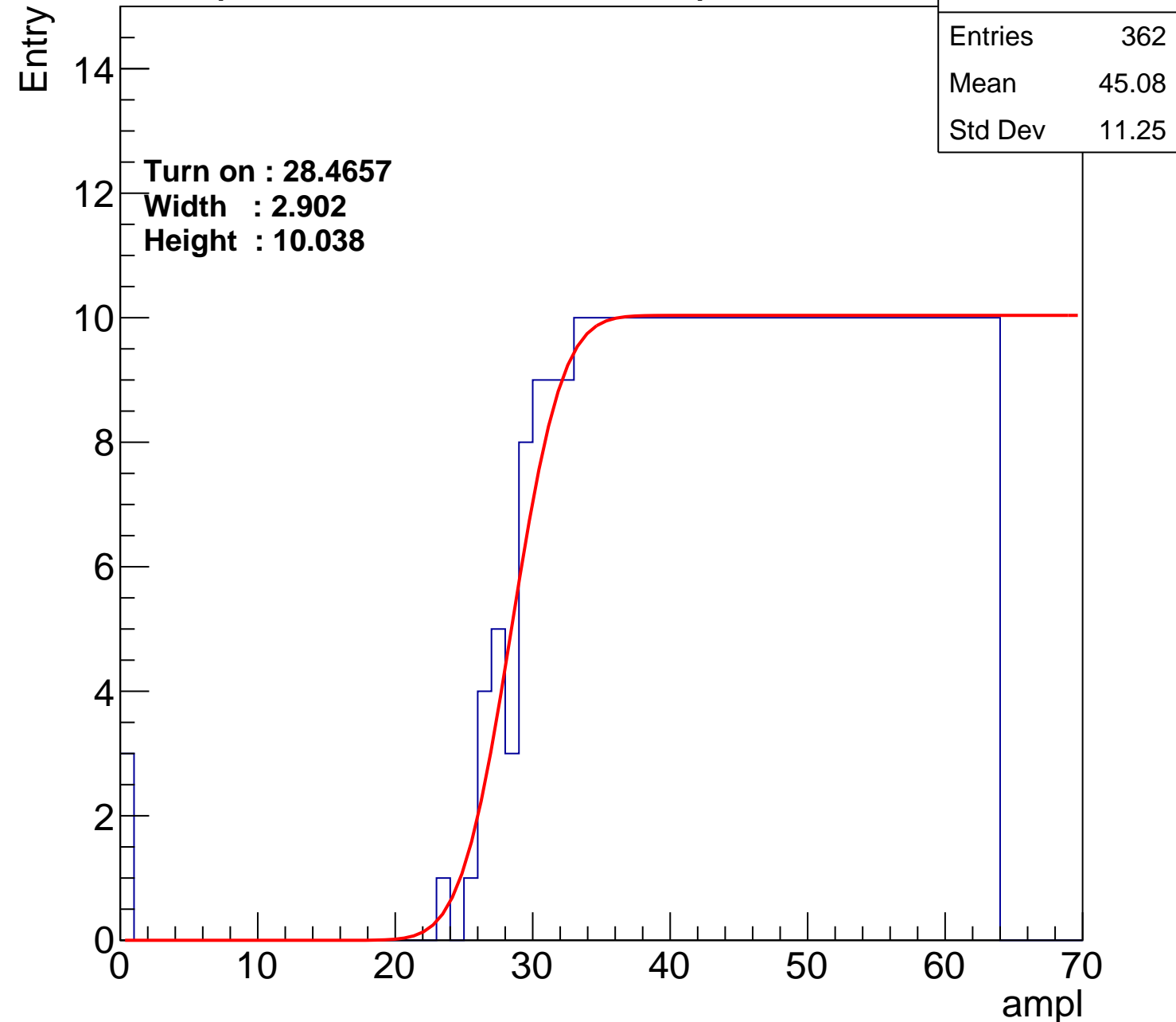
Width : 2.902

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch9

calib_packv5_042523_0143.root, FC#8, port C1

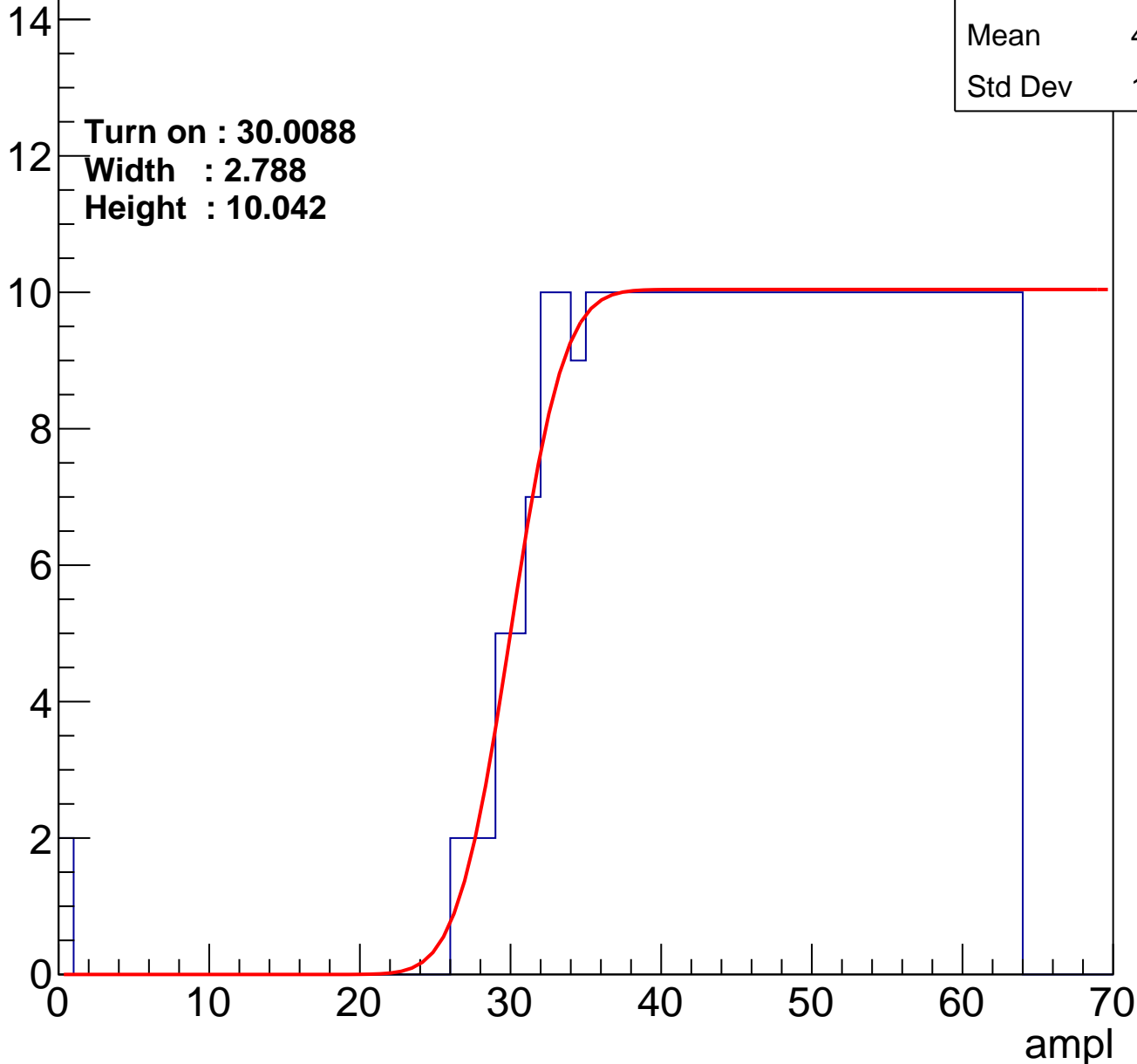
Entry

Entries	344
Mean	46.05
Std Dev	10.59

Turn on : 30.0088

Width : 2.788

Height : 10.042



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.3868
Width : 3.553
Height : 10.097



B0L002S, U12-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.81
Std Dev	10.47

Turn on : 29.1748

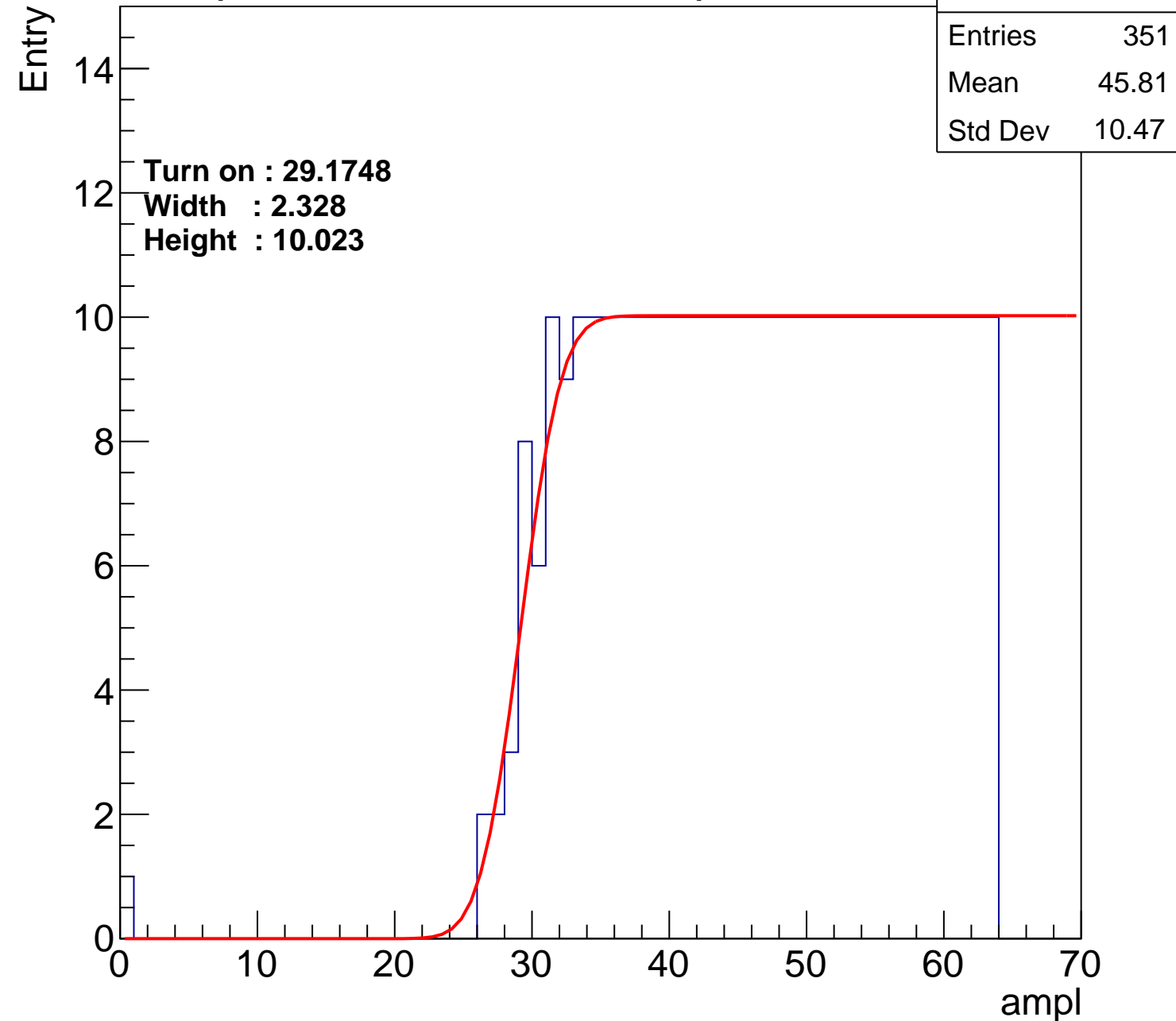
Width : 2.328

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.1151

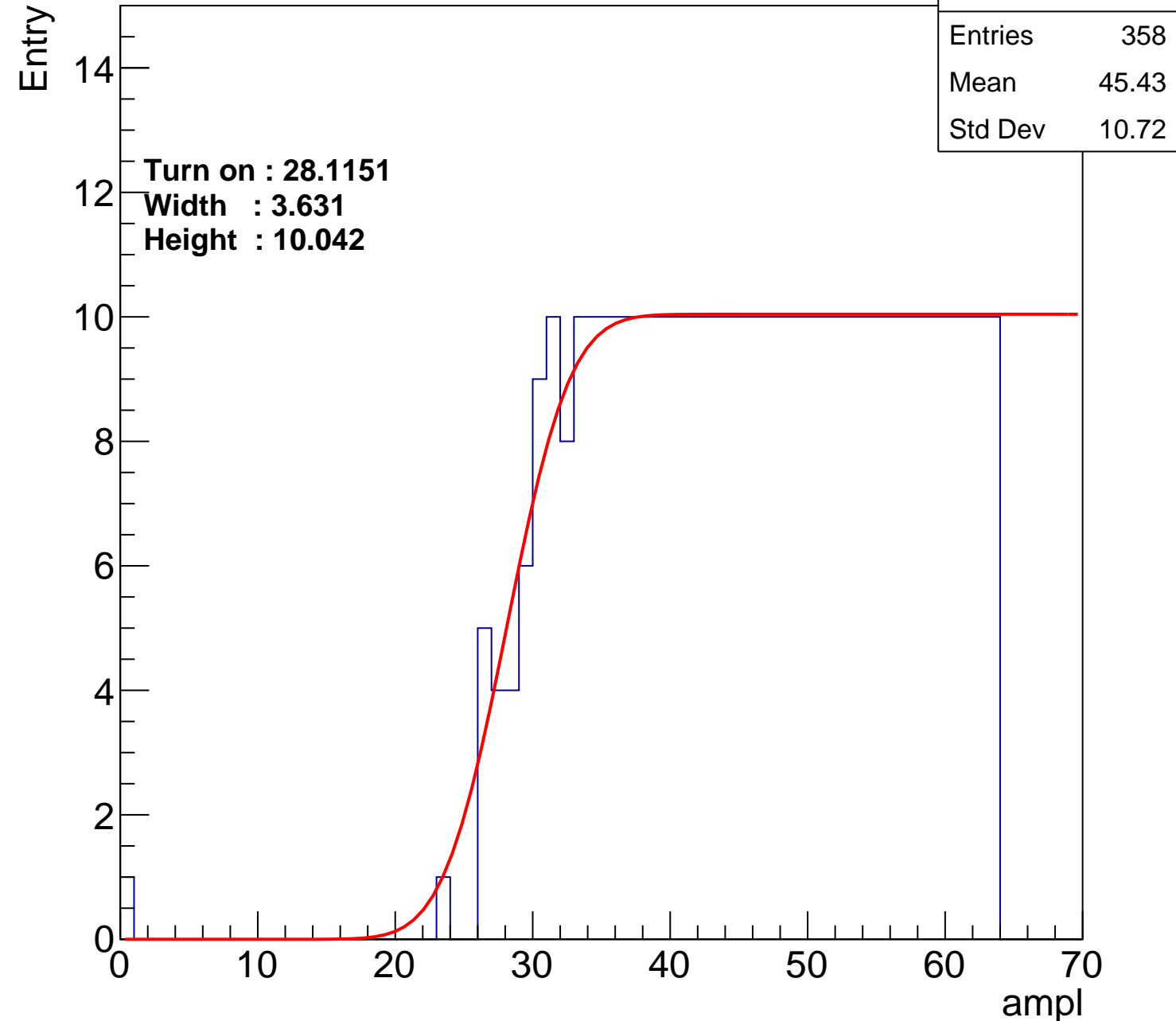
Width : 3.631

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.84
Std Dev	11.56

Turn on : 27.7911

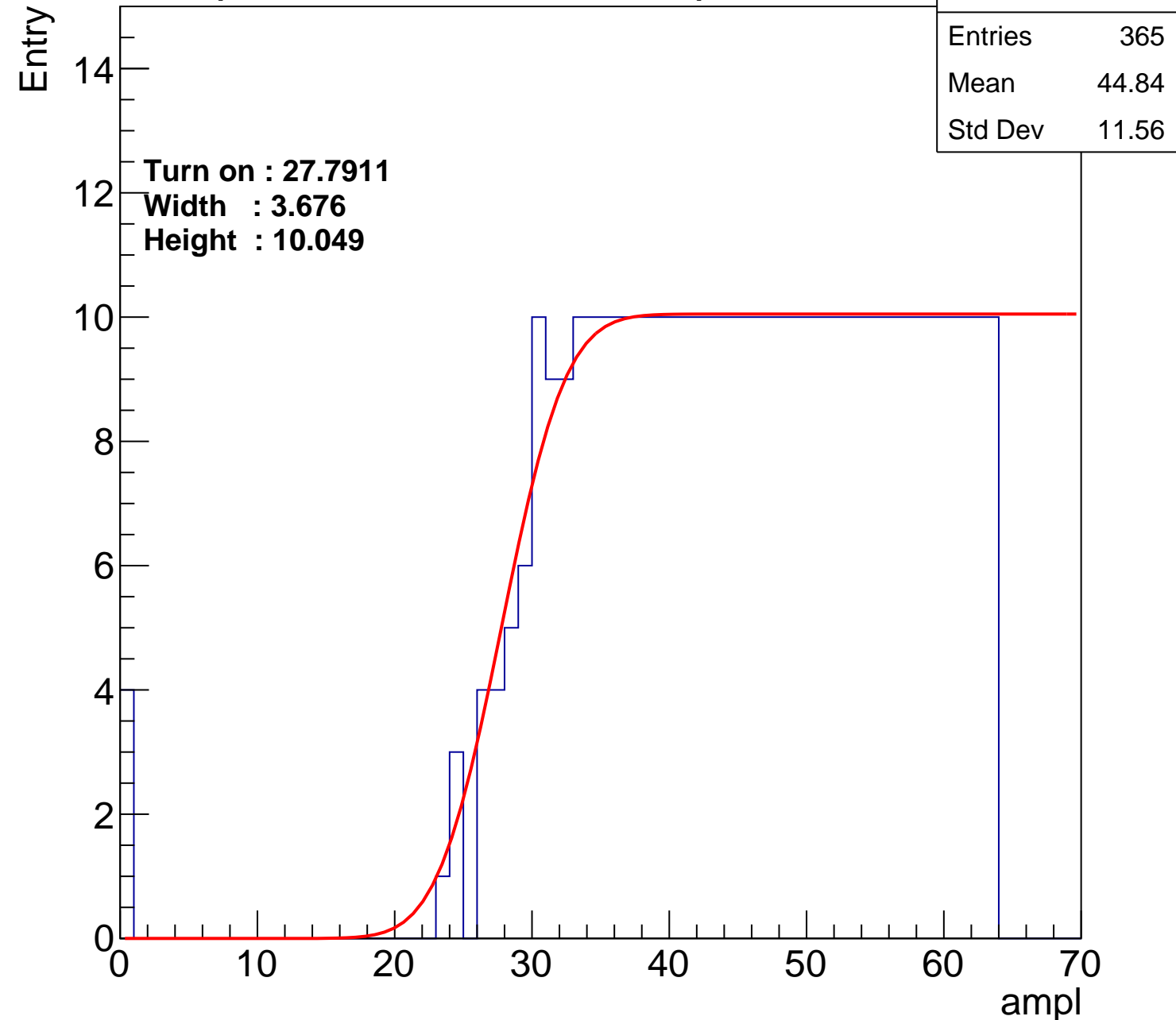
Width : 3.676

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch14

calib_packv5_042523_0143.root, FC#8, port C1

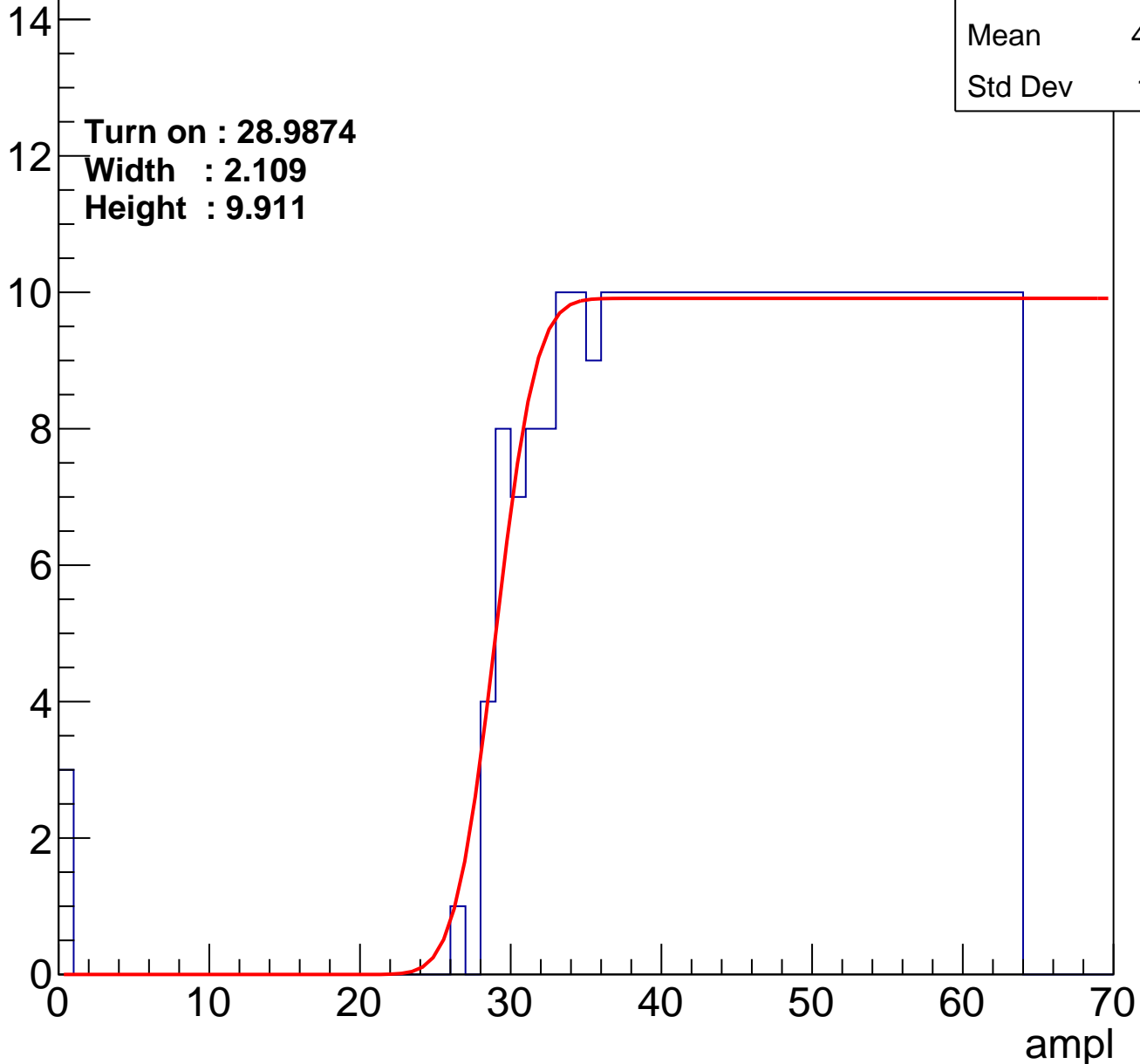
Entries	348
Mean	45.77
Std Dev	10.91

Turn on : 28.9874

Width : 2.109

Height : 9.911

Entry



B0L002S, U12-ch15

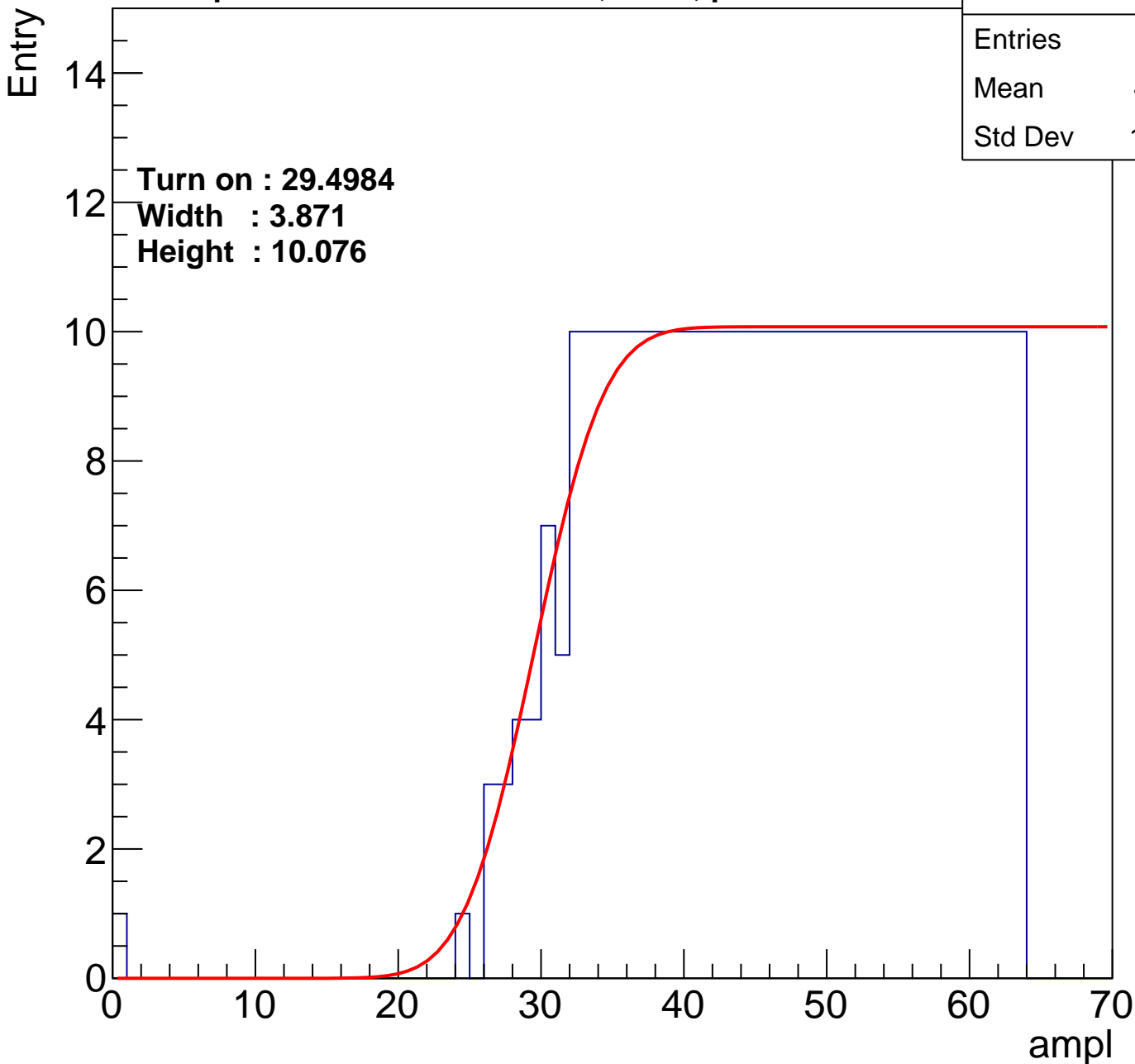
calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.91
Std Dev	10.48

Turn on : 29.4984

Width : 3.871

Height : 10.076



B0L002S, U12-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.47
Std Dev	11.07

Turn on : 29.0708

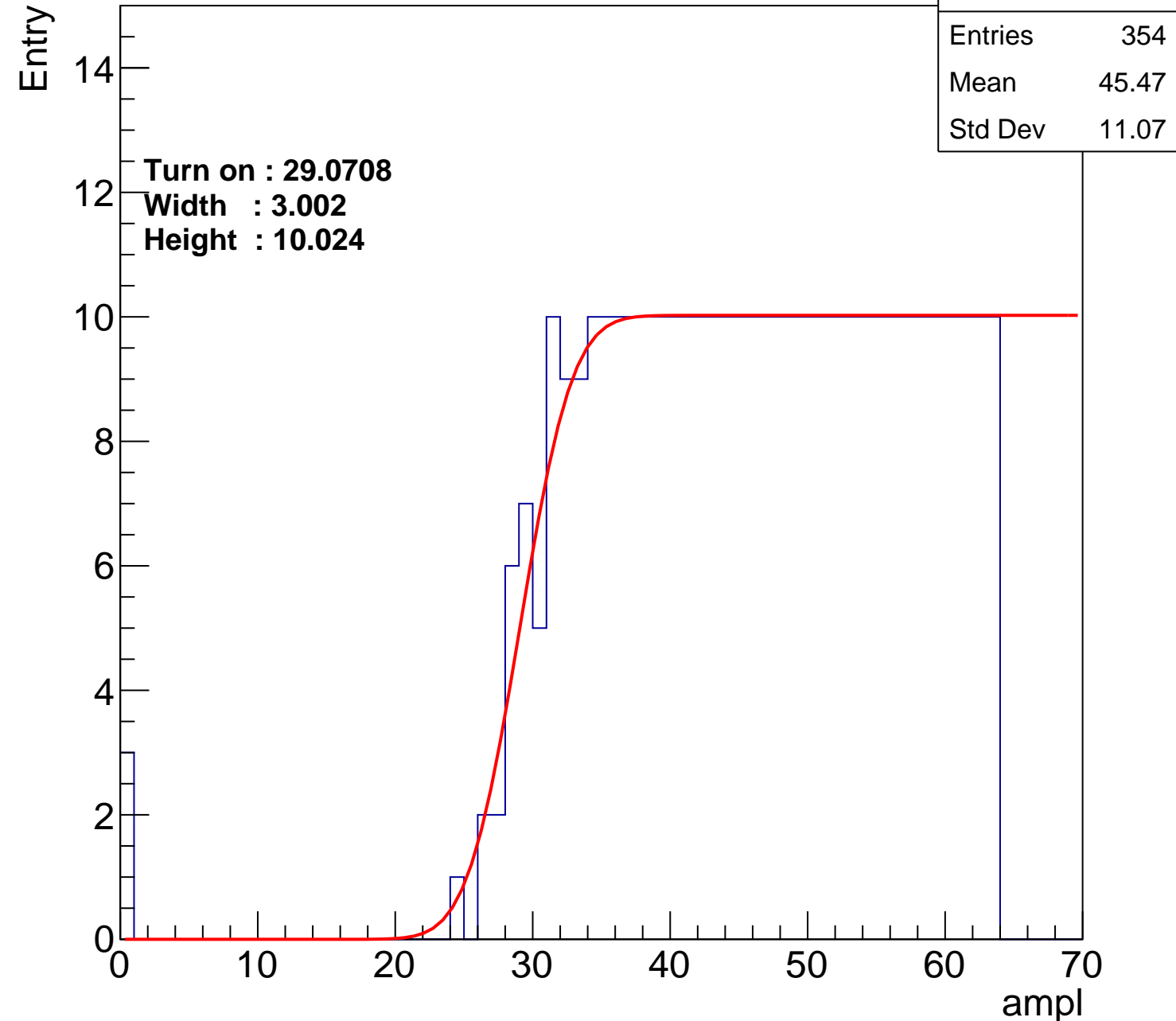
Width : 3.002

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.9
Std Dev	11.33

Turn on : 27.9740

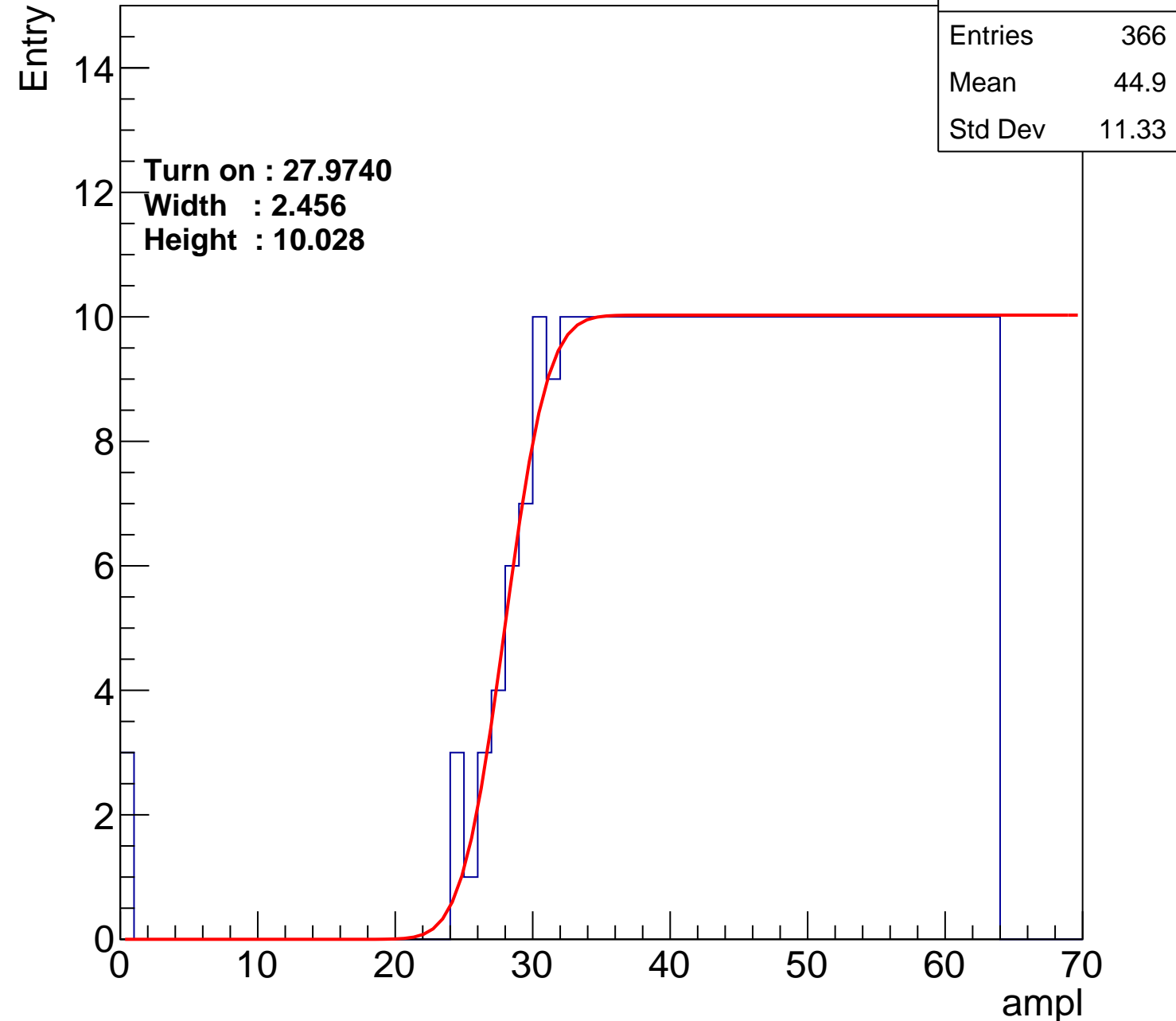
Width : 2.456

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.2993

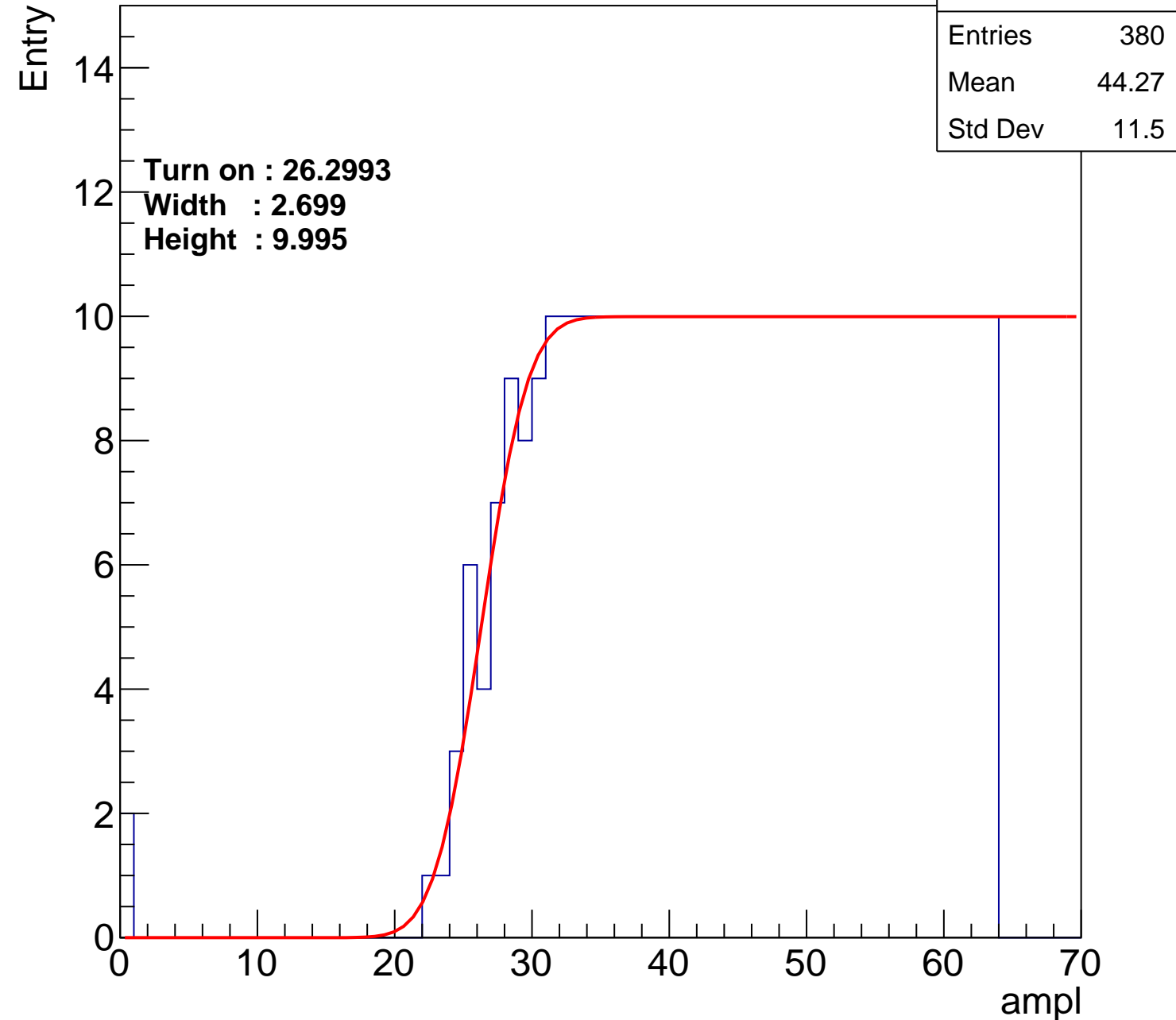
Width : 2.699

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.87
Std Dev	11.22

Turn on : 27.4218

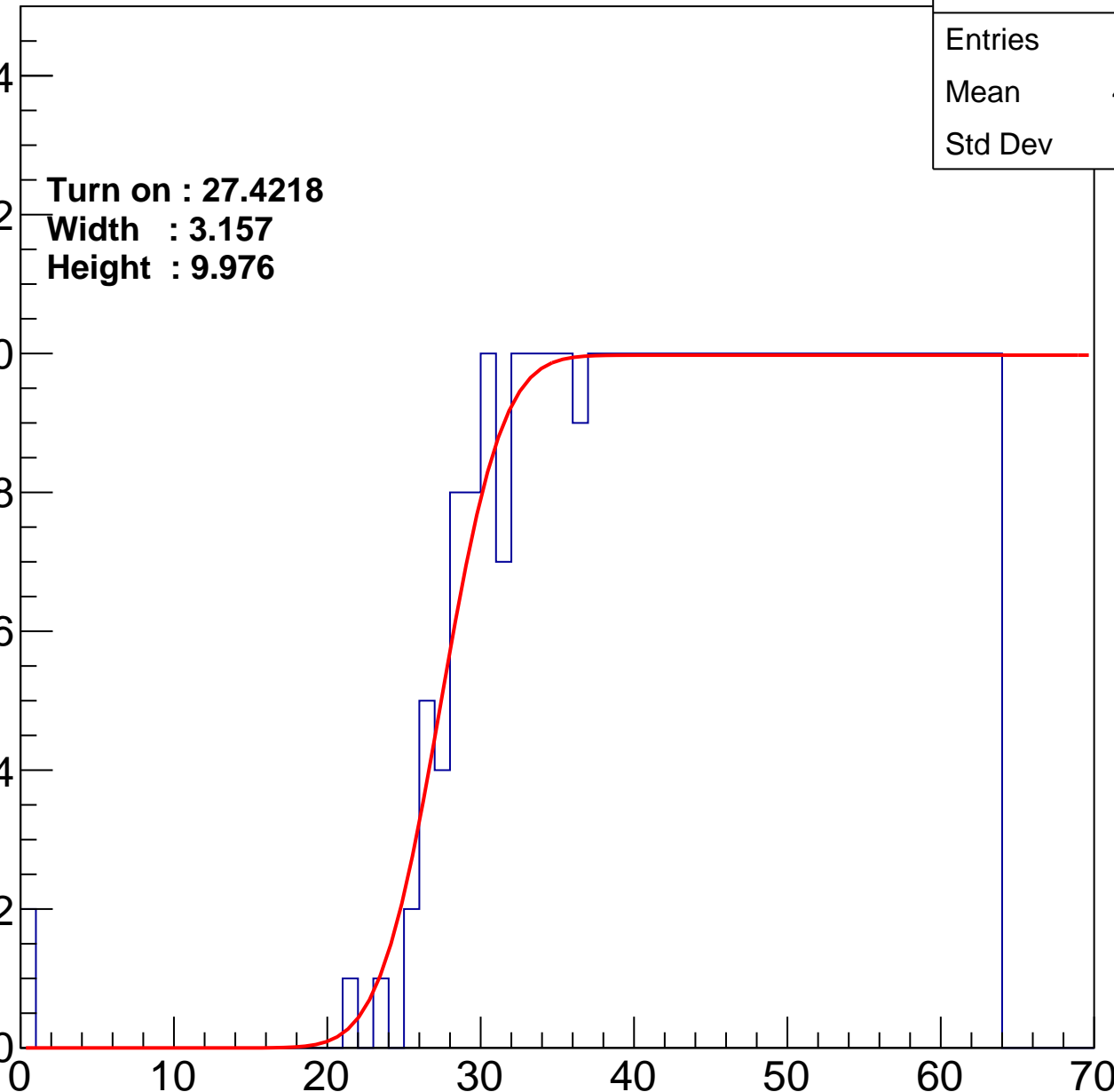
Width : 3.157

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.31
Std Dev	10.96

Turn on : 28.7023

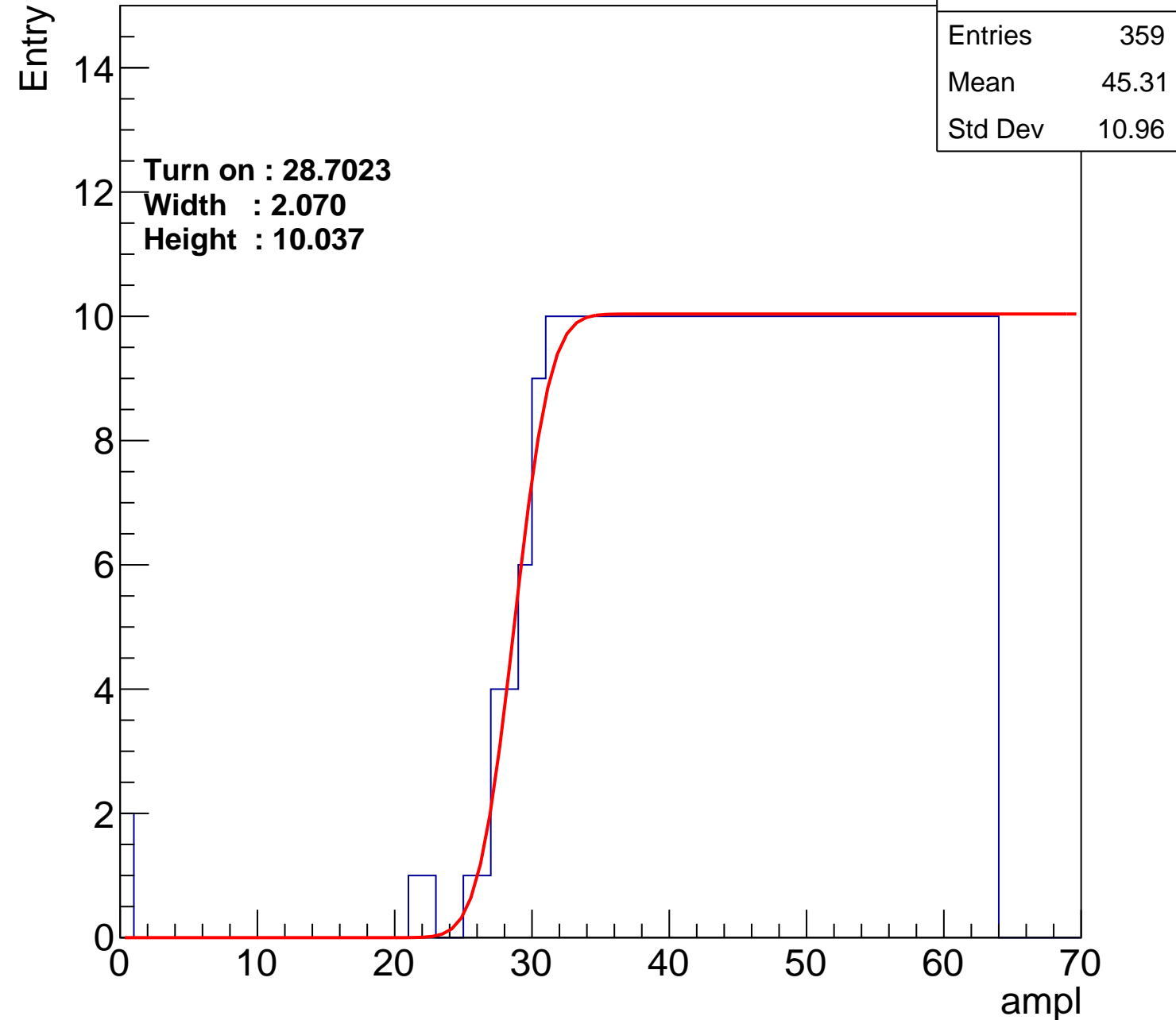
Width : 2.070

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.8065

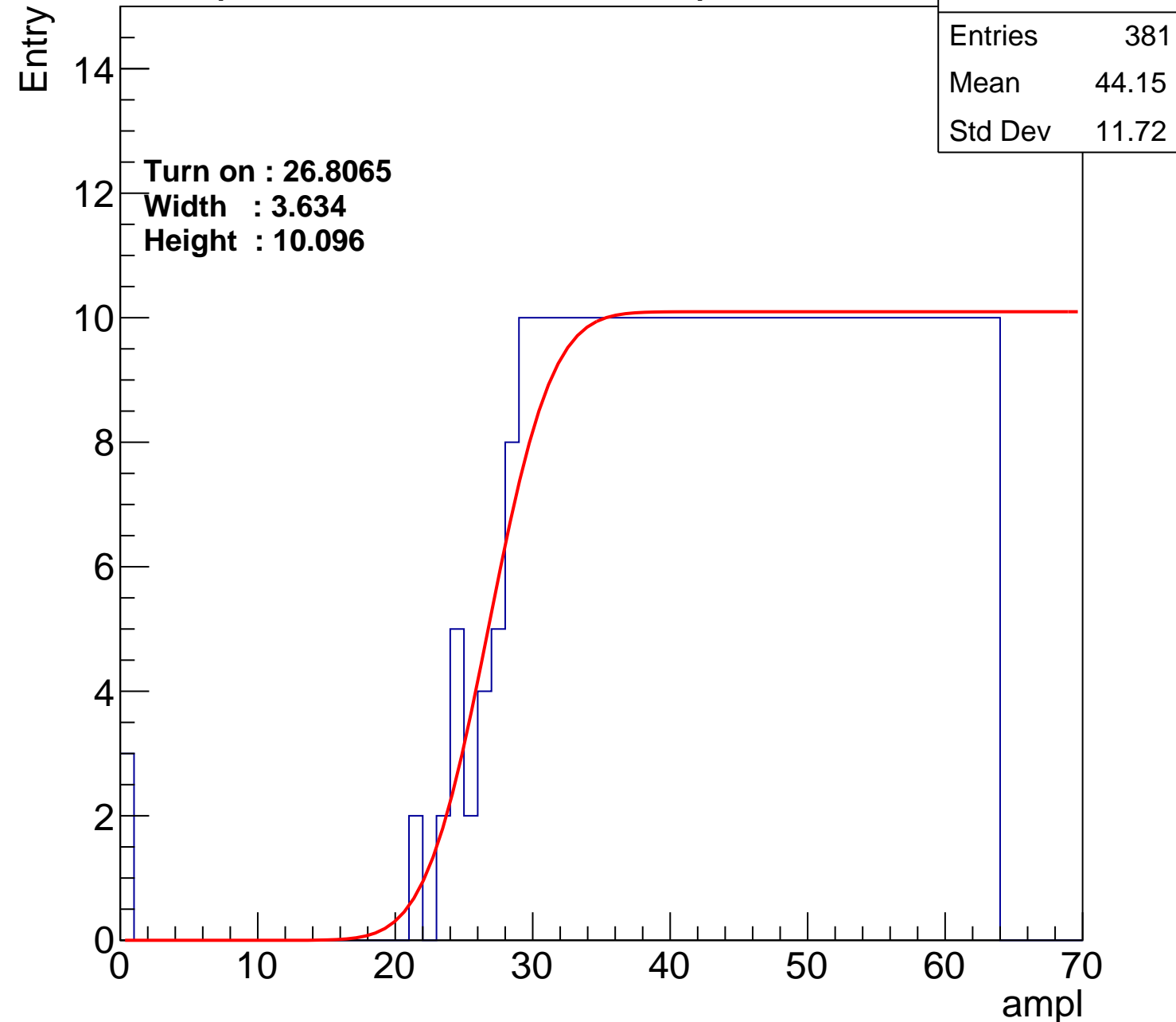
Width : 3.634

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.0634

Width : 2.903

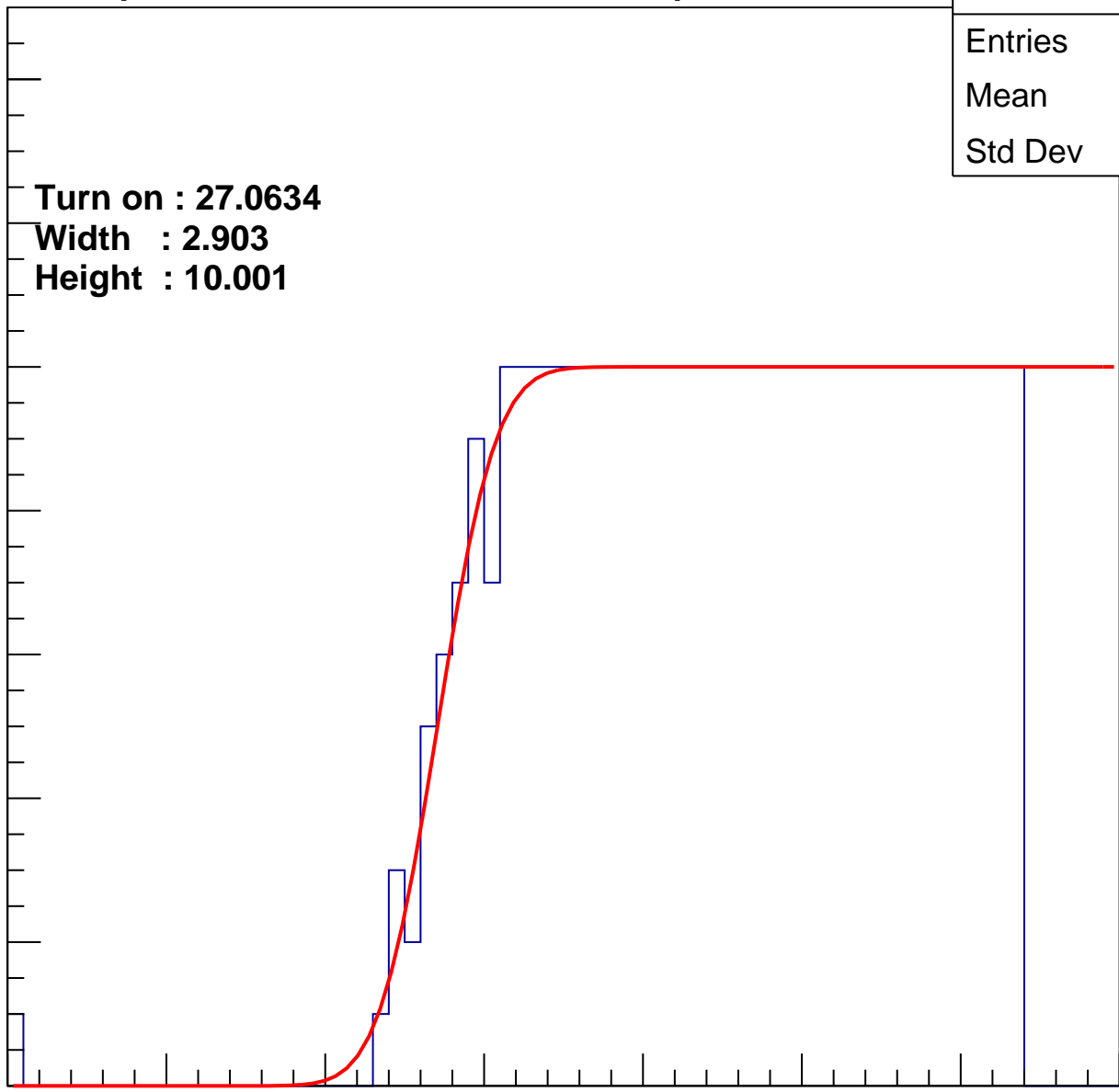
Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U12-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.2
Std Dev	11.18

Turn on : 29.0132

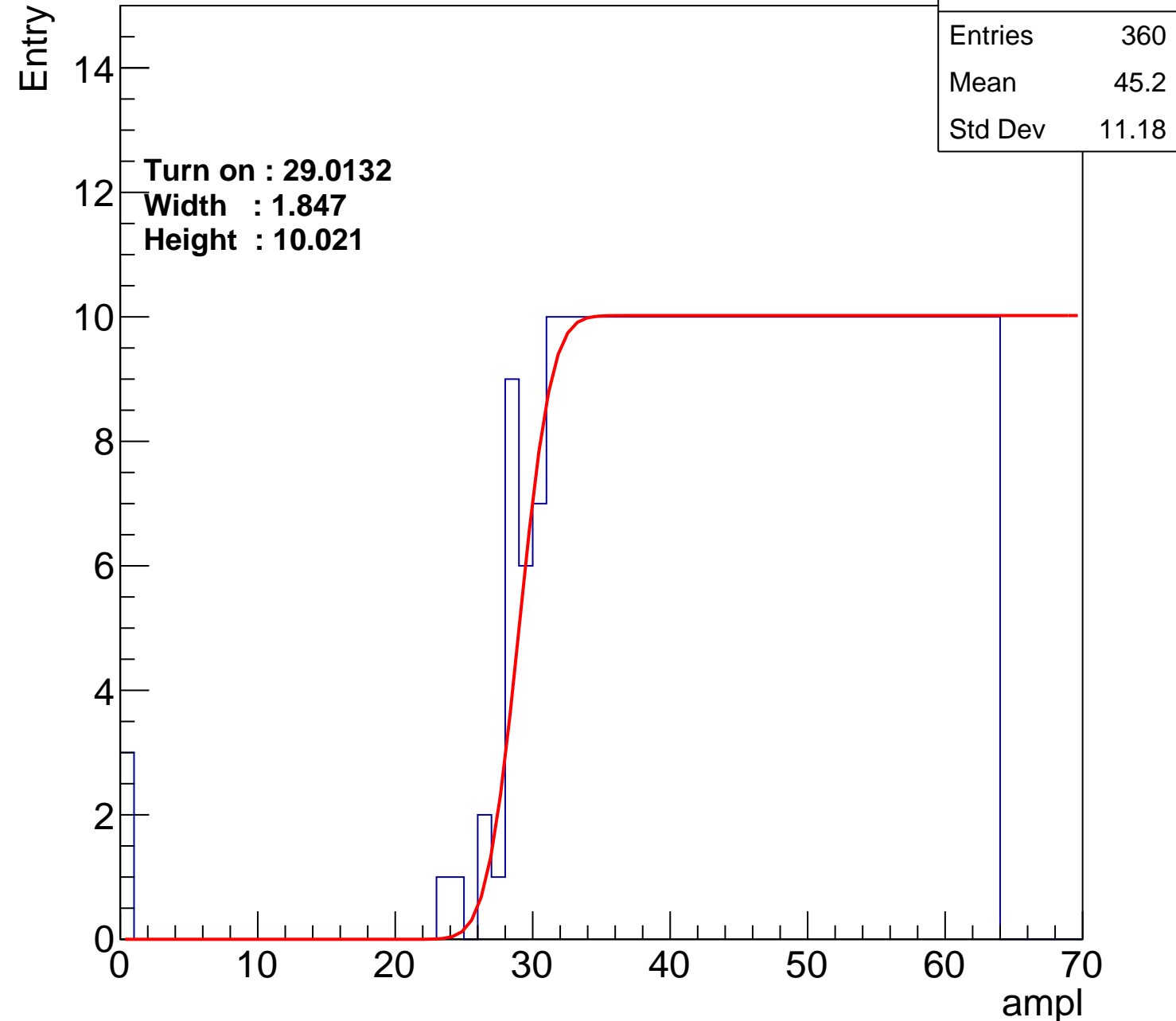
Width : 1.847

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch24

calib_packv5_042523_0143.root, FC#8, port C1

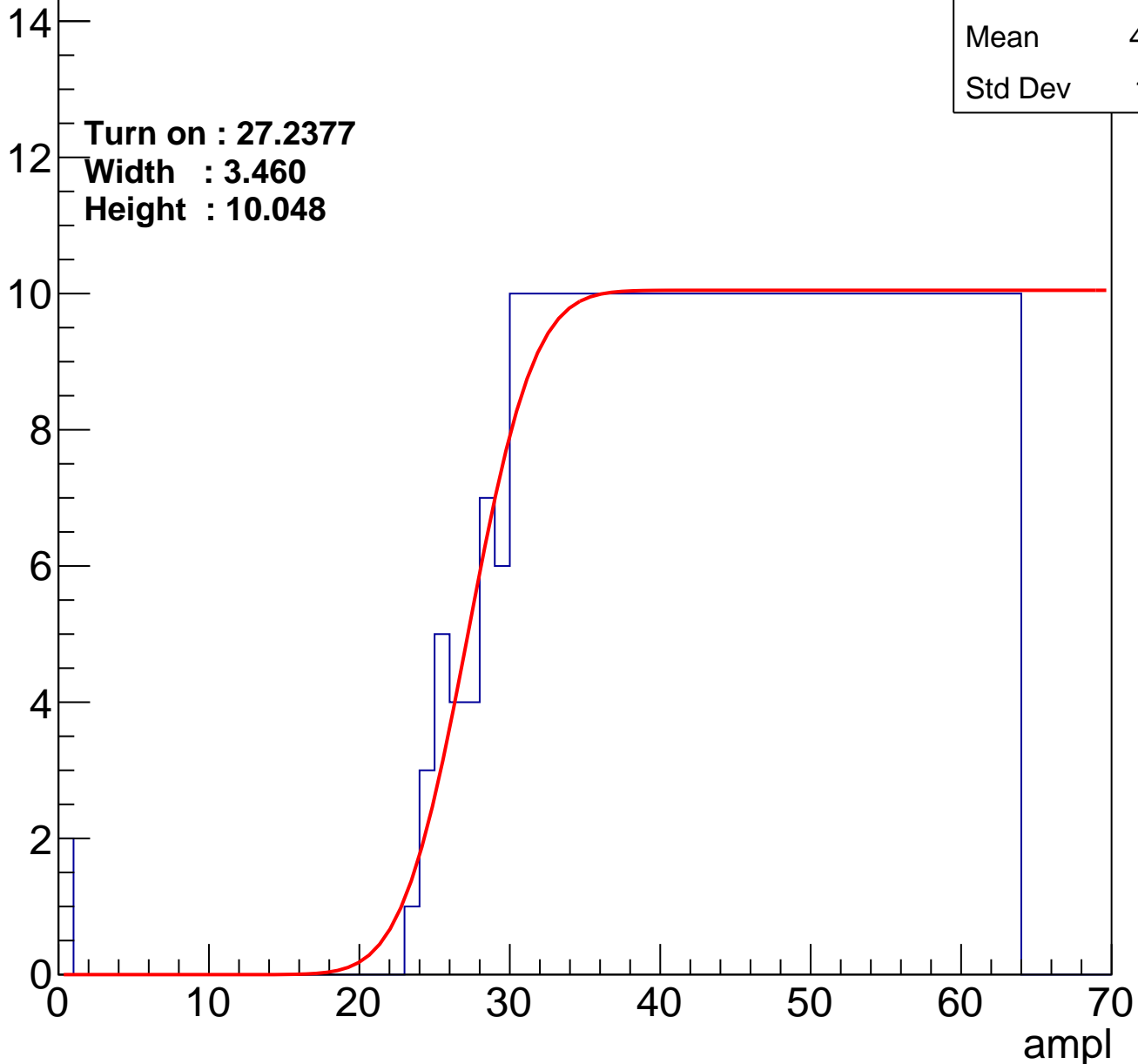
Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 27.2377

Width : 3.460

Height : 10.048

Entry



B0L002S, U12-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.08
Std Dev	11.46

Turn on : 28.7232

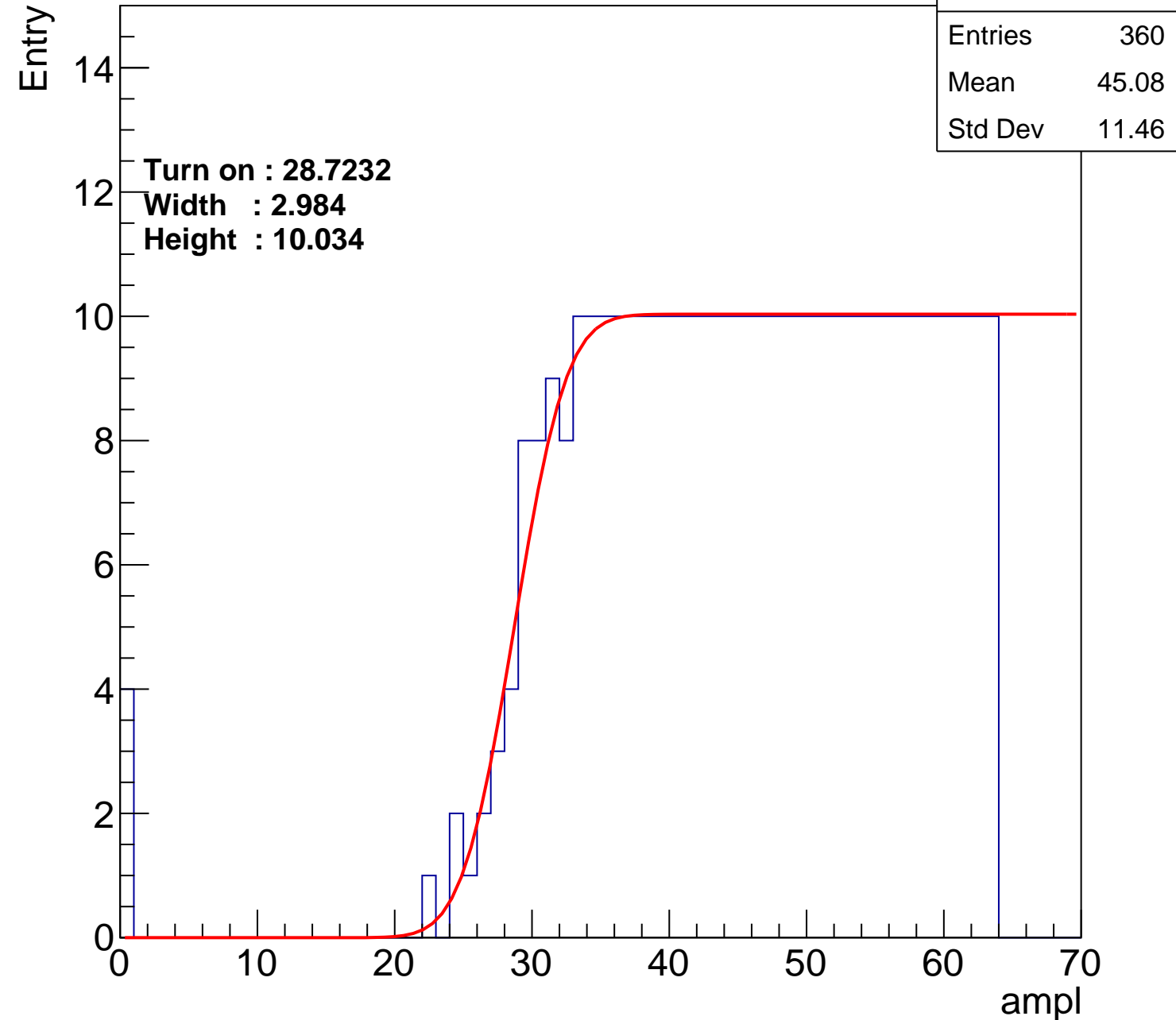
Width : 2.984

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.48
Std Dev	10.7

Turn on : 28.5287

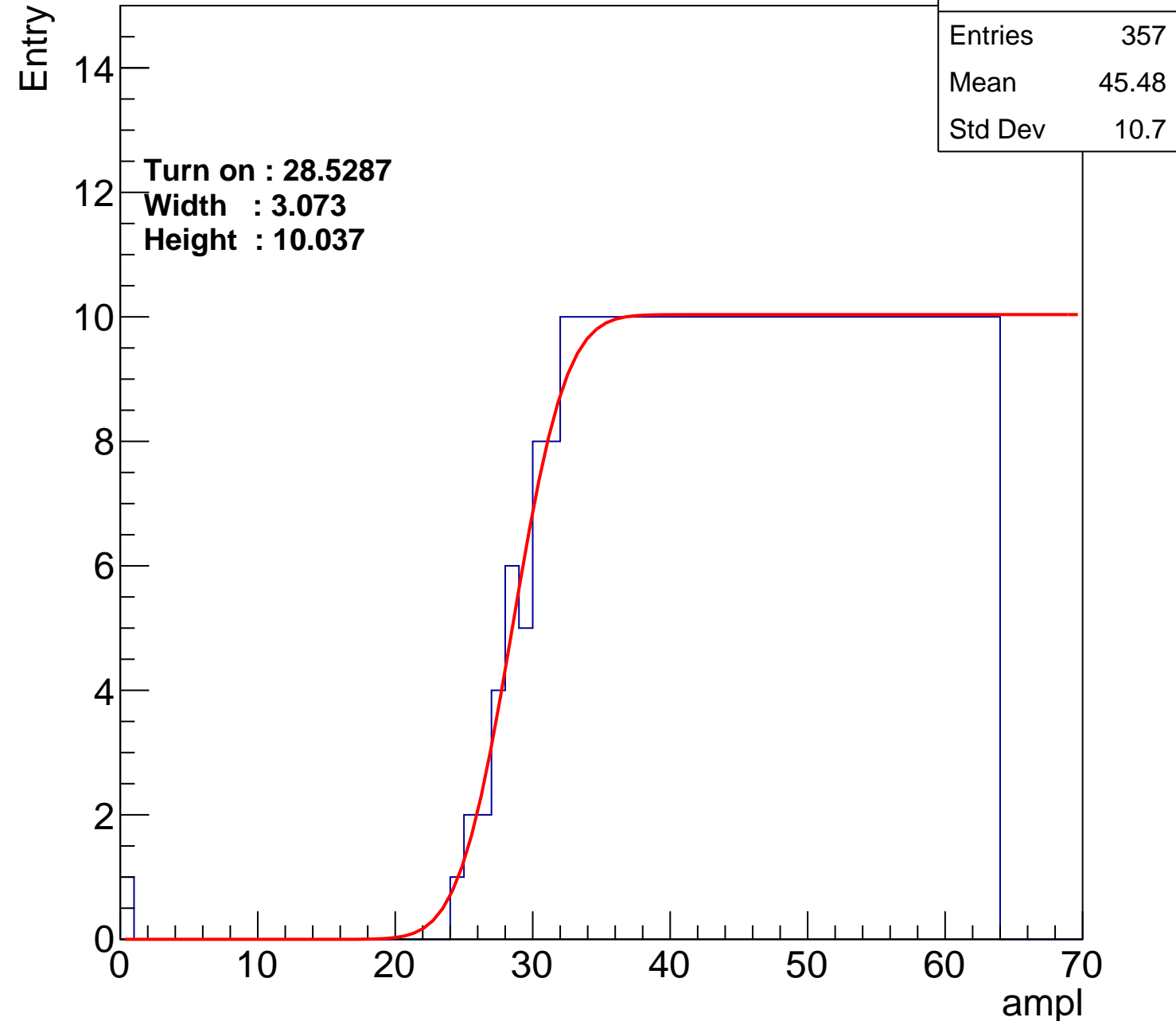
Width : 3.073

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch27

calib_packv5_042523_0143.root, FC#8, port C1

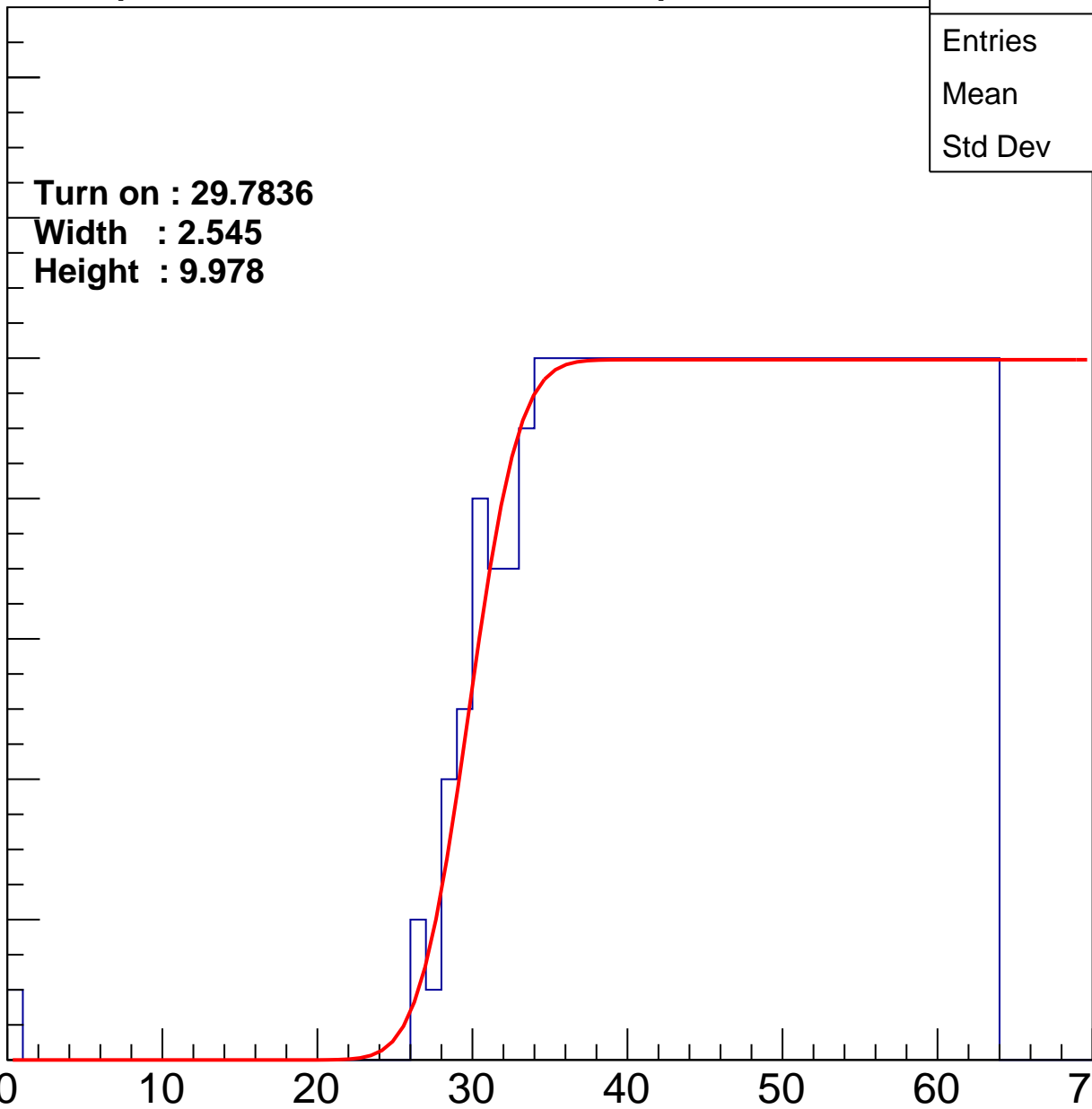
Entry

14
12
10
8
6
4
2
0

Turn on : 29.7836
Width : 2.545
Height : 9.978

Entries	344
Mean	46.12
Std Dev	10.35

ampl



B0L002S, U12-ch28

calib_packv5_042523_0143.root, FC#8, port C1

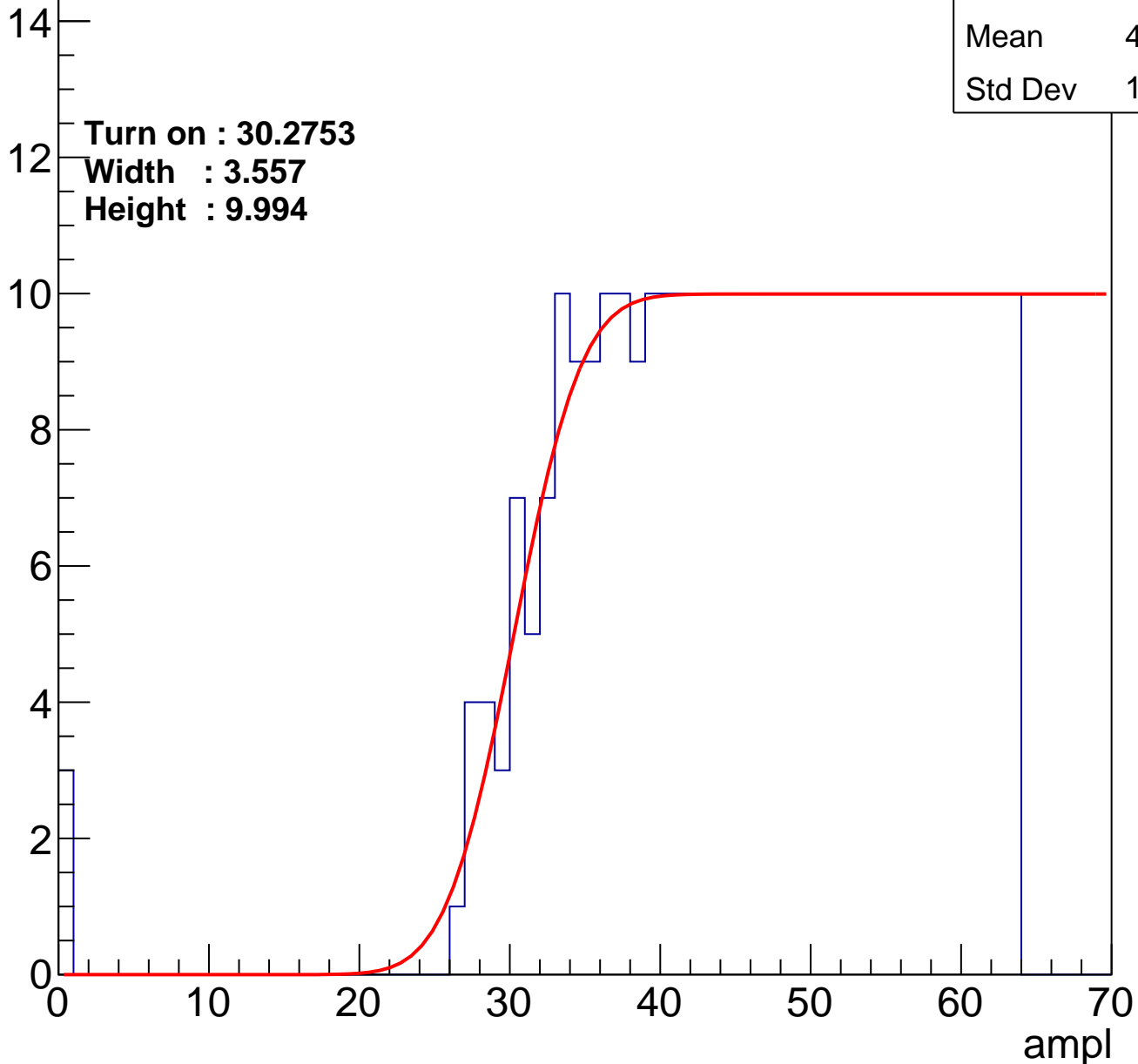
Entries	341
Mean	46.03
Std Dev	10.88

Turn on : 30.2753

Width : 3.557

Height : 9.994

Entry



B0L002S, U12-ch29

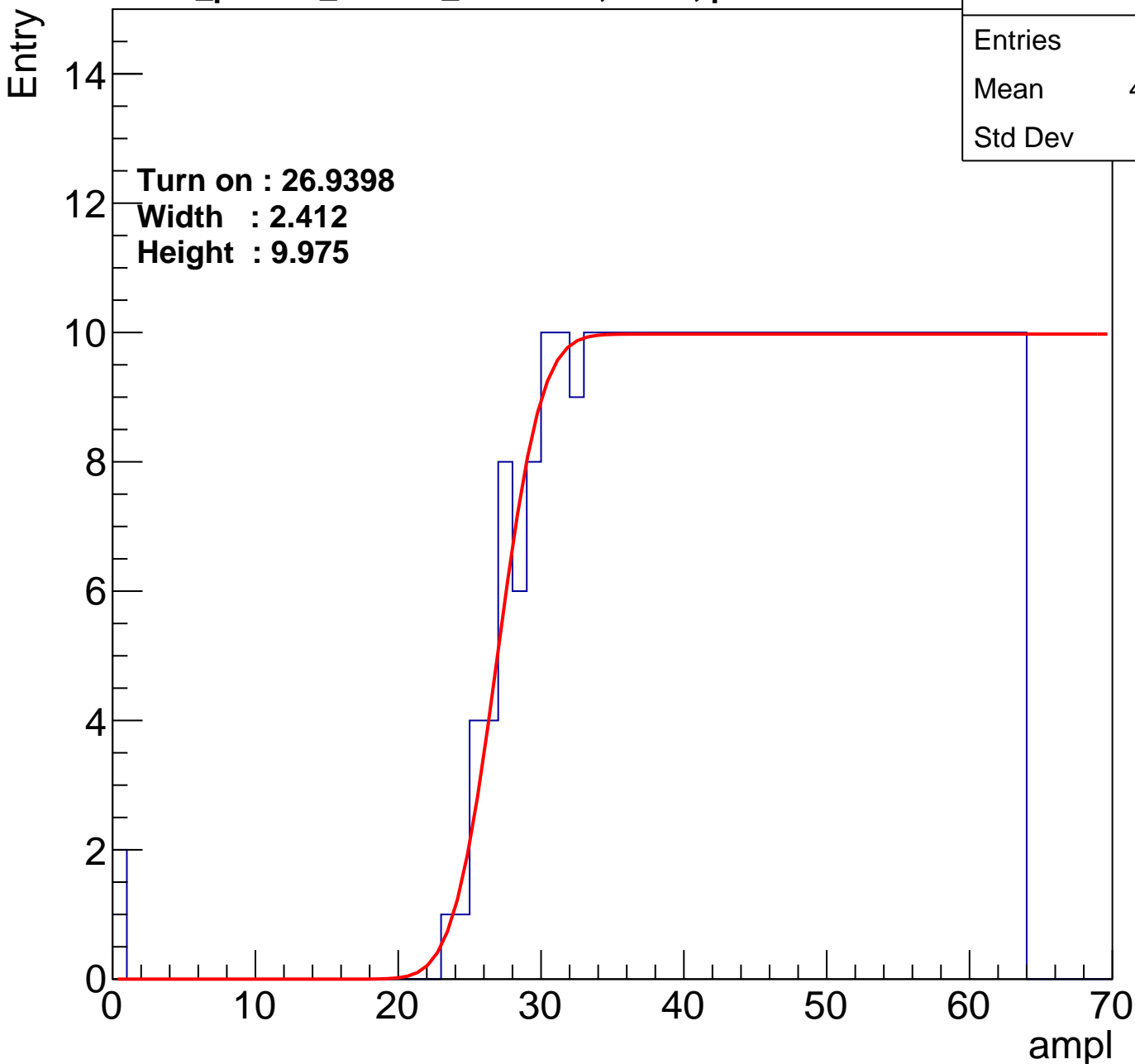
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.62
Std Dev	11.3

Turn on : 26.9398

Width : 2.412

Height : 9.975



B0L002S, U12-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.3855

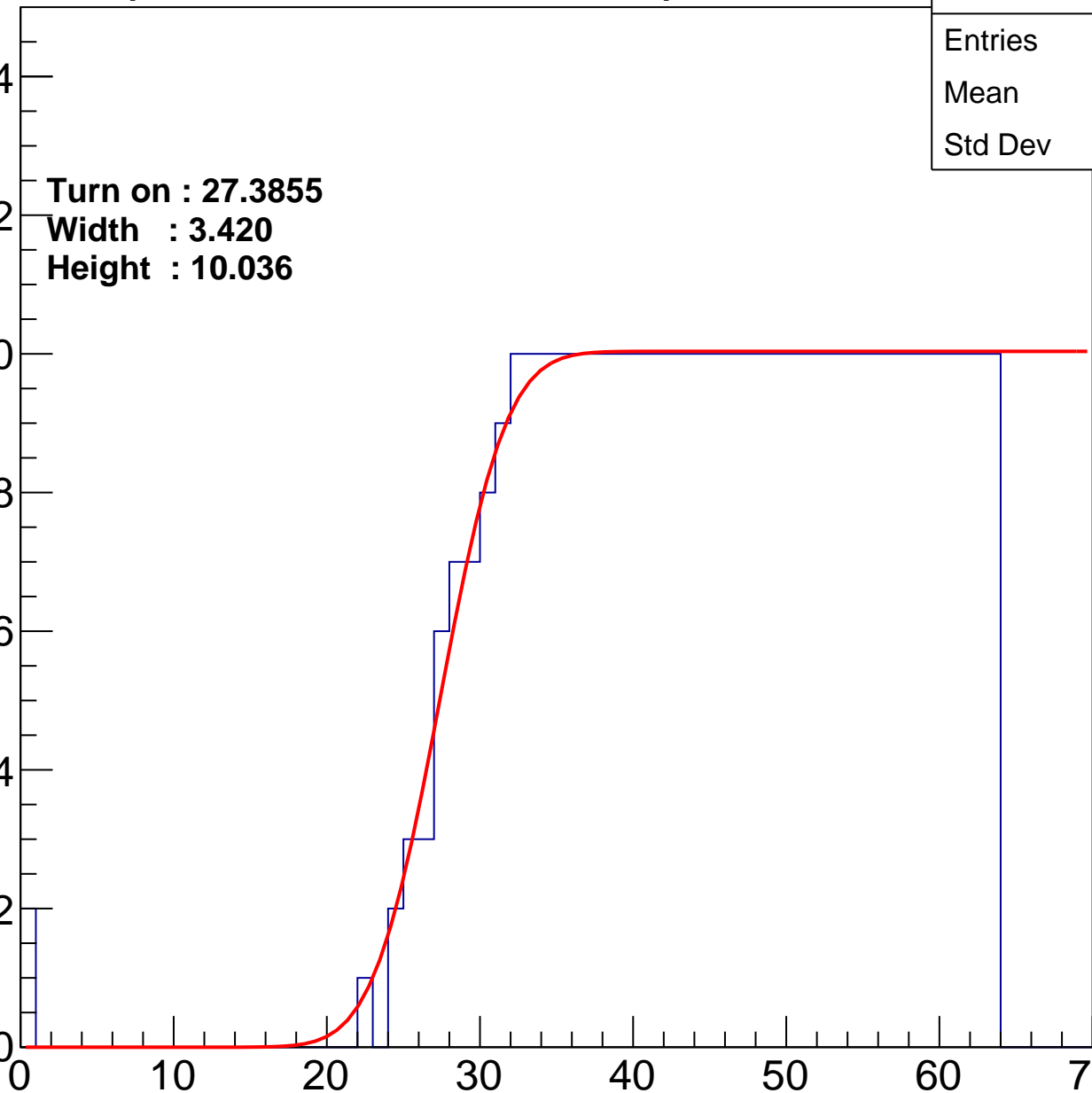
Width : 3.420

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch31

calib_packv5_042523_0143.root, FC#8, port C1

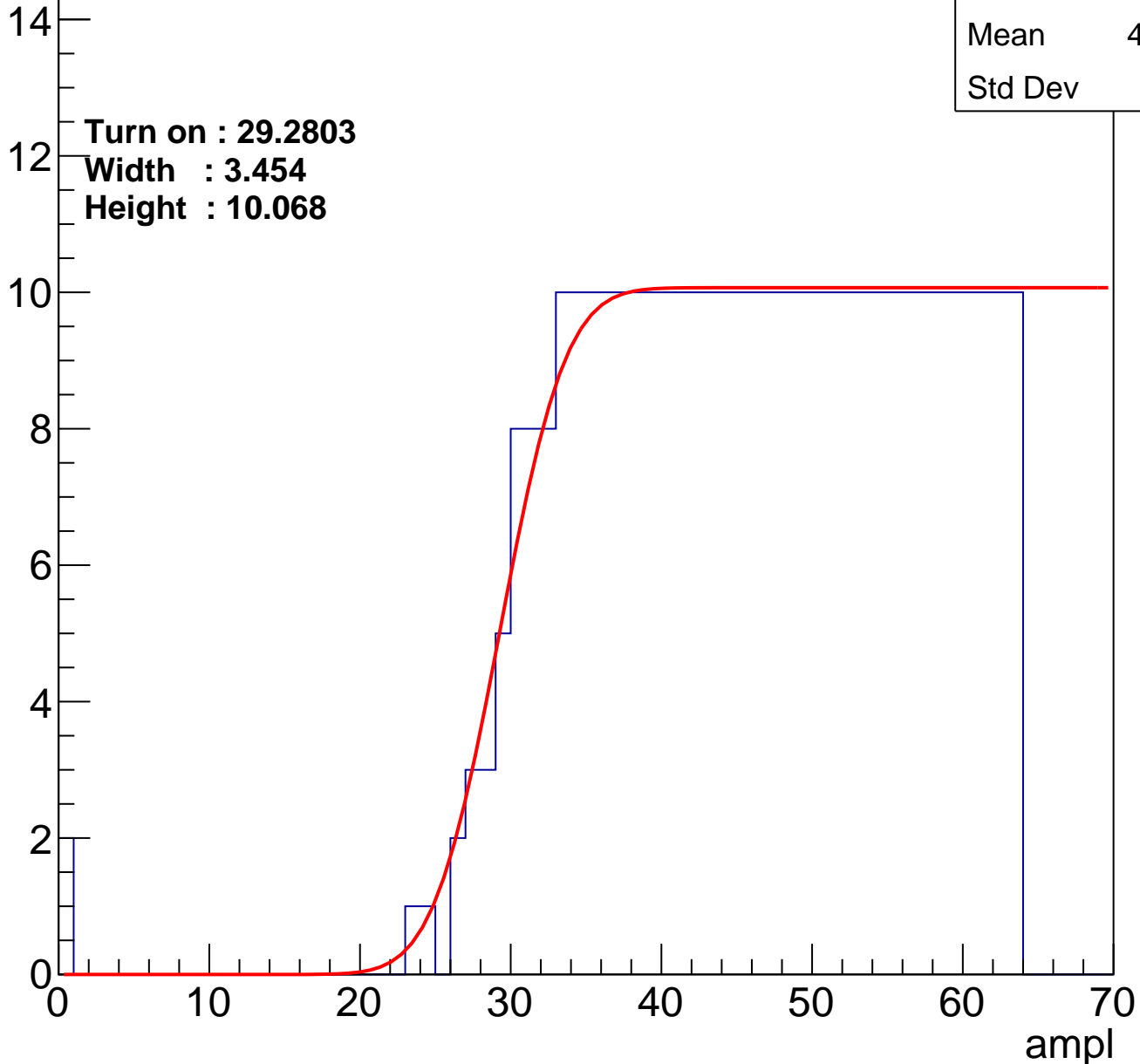
Entries	351
Mean	45.68
Std Dev	10.8

Turn on : 29.2803

Width : 3.454

Height : 10.068

Entry



B0L002S, U12-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.33
Std Dev	11.38

Turn on : 29.0101

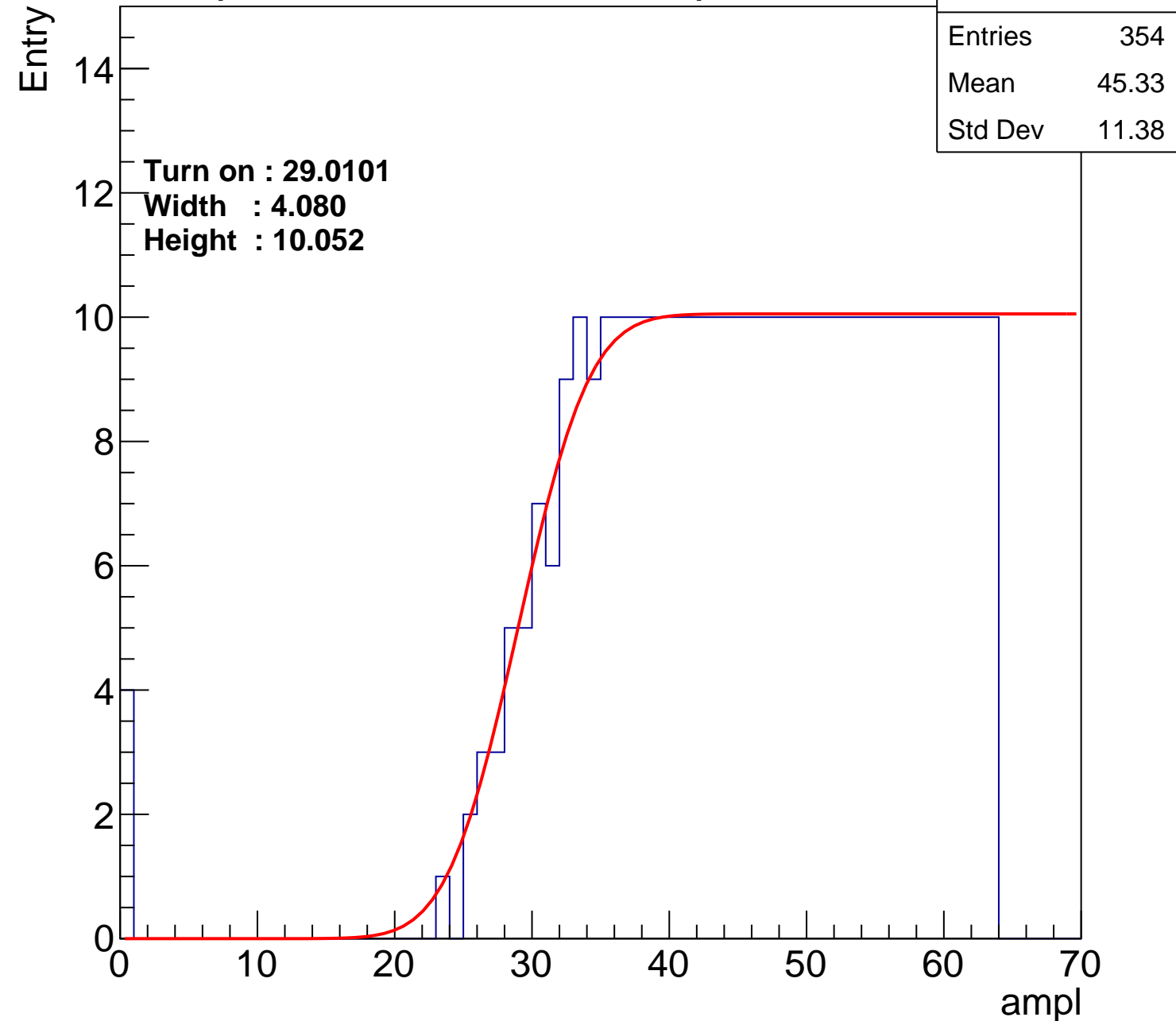
Width : 4.080

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.8602

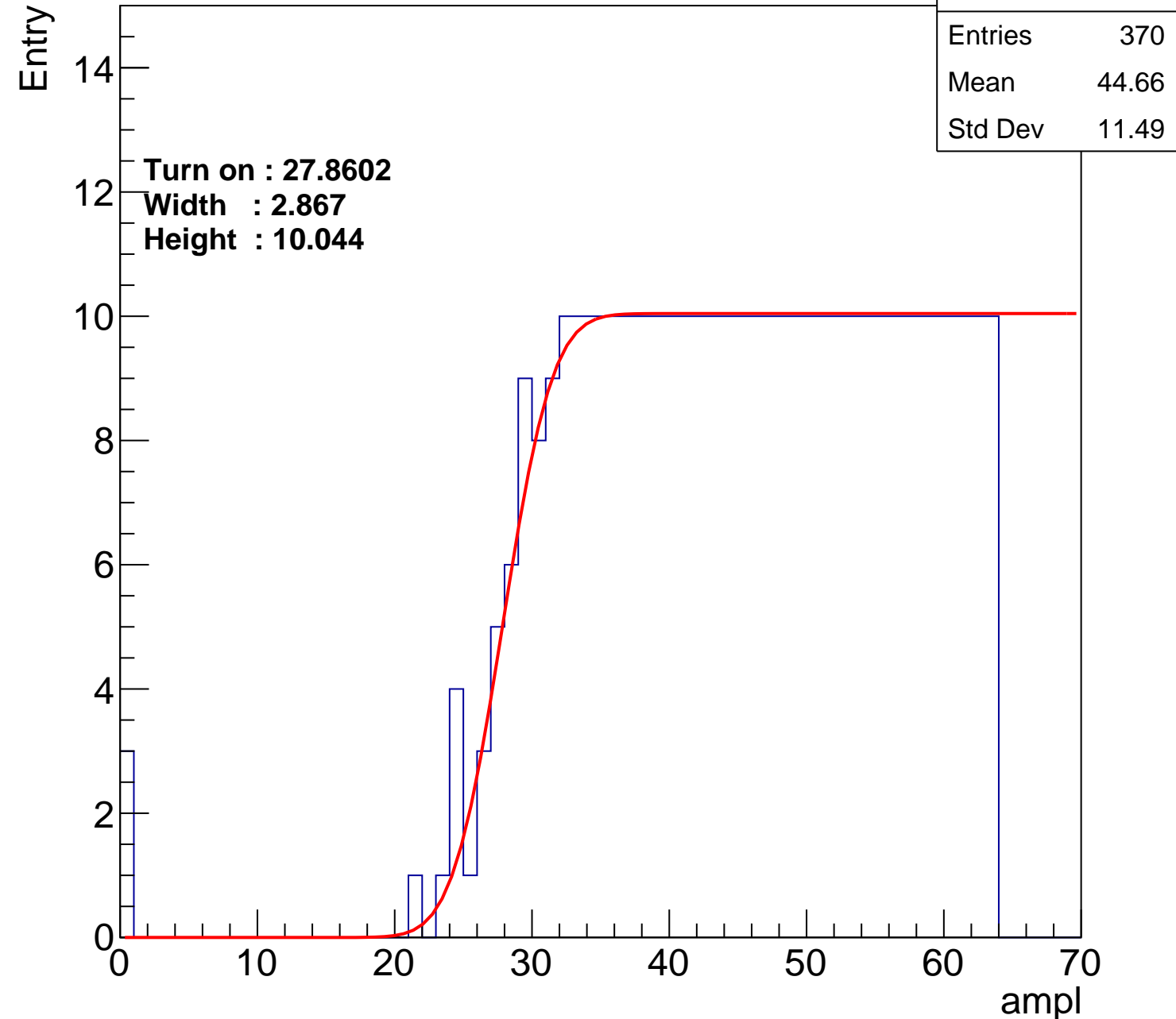
Width : 2.867

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.22
Std Dev	10.99

Turn on : 28.2393

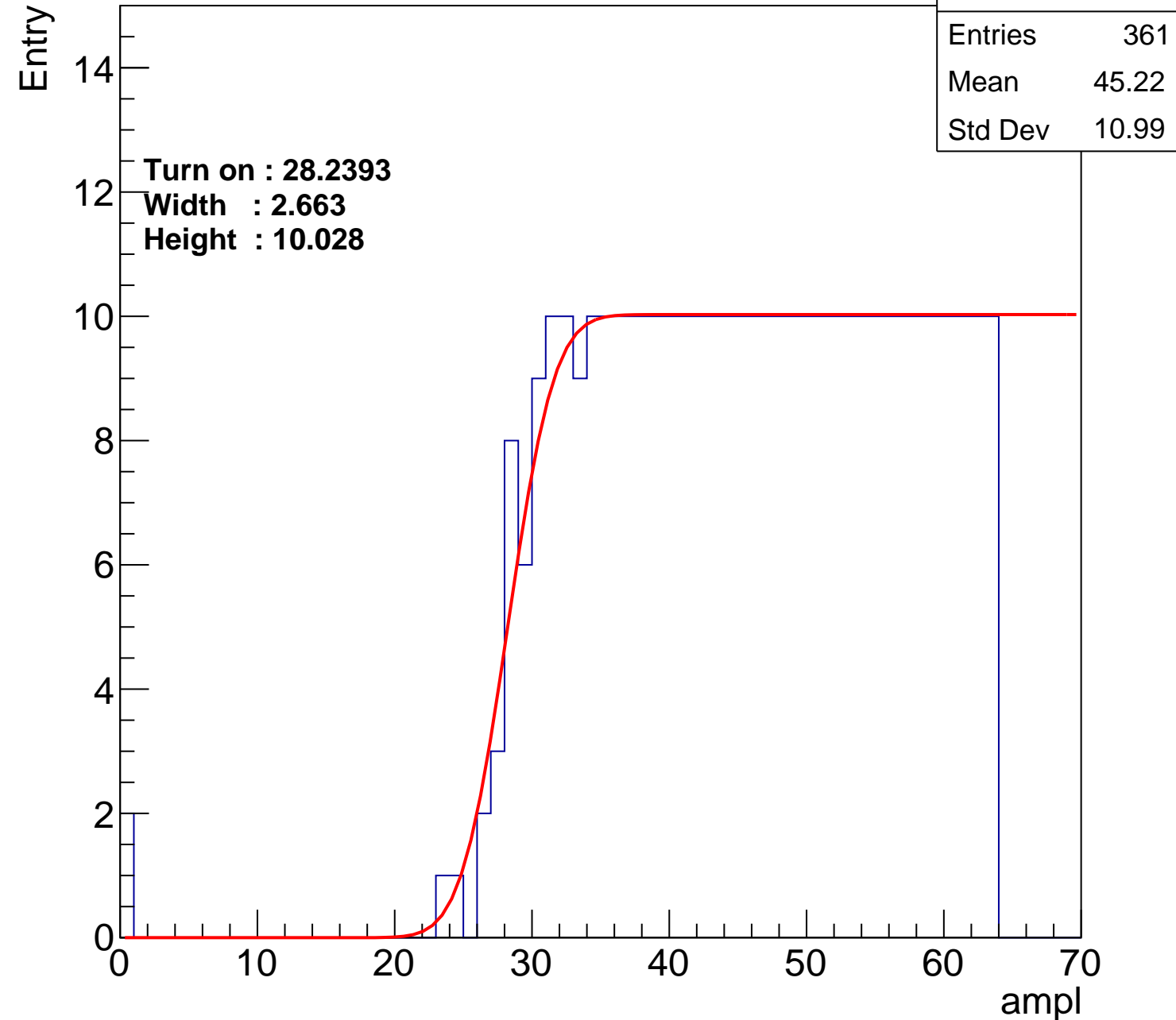
Width : 2.663

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.08
Std Dev	11.12

Turn on : 28.0349

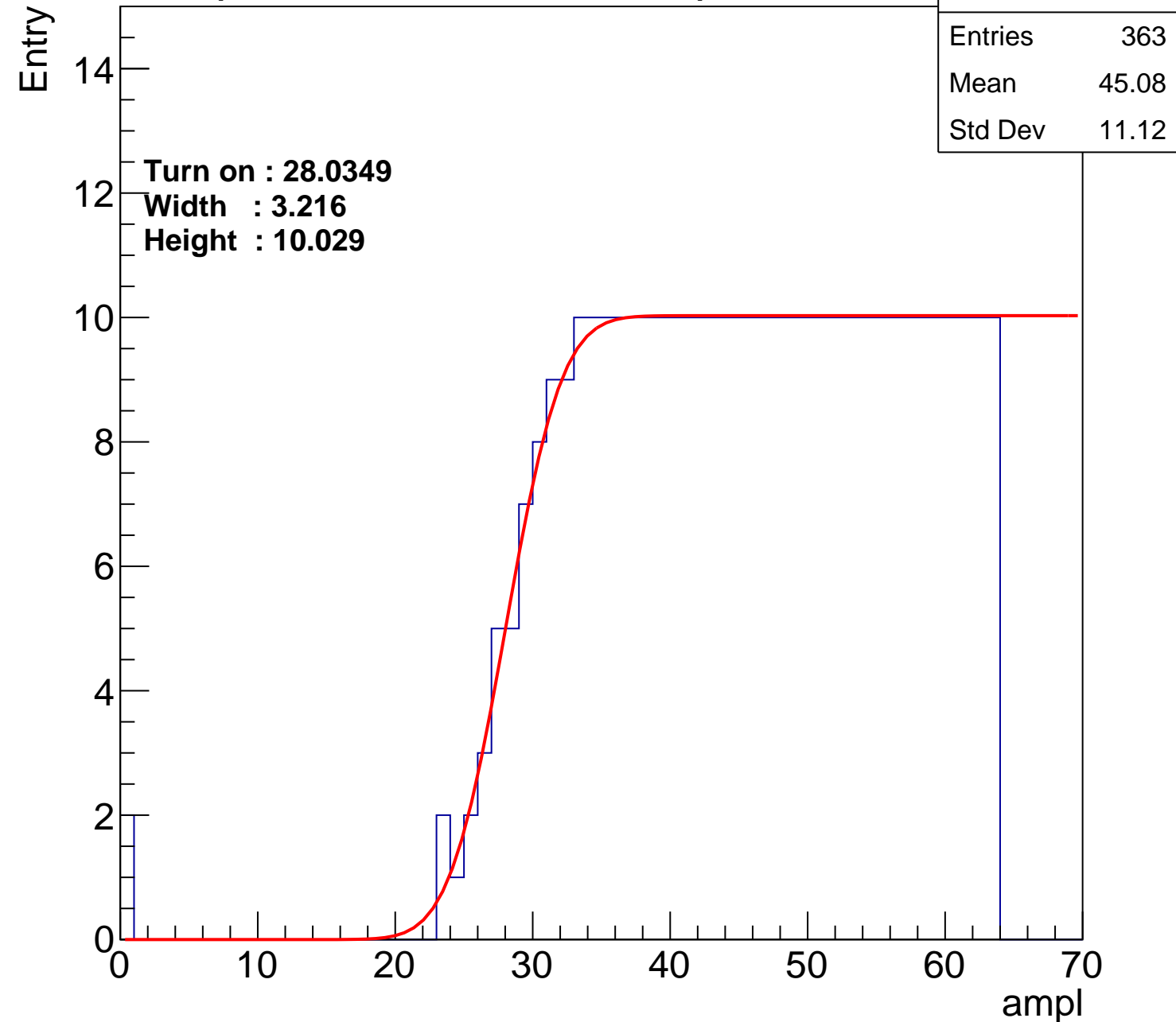
Width : 3.216

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch36

calib_packv5_042523_0143.root, FC#8, port C1

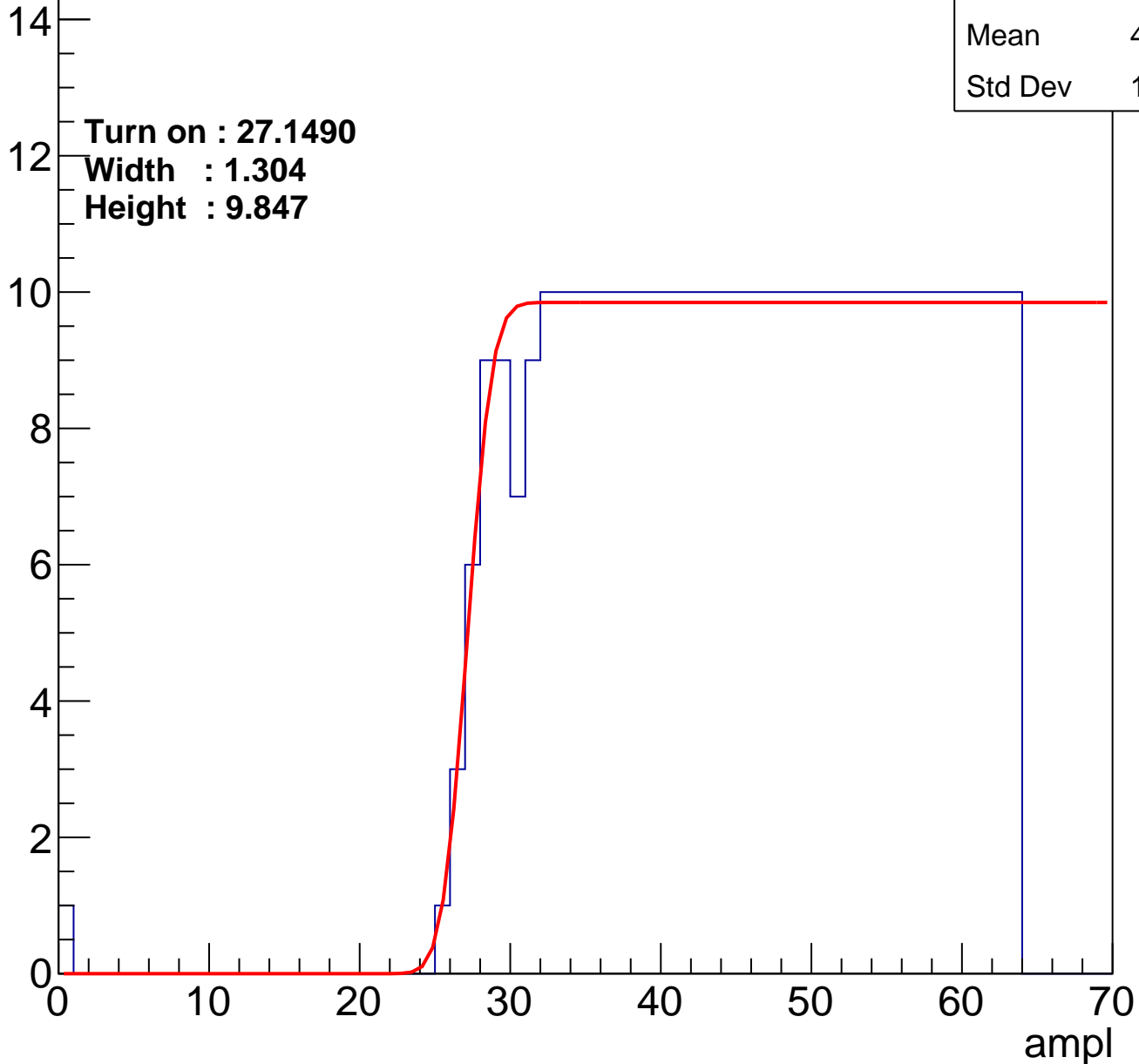
Entries	365
Mean	45.12
Std Dev	10.85

Turn on : 27.1490

Width : 1.304

Height : 9.847

Entry



B0L002S, U12-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.41
Std Dev	10.71

Turn on : 28.4043

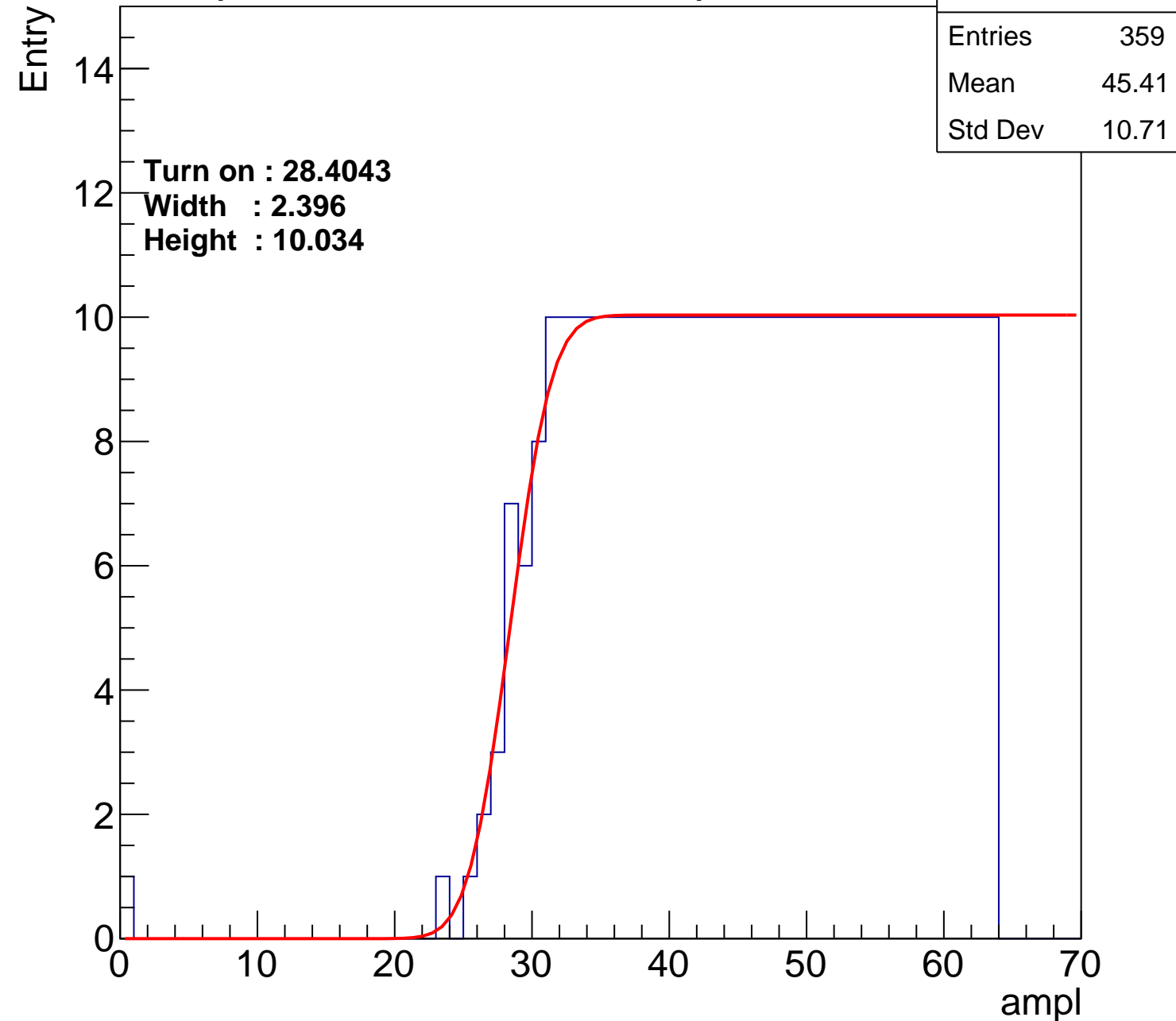
Width : 2.396

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.33
Std Dev	10.69

Turn on : 30.3713

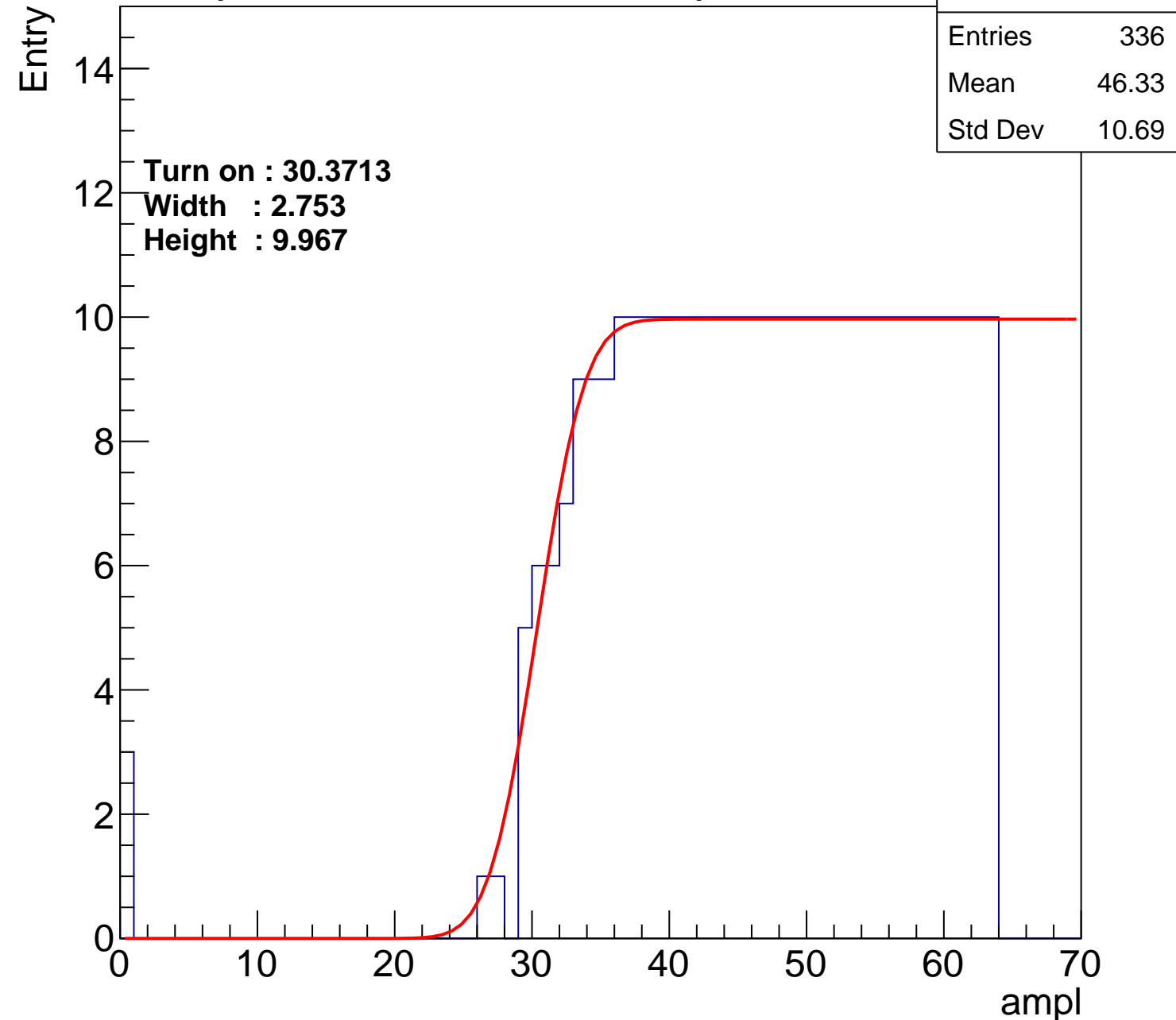
Width : 2.753

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch39

calib_packv5_042523_0143.root, FC#8, port C1

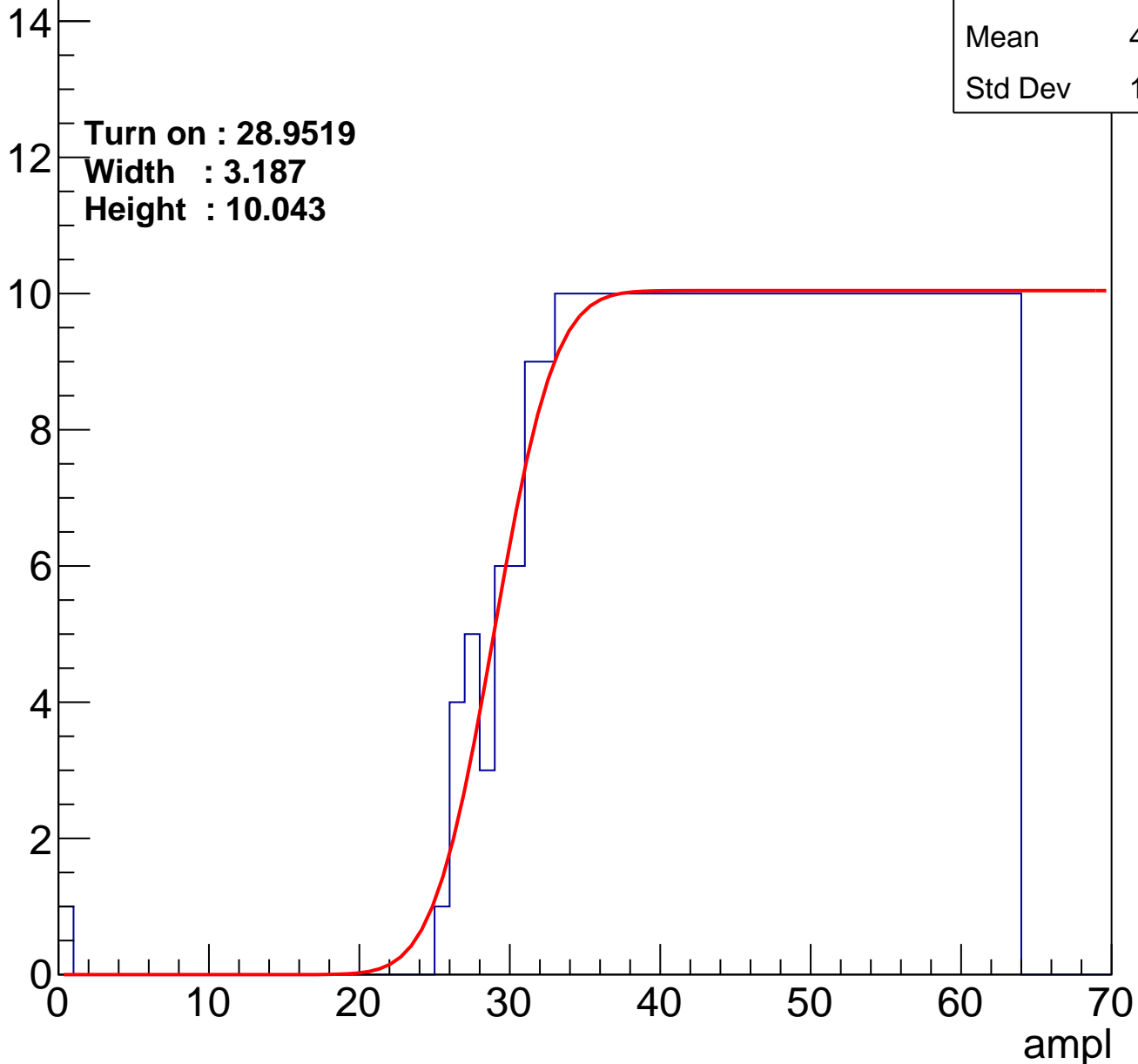
Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 28.9519

Width : 3.187

Height : 10.043

Entry



B0L002S, U12-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 27.7202

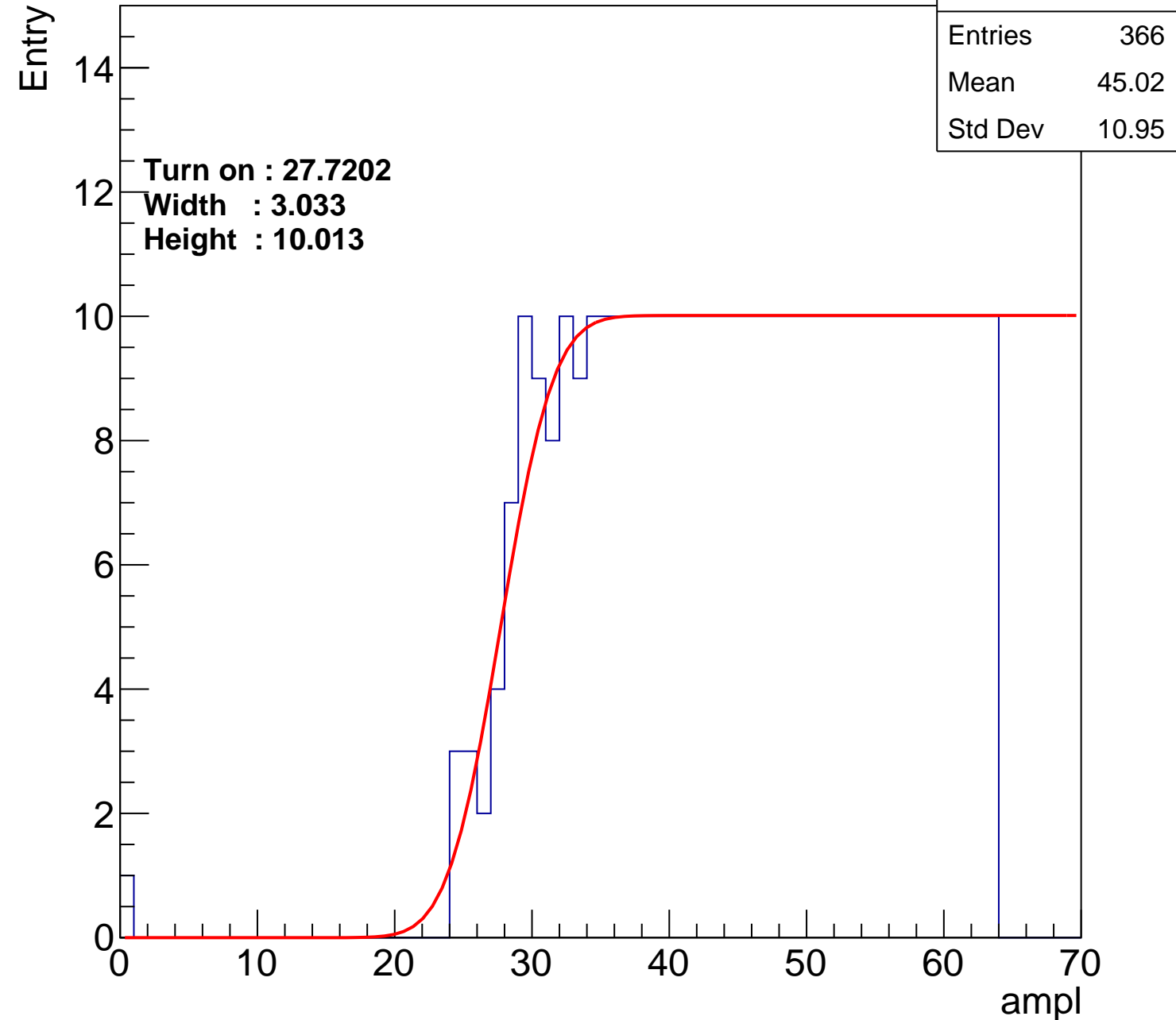
Width : 3.033

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.27
Std Dev	10.77

Turn on : 28.1996

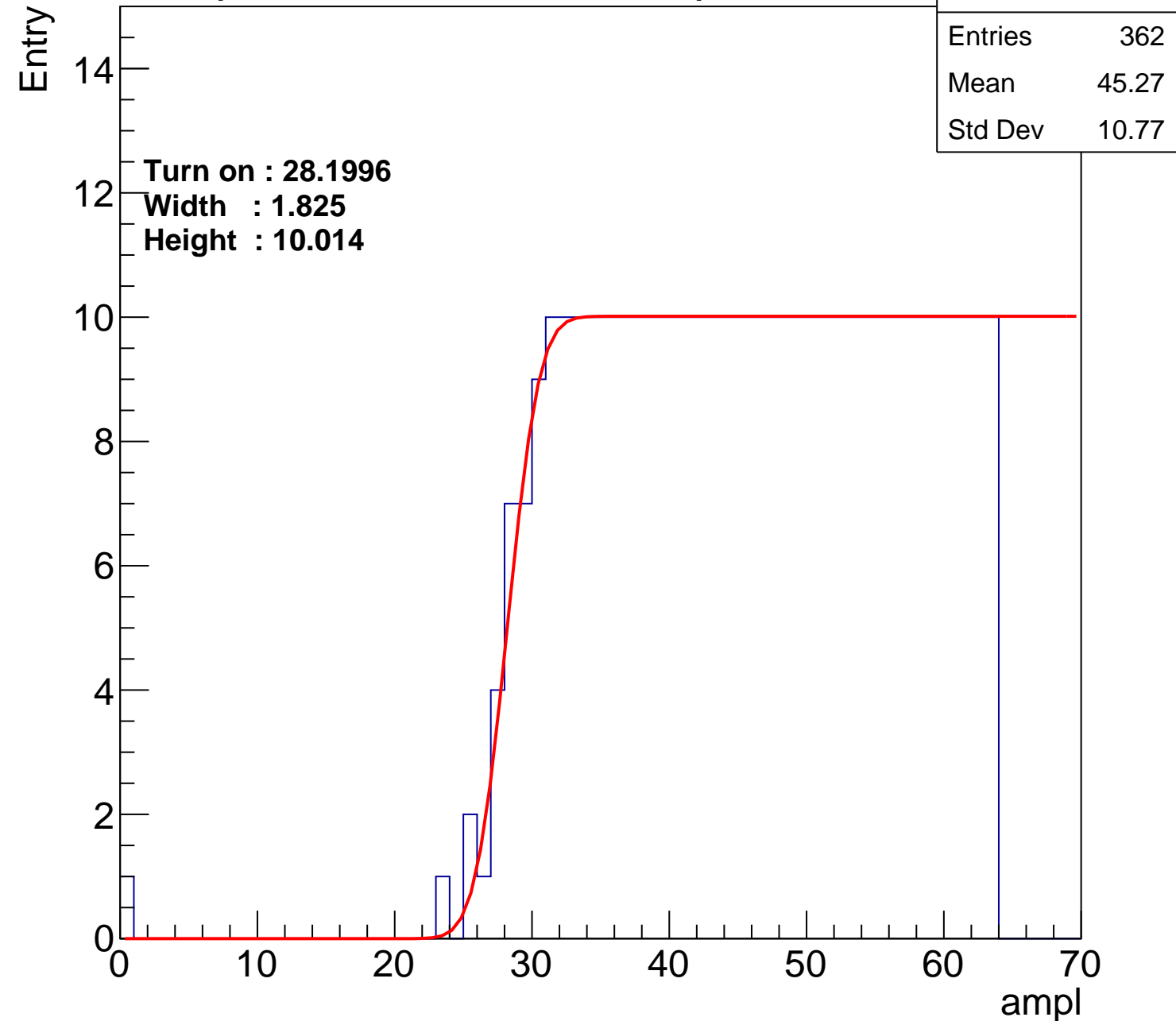
Width : 1.825

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.31
Std Dev	10.95

Turn on : 28.5970

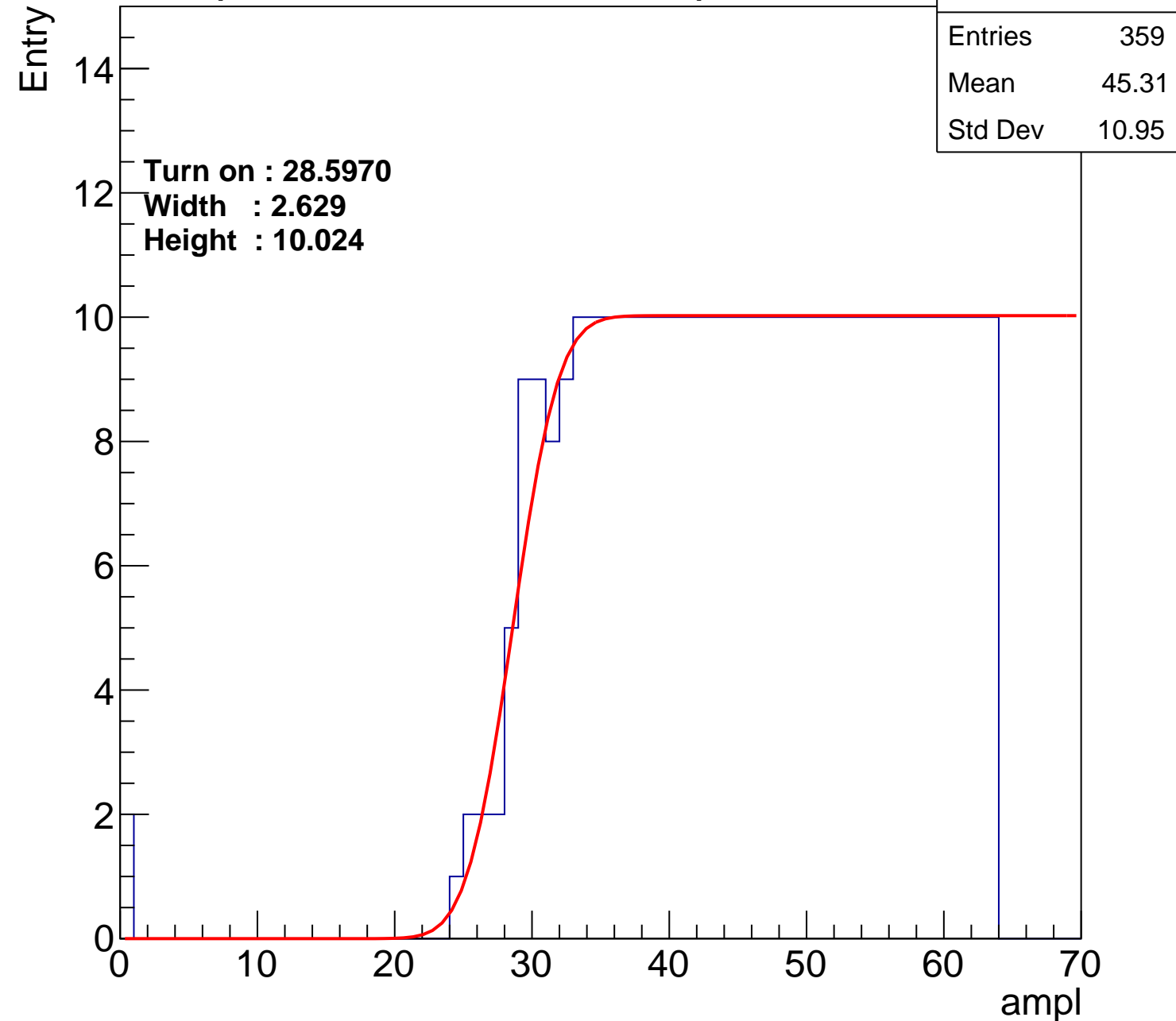
Width : 2.629

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.1524
Width : 3.312
Height : 9.975



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.5227
Width : 2.598
Height : 10.003



B0L002S, U12-ch45

calib_packv5_042523_0143.root, FC#8, port C1

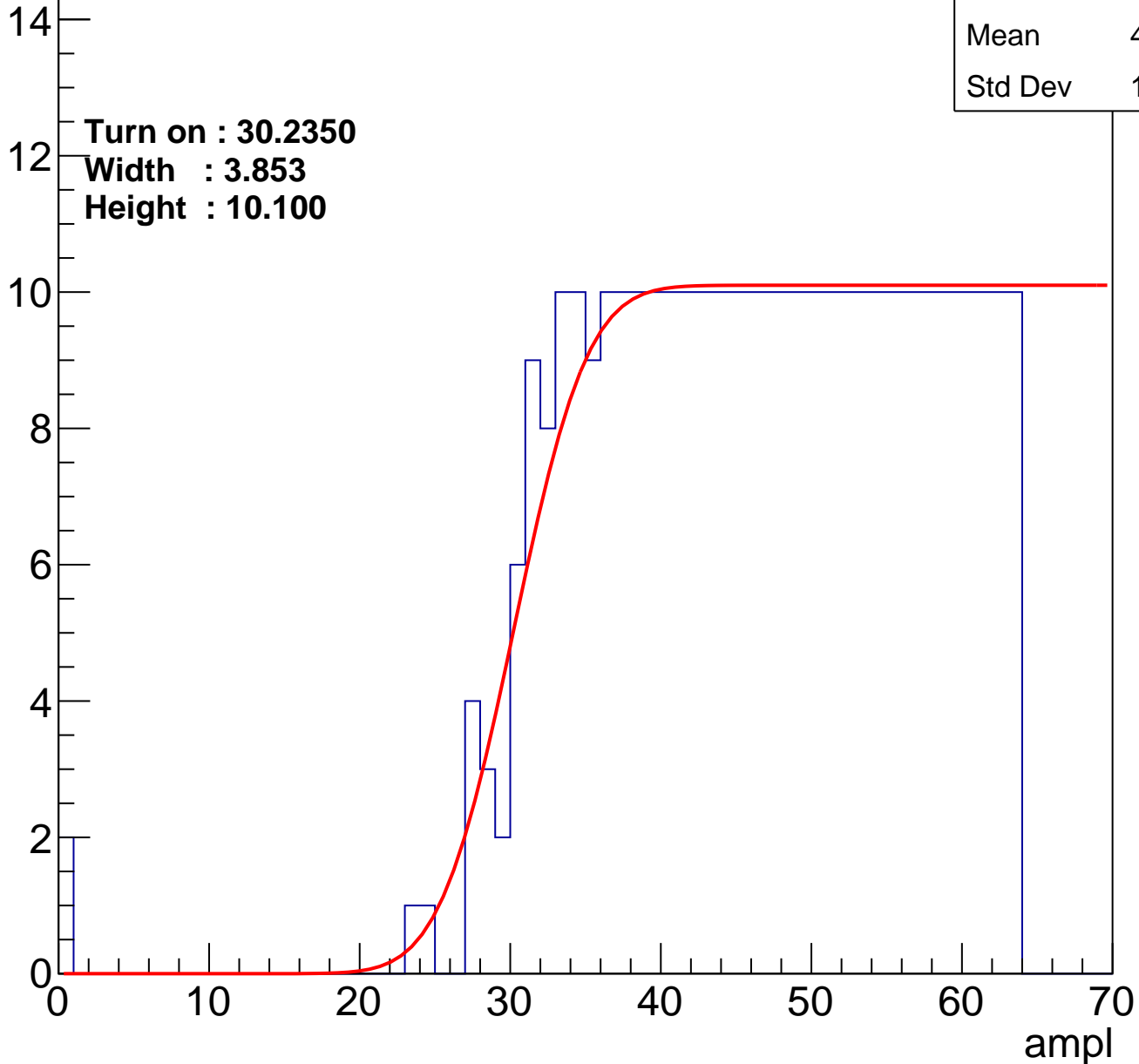
Entries	345
Mean	45.96
Std Dev	10.67

Turn on : 30.2350

Width : 3.853

Height : 10.100

Entry



B0L002S, U12-ch46

calib_packv5_042523_0143.root, FC#8, port C1

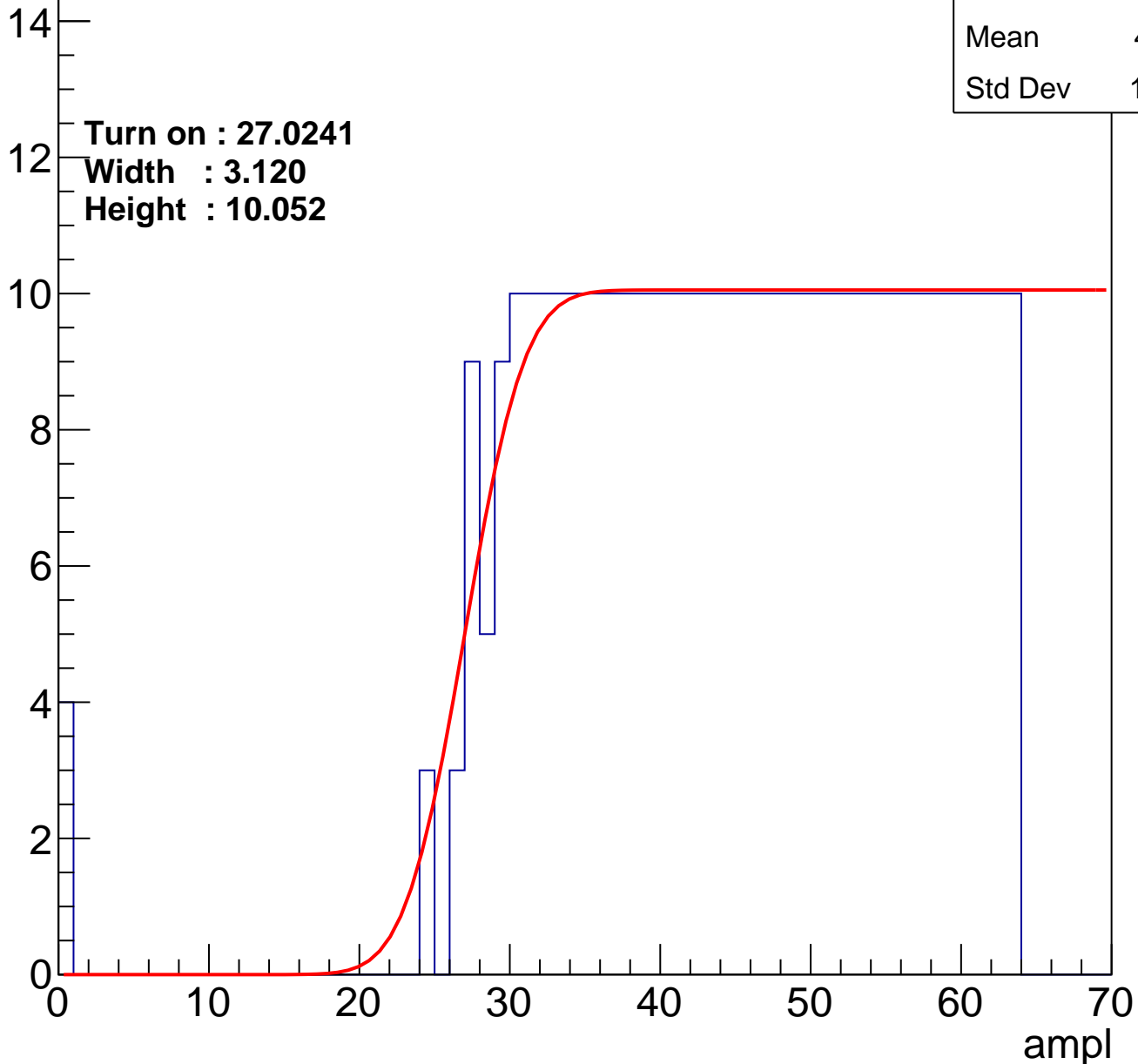
Entries	373
Mean	44.51
Std Dev	11.64

Turn on : 27.0241

Width : 3.120

Height : 10.052

Entry



B0L002S, U12-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.17
Std Dev	11.07

Turn on : 28.2628

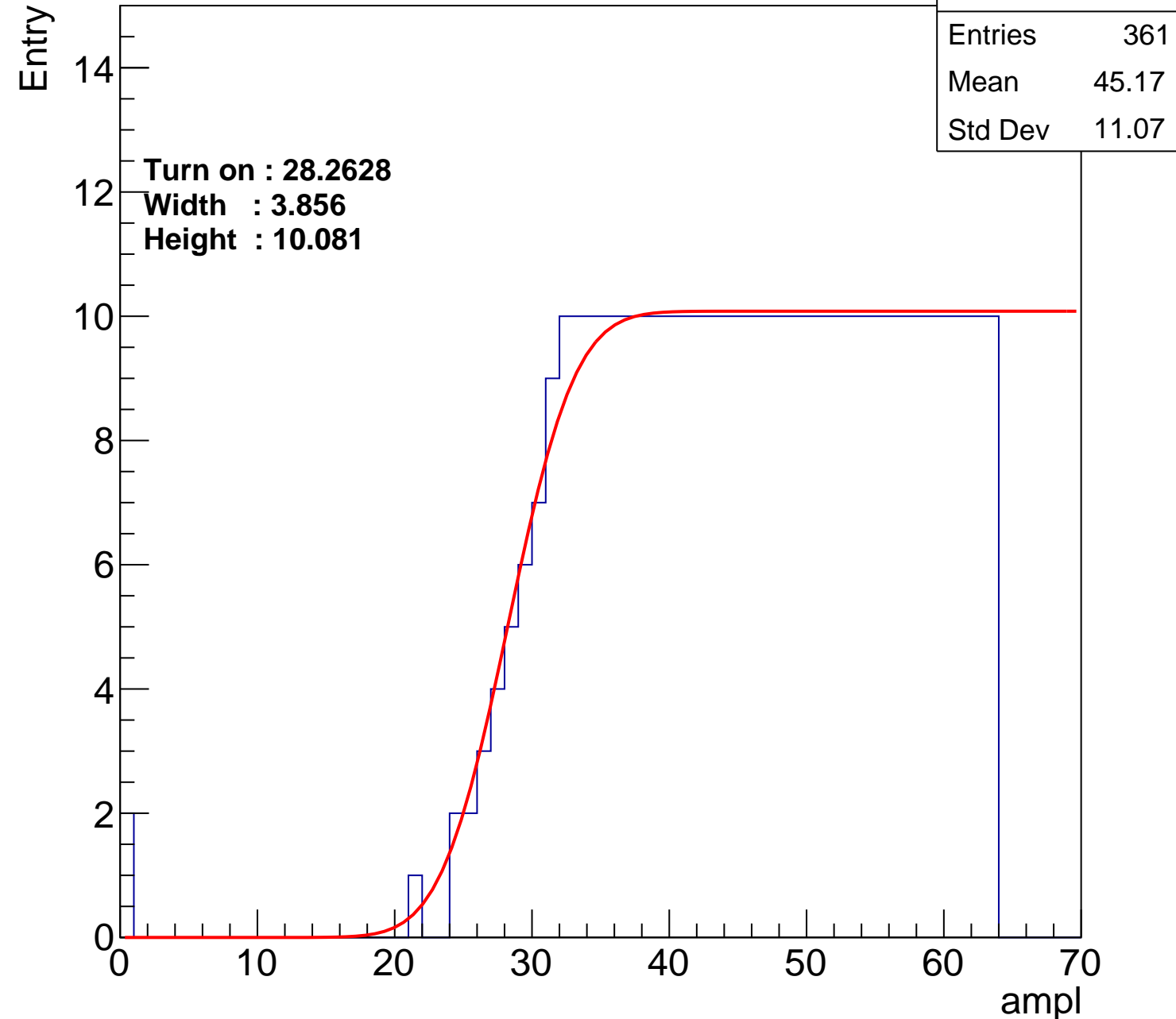
Width : 3.856

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.78
Std Dev	10.53

Turn on : 28.3191

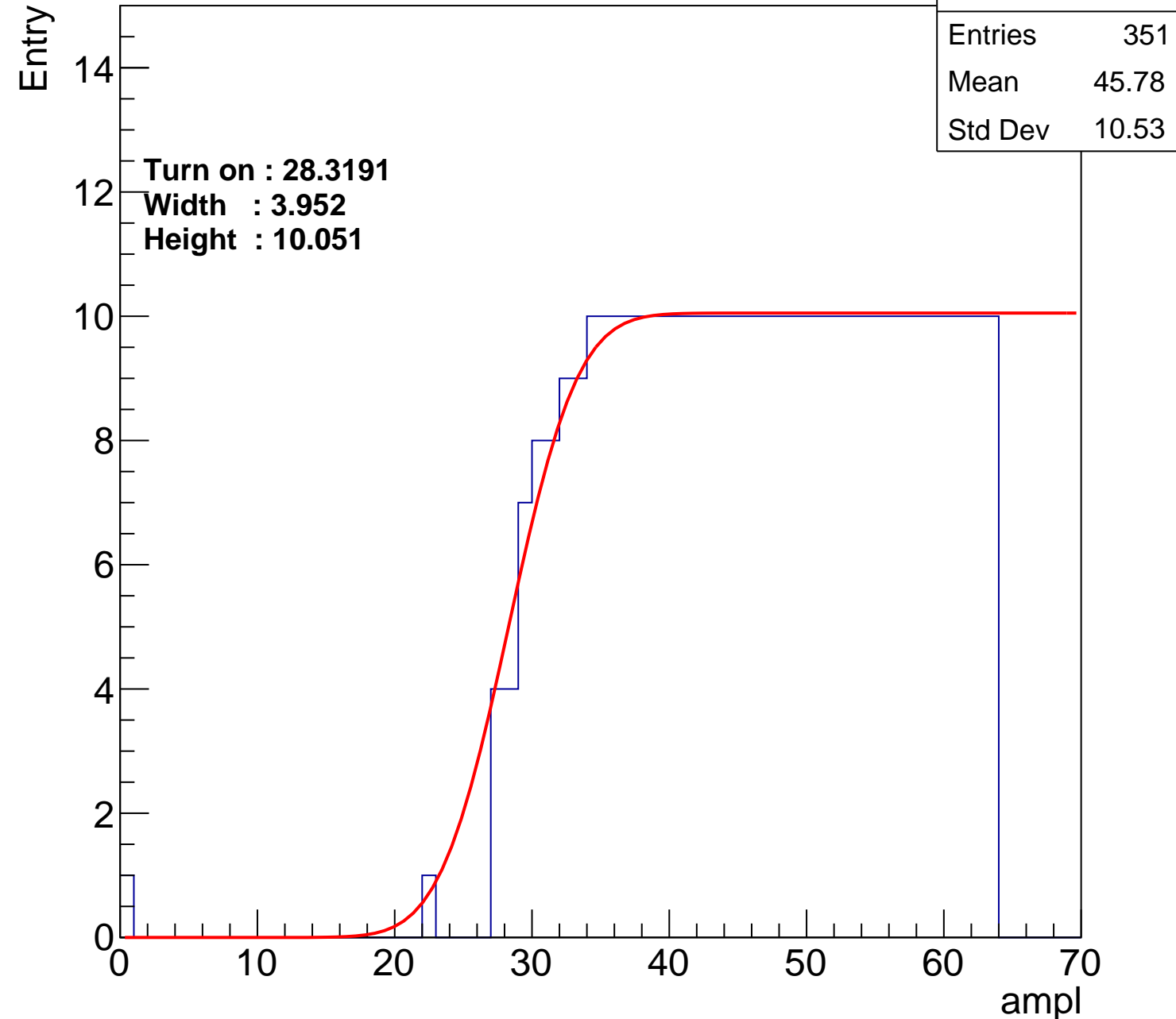
Width : 3.952

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.17
Std Dev	10.61

Turn on : 30.5890

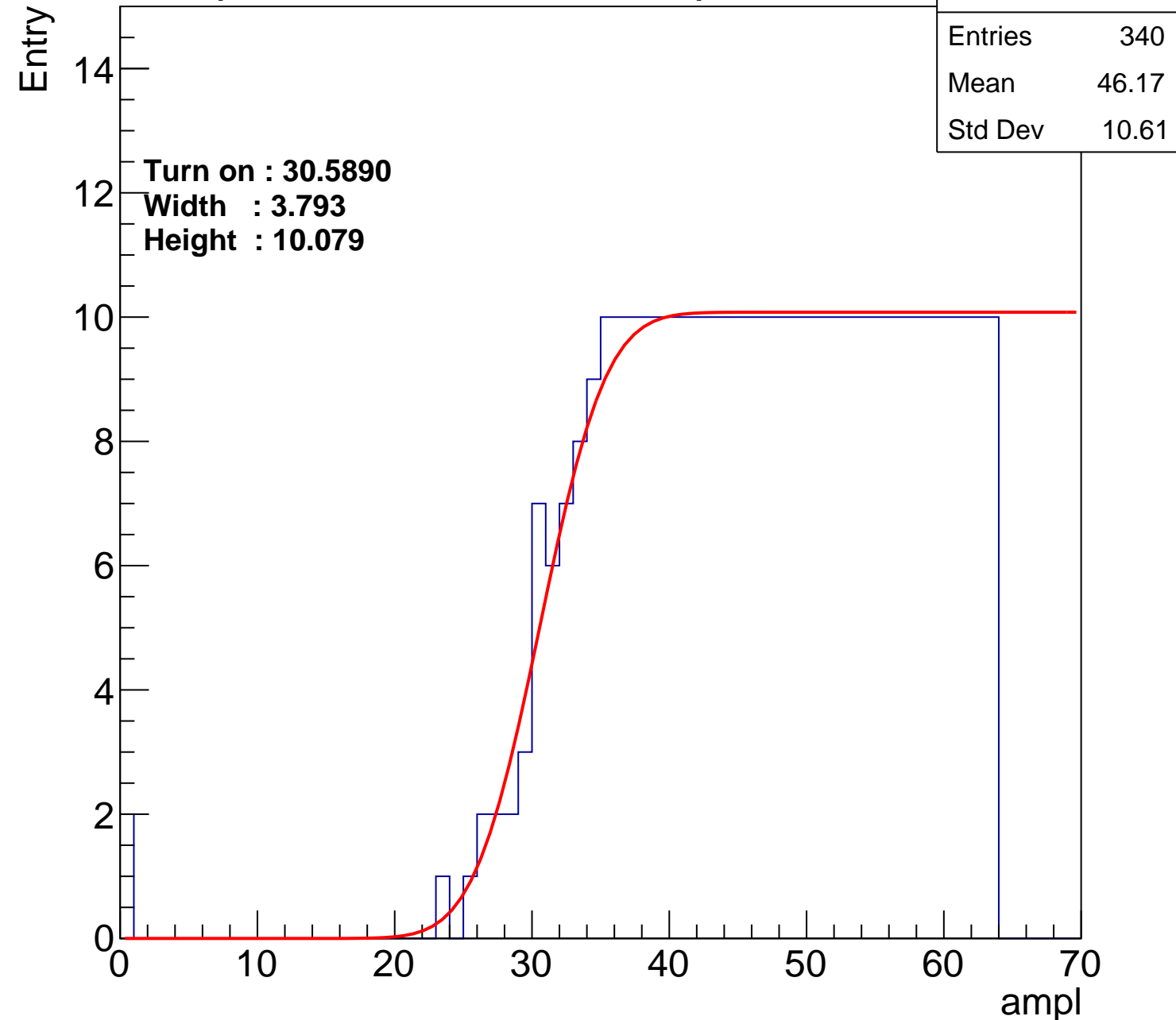
Width : 3.793

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch50

calib_packv5_042523_0143.root, FC#8, port C1

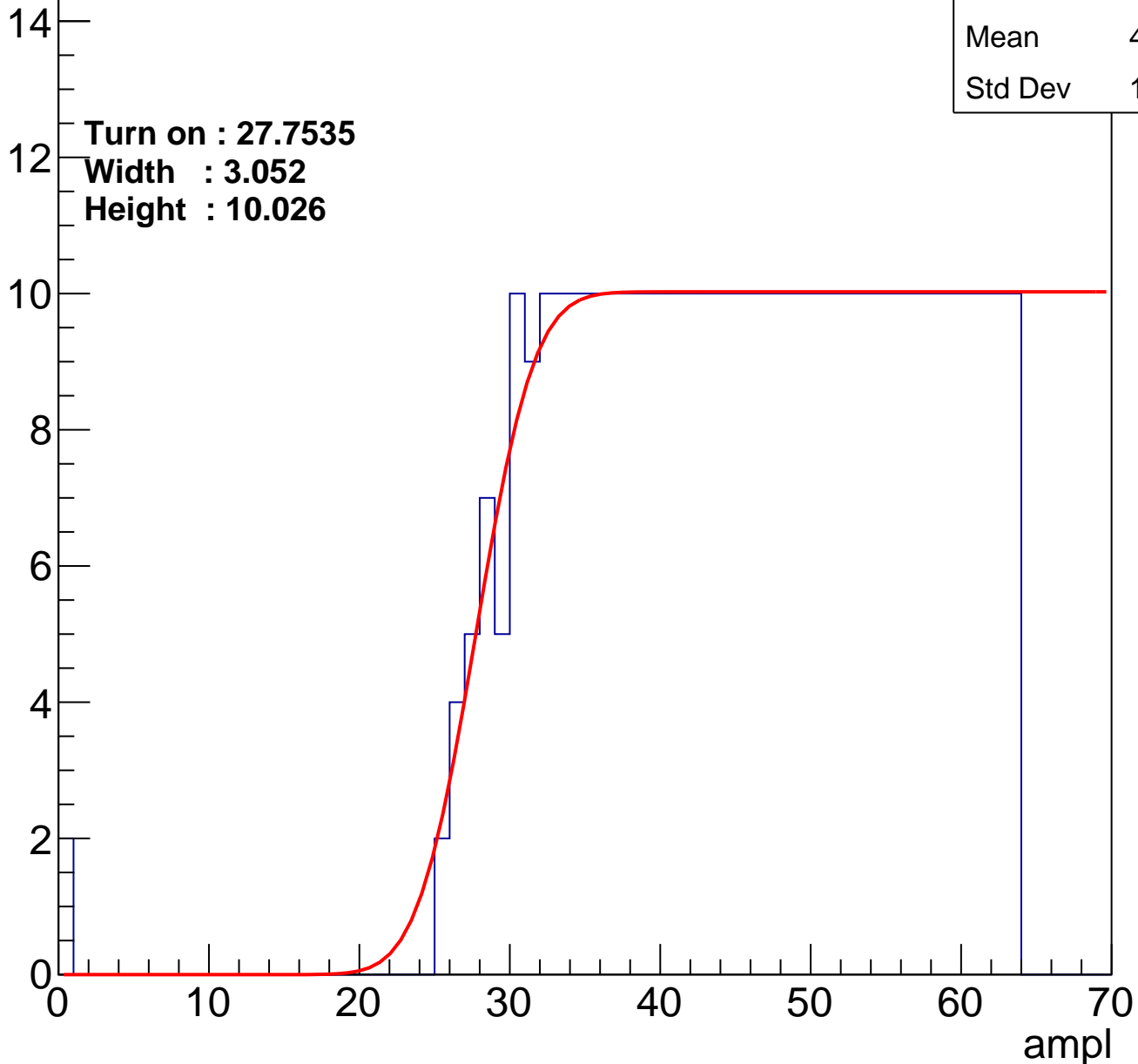
Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 27.7535

Width : 3.052

Height : 10.026

Entry

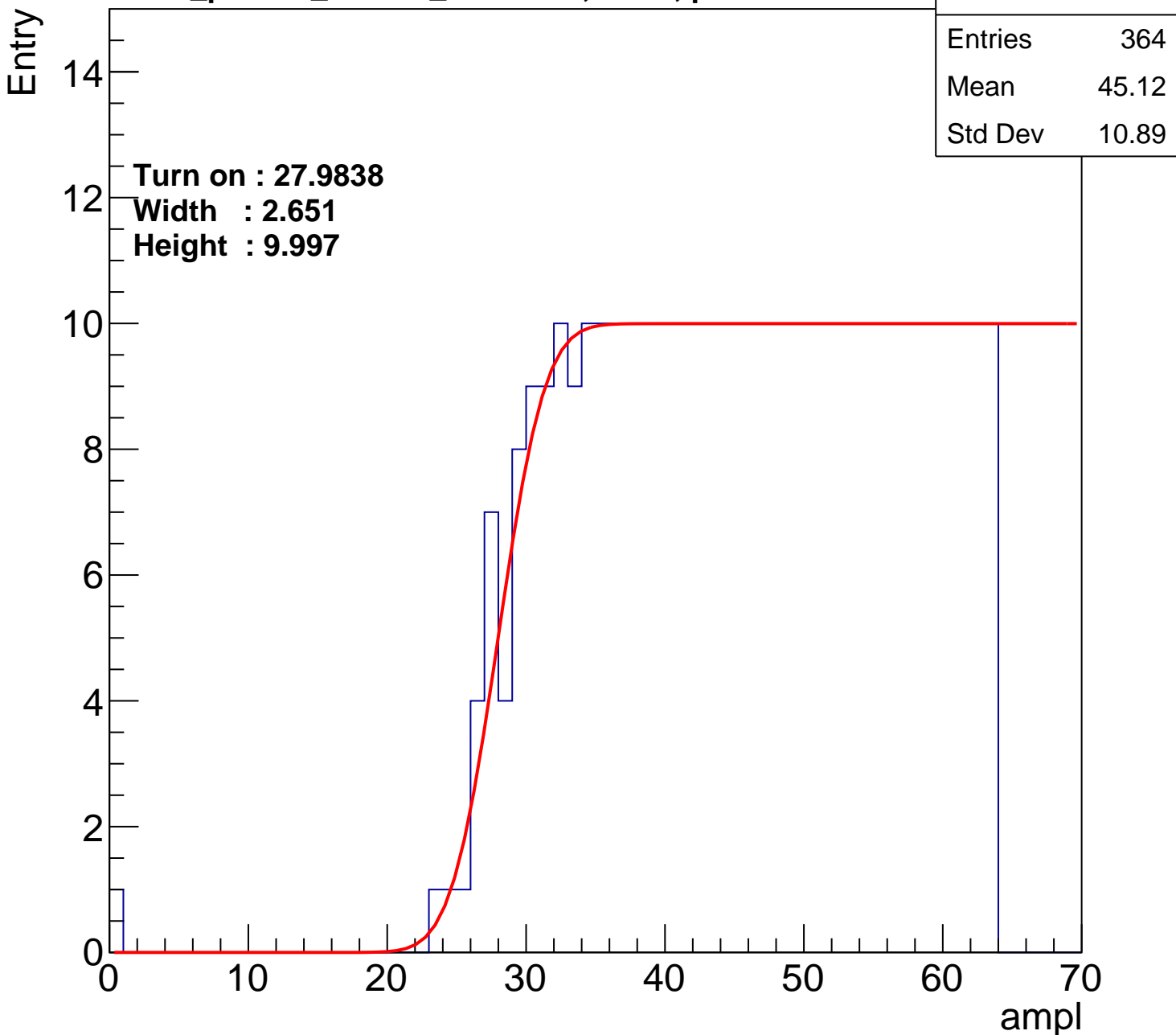


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.89

Height : 9.997



B0L002S, U12-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.85
Std Dev	11.35

Turn on : 27.5255

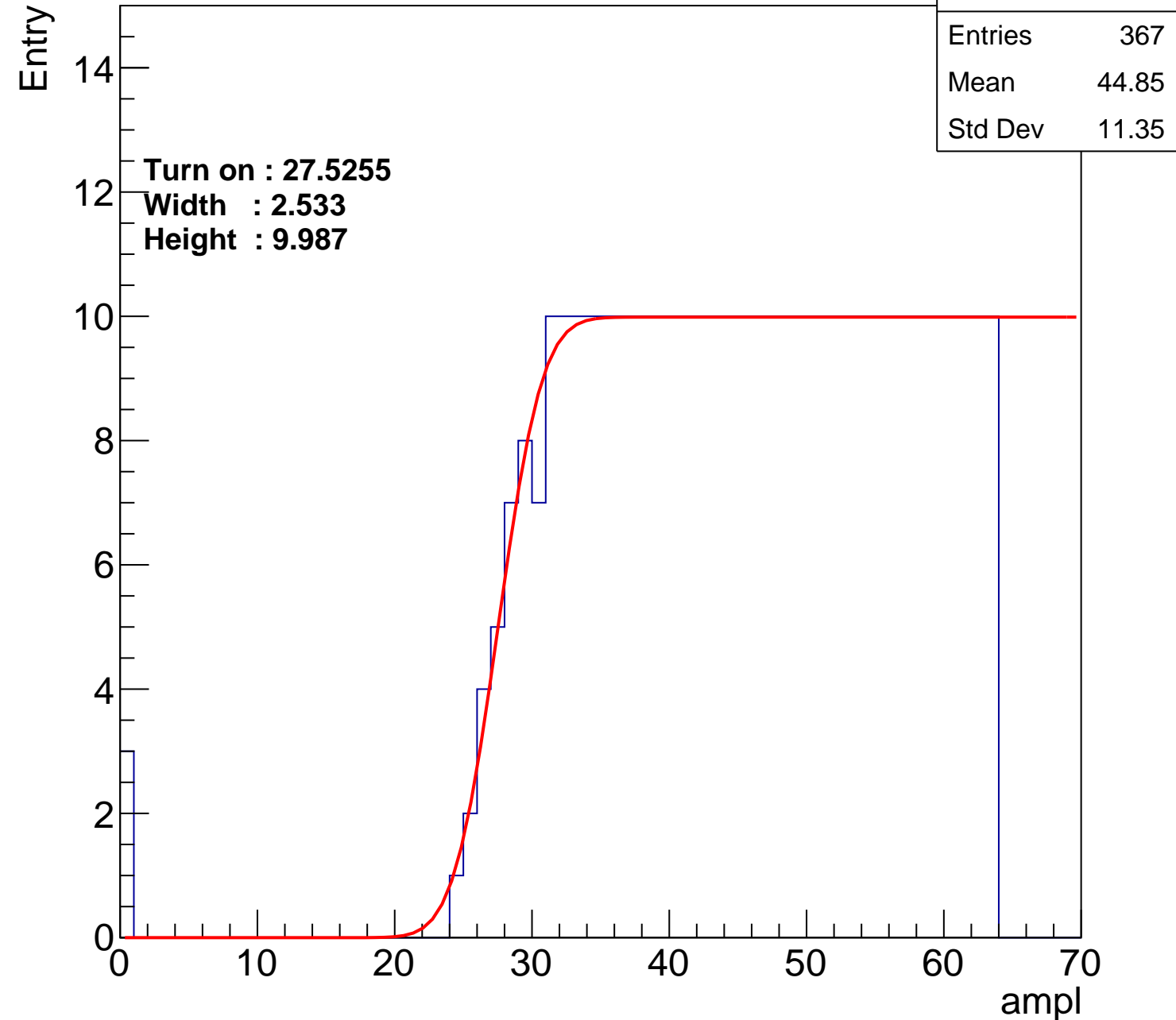
Width : 2.533

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch53

calib_packv5_042523_0143.root, FC#8, port C1

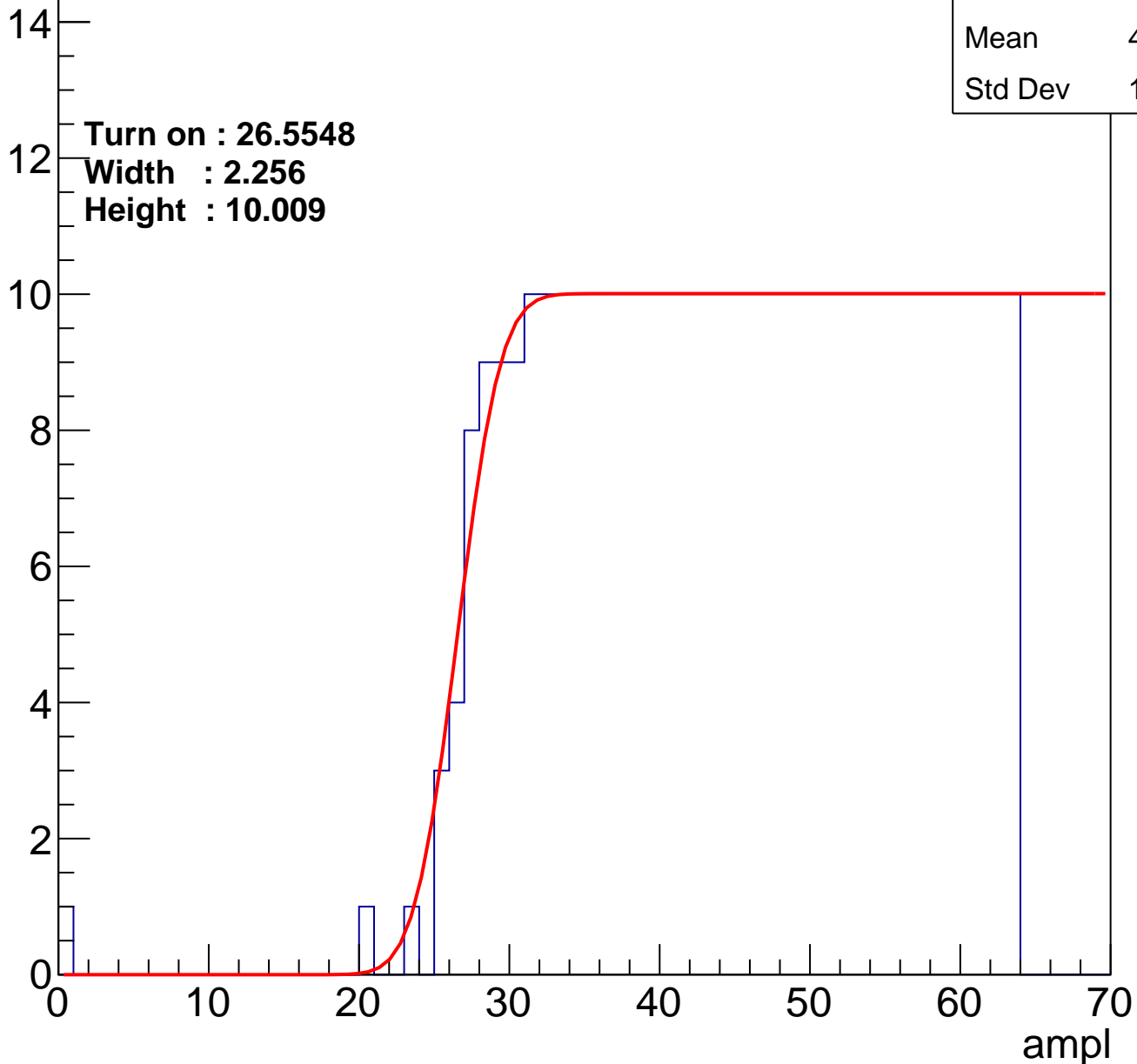
Entries	375
Mean	44.62
Std Dev	11.13

Turn on : 26.5548

Width : 2.256

Height : 10.009

Entry



B0L002S, U12-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.89
Std Dev	11.29

Turn on : 27.8637

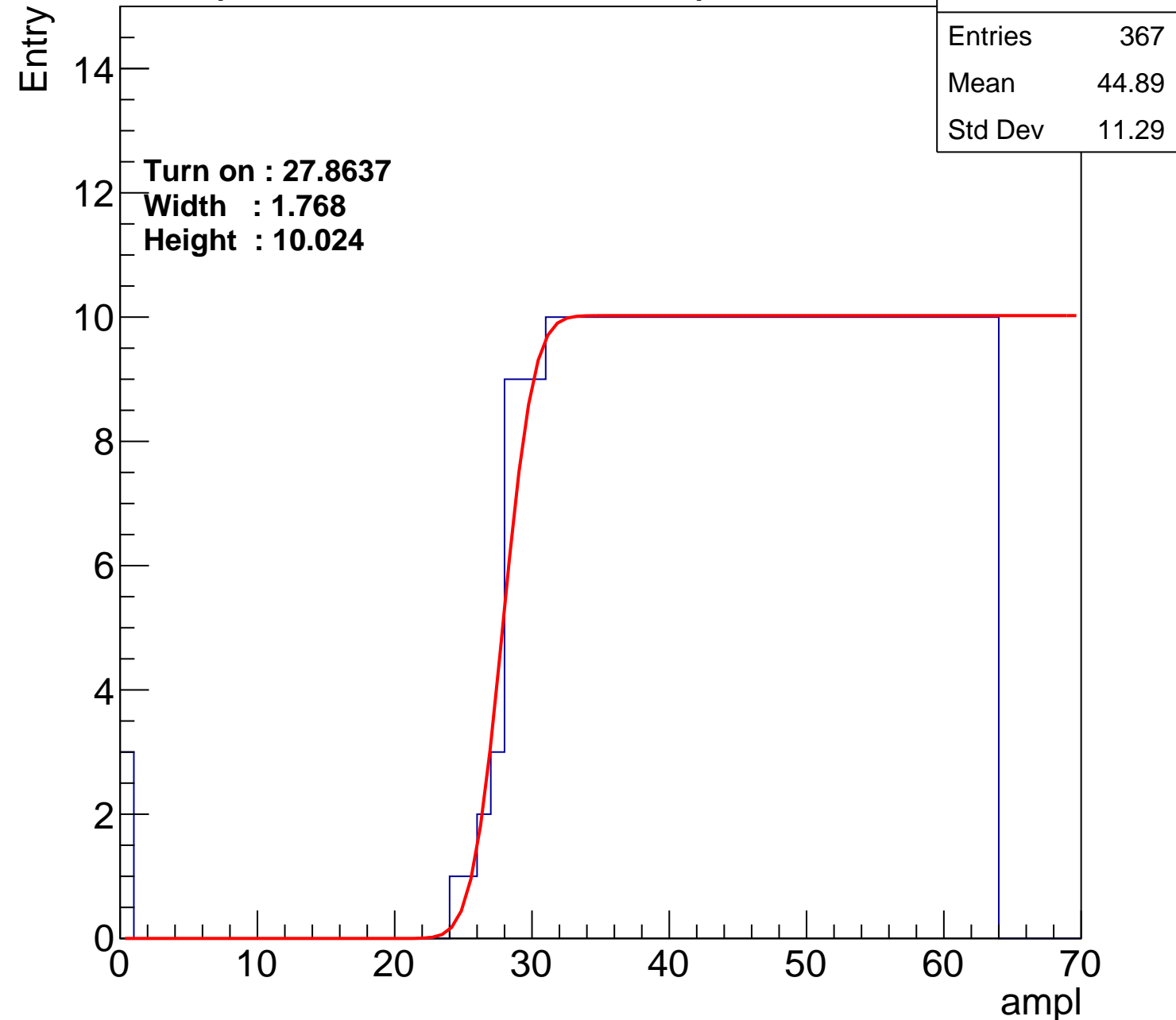
Width : 1.768

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.97
Std Dev	11.17

Turn on : 27.1359

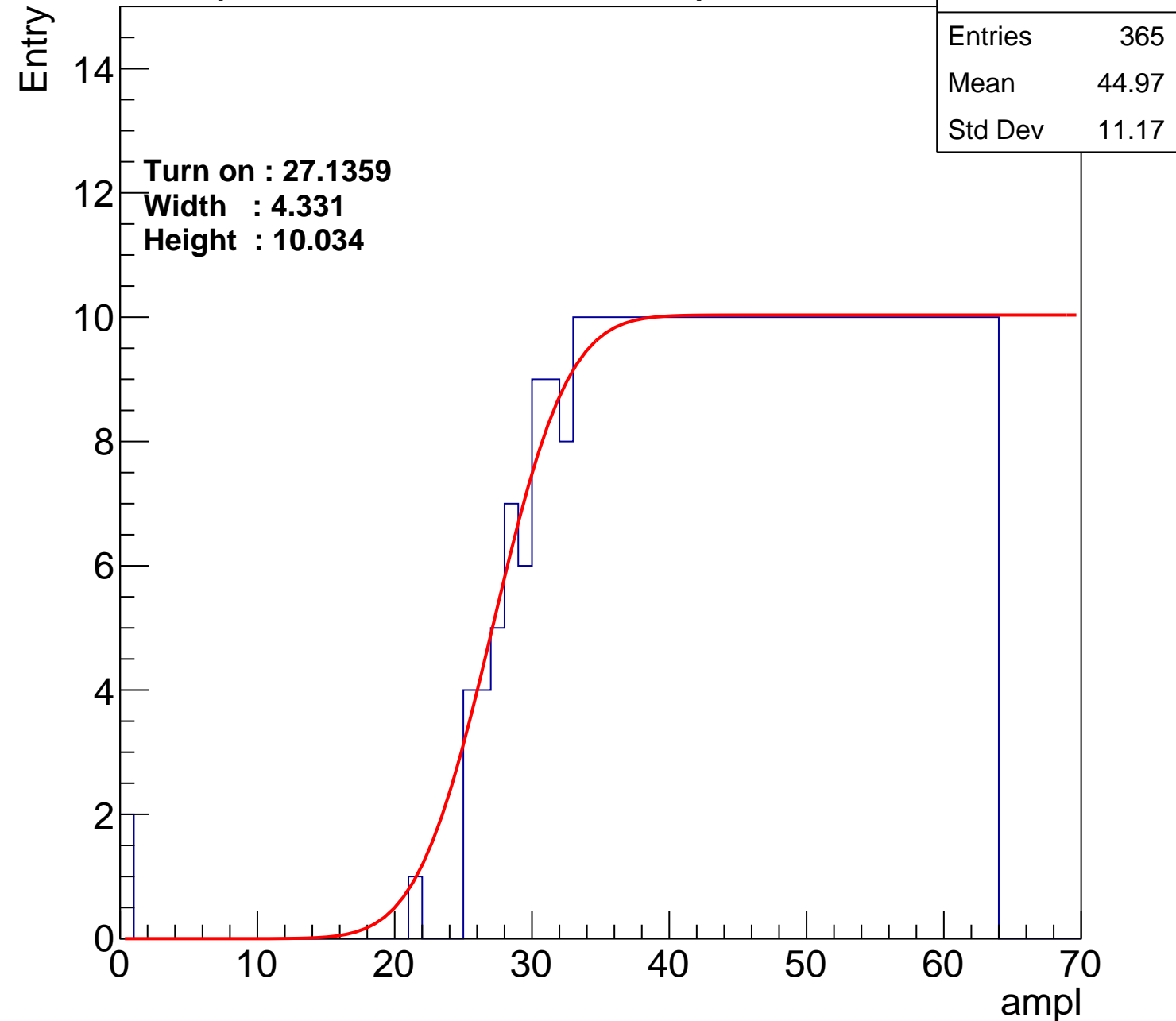
Width : 4.331

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.7641

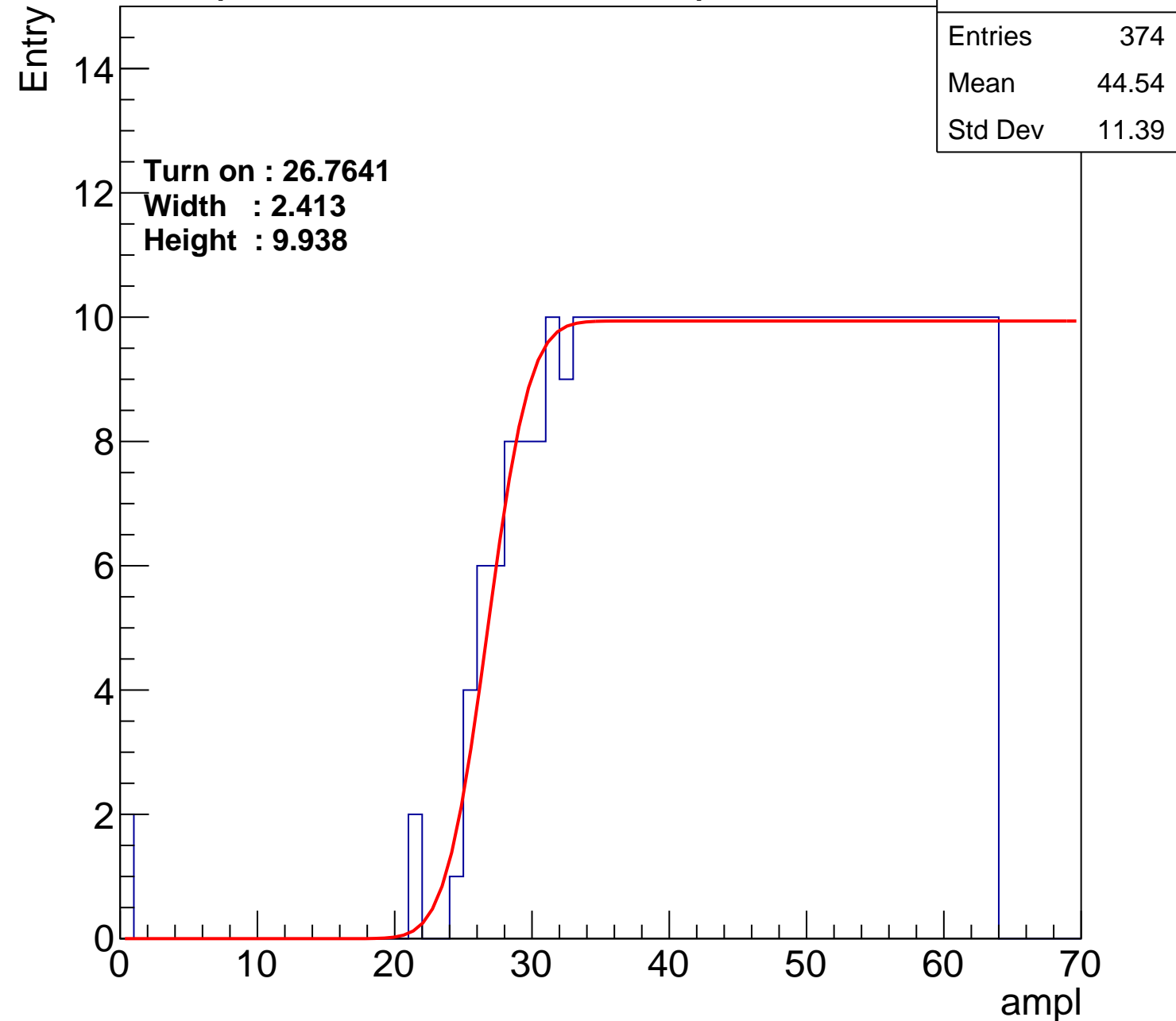
Width : 2.413

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.1
Std Dev	11.75

Turn on : 25.4903

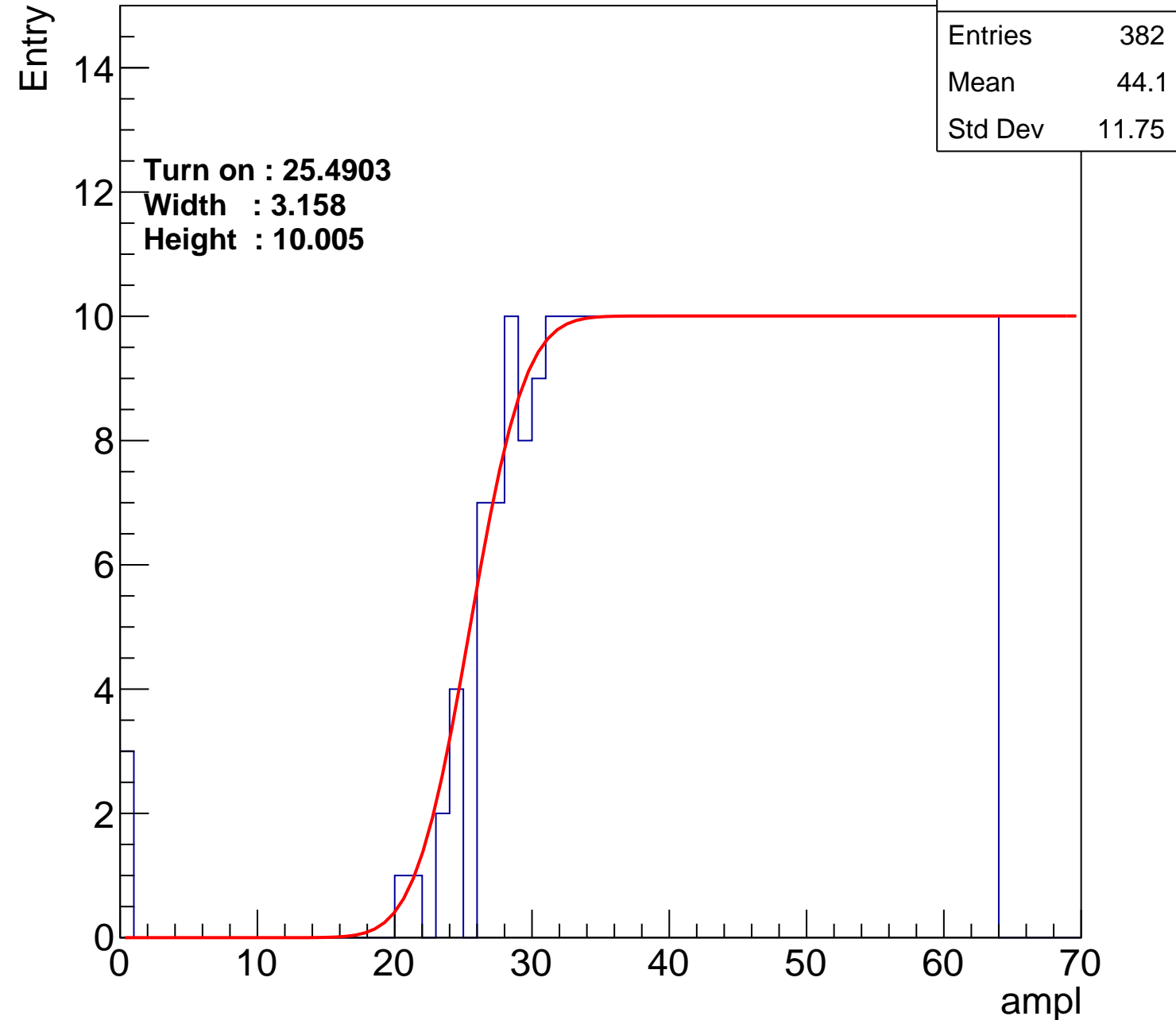
Width : 3.158

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.28
Std Dev	10.95

Turn on : 28.7826

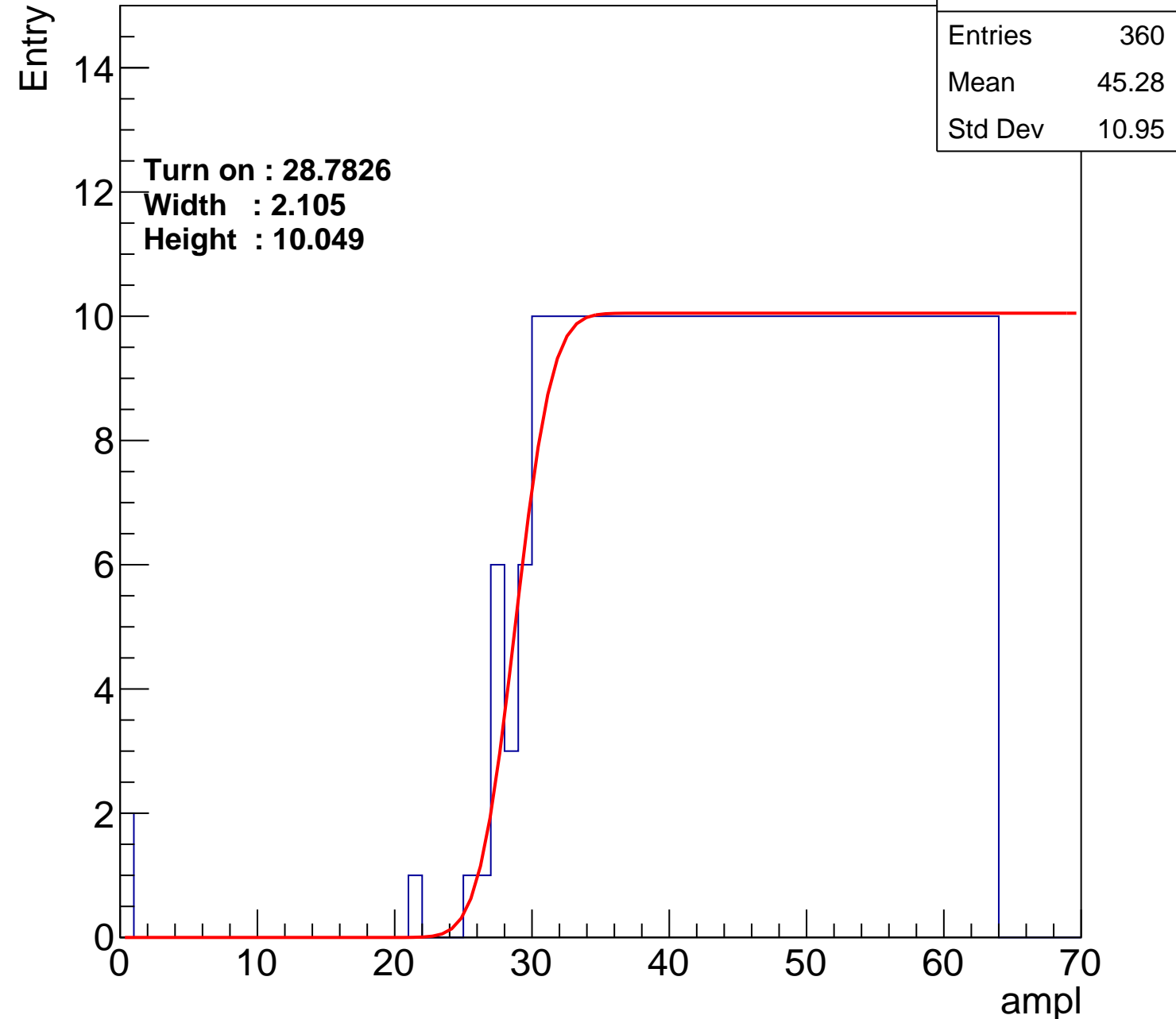
Width : 2.105

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch59

calib_packv5_042523_0143.root, FC#8, port C1

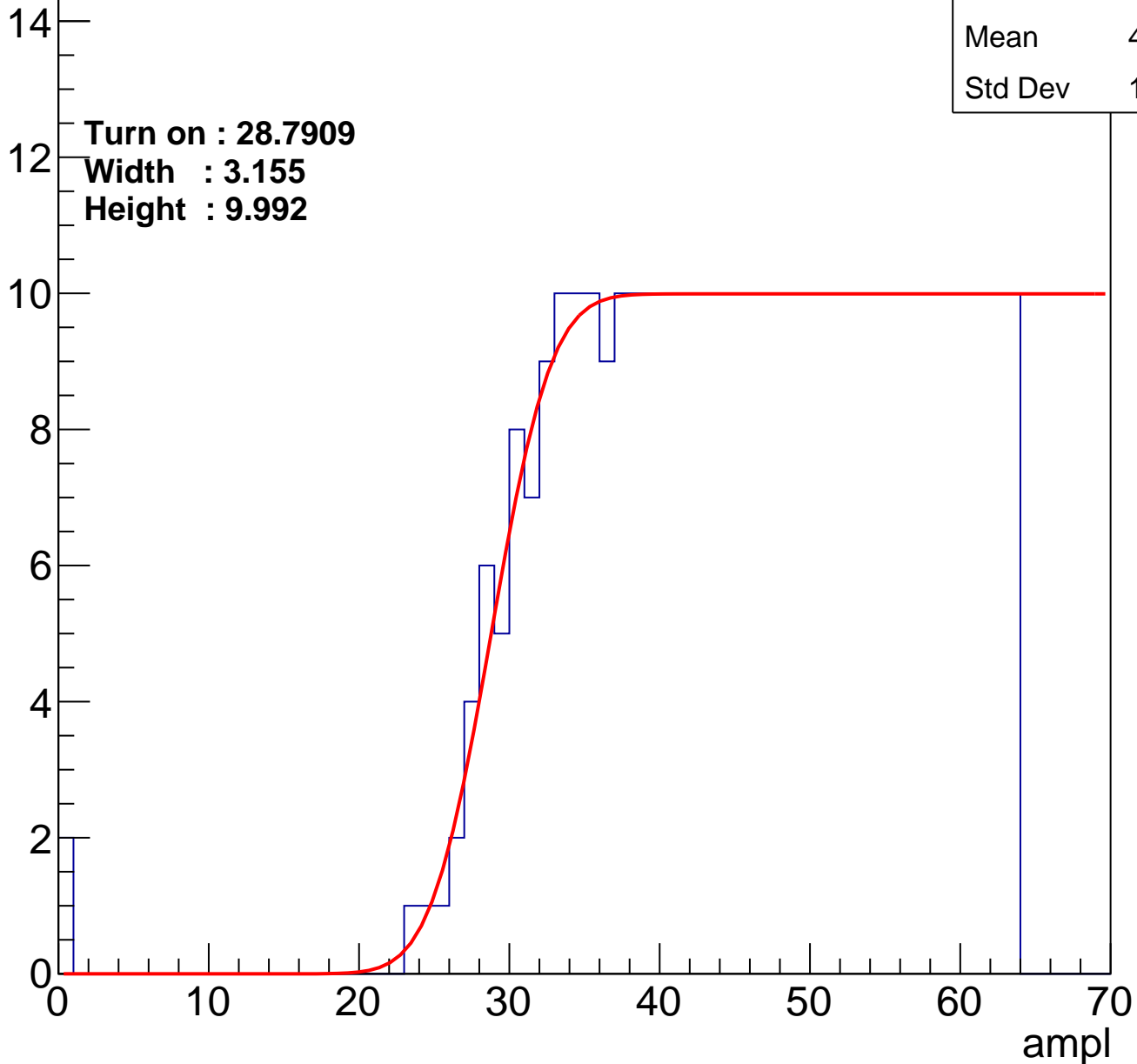
Entries	355
Mean	45.45
Std Dev	10.95

Turn on : 28.7909

Width : 3.155

Height : 9.992

Entry



B0L002S, U12-ch60

calib_packv5_042523_0143.root, FC#8, port C1

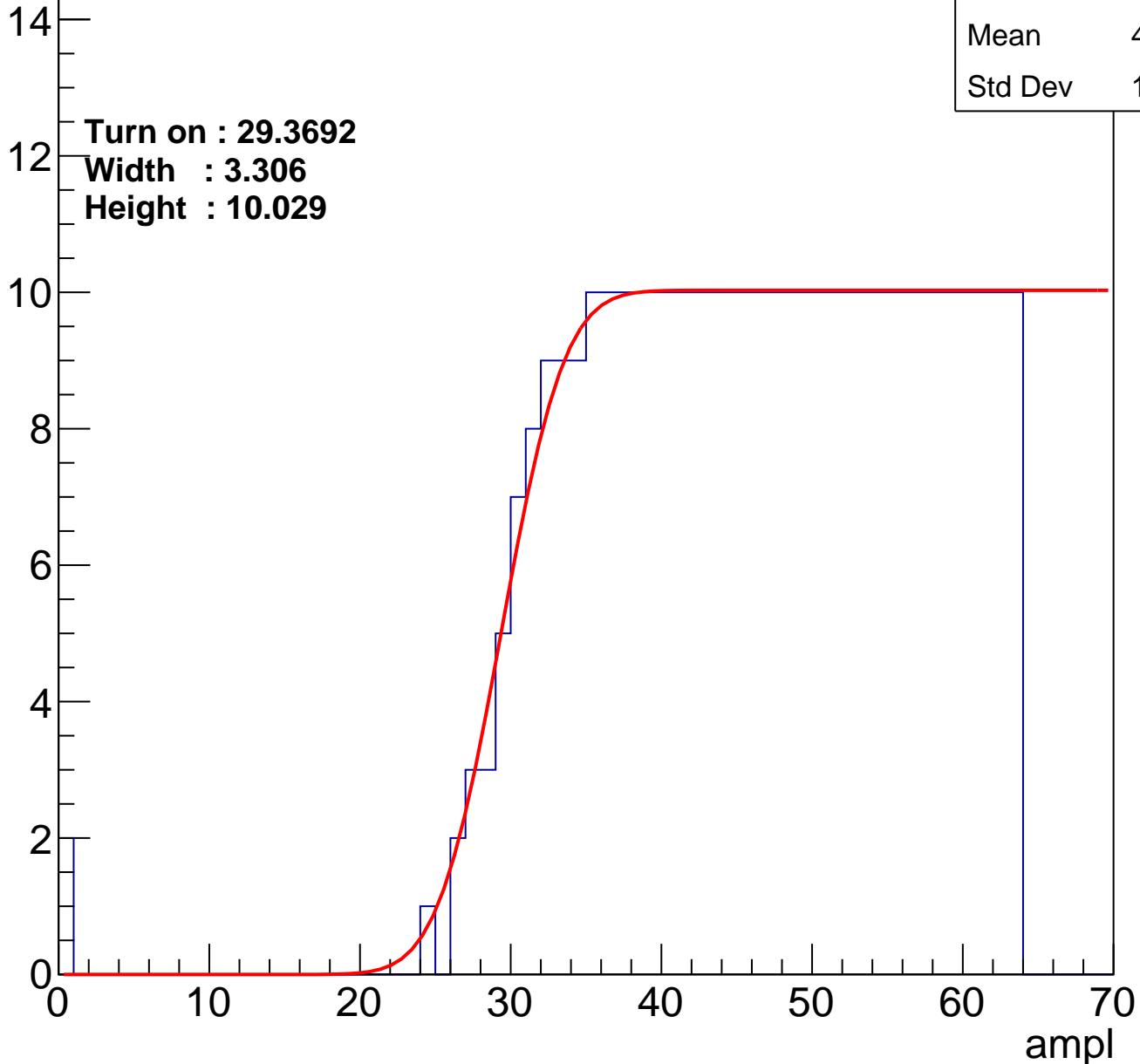
Entries	348
Mean	45.82
Std Dev	10.73

Turn on : 29.3692

Width : 3.306

Height : 10.029

Entry



B0L002S, U12-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.09
Std Dev	11.09

Turn on : 27.9201

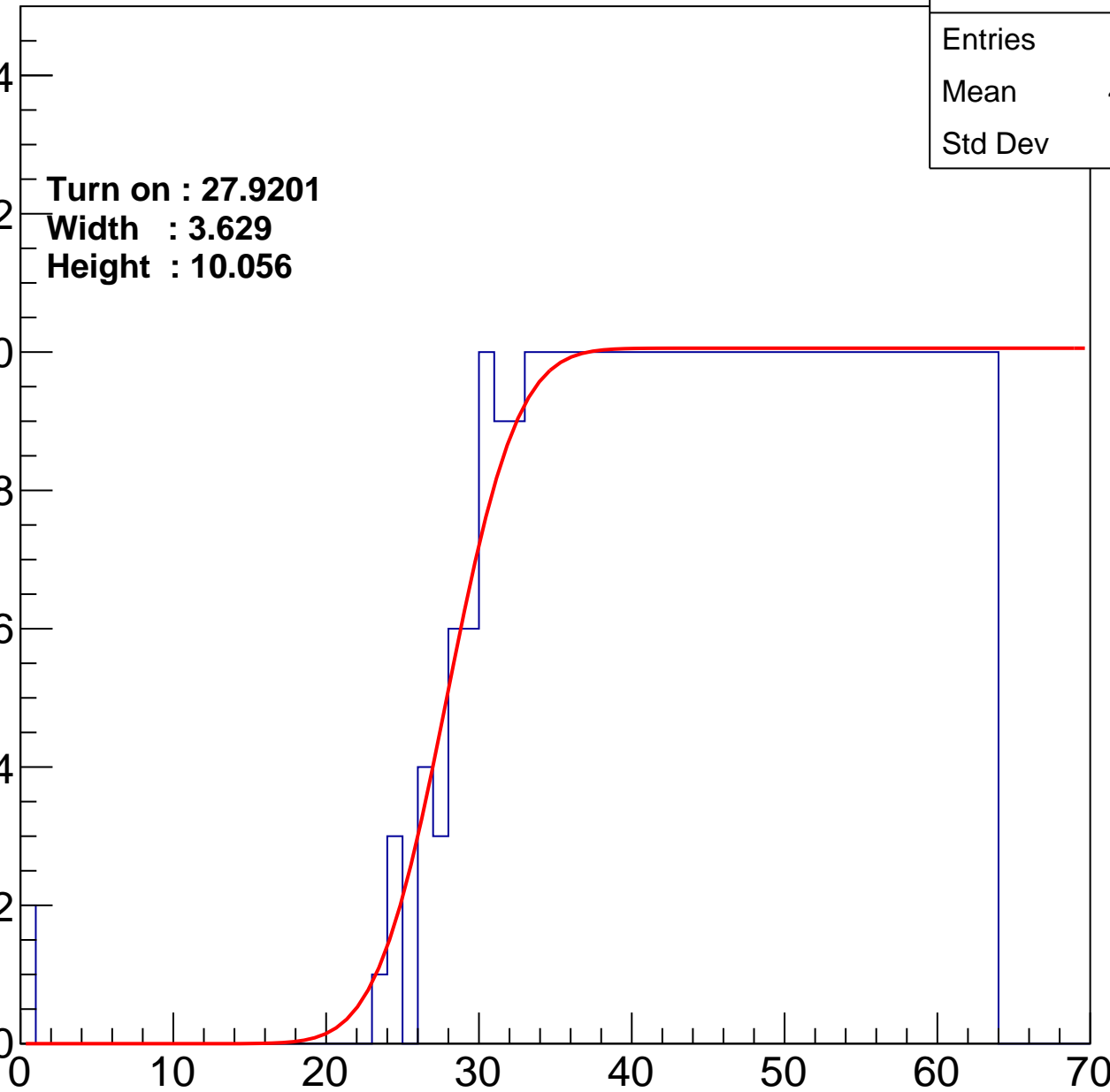
Width : 3.629

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.7
Std Dev	10.81

Turn on : 29.0483

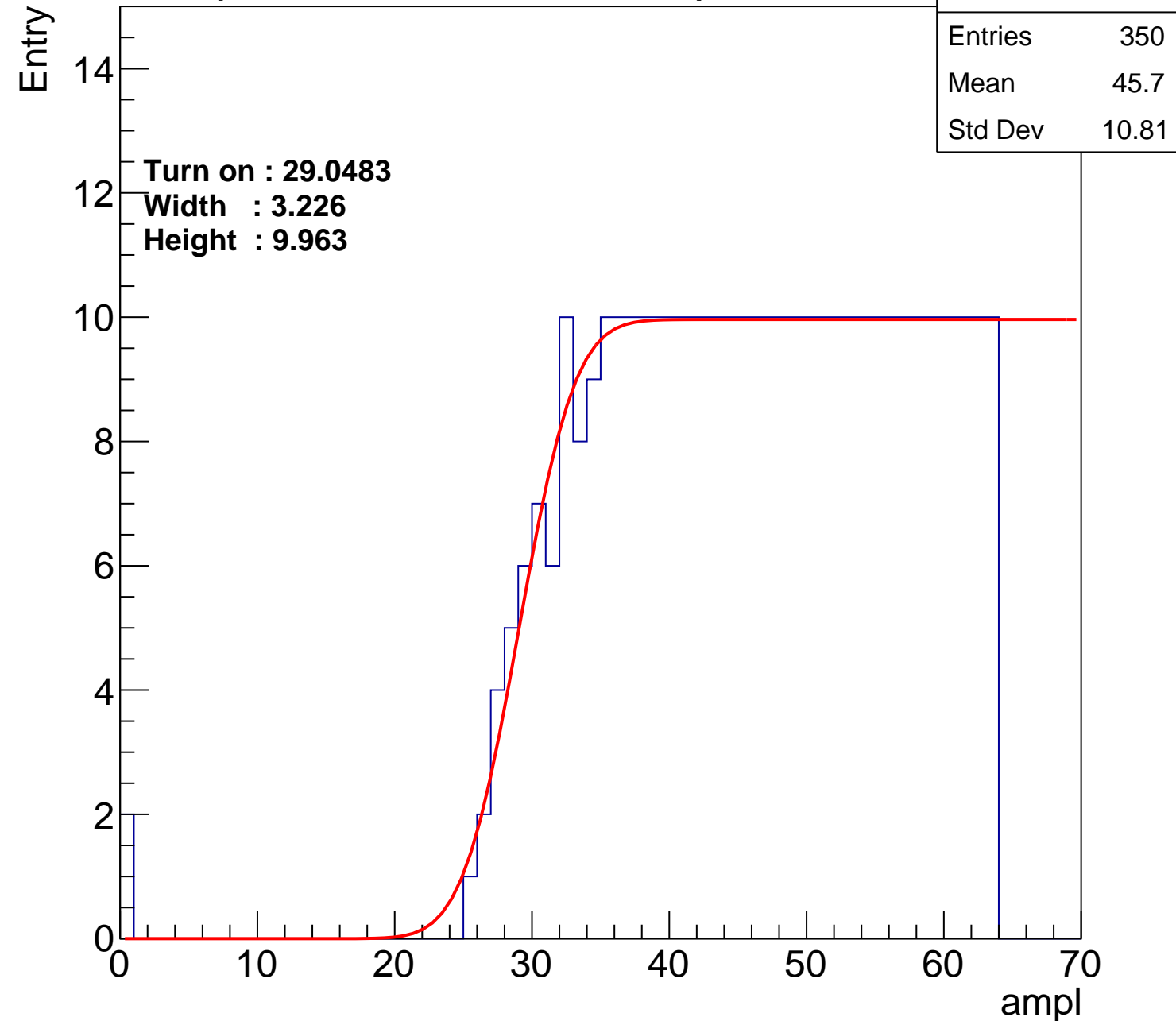
Width : 3.226

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.9
Std Dev	11.32

Turn on : 27.7687

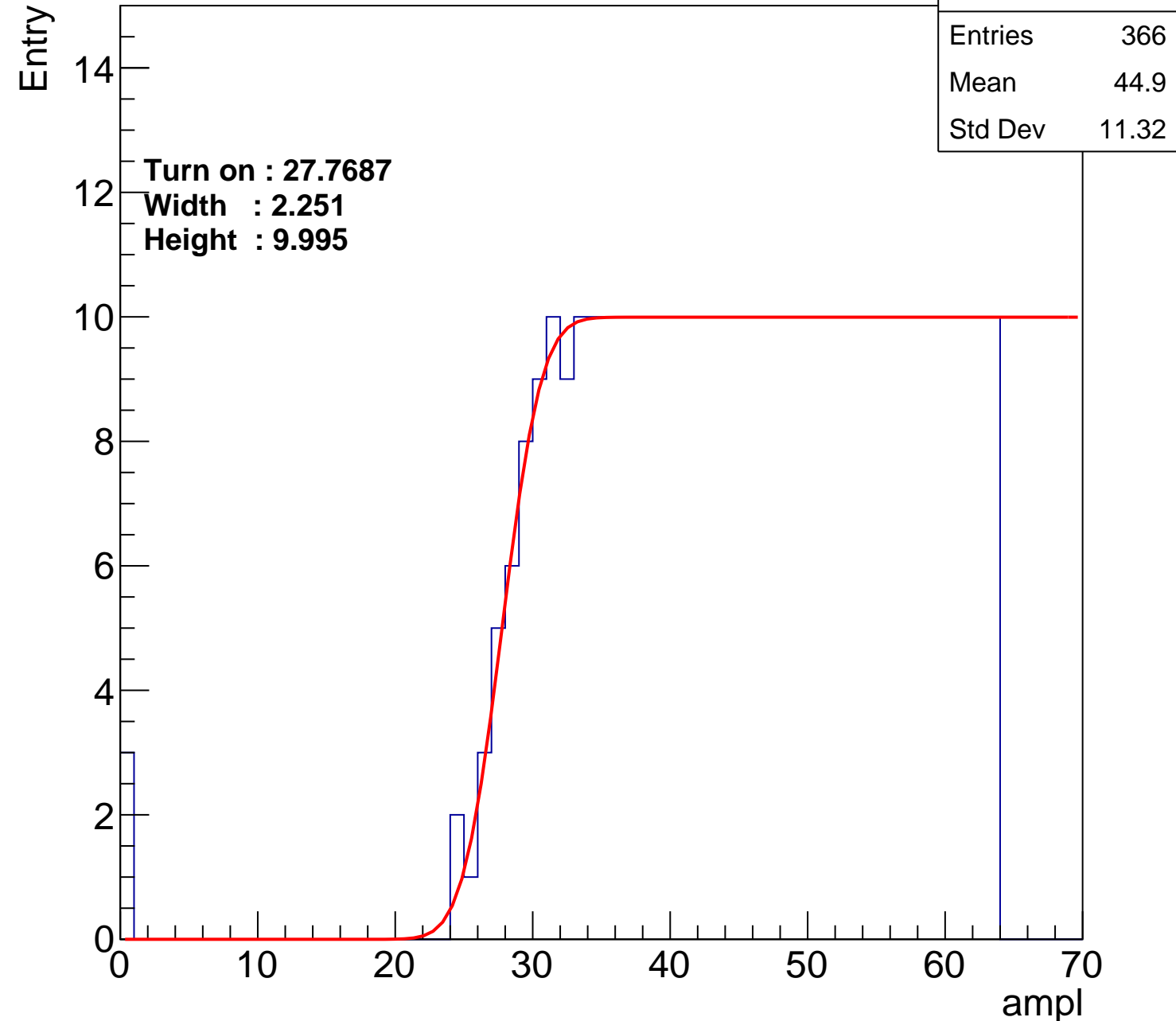
Width : 2.251

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.44
Std Dev	10.39

Turn on : 30.7291

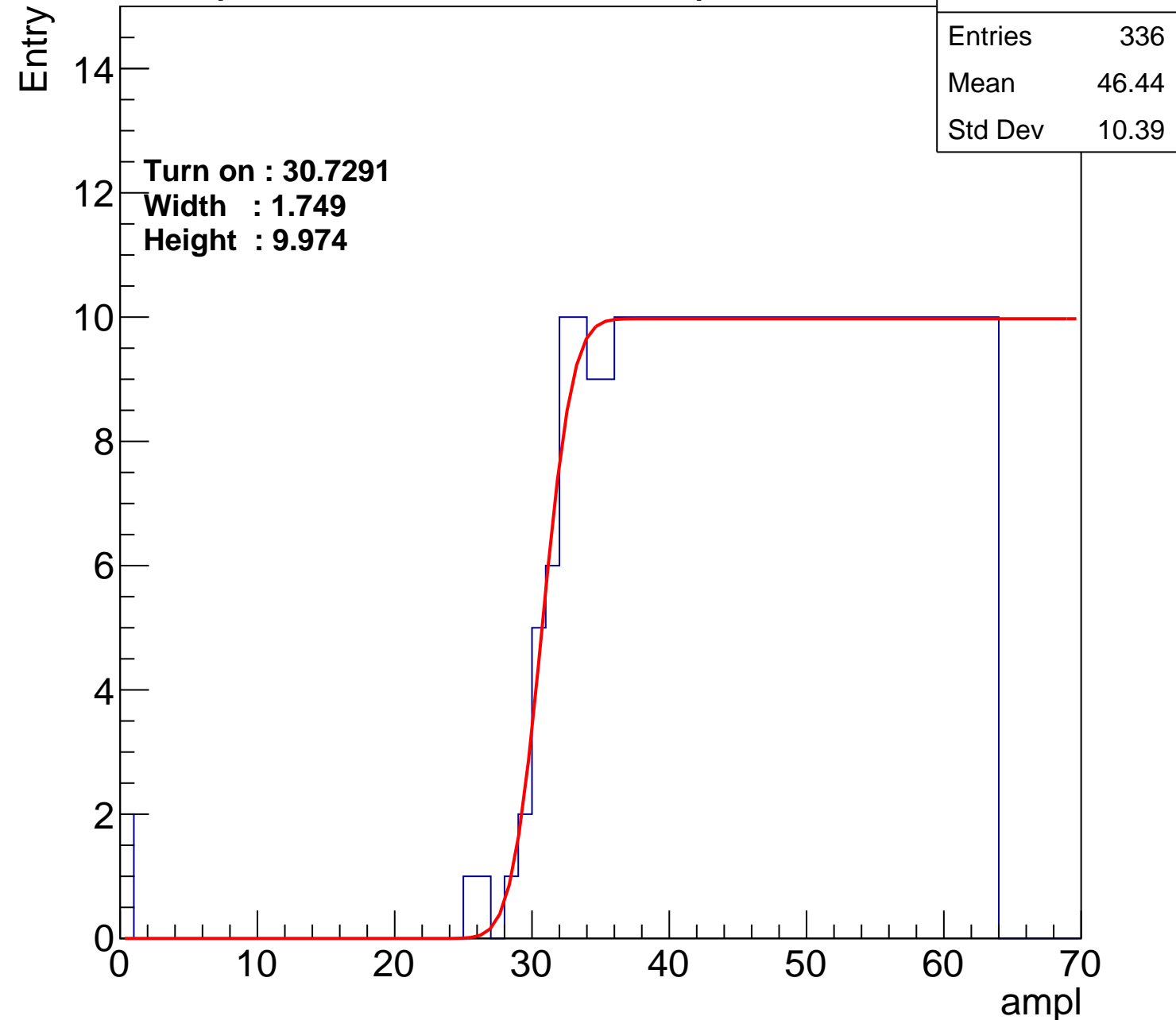
Width : 1.749

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.27
Std Dev	10.28

Turn on : 30.3877

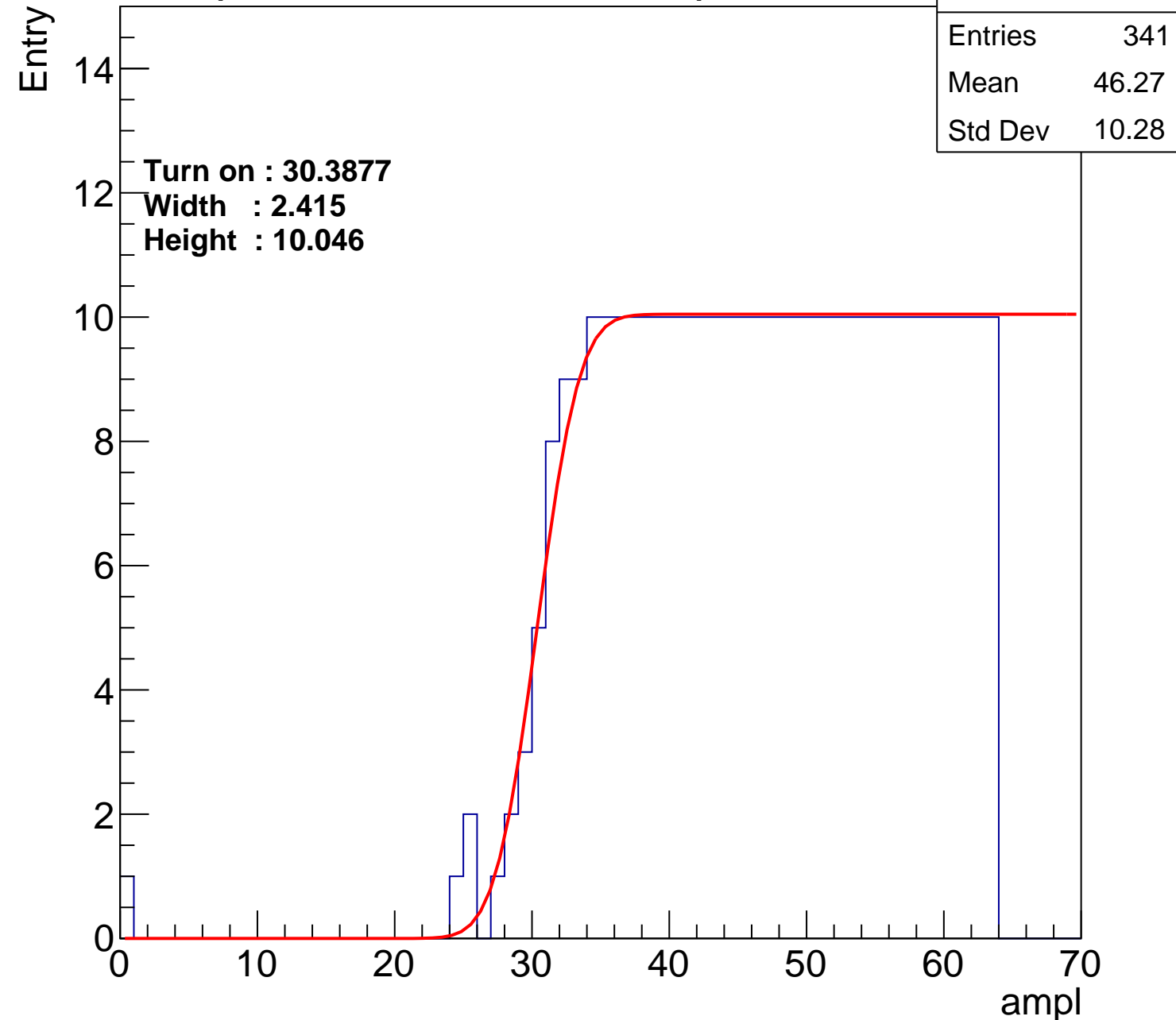
Width : 2.415

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.17
Std Dev	10.56

Turn on : 30.4798

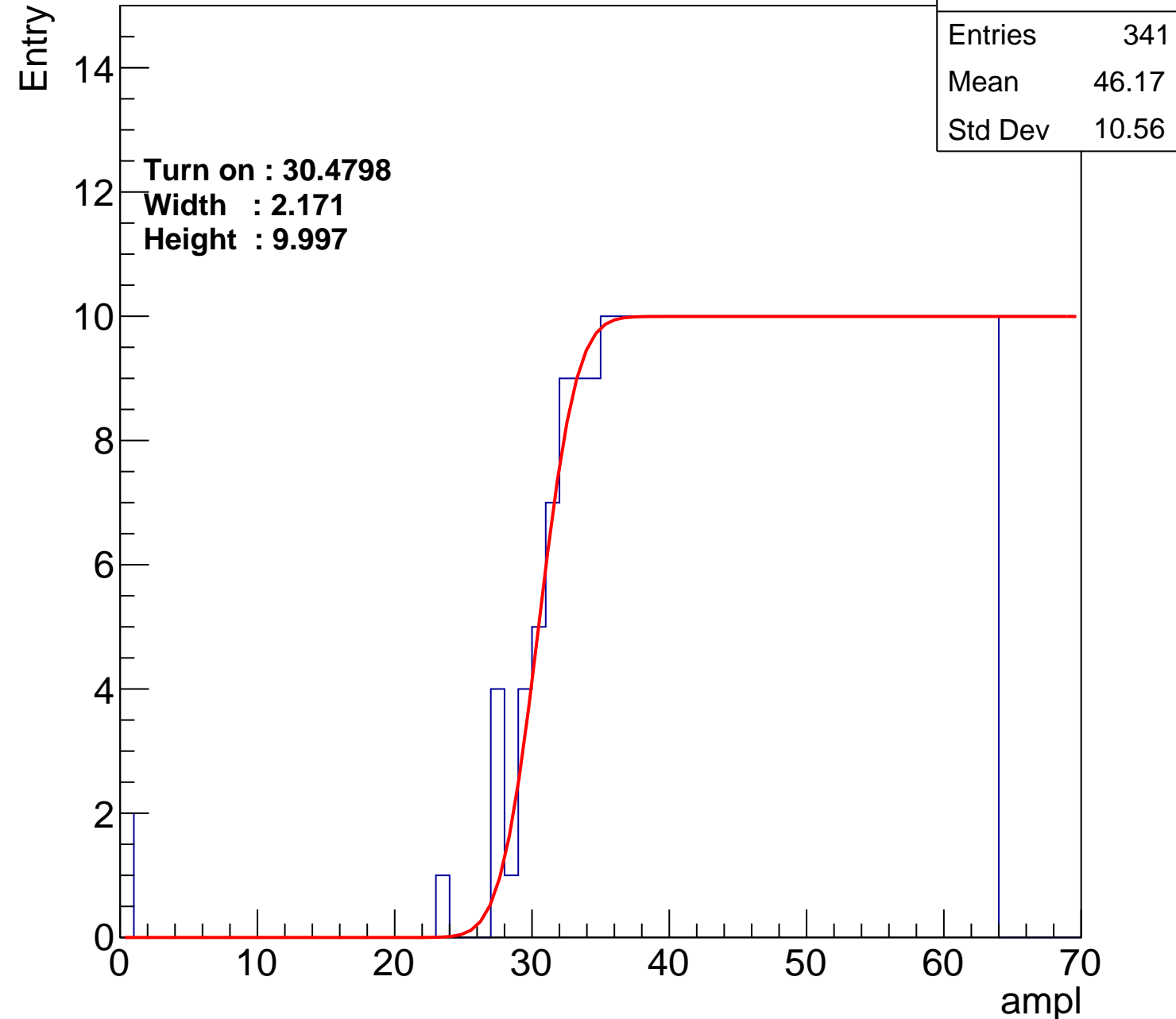
Width : 2.171

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 27.1229

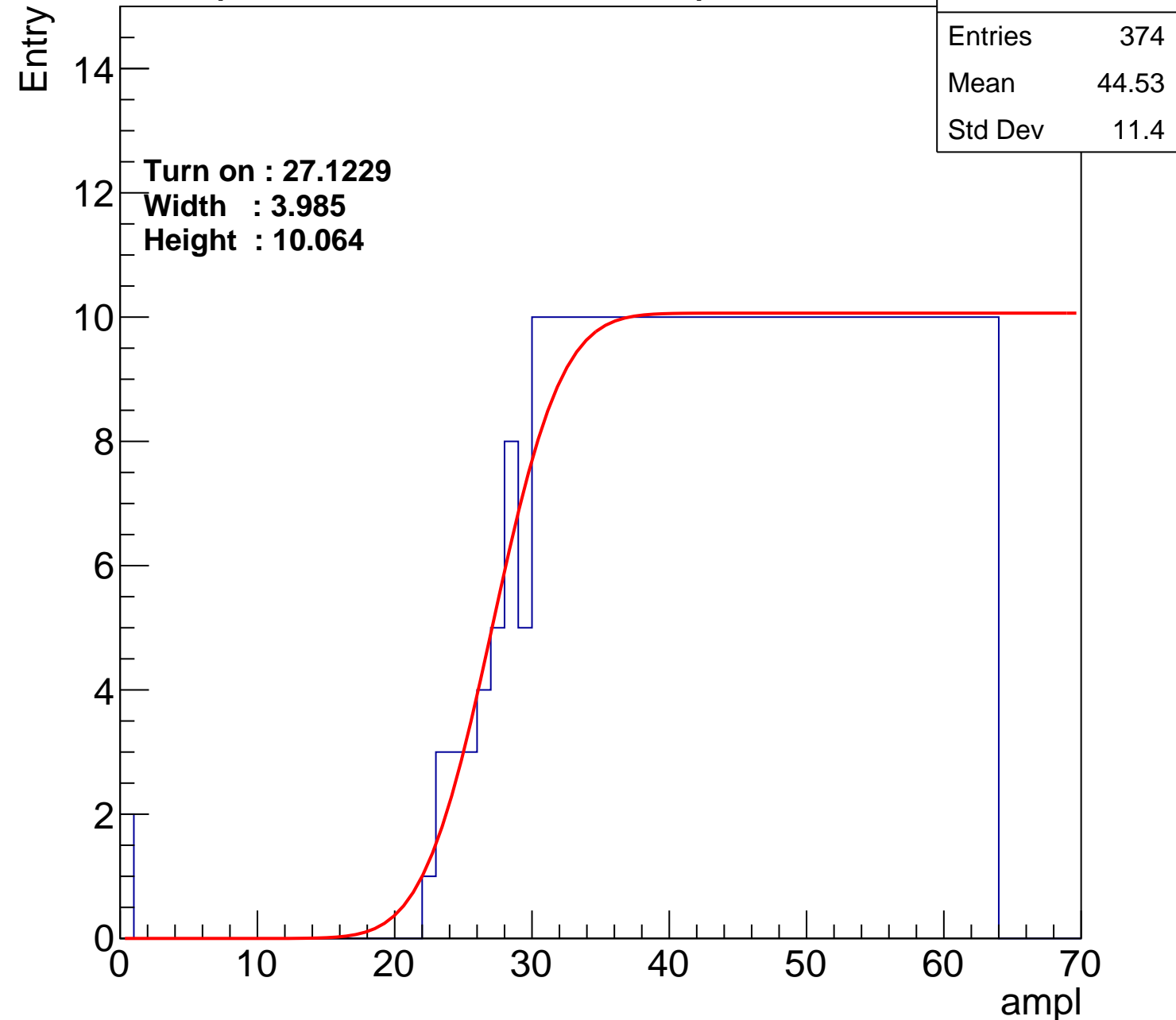
Width : 3.985

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.09
Std Dev	11.24

Turn on : 28.2175

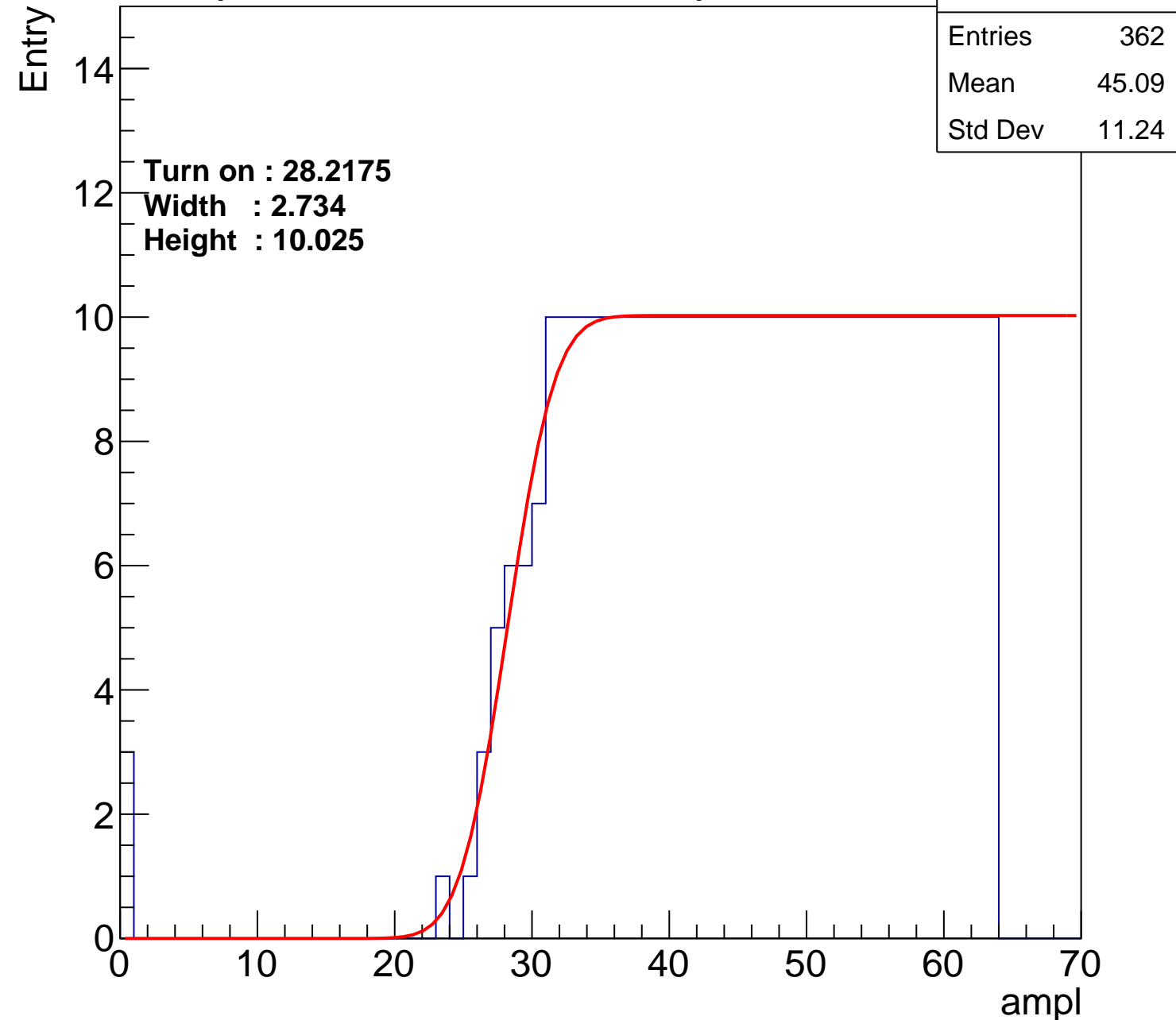
Width : 2.734

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.41
Std Dev	10.74

Turn on : 28.4990

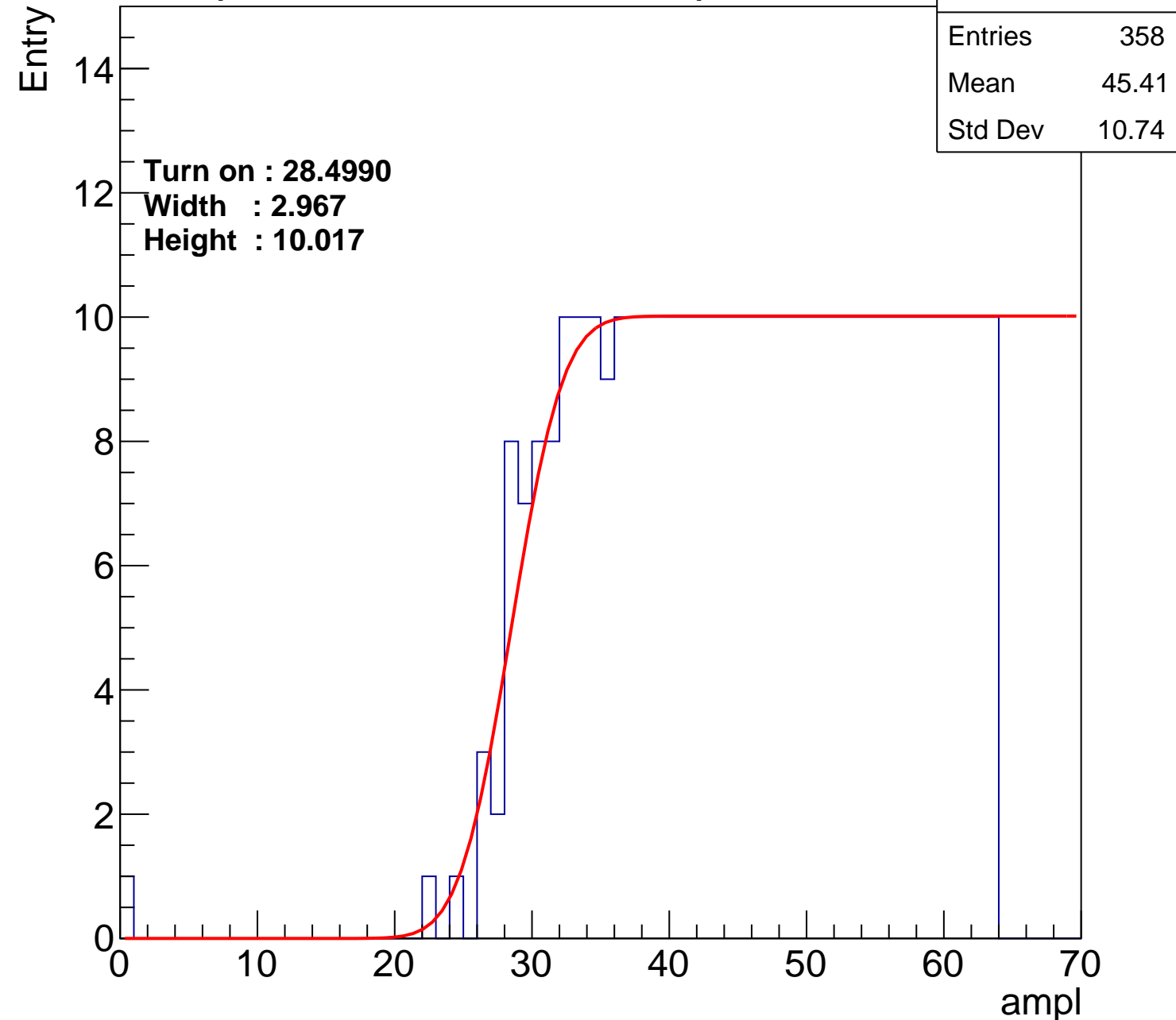
Width : 2.967

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.71
Std Dev	10.59

Turn on : 29.0257

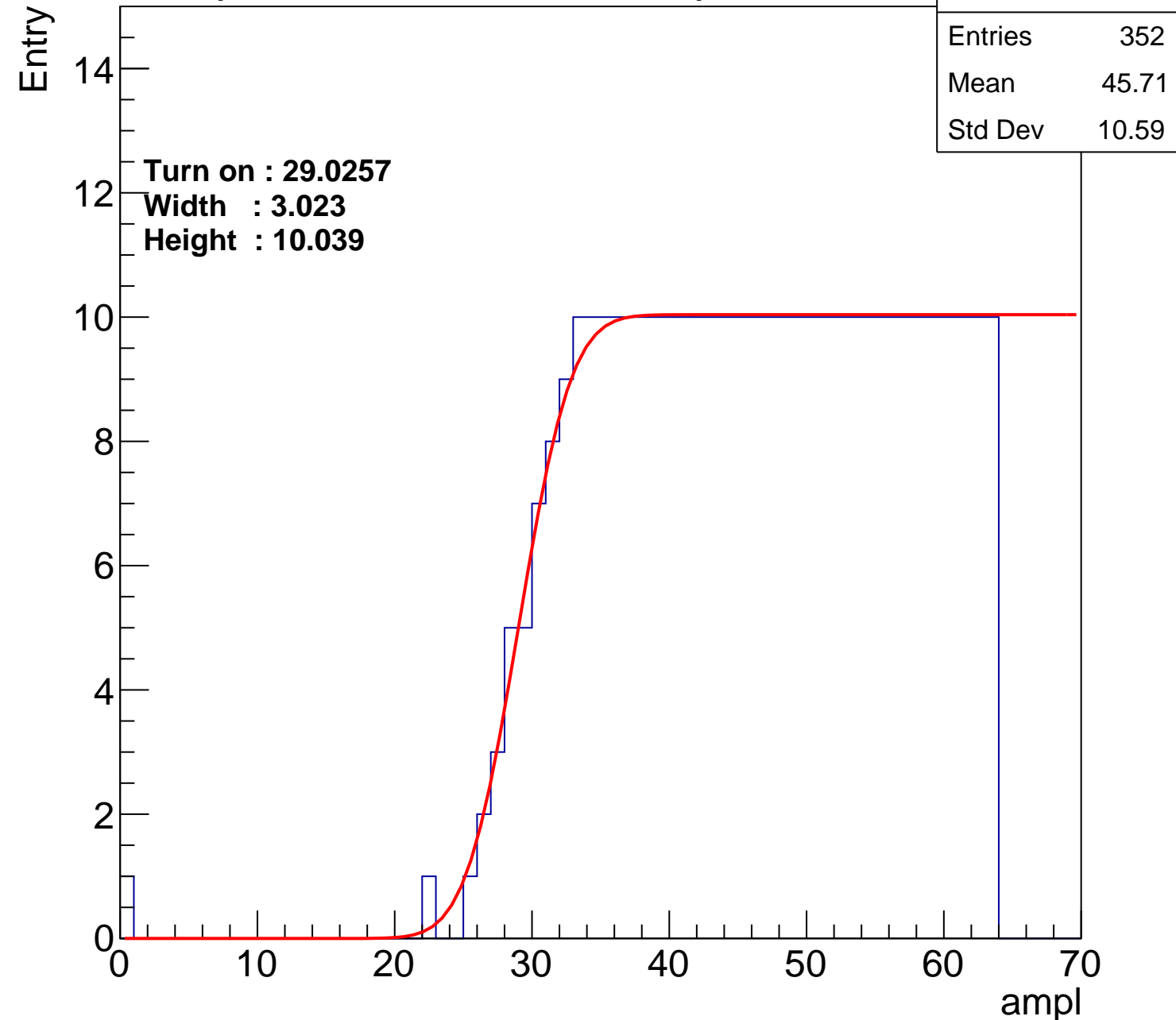
Width : 3.023

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.31
Std Dev	11.68

Turn on : 26.9618

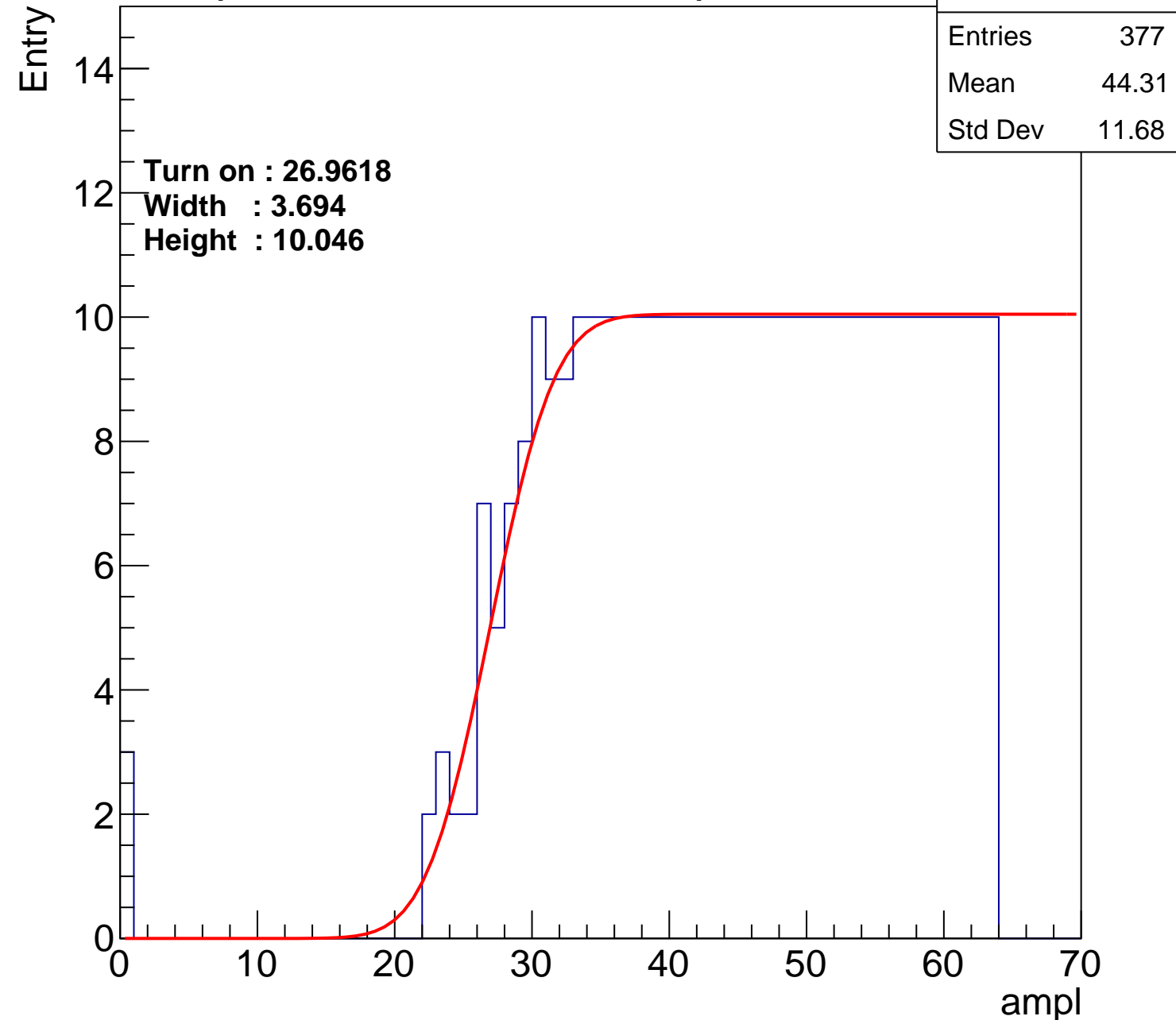
Width : 3.694

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.81
Std Dev	10.48

Turn on : 28.8655

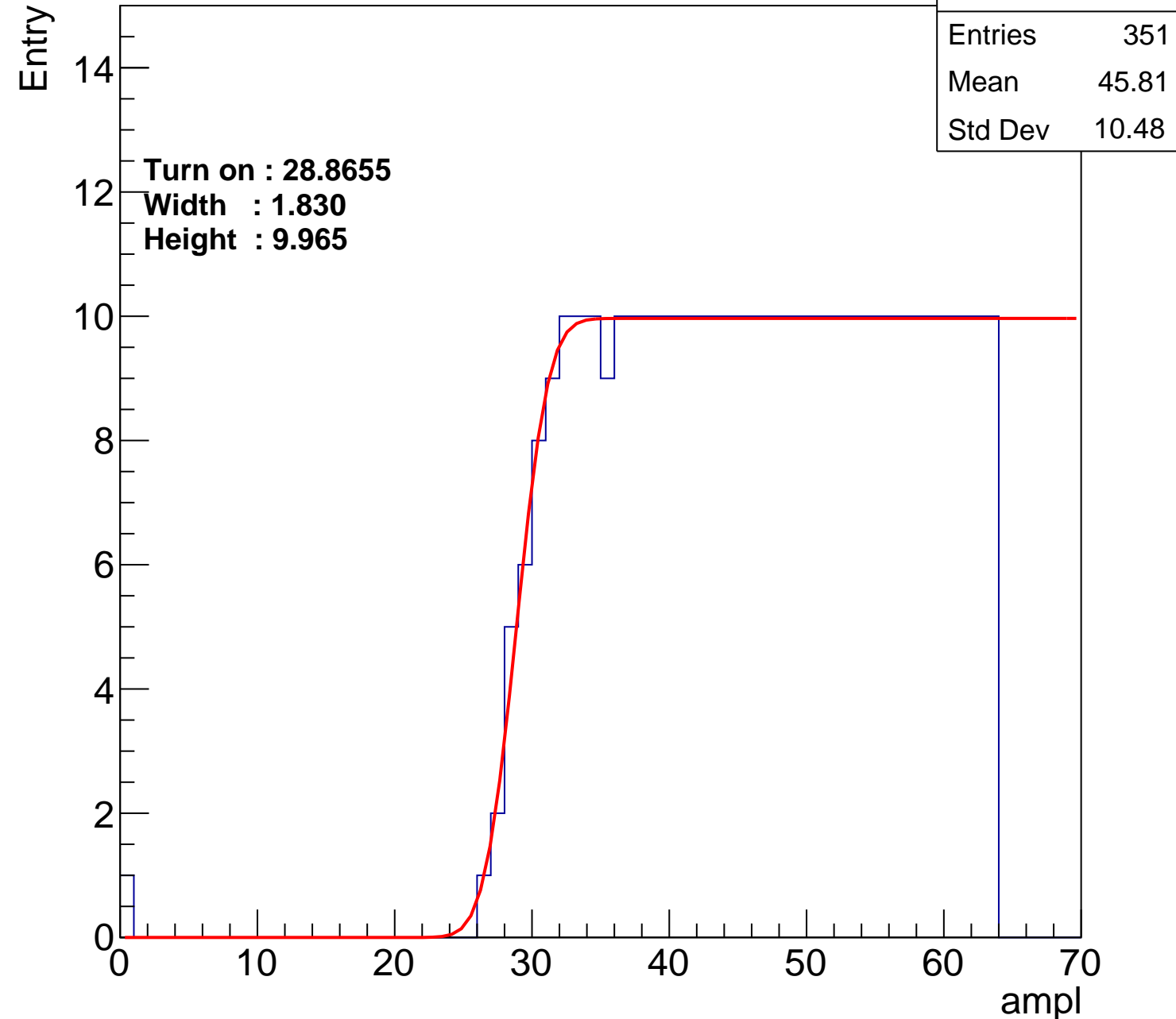
Width : 1.830

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.7
Std Dev	11.16

Turn on : 27.5964

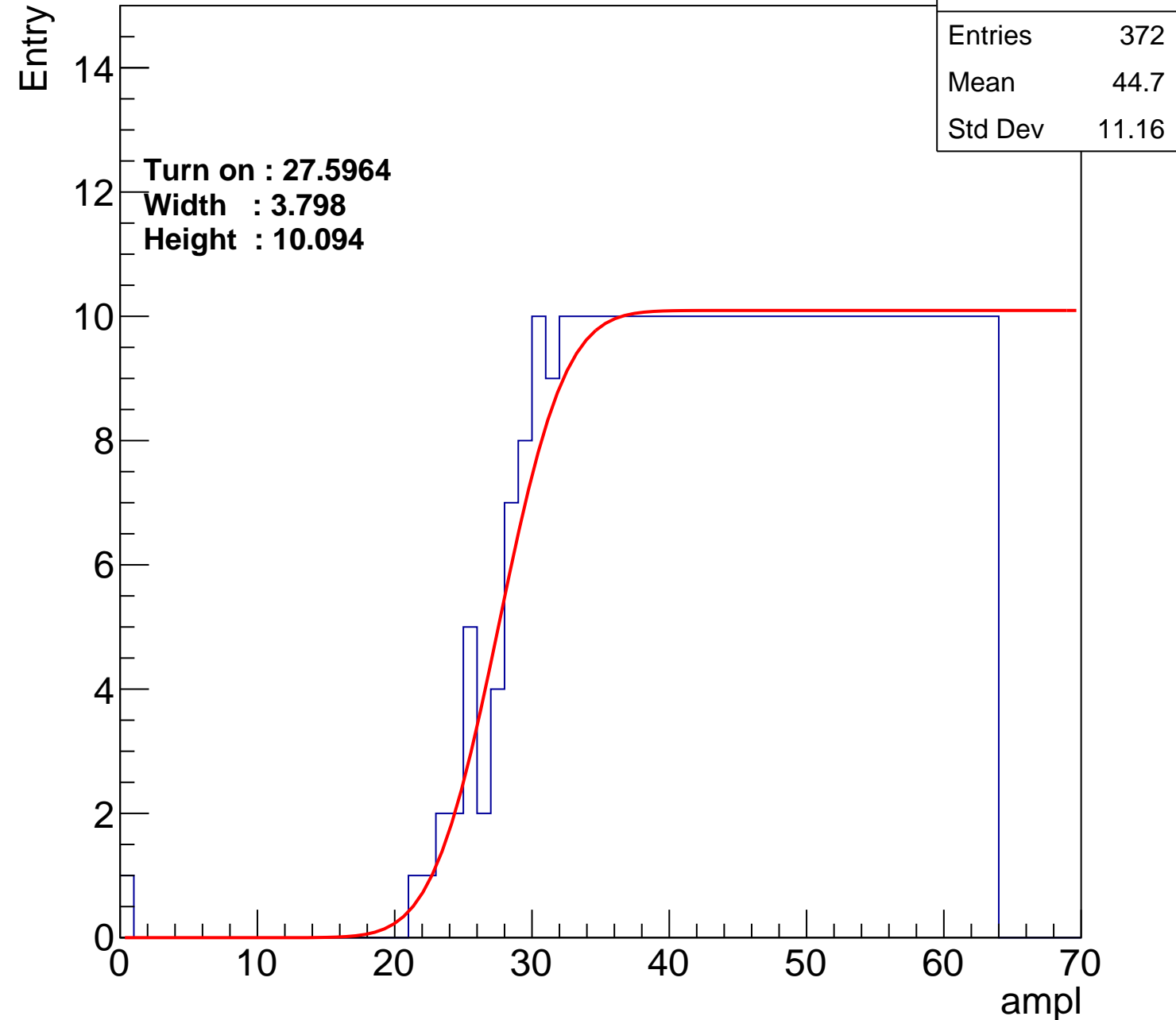
Width : 3.798

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.68
Std Dev	11.65

Turn on : 27.2561

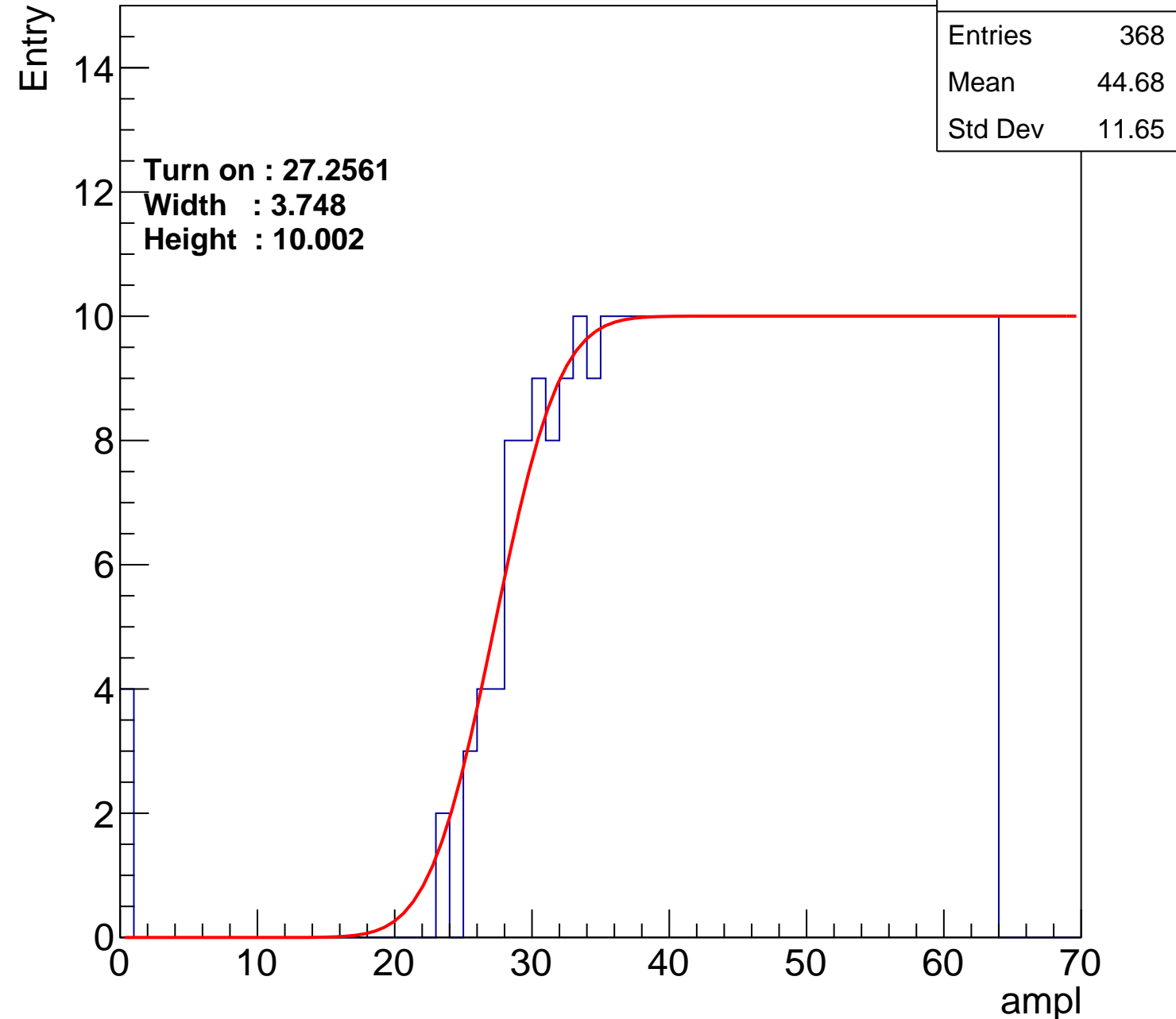
Width : 3.748

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch75

calib_packv5_042523_0143.root, FC#8, port C1

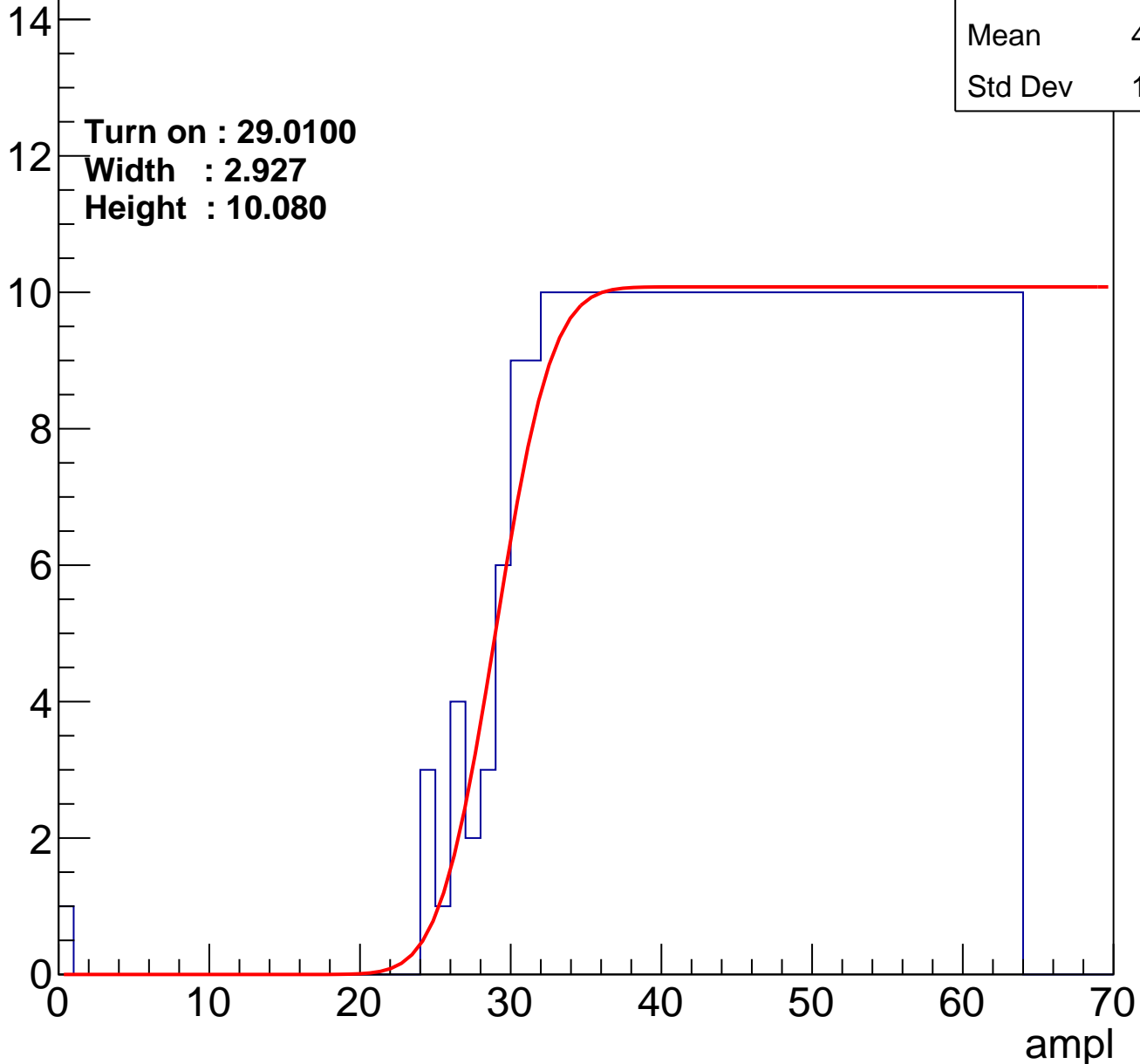
Entries	358
Mean	45.42
Std Dev	10.73

Turn on : 29.0100

Width : 2.927

Height : 10.080

Entry



B0L002S, U12-ch76

calib_packv5_042523_0143.root, FC#8, port C1

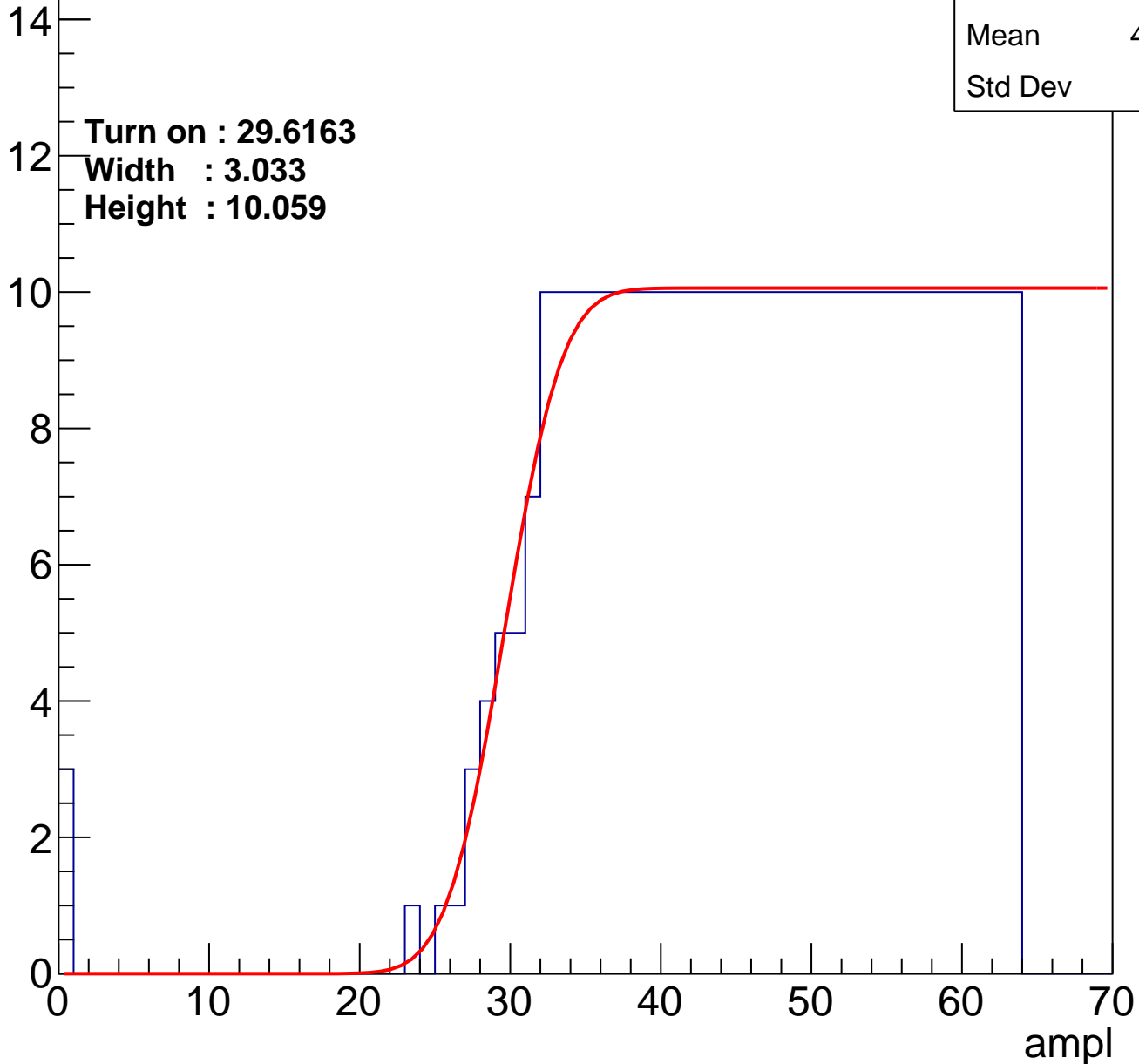
Entries	350
Mean	45.65
Std Dev	11

Turn on : 29.6163

Width : 3.033

Height : 10.059

Entry



B0L002S, U12-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.12
Std Dev	11.51

Turn on : 25.8233

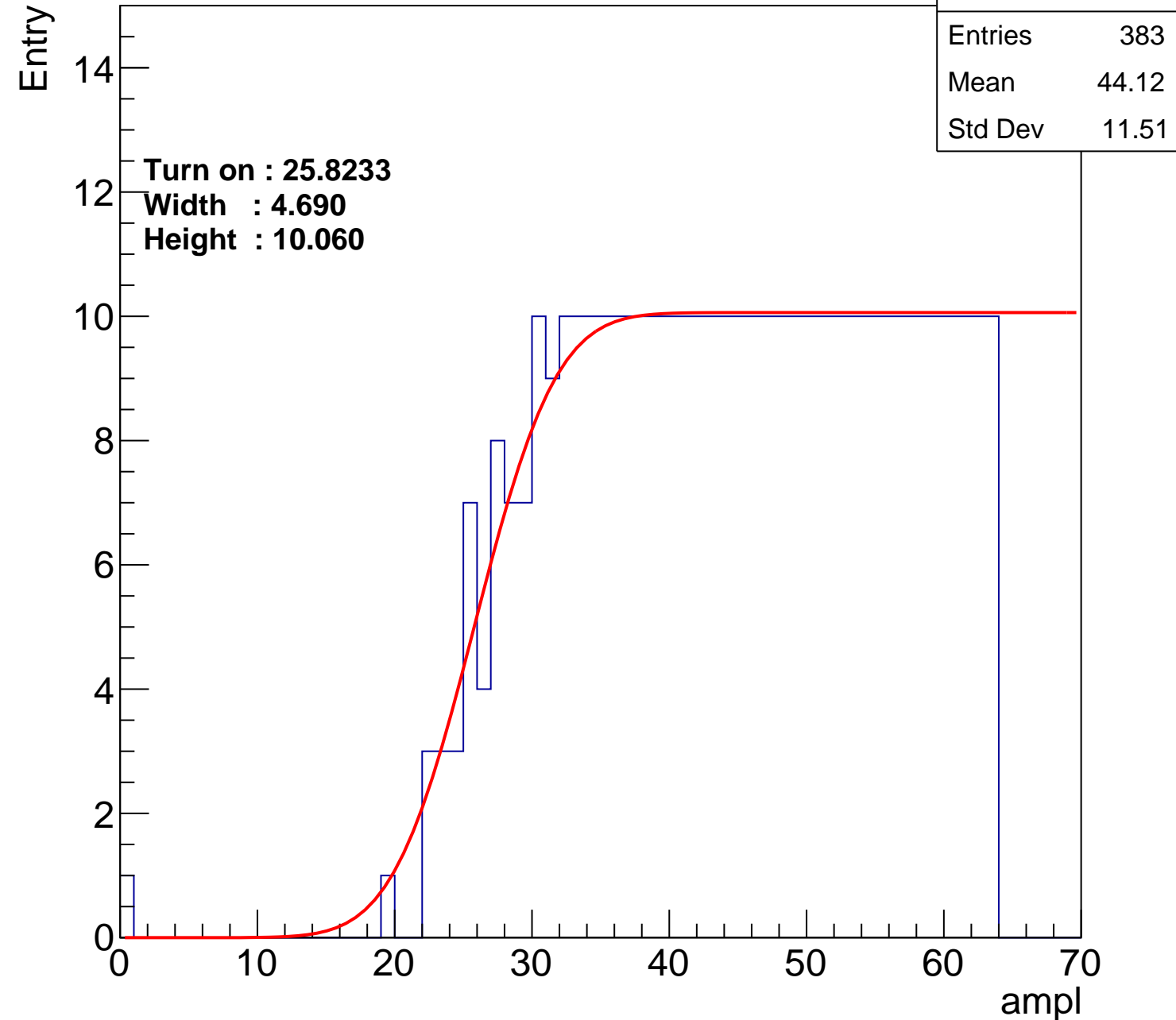
Width : 4.690

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.88
Std Dev	11.23

Turn on : 28.2372

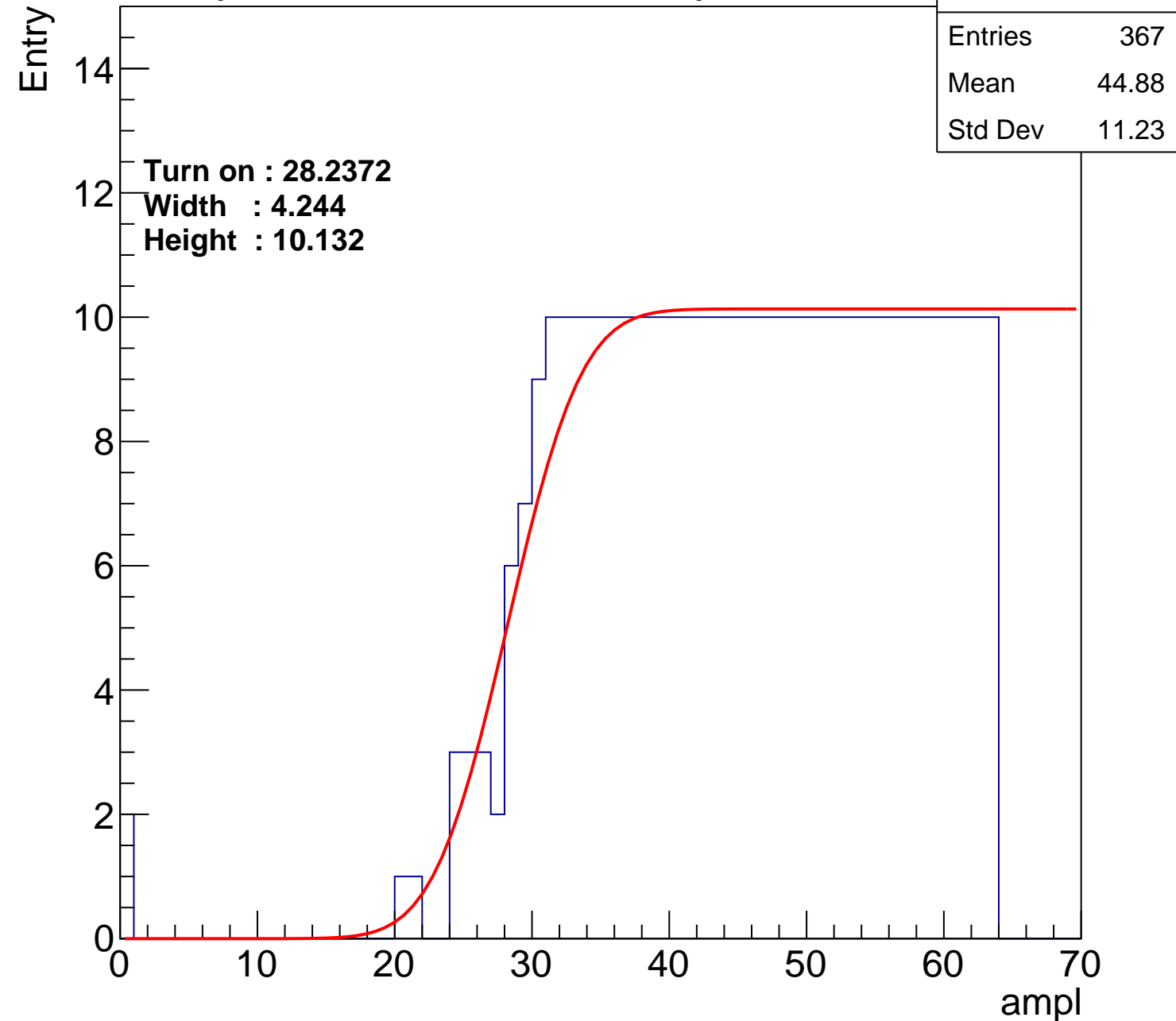
Width : 4.244

Height : 10.132

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch79

calib_packv5_042523_0143.root, FC#8, port C1

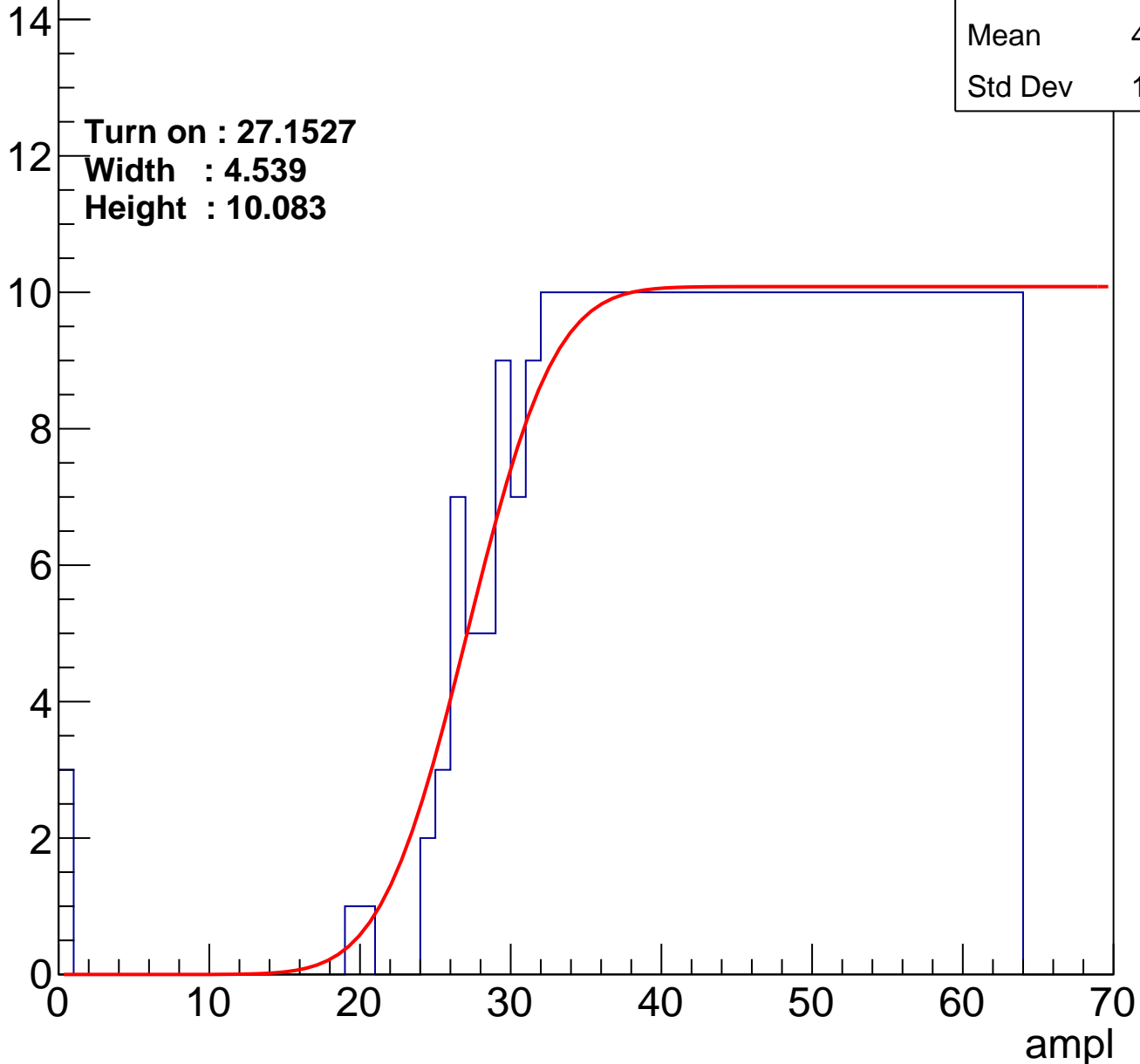
Entries	372
Mean	44.54
Std Dev	11.58

Turn on : 27.1527

Width : 4.539

Height : 10.083

Entry



B0L002S, U12-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 27.4748

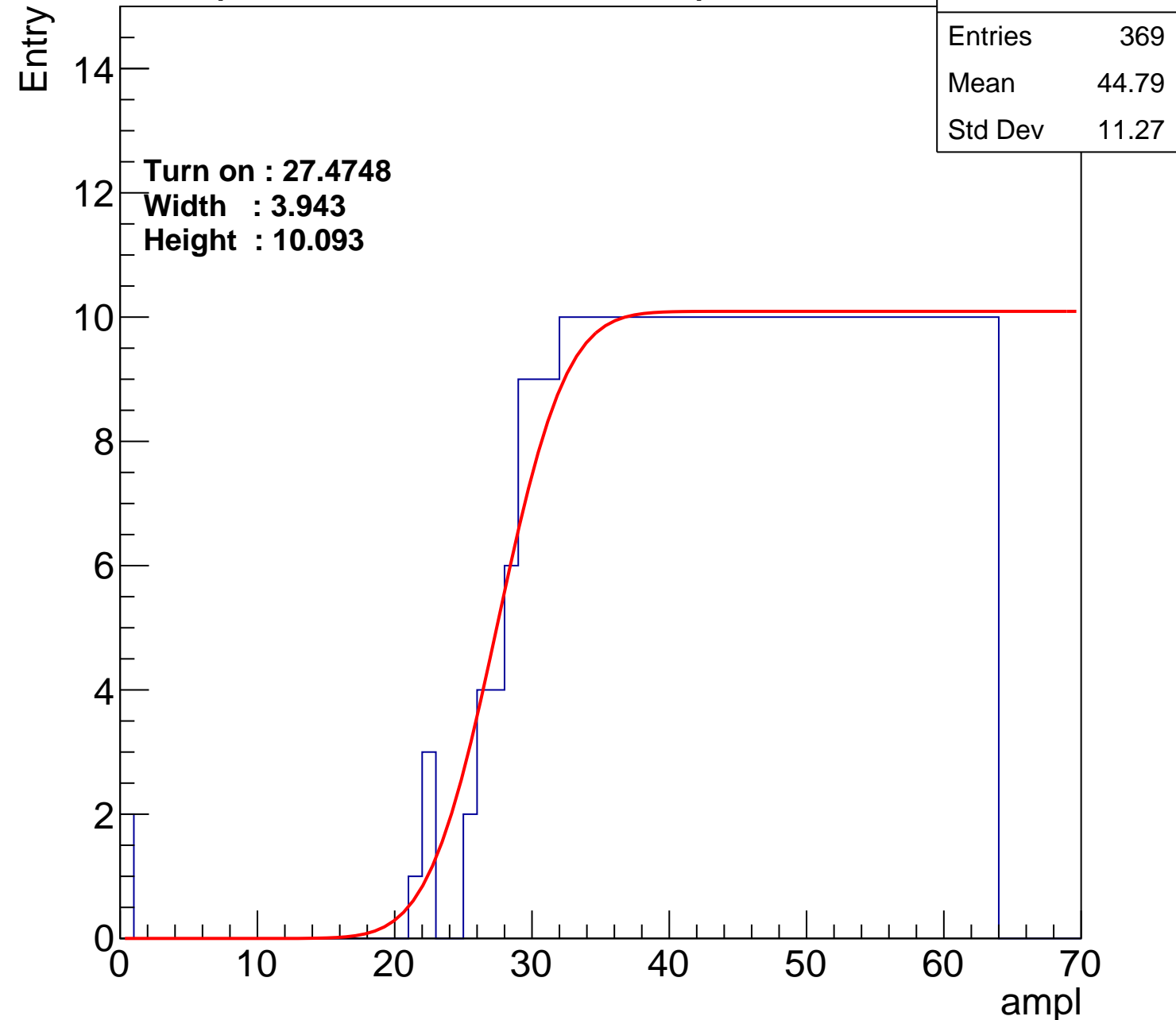
Width : 3.943

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	342
Mean	46.13
Std Dev	10.57

Turn on : 29.9337

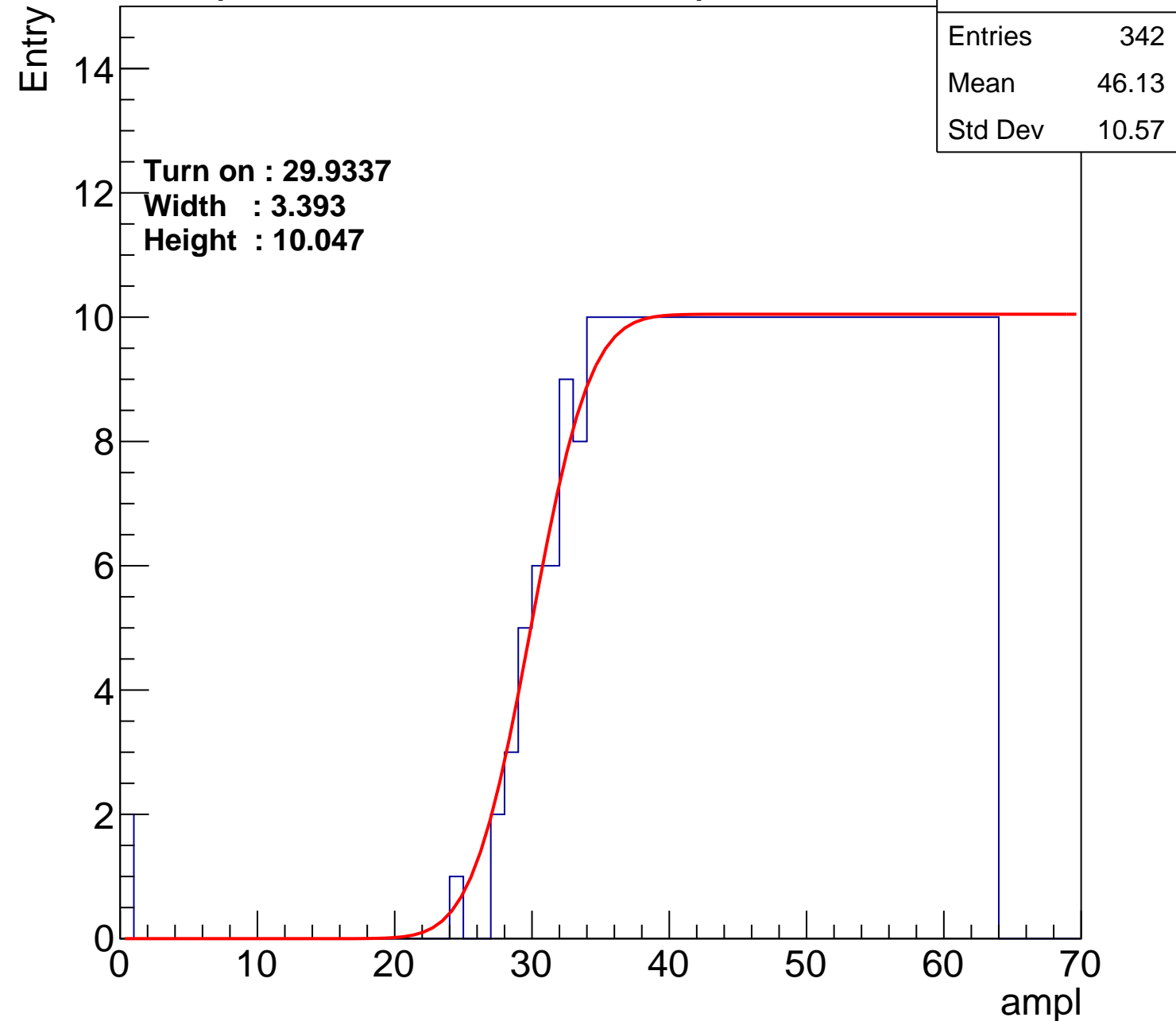
Width : 3.393

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch82

calib_packv5_042523_0143.root, FC#8, port C1

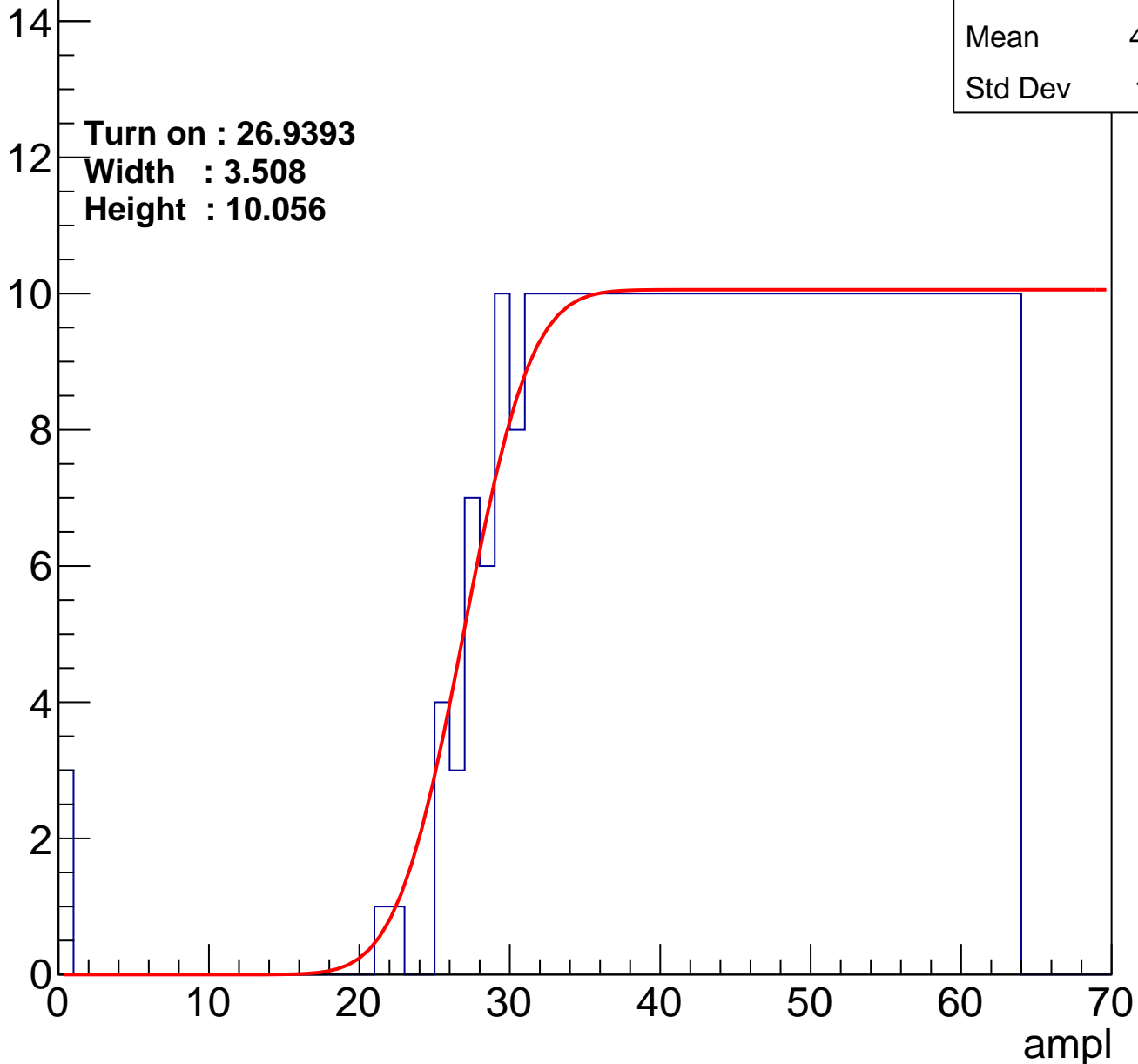
Entries	373
Mean	44.55
Std Dev	11.51

Turn on : 26.9393

Width : 3.508

Height : 10.056

Entry



B0L002S, U12-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.41
Std Dev	11.27

Turn on : 29.0685

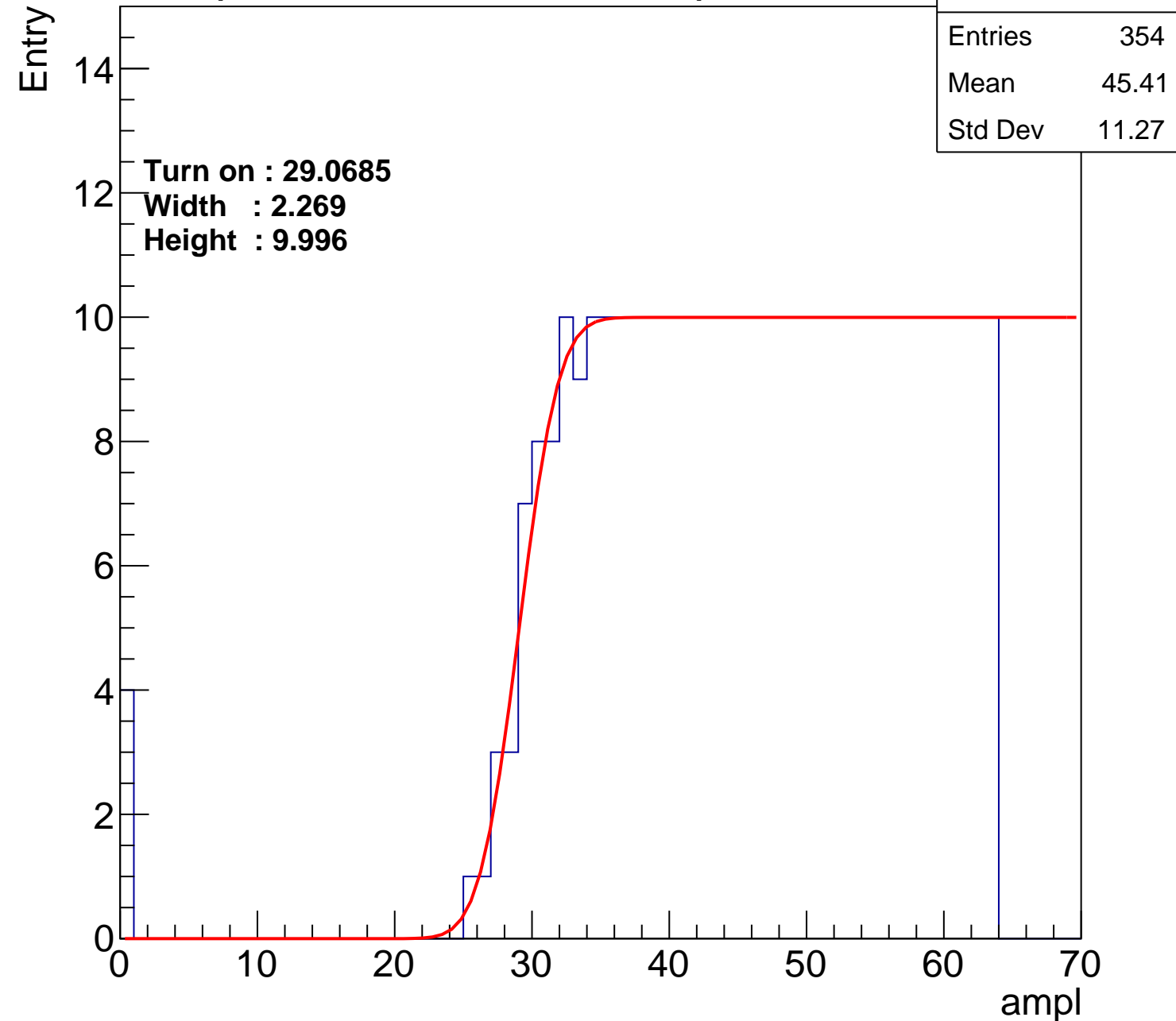
Width : 2.269

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.76
Std Dev	11.86

Turn on : 25.0917

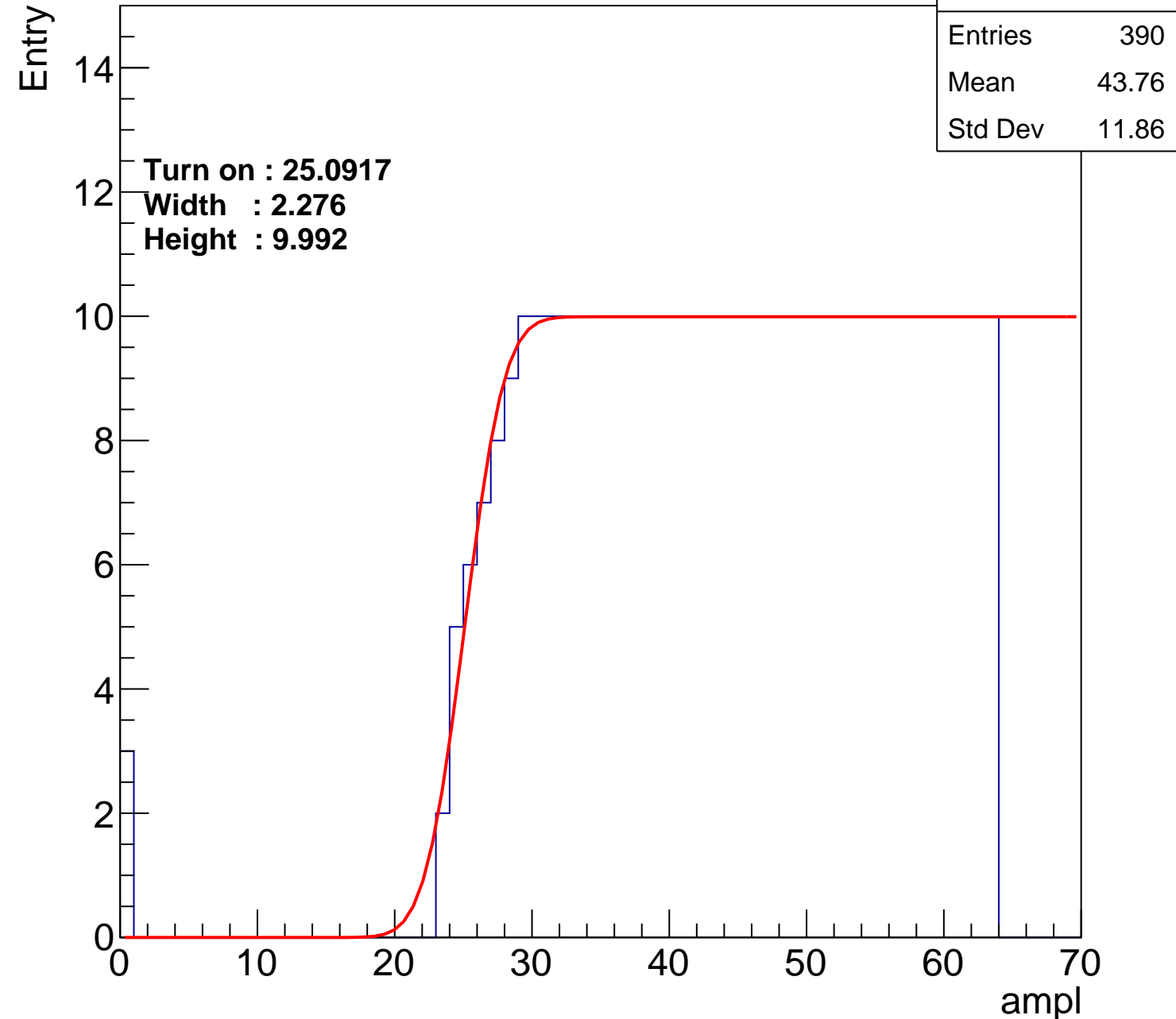
Width : 2.276

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch85

calib_packv5_042523_0143.root, FC#8, port C1

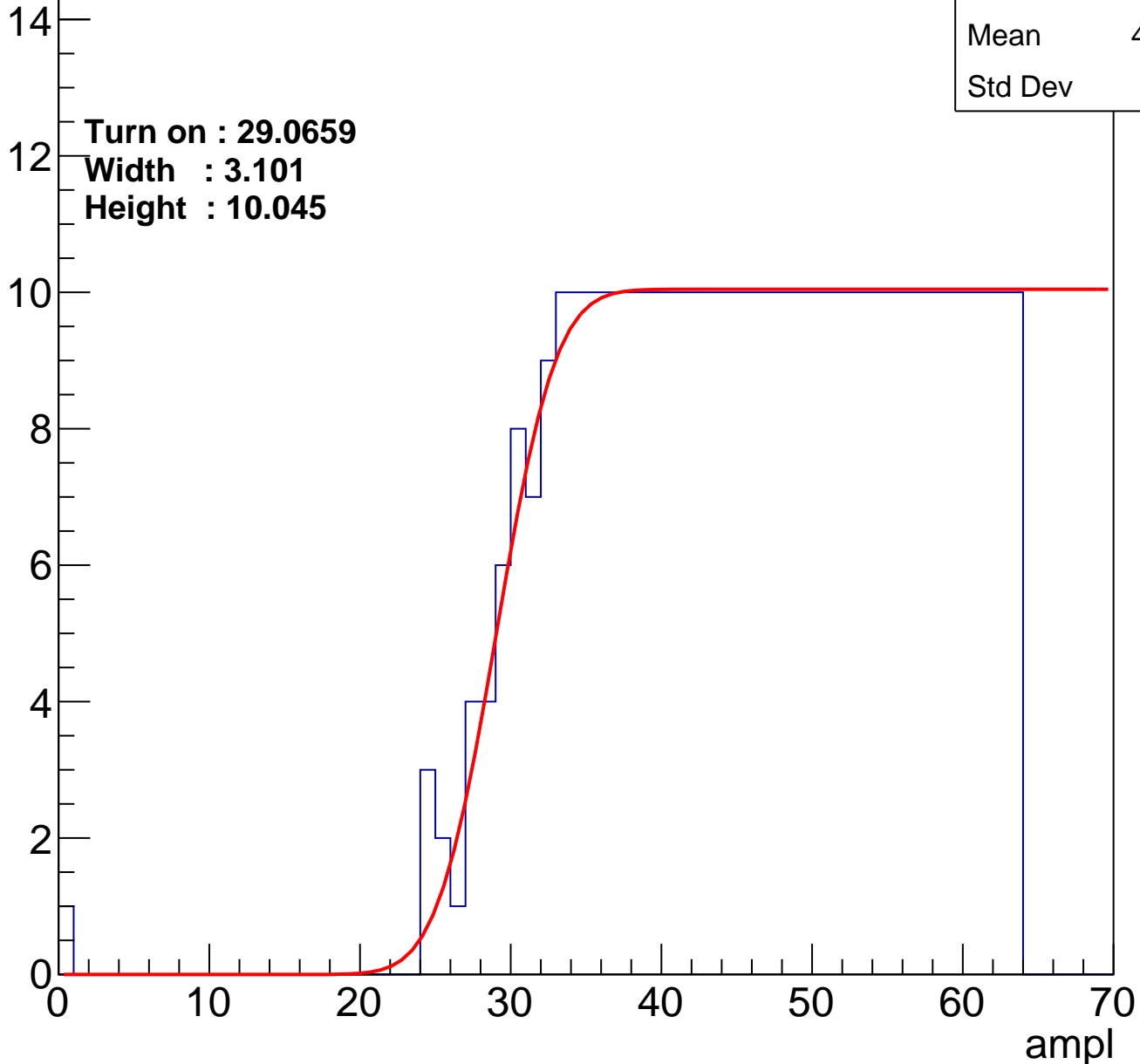
Entries	355
Mean	45.54
Std Dev	10.7

Turn on : 29.0659

Width : 3.101

Height : 10.045

Entry



B0L002S, U12-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.63
Std Dev	10.76

Turn on : 28.7552

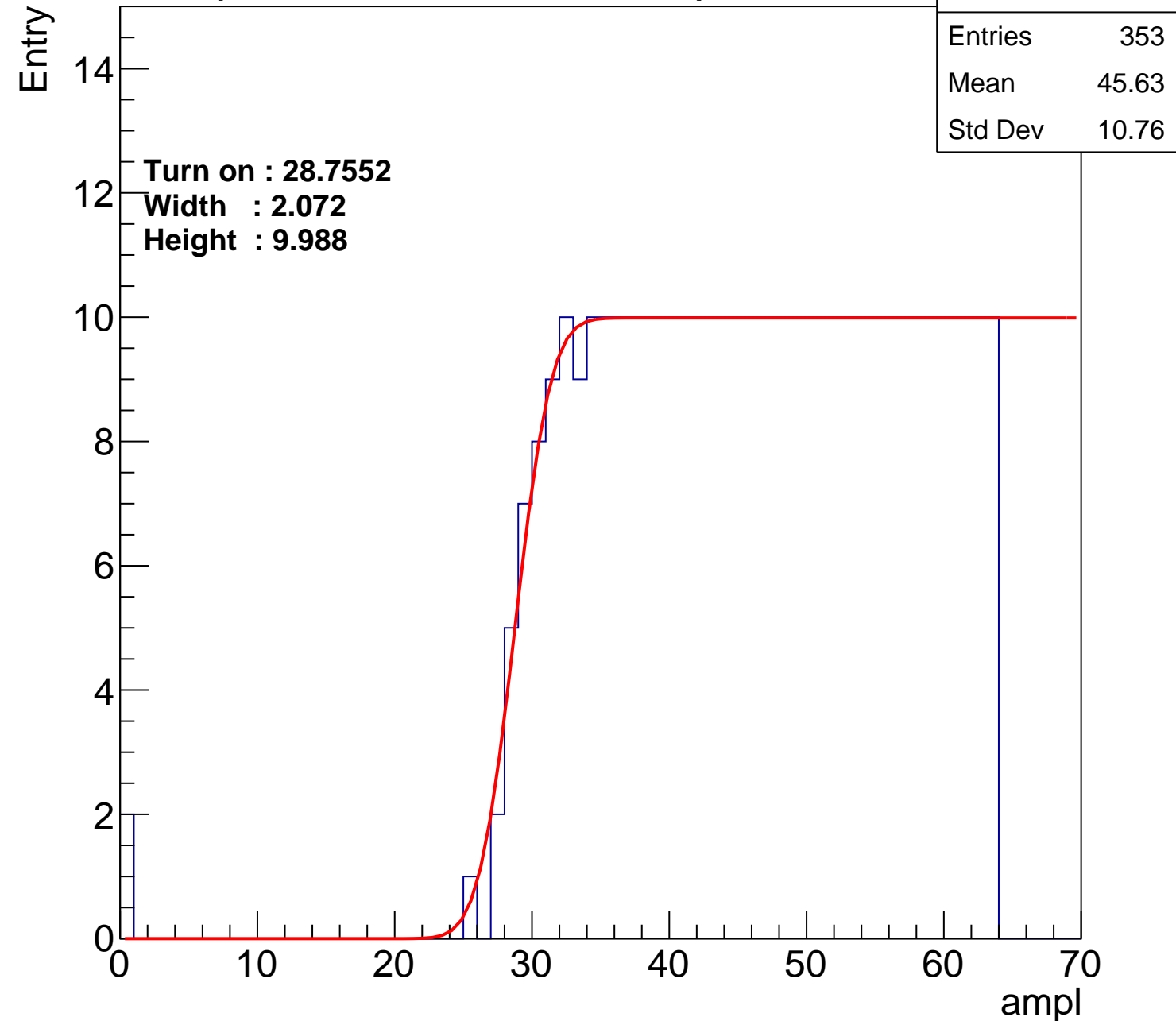
Width : 2.072

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.49
Std Dev	10.88

Turn on : 29.1636

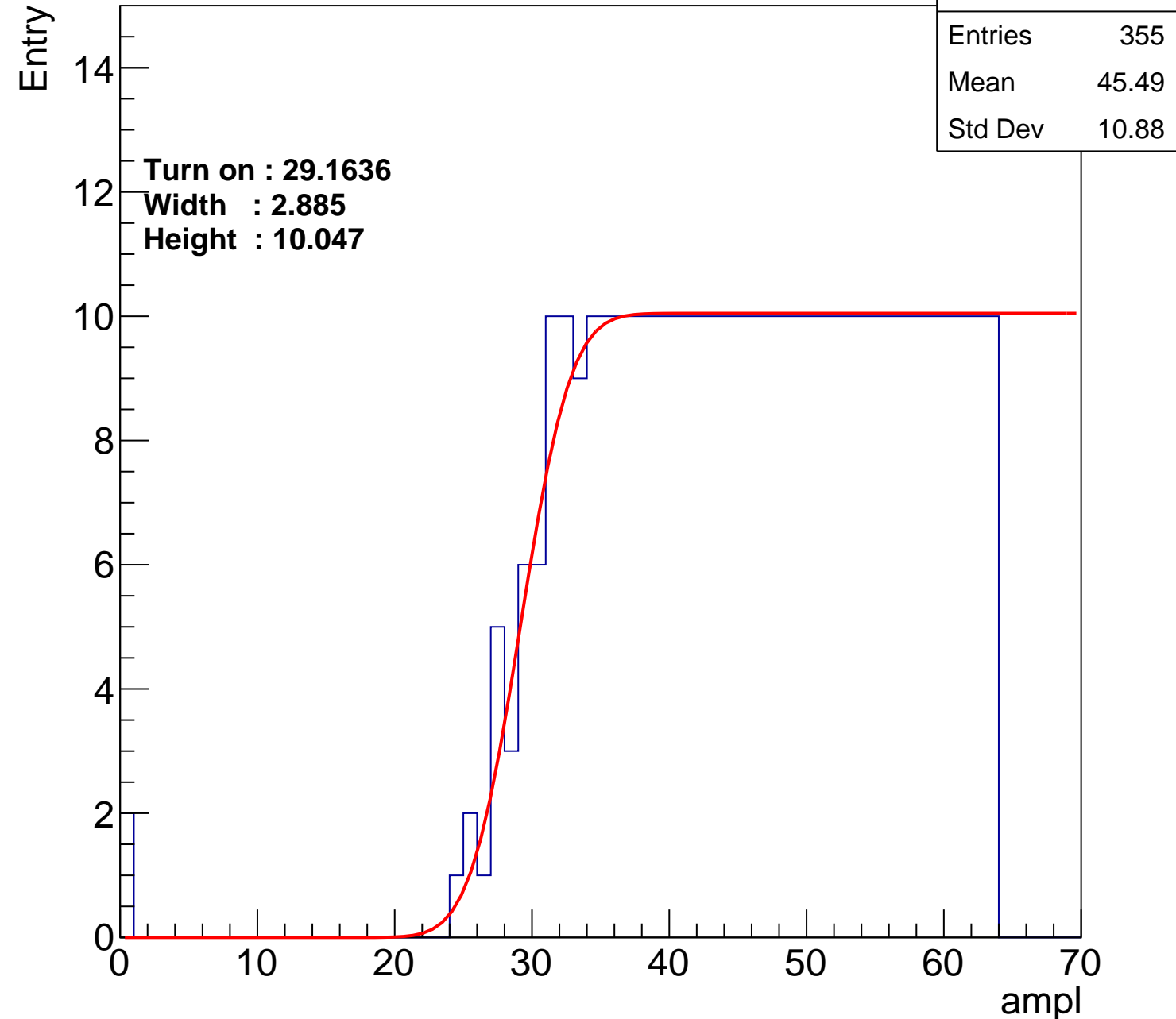
Width : 2.885

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch88

calib_packv5_042523_0143.root, FC#8, port C1

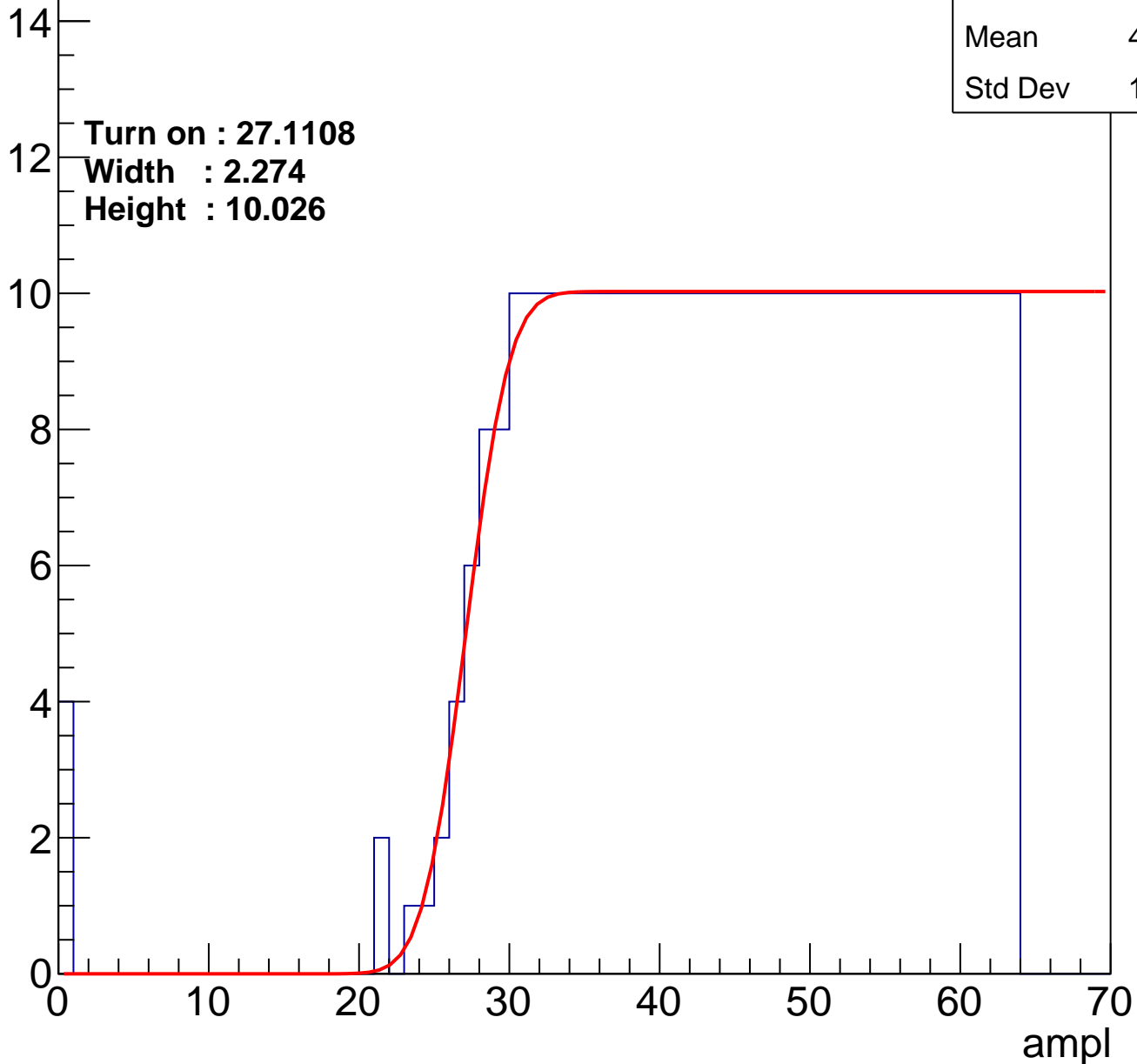
Entries	376
Mean	44.34
Std Dev	11.77

Turn on : 27.1108

Width : 2.274

Height : 10.026

Entry



B0L002S, U12-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.54
Std Dev	11.22

Turn on : 29.3750

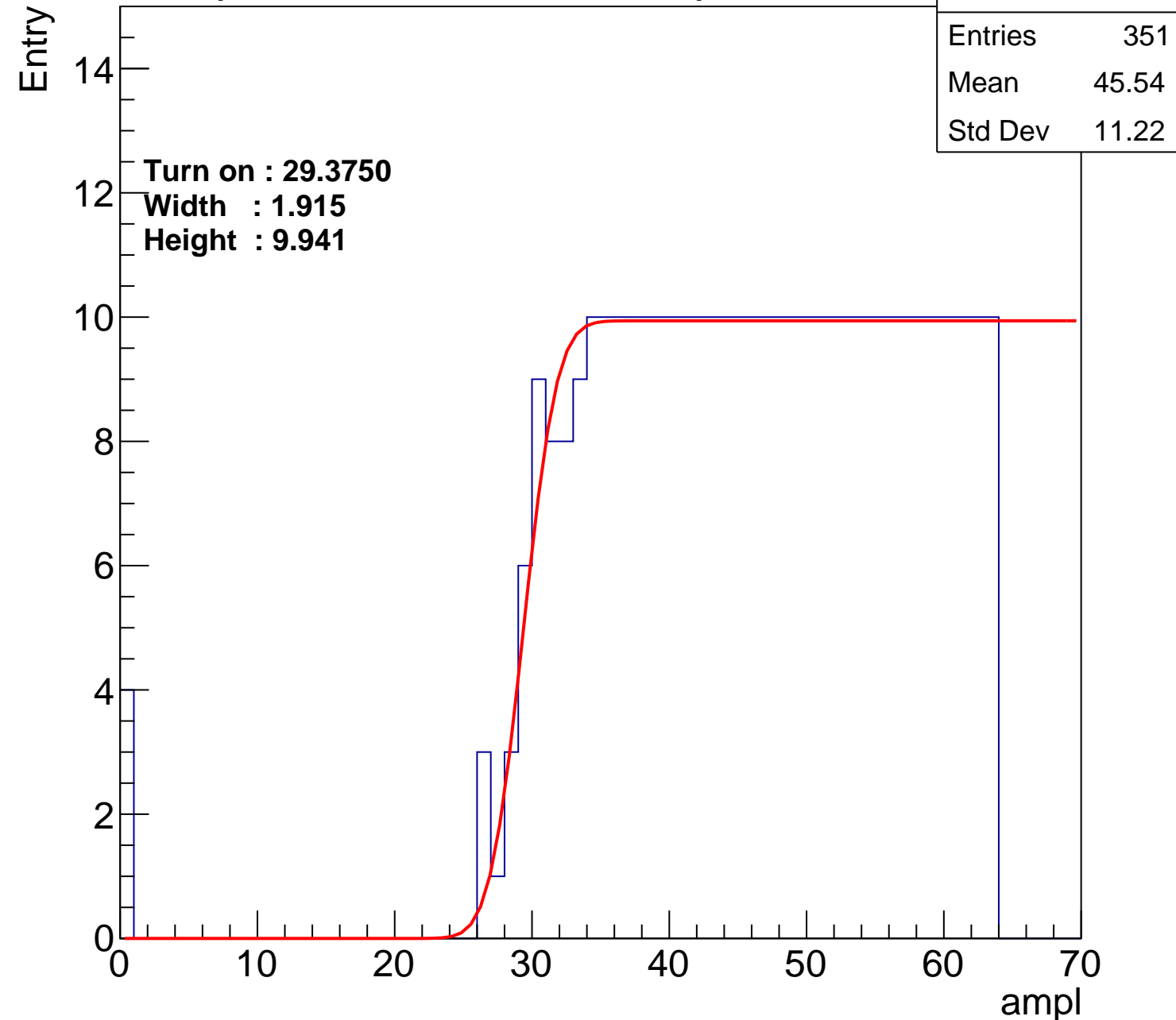
Width : 1.915

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.5275

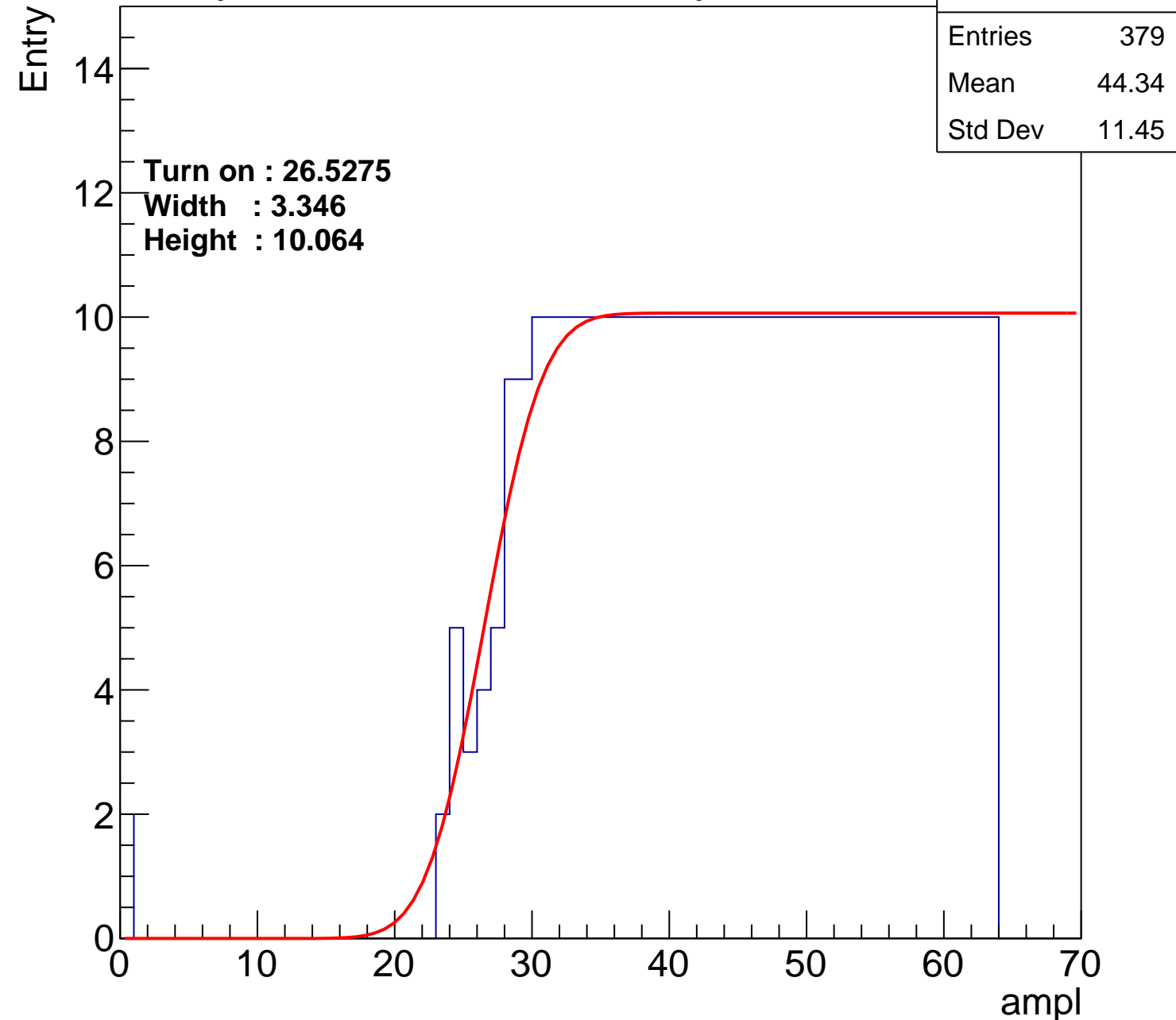
Width : 3.346

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.53
Std Dev	11.73

Turn on : 27.4234

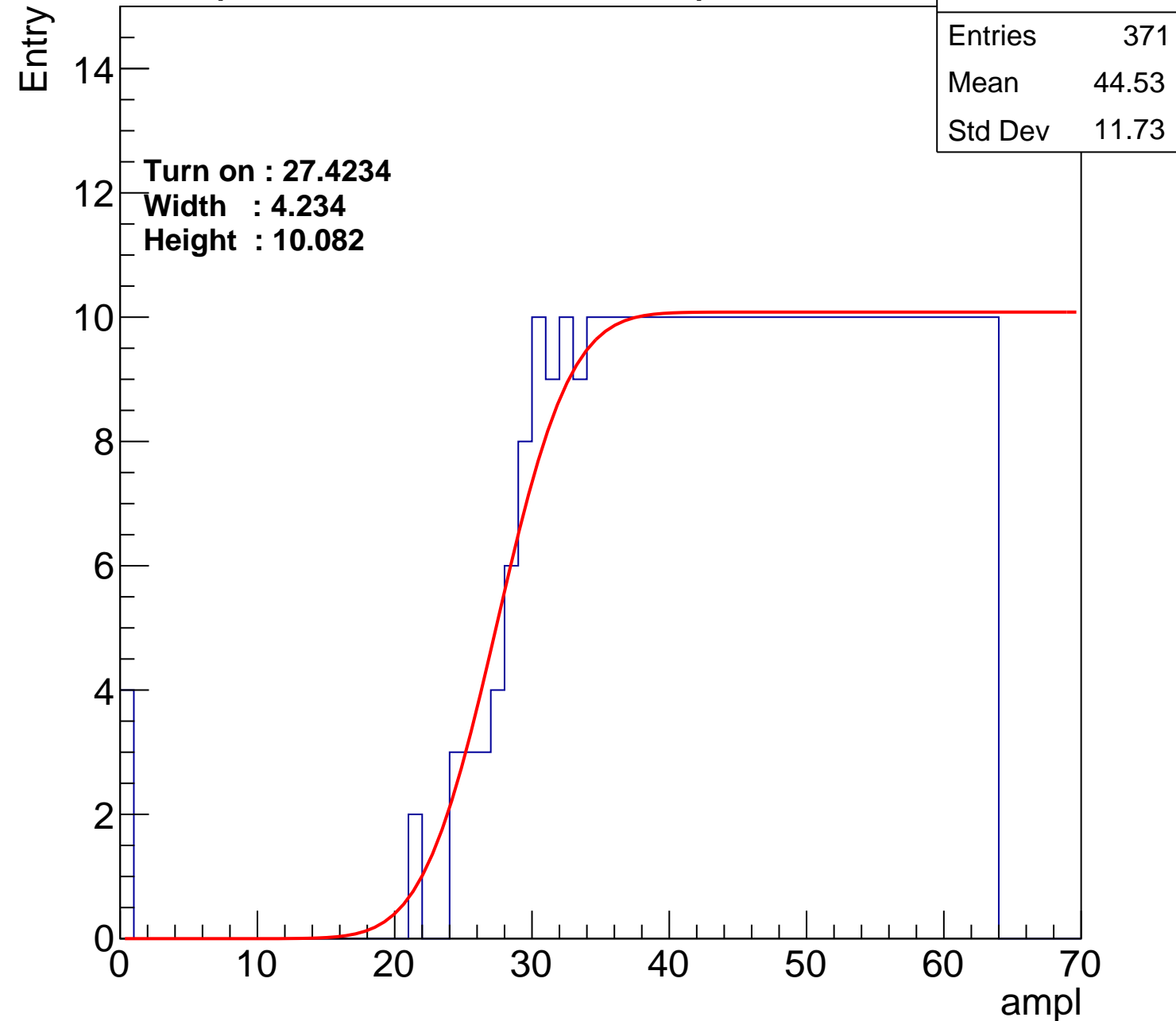
Width : 4.234

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.2172

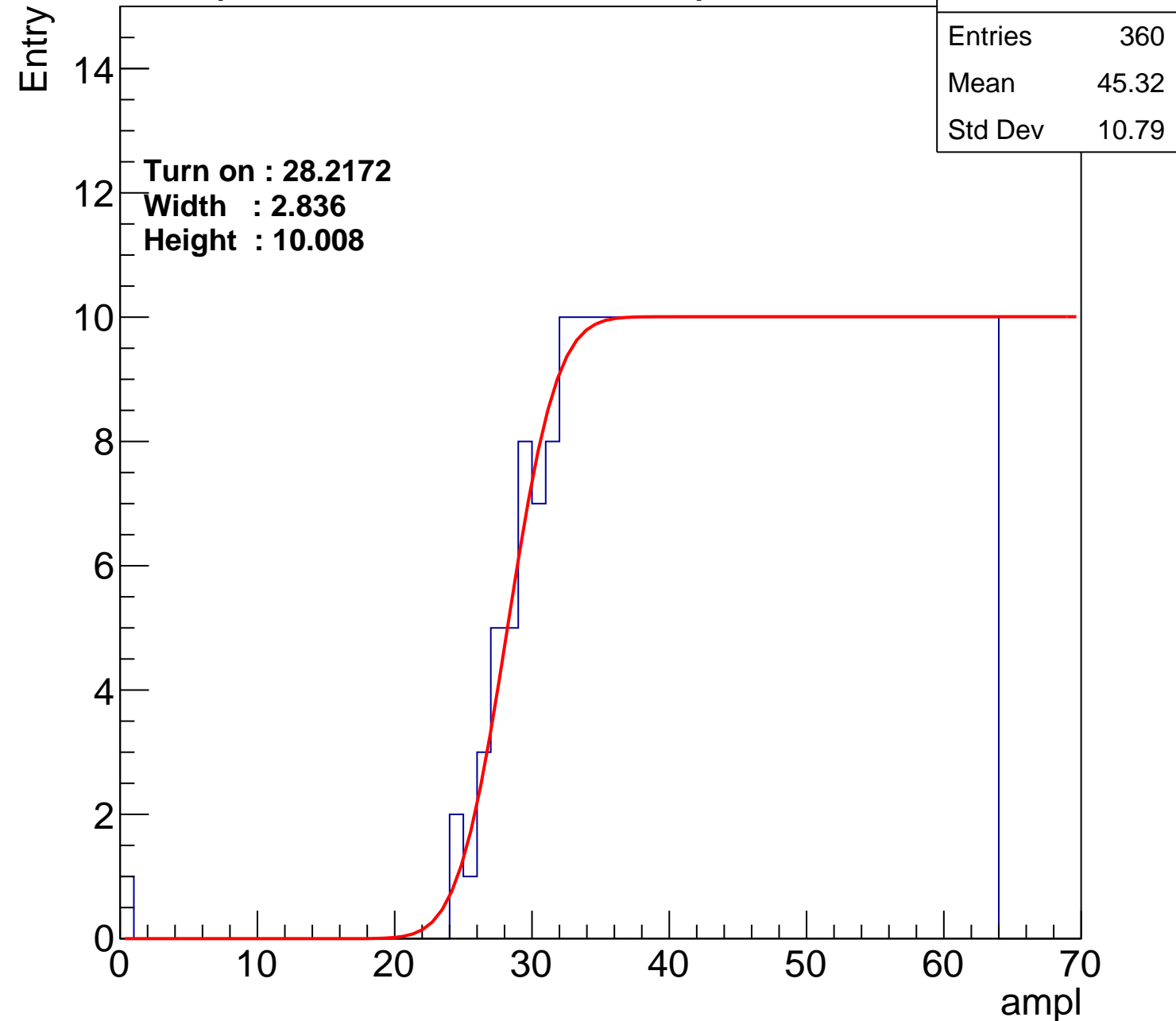
Width : 2.836

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch93

calib_packv5_042523_0143.root, FC#8, port C1

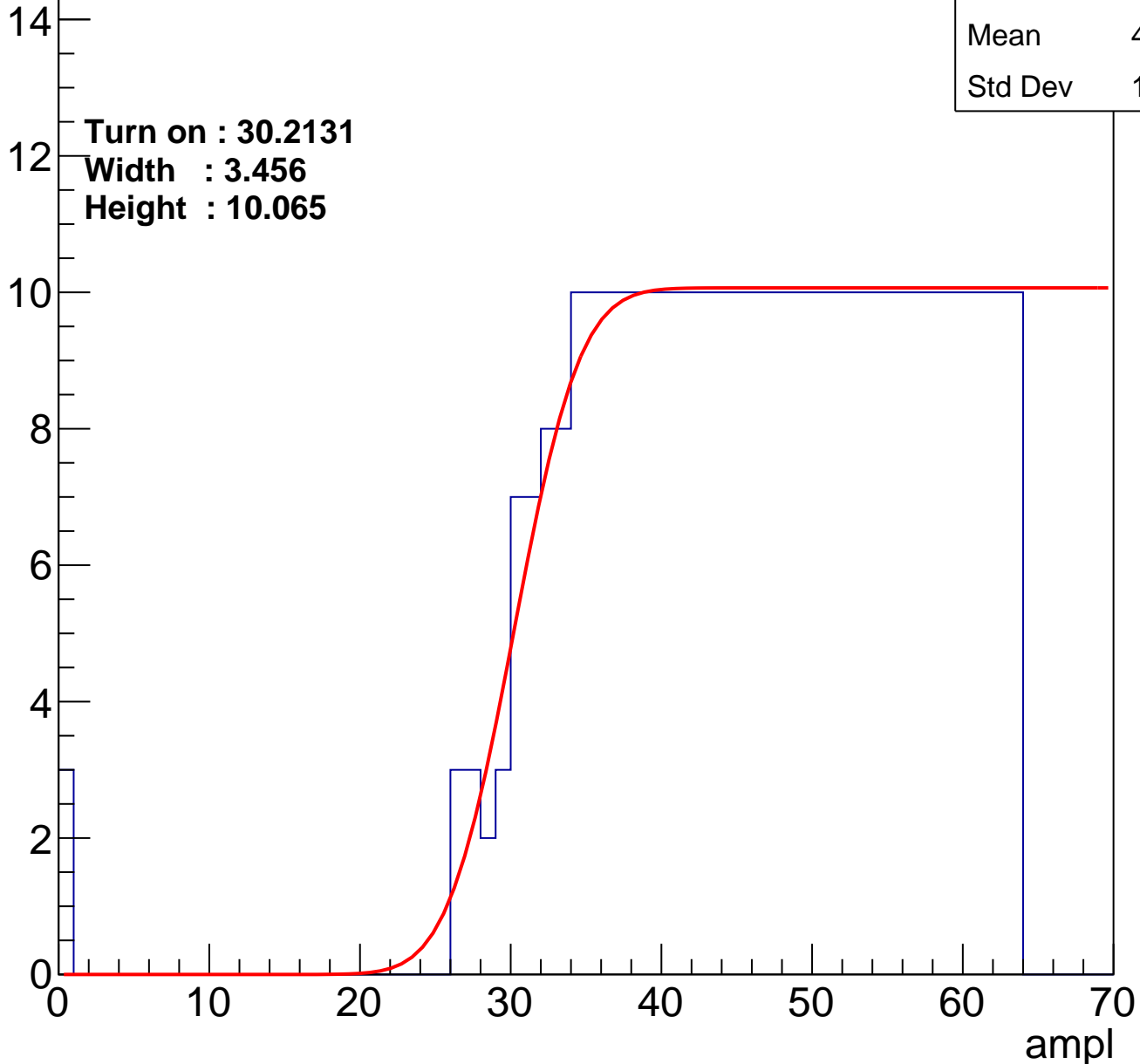
Entries	344
Mean	45.93
Std Dev	10.89

Turn on : 30.2131

Width : 3.456

Height : 10.065

Entry



B0L002S, U12-ch94

calib_packv5_042523_0143.root, FC#8, port C1

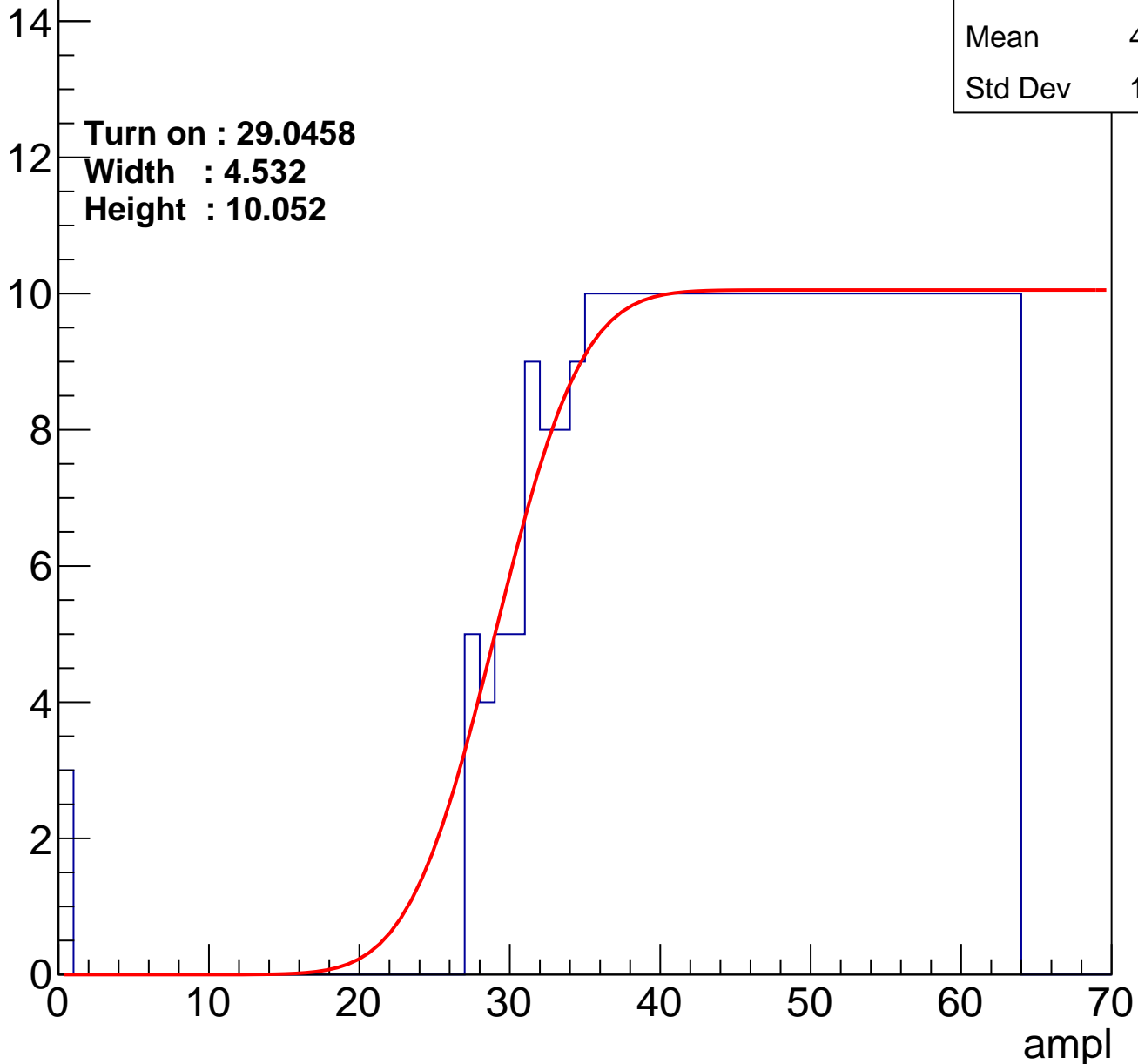
Entries	346
Mean	45.83
Std Dev	10.93

Turn on : 29.0458

Width : 4.532

Height : 10.052

Entry



calib_packv5_042523_0143.root, FC#8, port C1

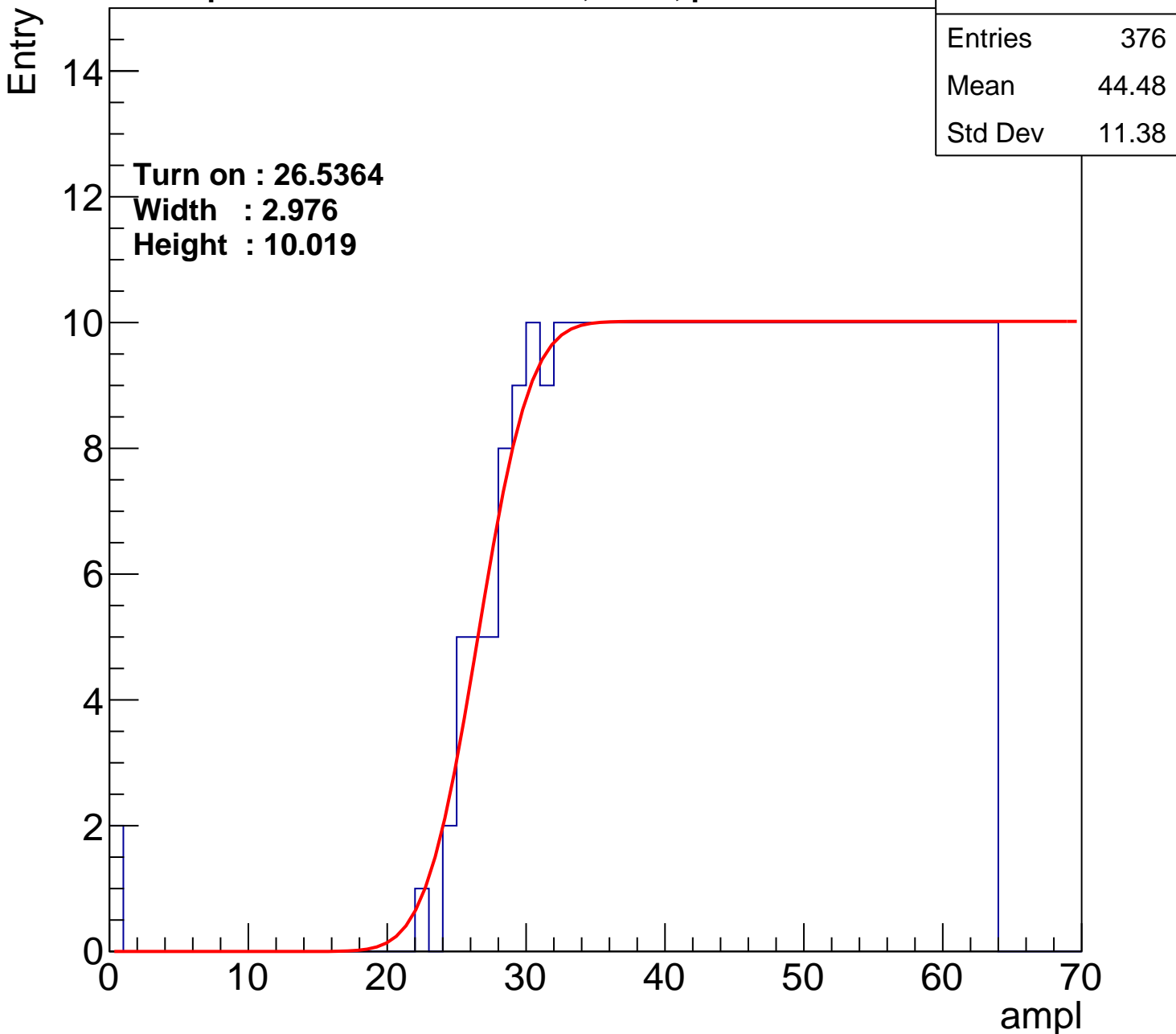
Entries	376
Mean	44.48
Std Dev	11.38

Mean	44.48
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Std Dev	11.38
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Width : 2.976

Height : 10.019



B0L002S, U12-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.27
Std Dev	10.27

Turn on : 30.3544

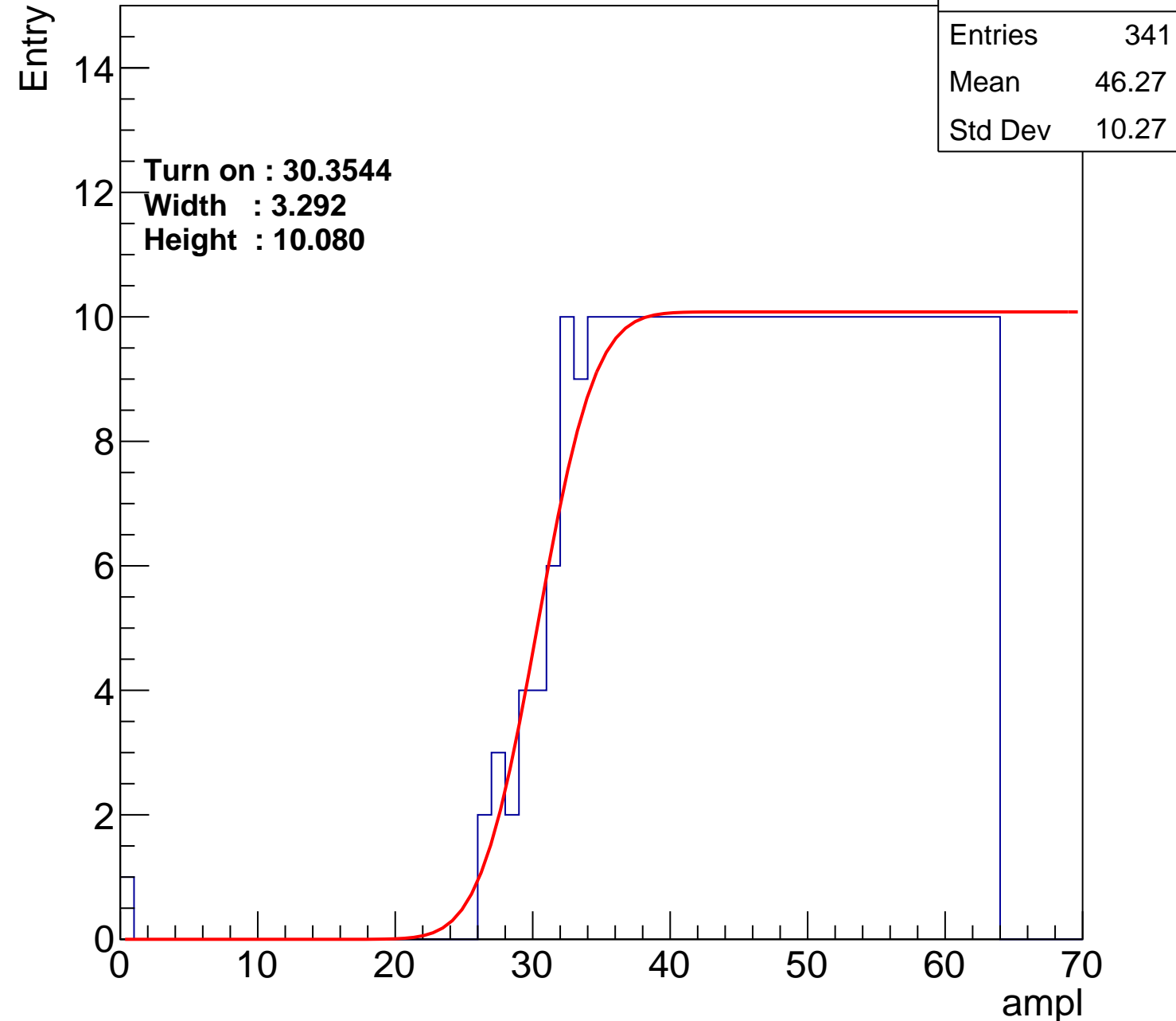
Width : 3.292

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch97

calib_packv5_042523_0143.root, FC#8, port C1

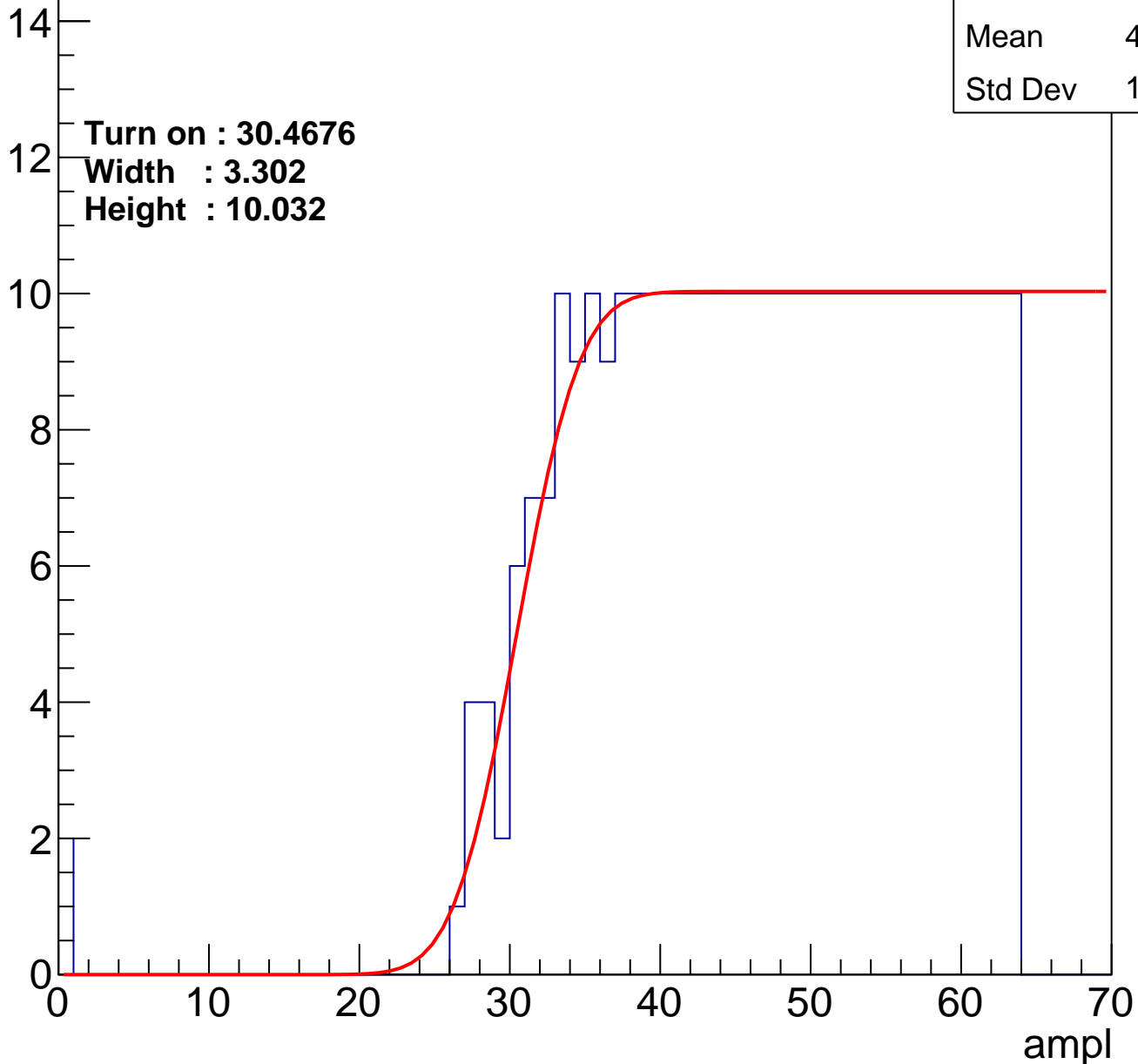
Entries	341
Mean	46.14
Std Dev	10.59

Turn on : 30.4676

Width : 3.302

Height : 10.032

Entry



B0L002S, U12-ch98

calib_packv5_042523_0143.root, FC#8, port C1

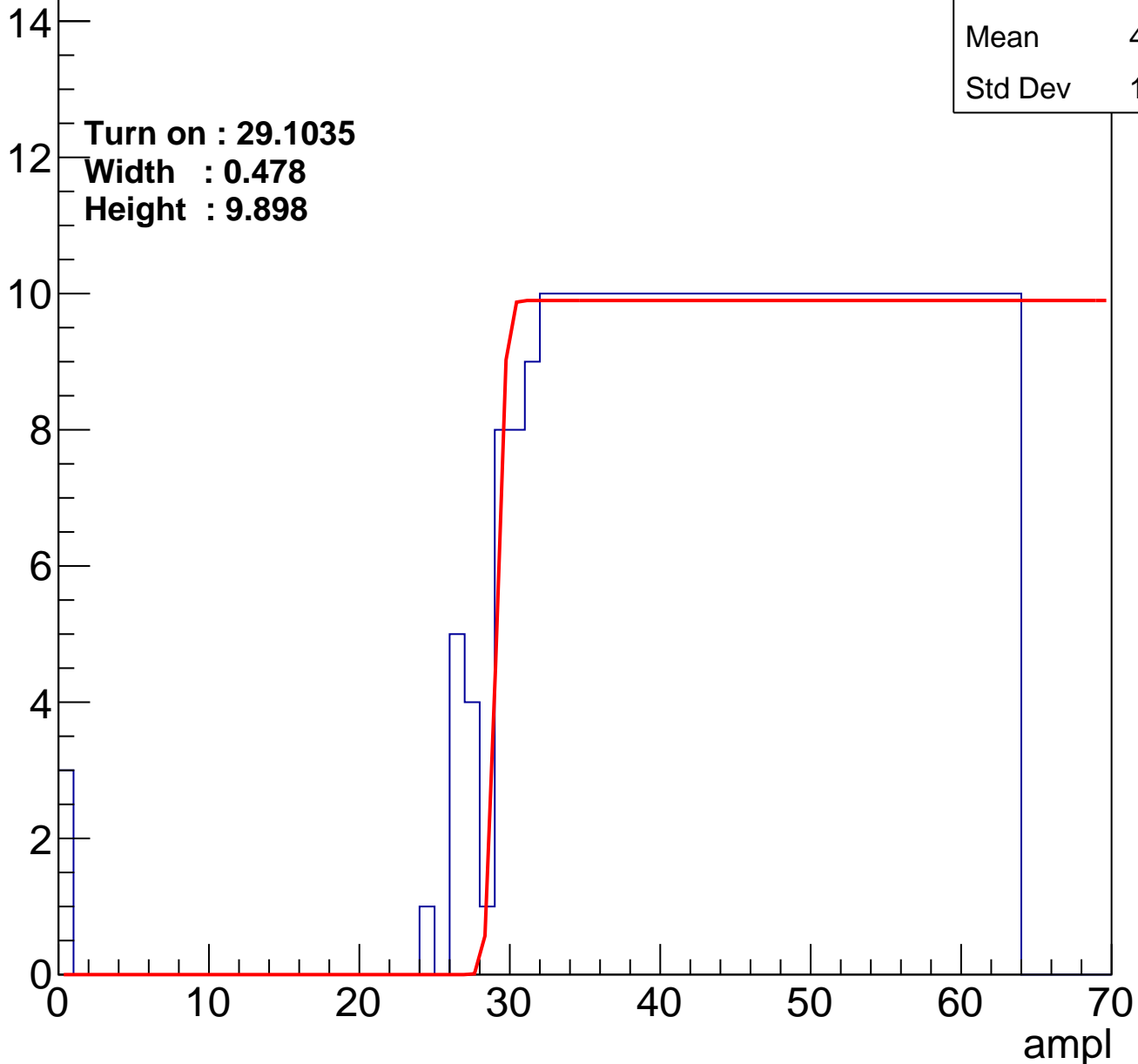
Entries	359
Mean	45.24
Std Dev	11.17

Turn on : 29.1035

Width : 0.478

Height : 9.898

Entry



B0L002S, U12-ch99

calib_packv5_042523_0143.root, FC#8, port C1

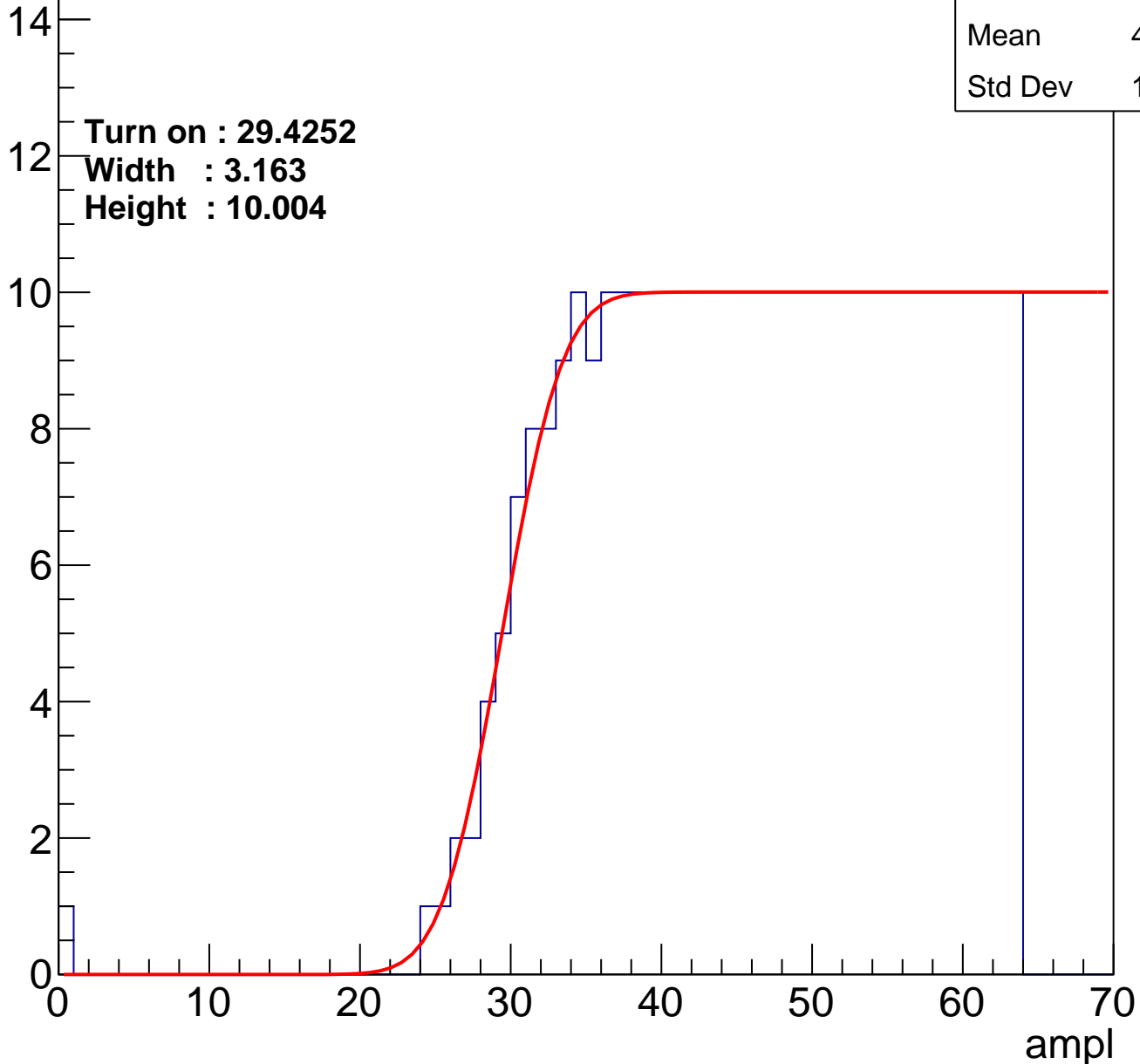
Entries	347
Mean	45.93
Std Dev	10.49

Turn on : 29.4252

Width : 3.163

Height : 10.004

Entry



B0L002S, U12-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	339
Mean	46.36
Std Dev	10.23

Turn on : 30.5720

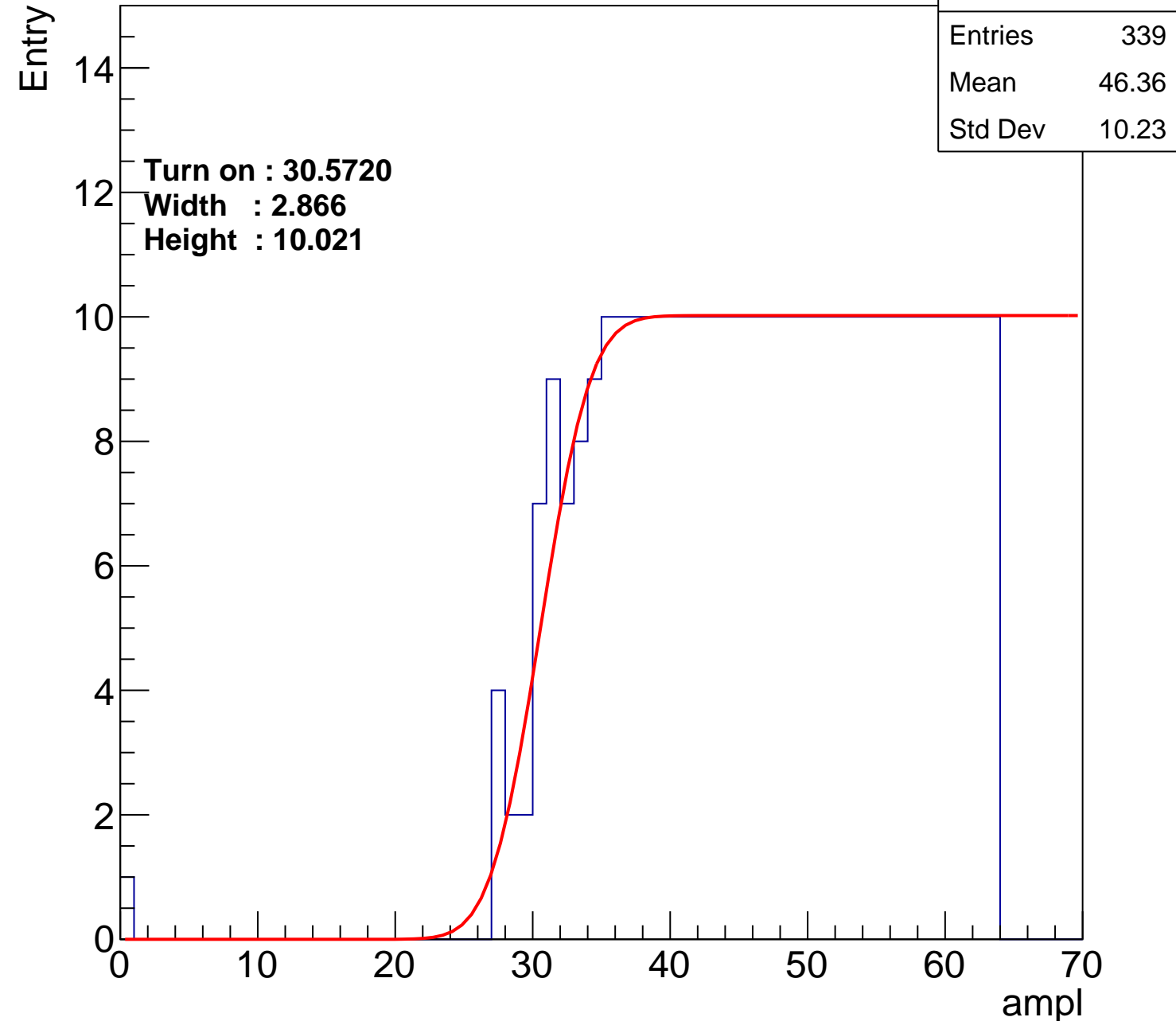
Width : 2.866

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.65
Std Dev	10.63

Turn on : 29.1413

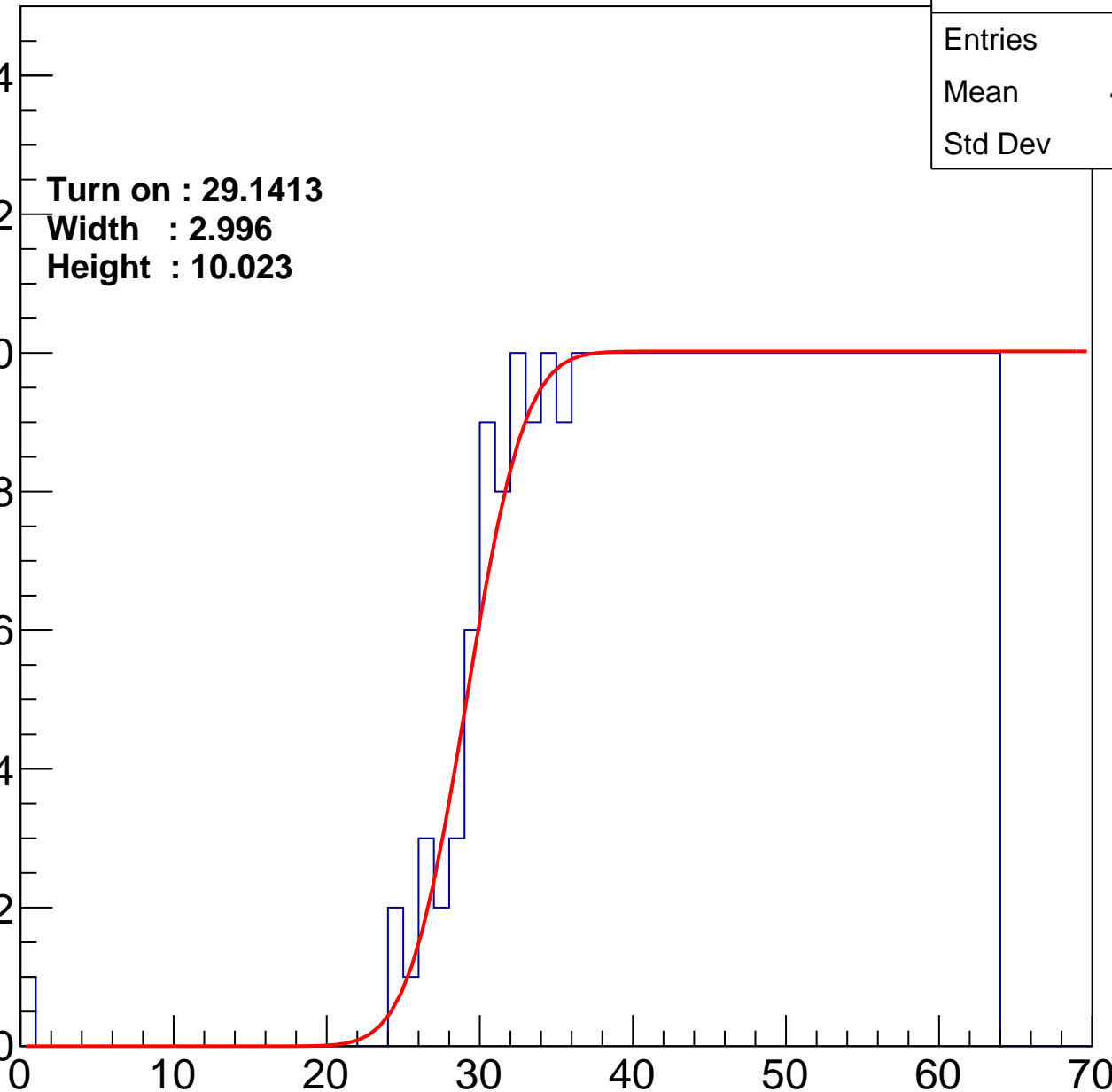
Width : 2.996

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	45.95
Std Dev	10.9

Turn on : 30.6818

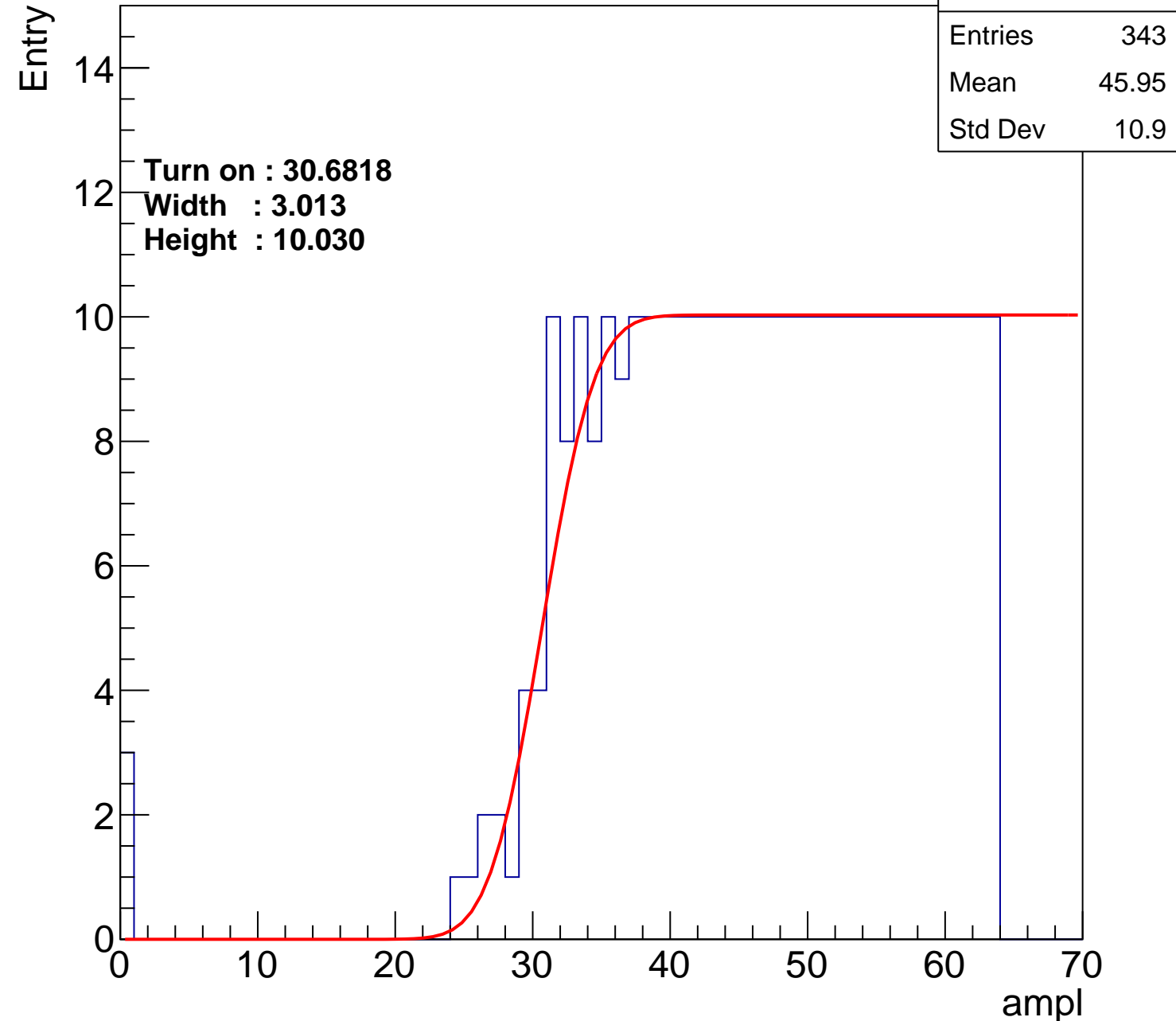
Width : 3.013

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.84
Std Dev	11.38

Turn on : 28.3408

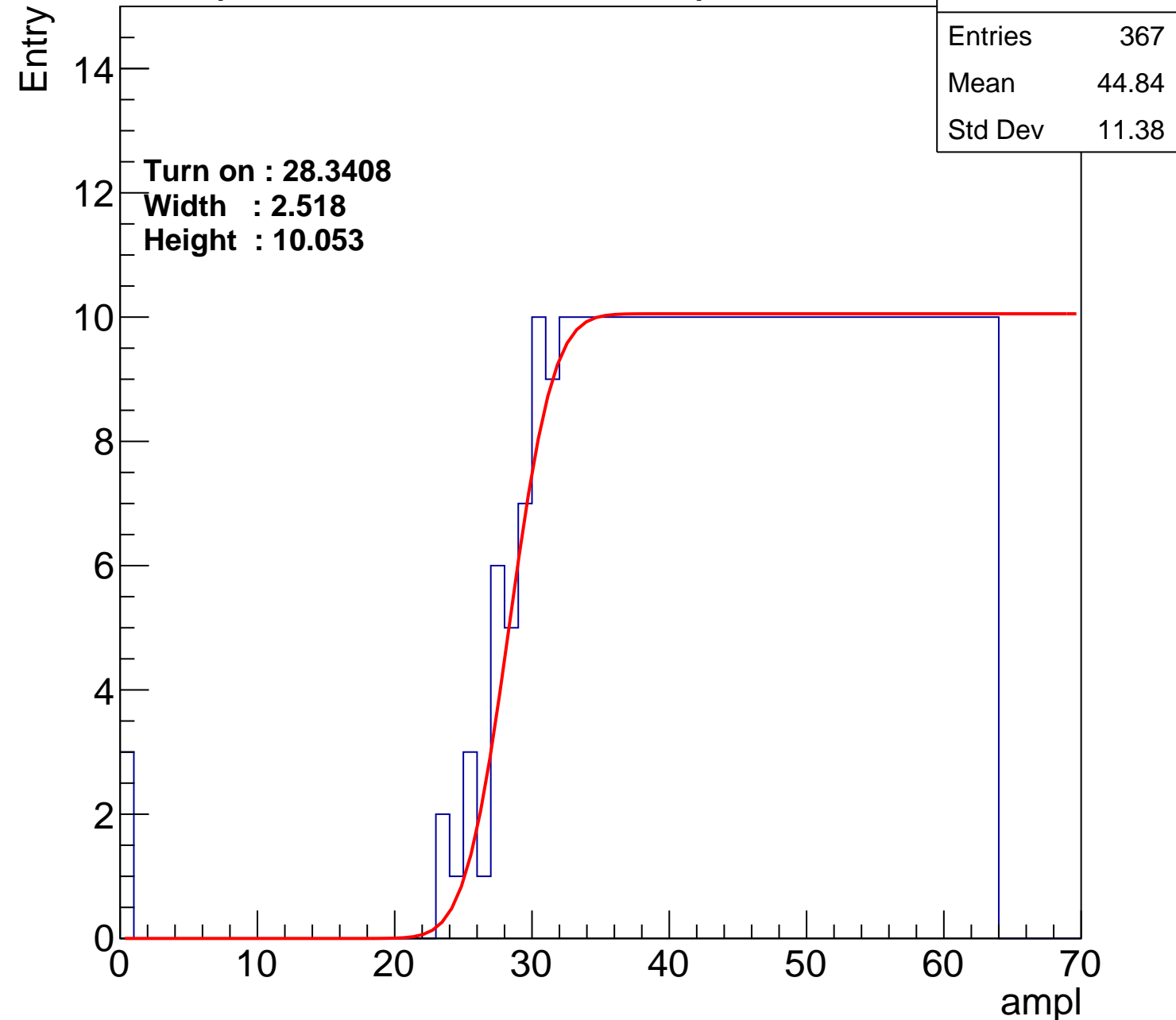
Width : 2.518

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.0466

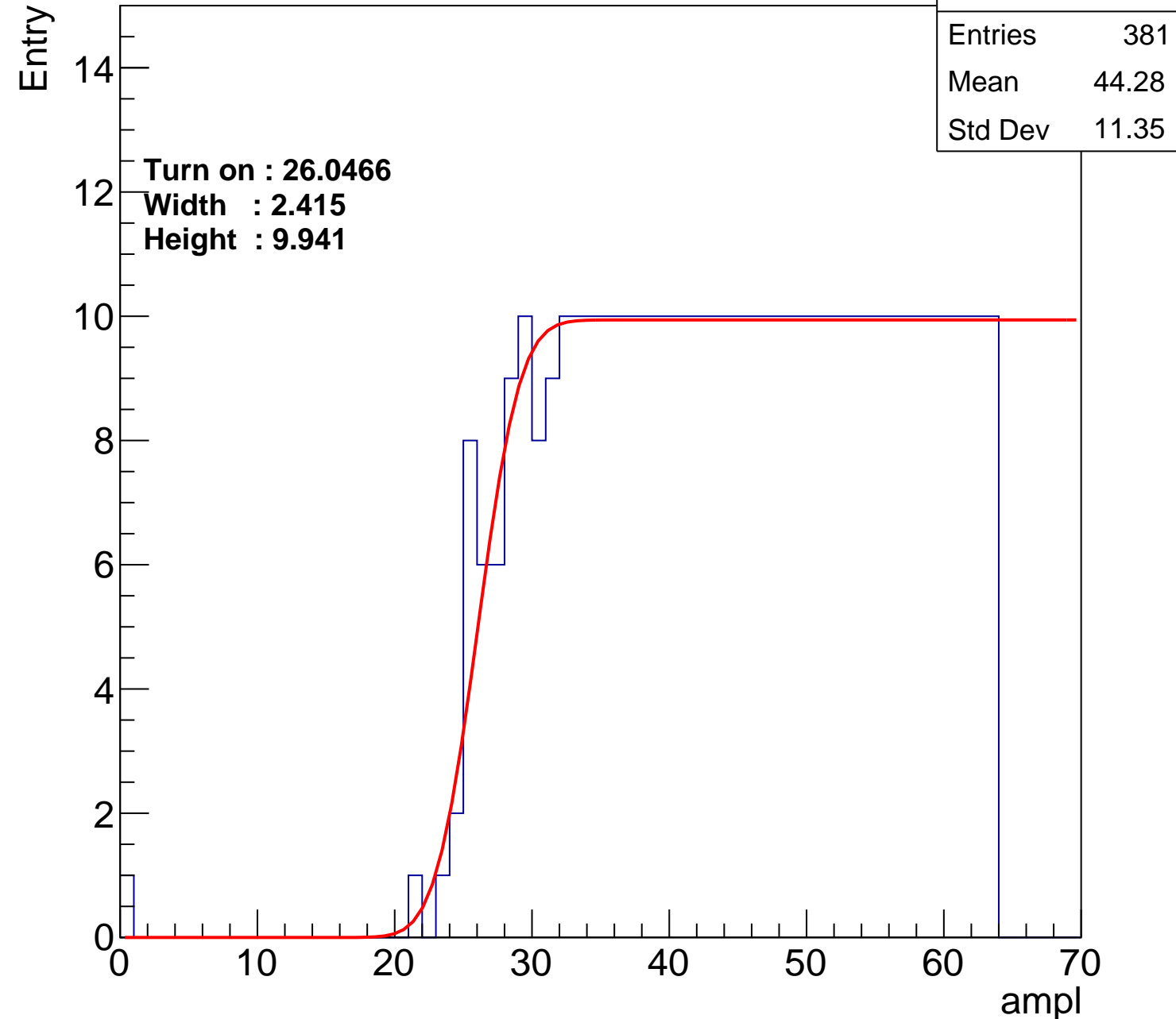
Width : 2.415

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.79
Std Dev	10.73

Turn on : 29.7433

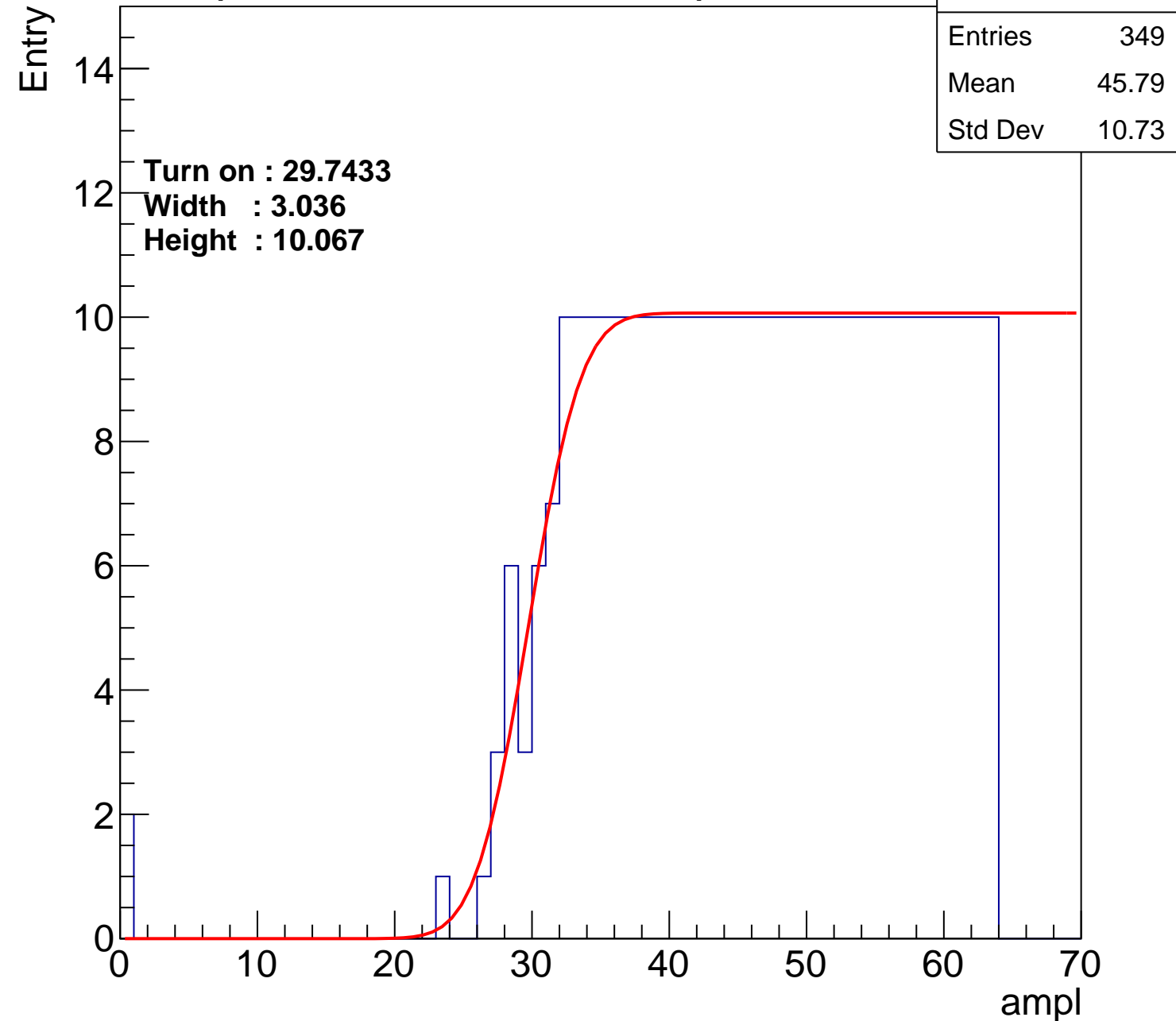
Width : 3.036

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.88
Std Dev	10.47

Turn on : 29.3352

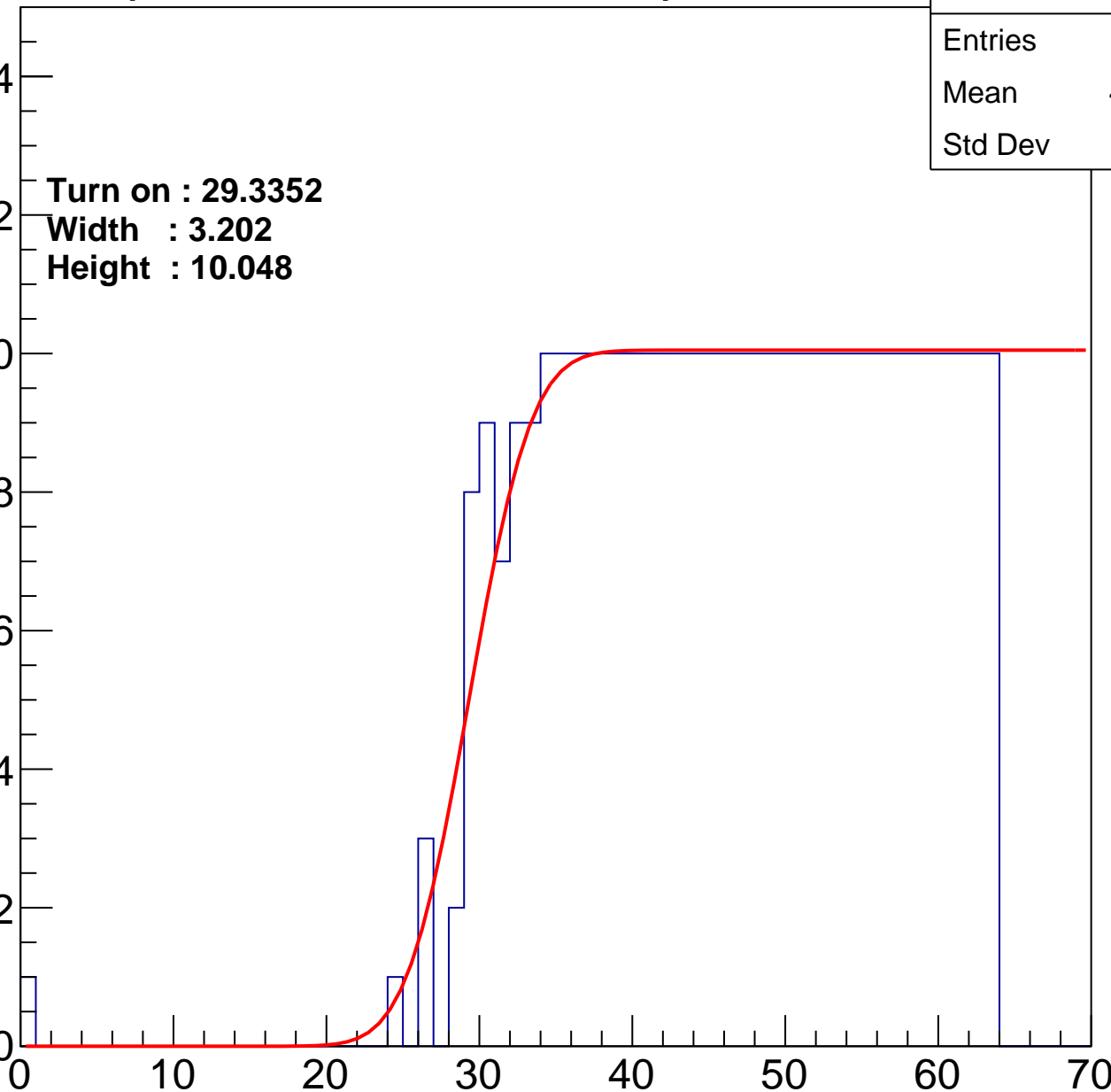
Width : 3.202

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.63
Std Dev	11.86

Turn on : 28.3005

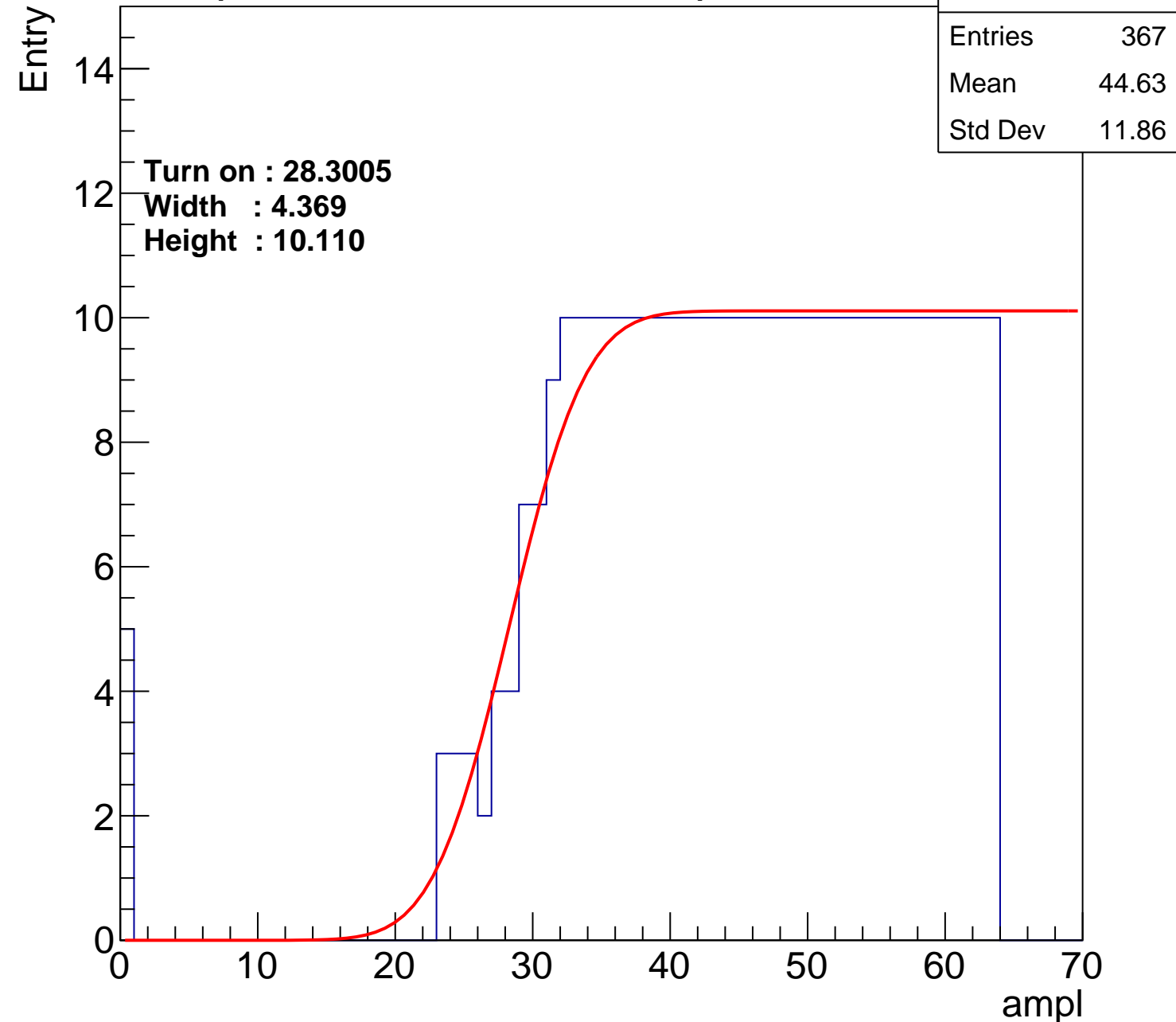
Width : 4.369

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.2748

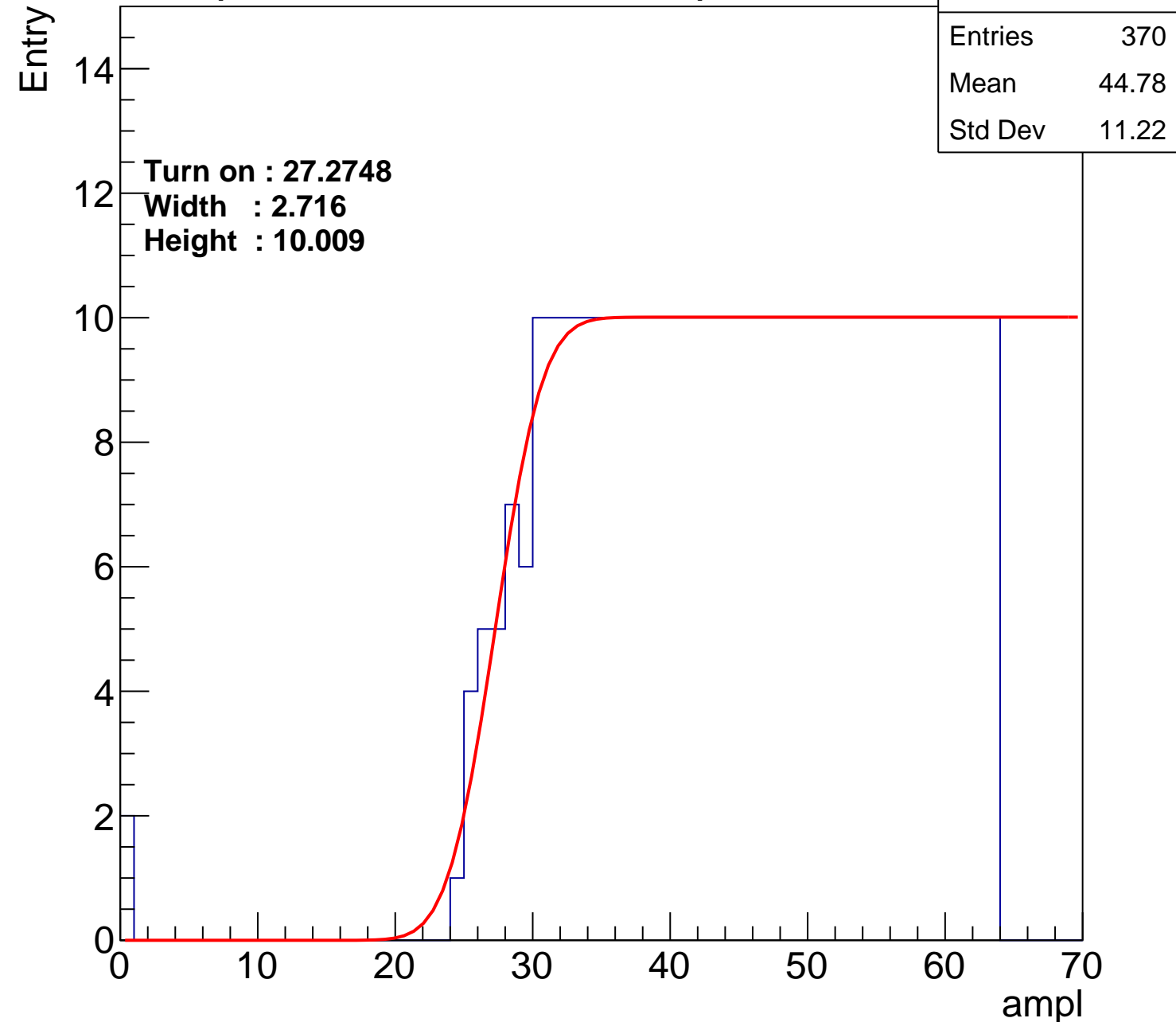
Width : 2.716

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.74
Std Dev	10.6

Turn on : 28.9298

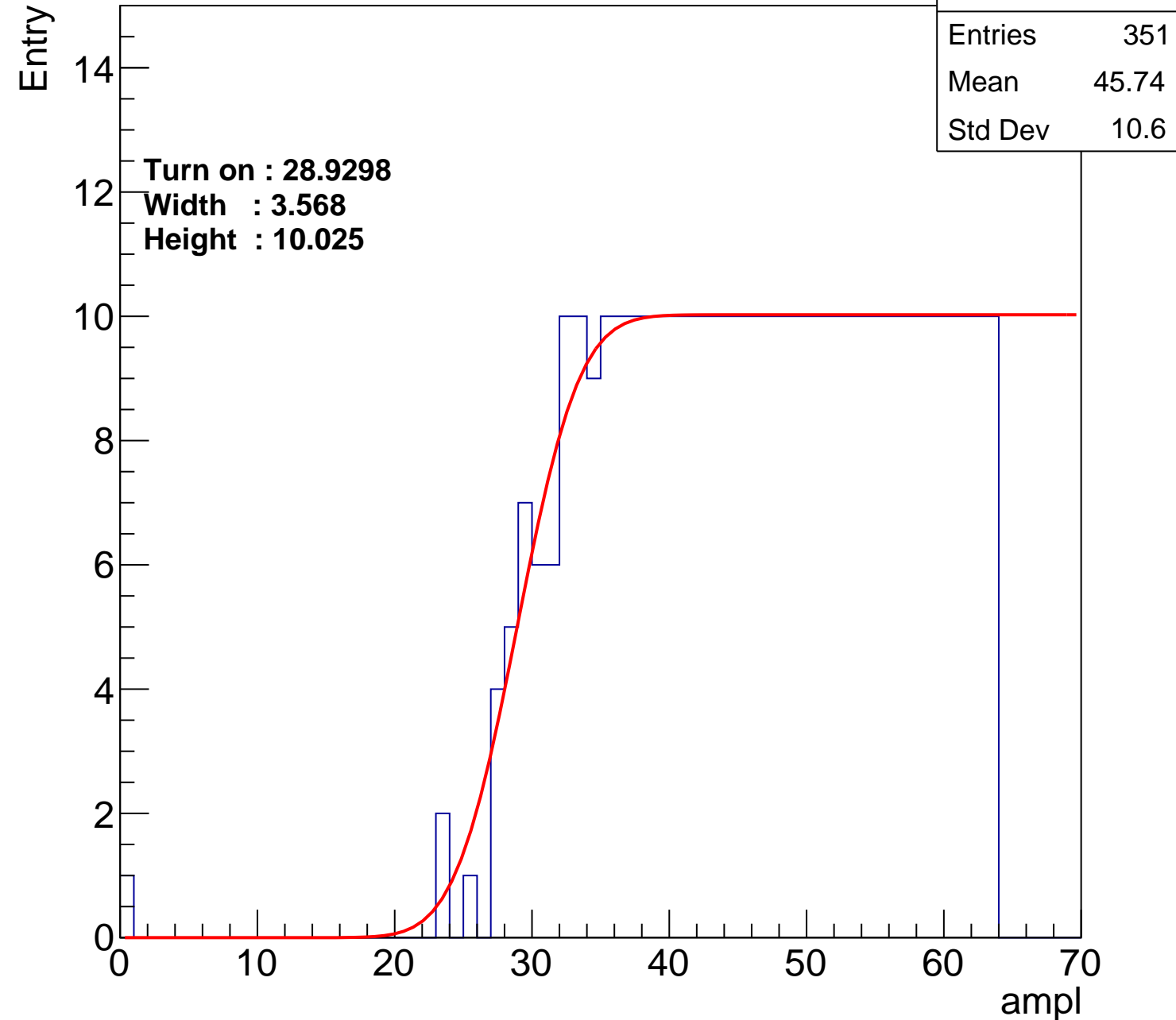
Width : 3.568

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.17
Std Dev	11.22

Turn on : 28.3554

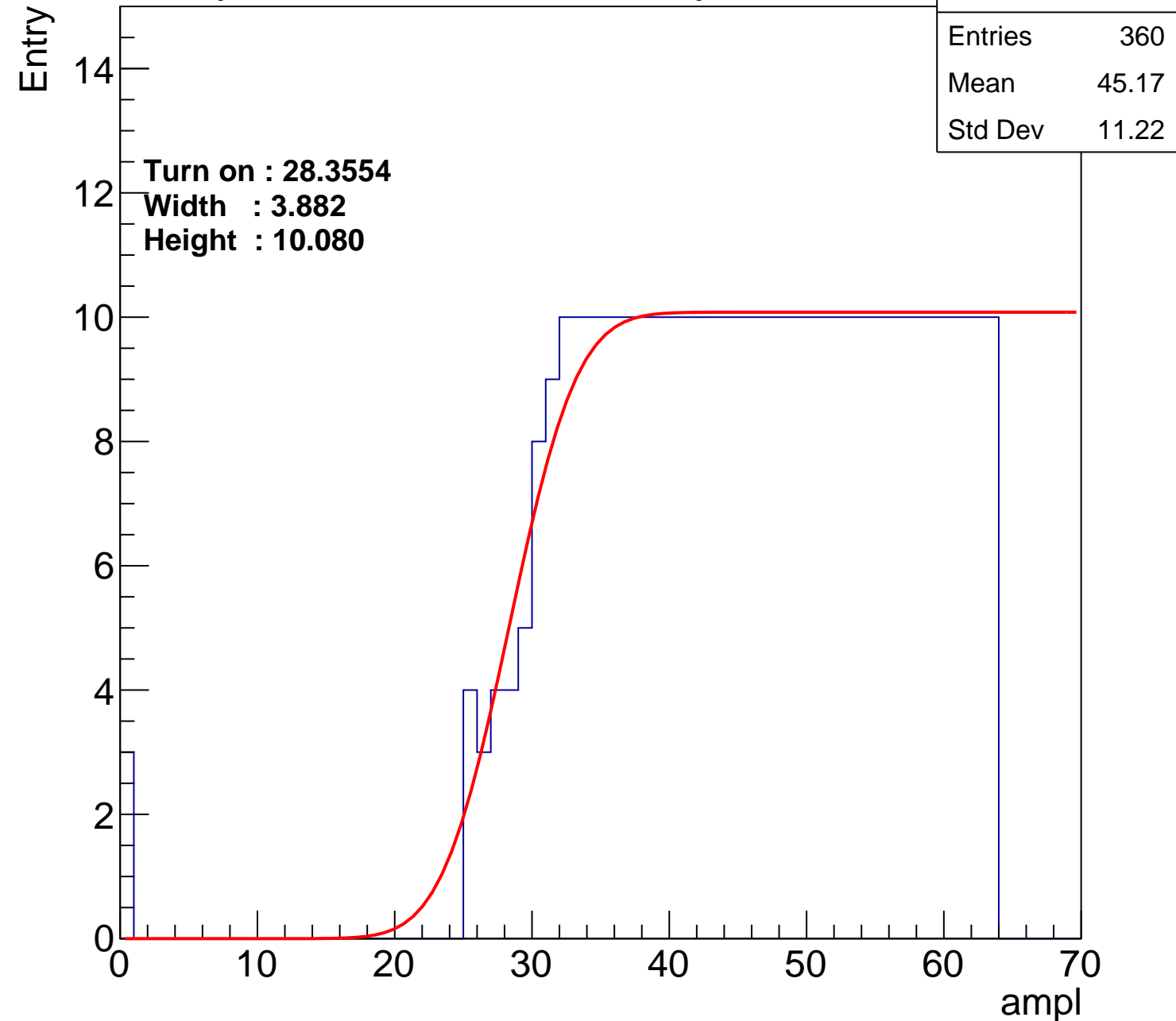
Width : 3.882

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.8207

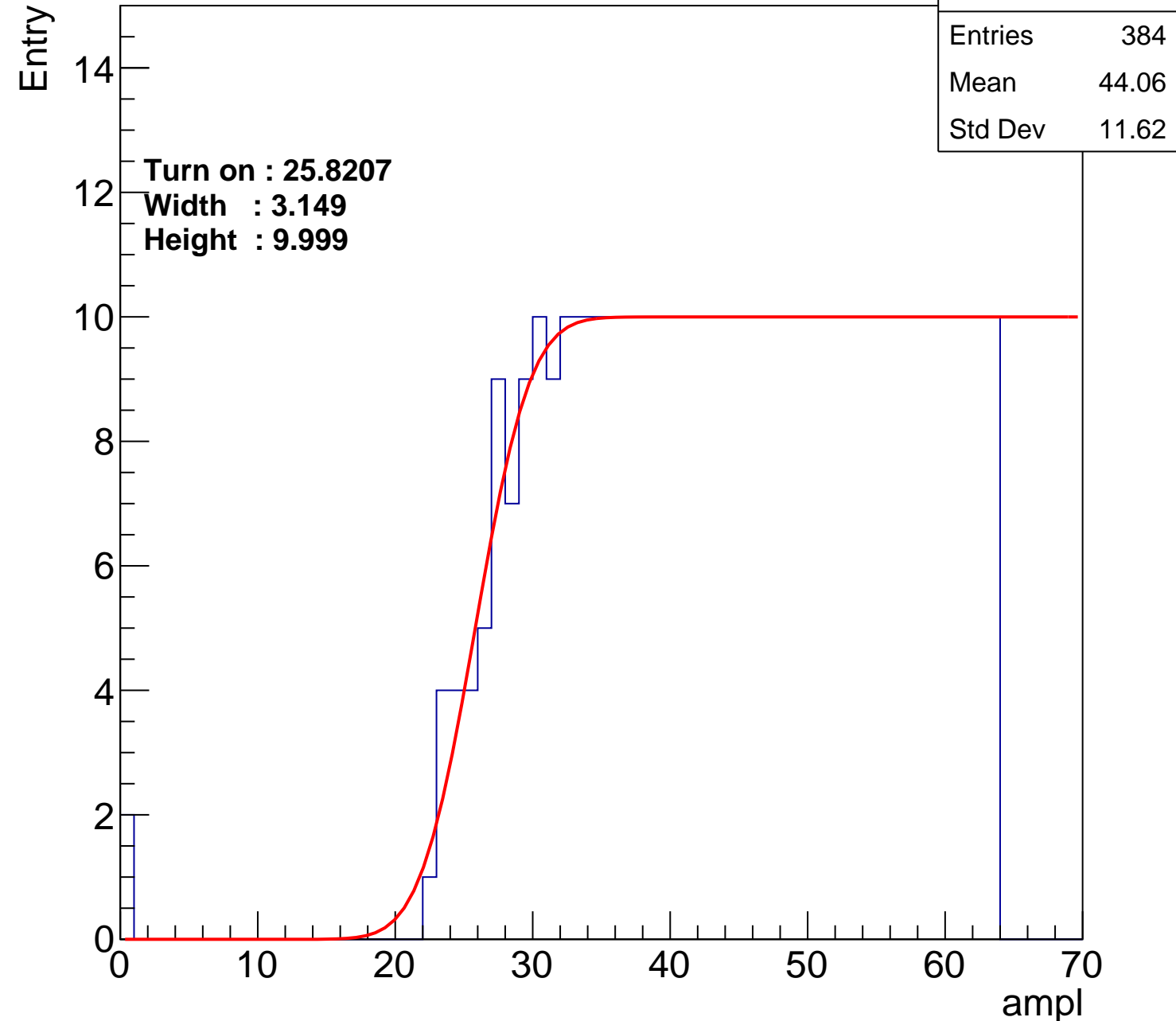
Width : 3.149

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	44.85
Std Dev	11.88

Turn on : 28.7413

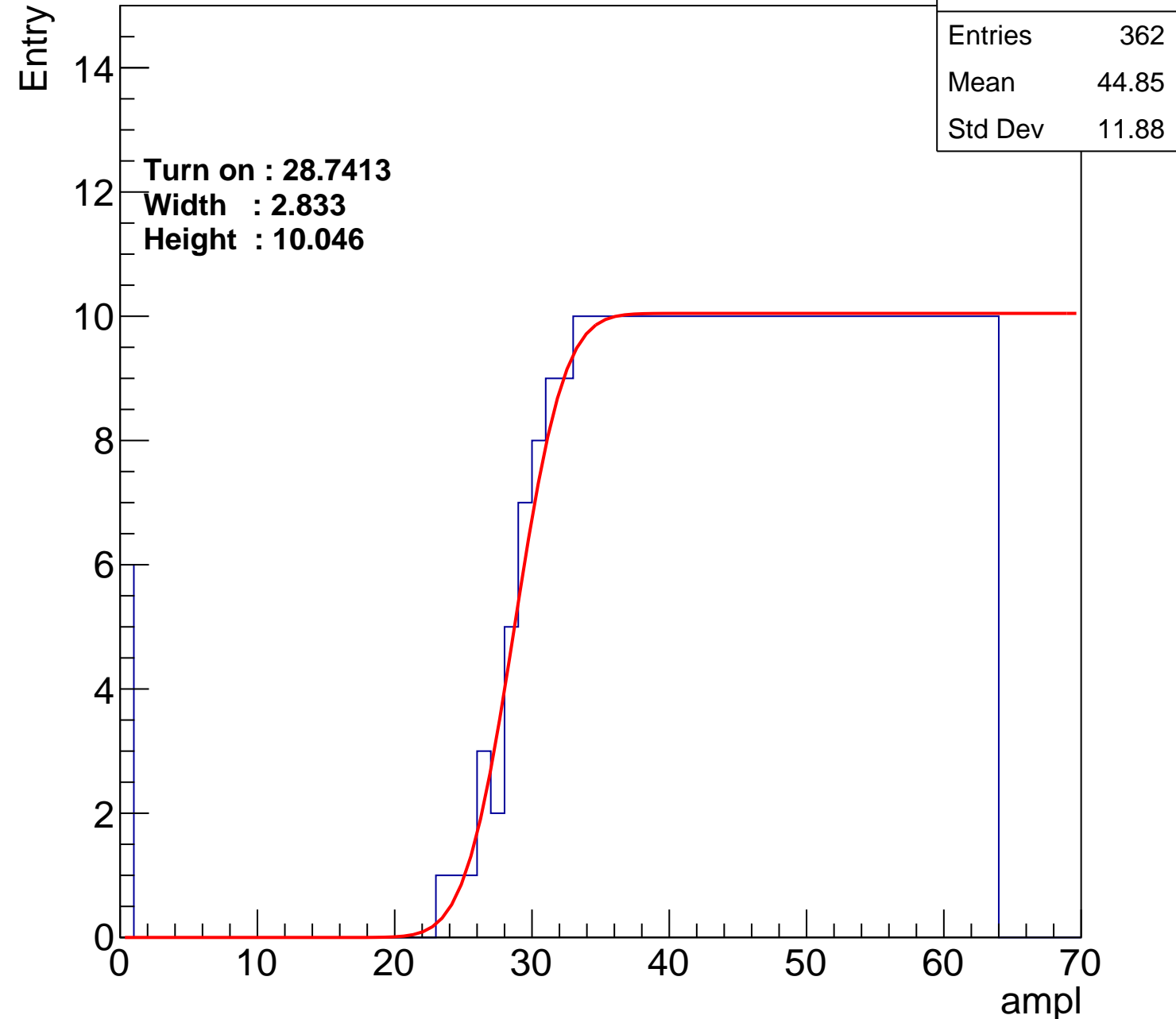
Width : 2.833

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.1887

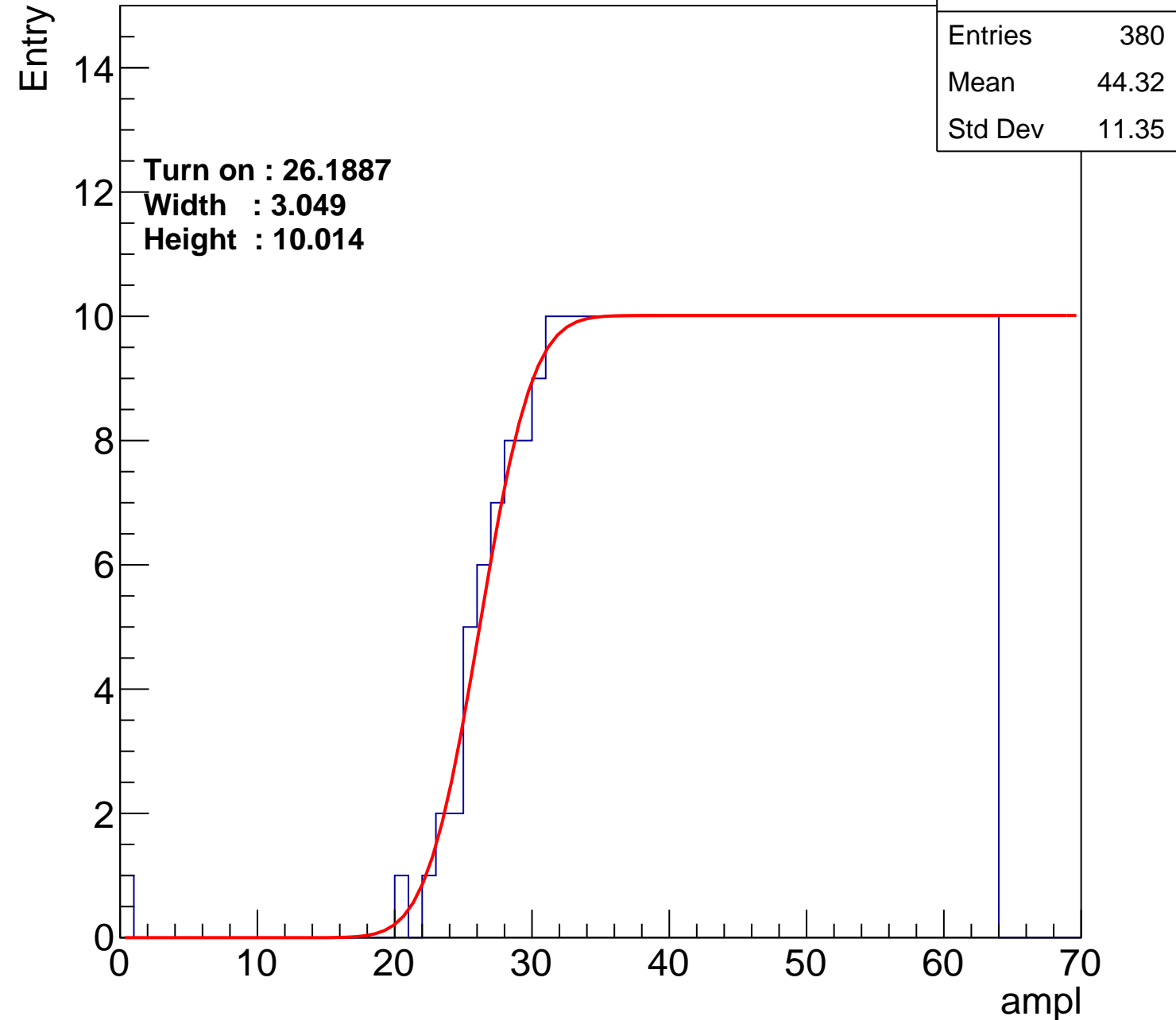
Width : 3.049

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.05
Std Dev	11.63

Turn on : 28.5398

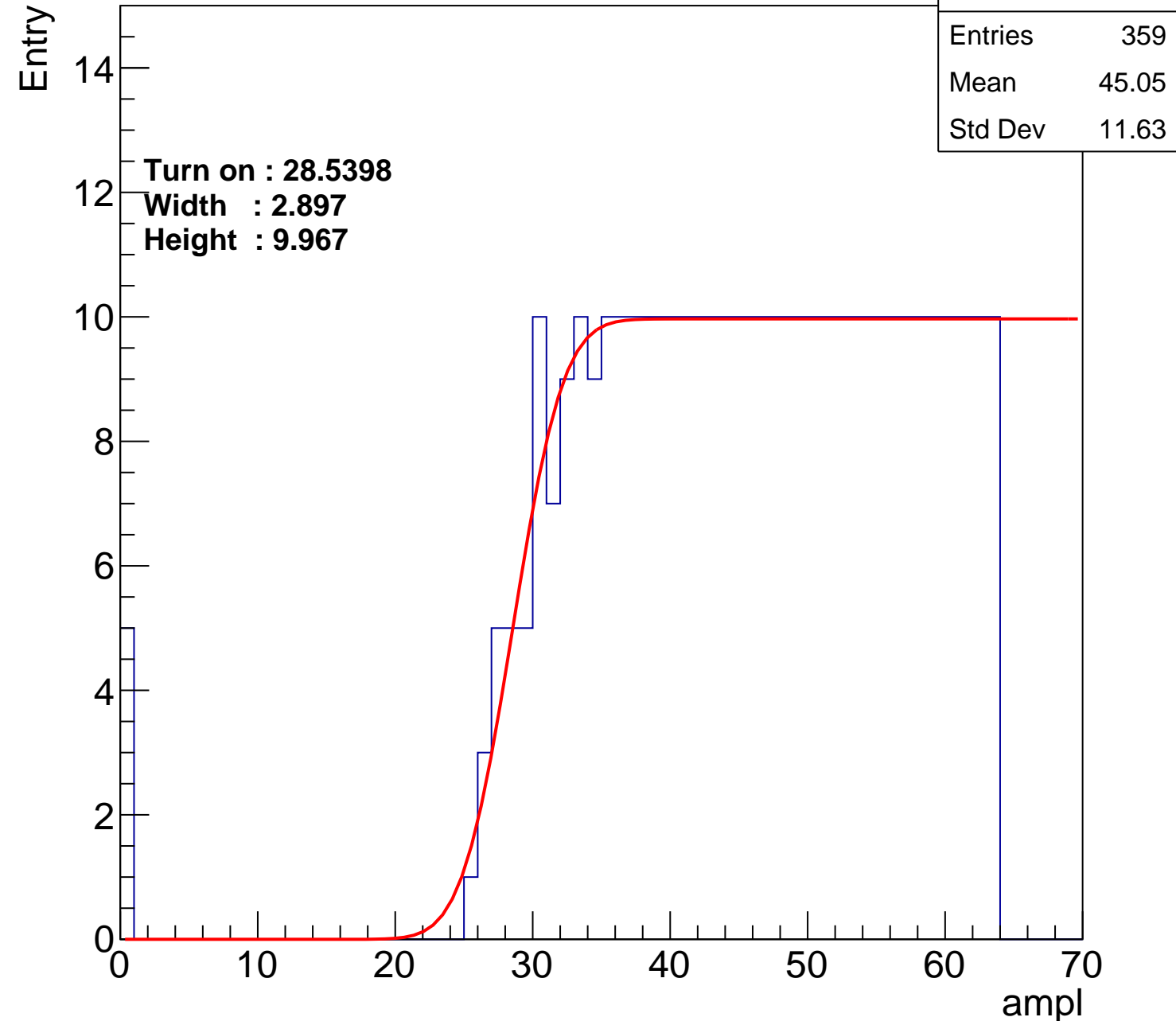
Width : 2.897

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.98
Std Dev	10.58

Turn on : 29.5344

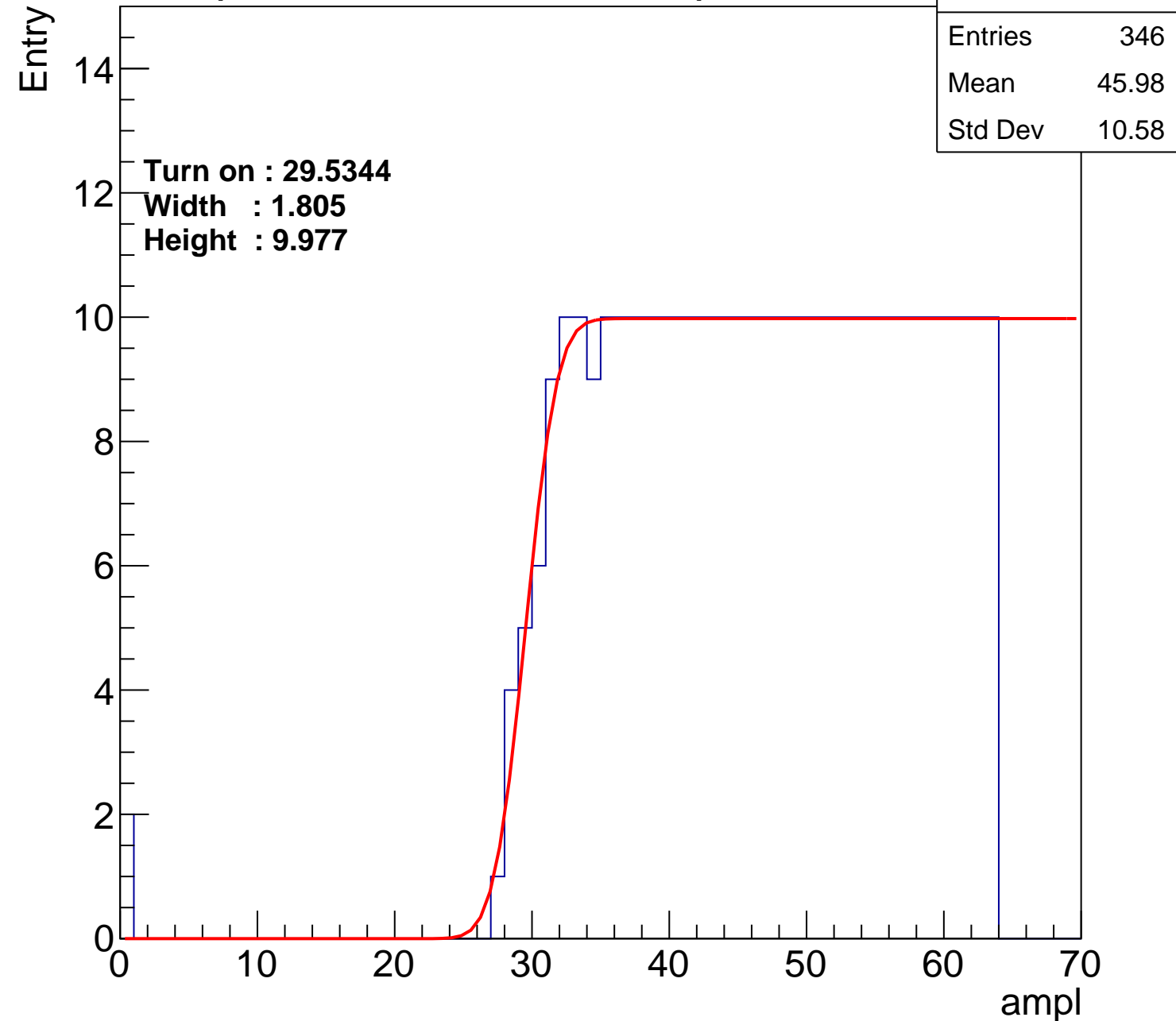
Width : 1.805

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.07
Std Dev	10.6

Turn on : 30.1605

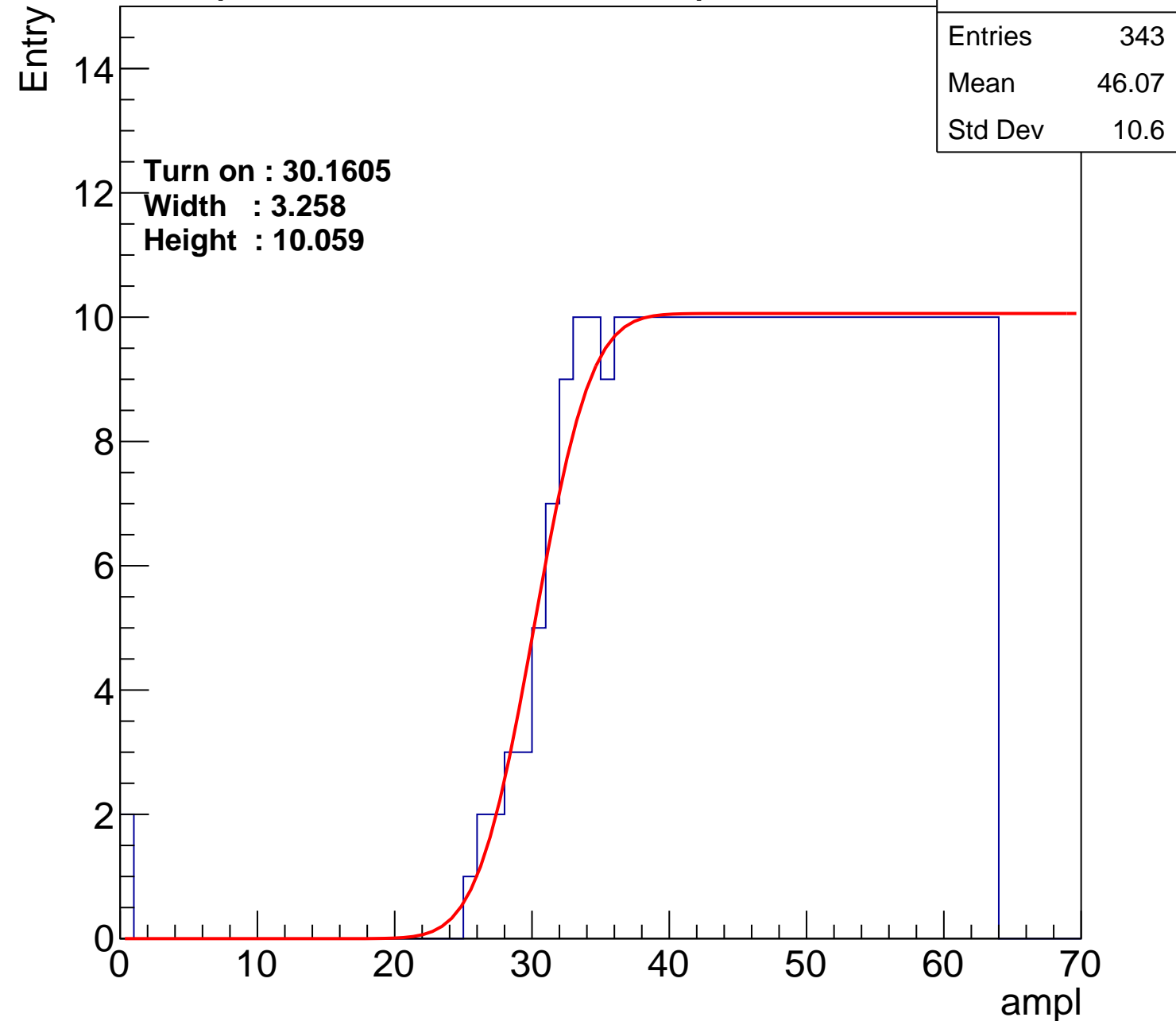
Width : 3.258

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.07
Std Dev	10.55

Turn on : 30.2096

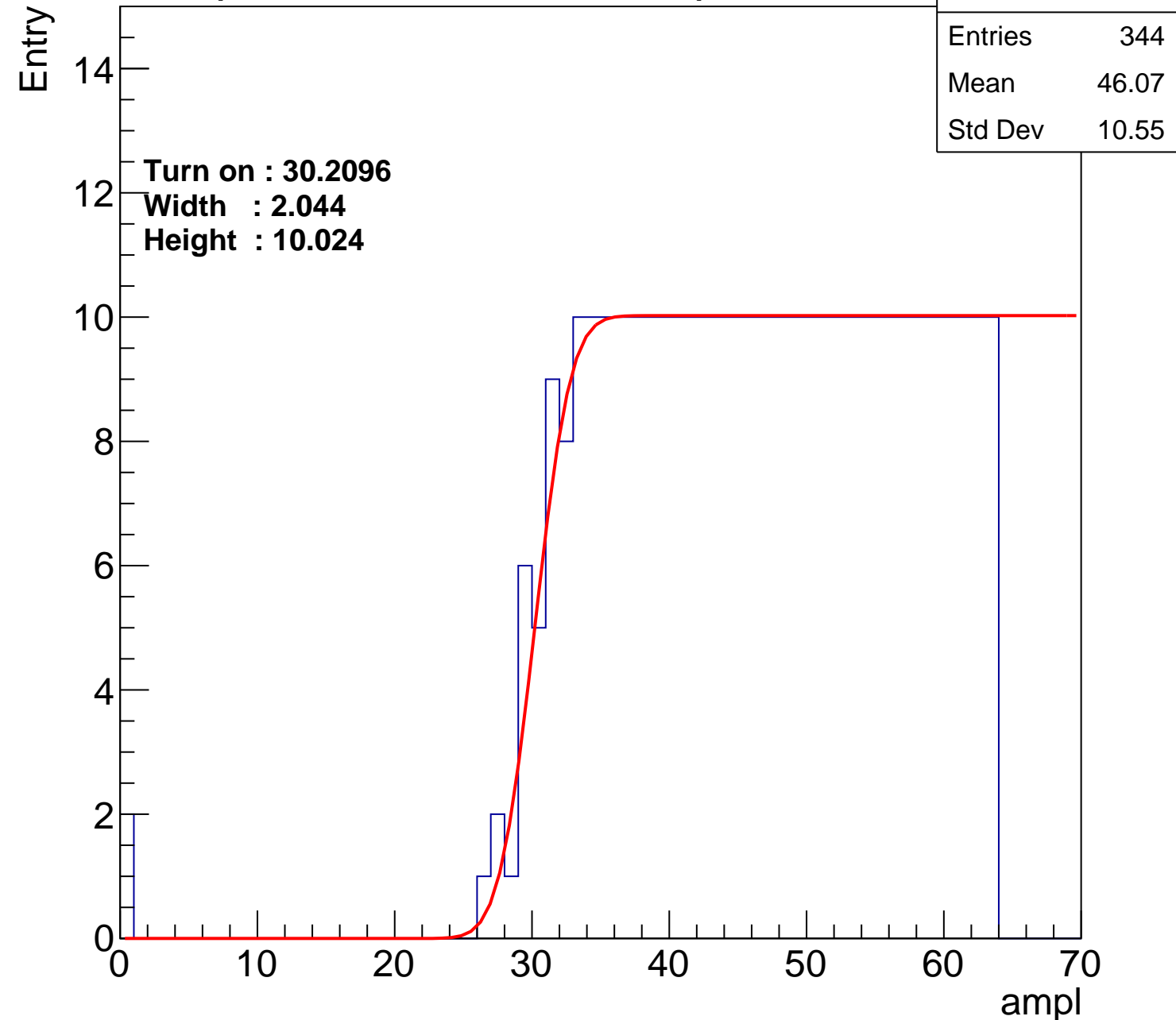
Width : 2.044

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.89
Std Dev	12.03

Turn on : 26.1871

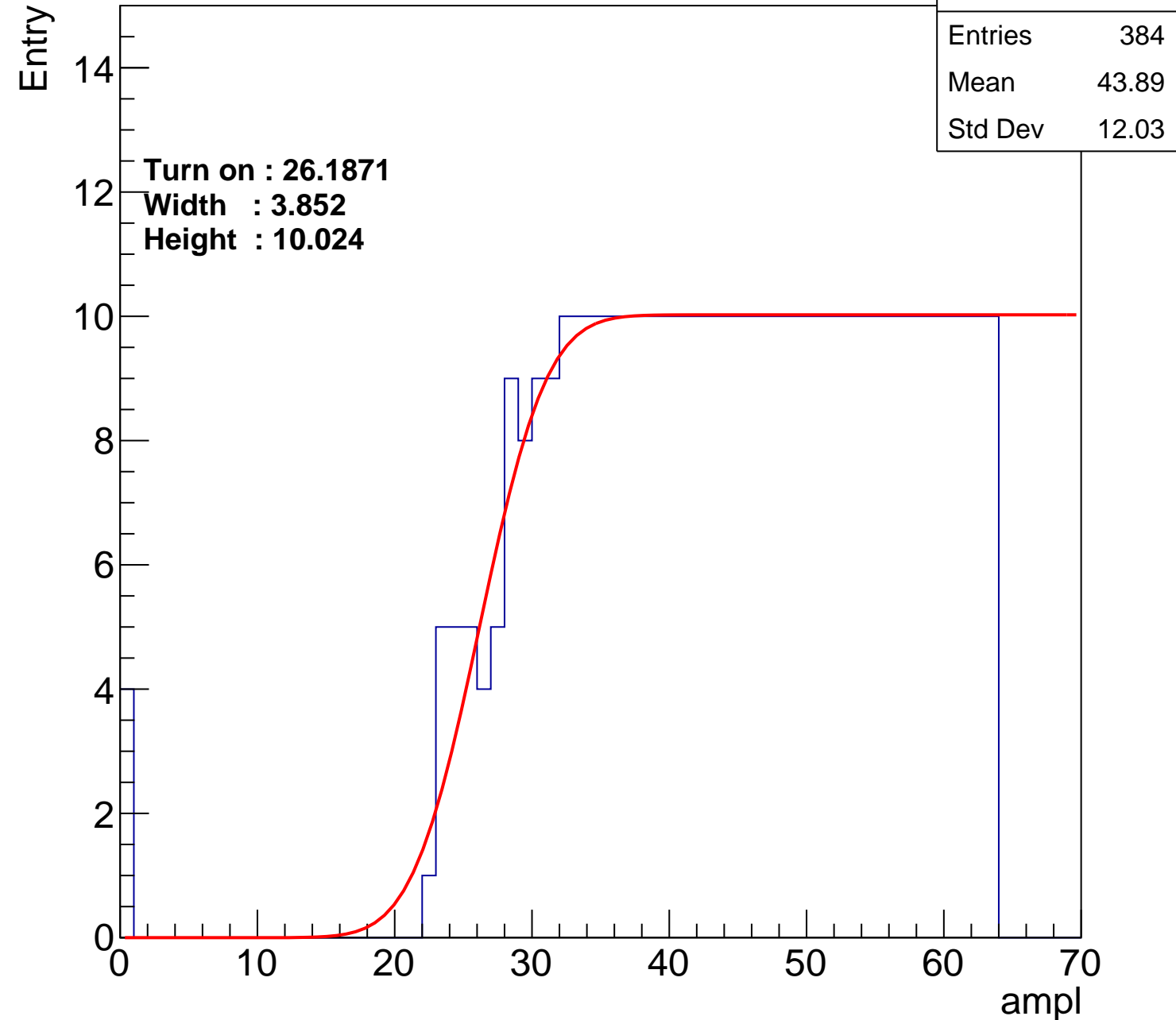
Width : 3.852

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	45.98
Std Dev	10.64

Turn on : 29.7719

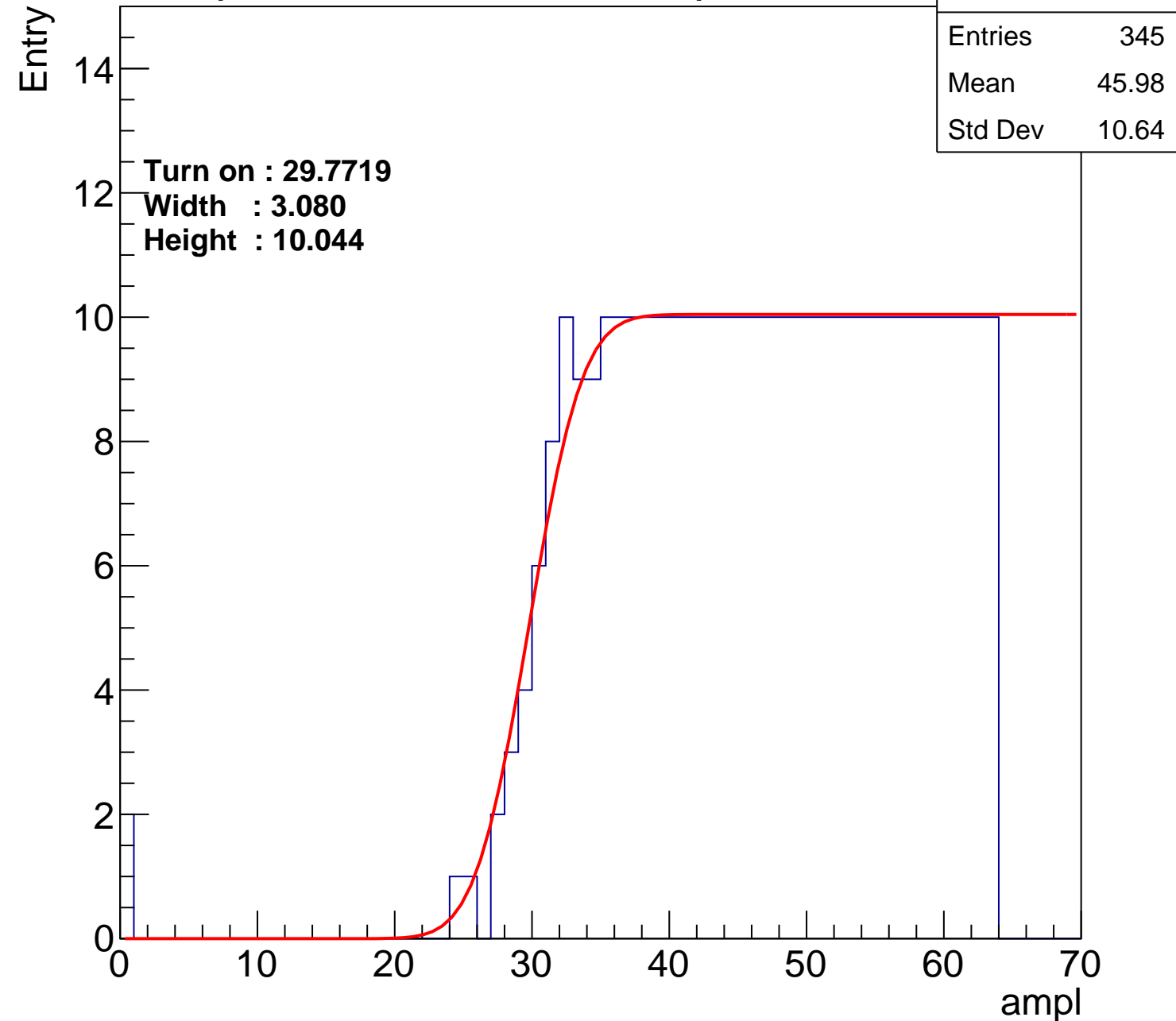
Width : 3.080

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch120

calib_packv5_042523_0143.root, FC#8, port C1

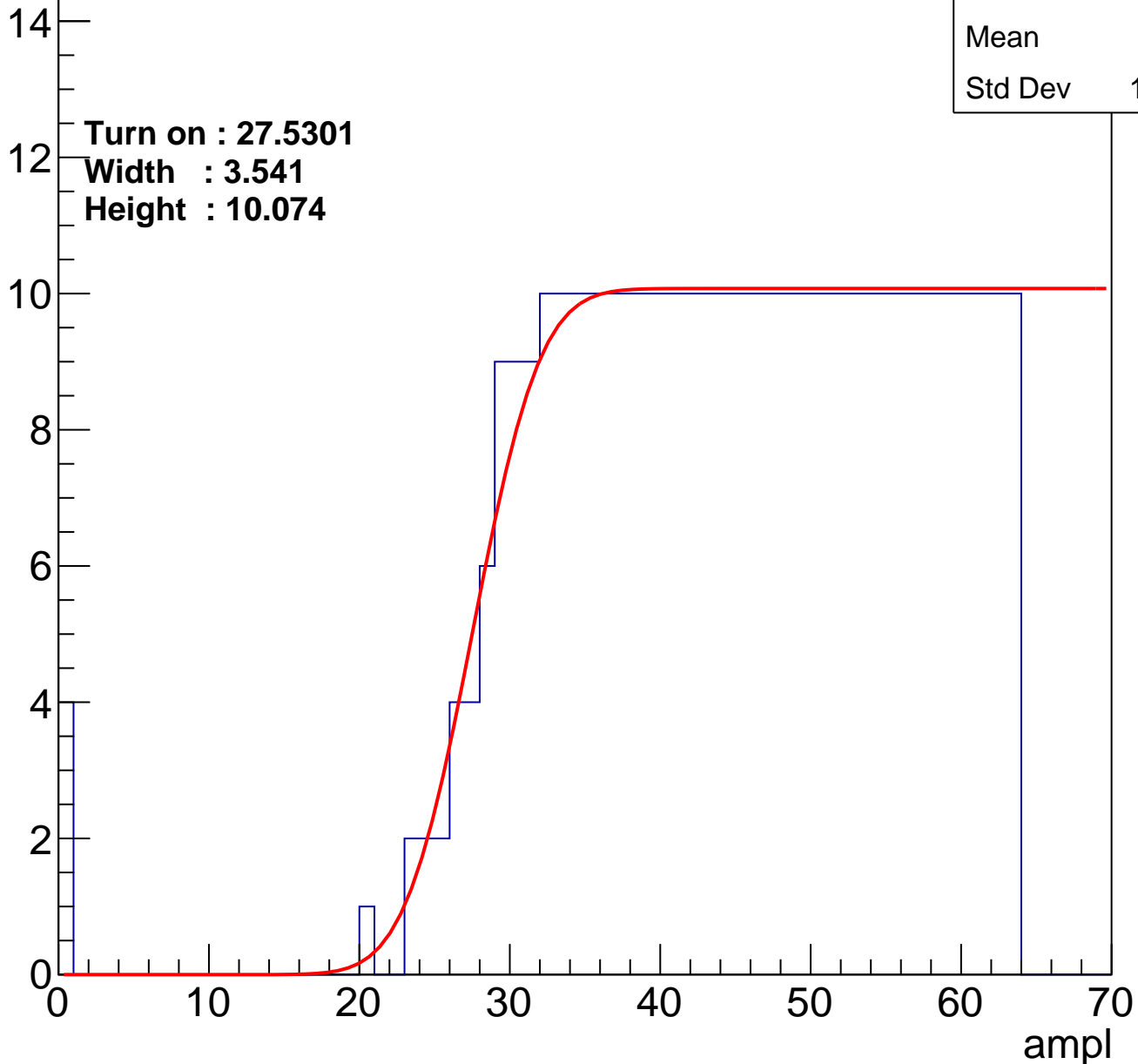
Entries	372
Mean	44.5
Std Dev	11.72

Turn on : 27.5301

Width : 3.541

Height : 10.074

Entry



B0L002S, U12-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.91
Std Dev	11.5

Turn on : 28.3085

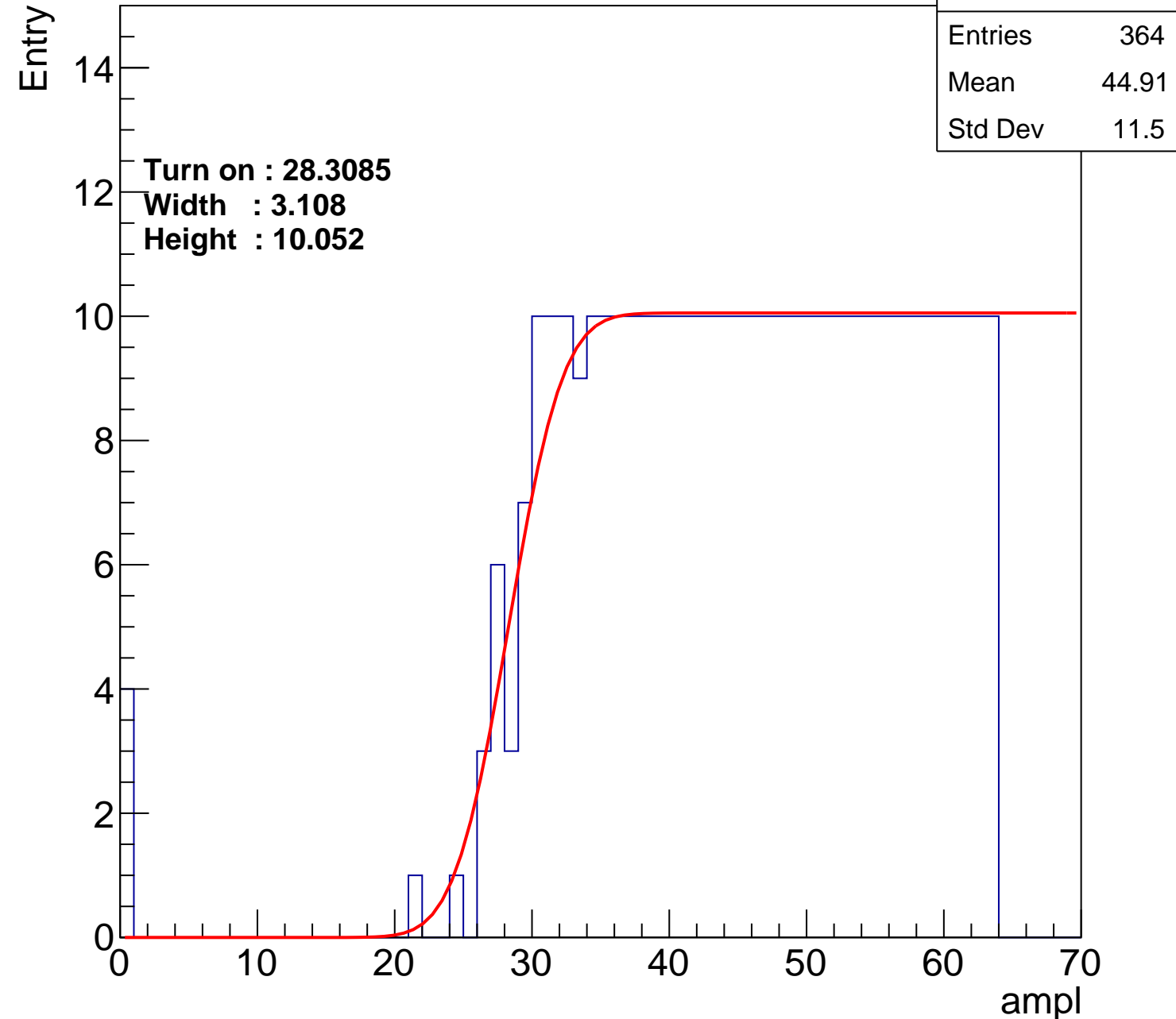
Width : 3.108

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch122

calib_packv5_042523_0143.root, FC#8, port C1

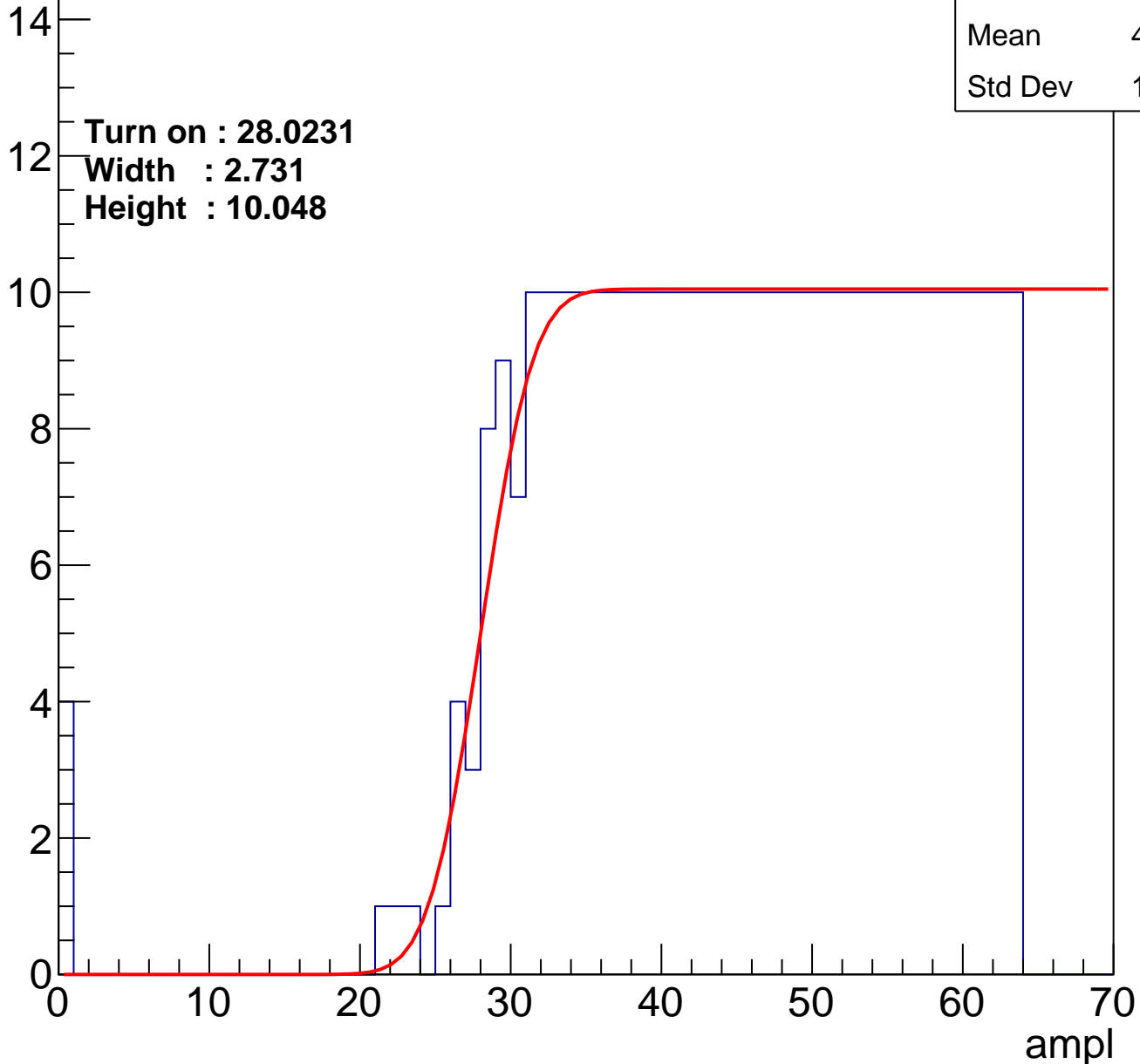
Entries	369
Mean	44.66
Std Dev	11.63

Turn on : 28.0231

Width : 2.731

Height : 10.048

Entry



B0L002S, U12-ch123

calib_packv5_042523_0143.root, FC#8, port C1

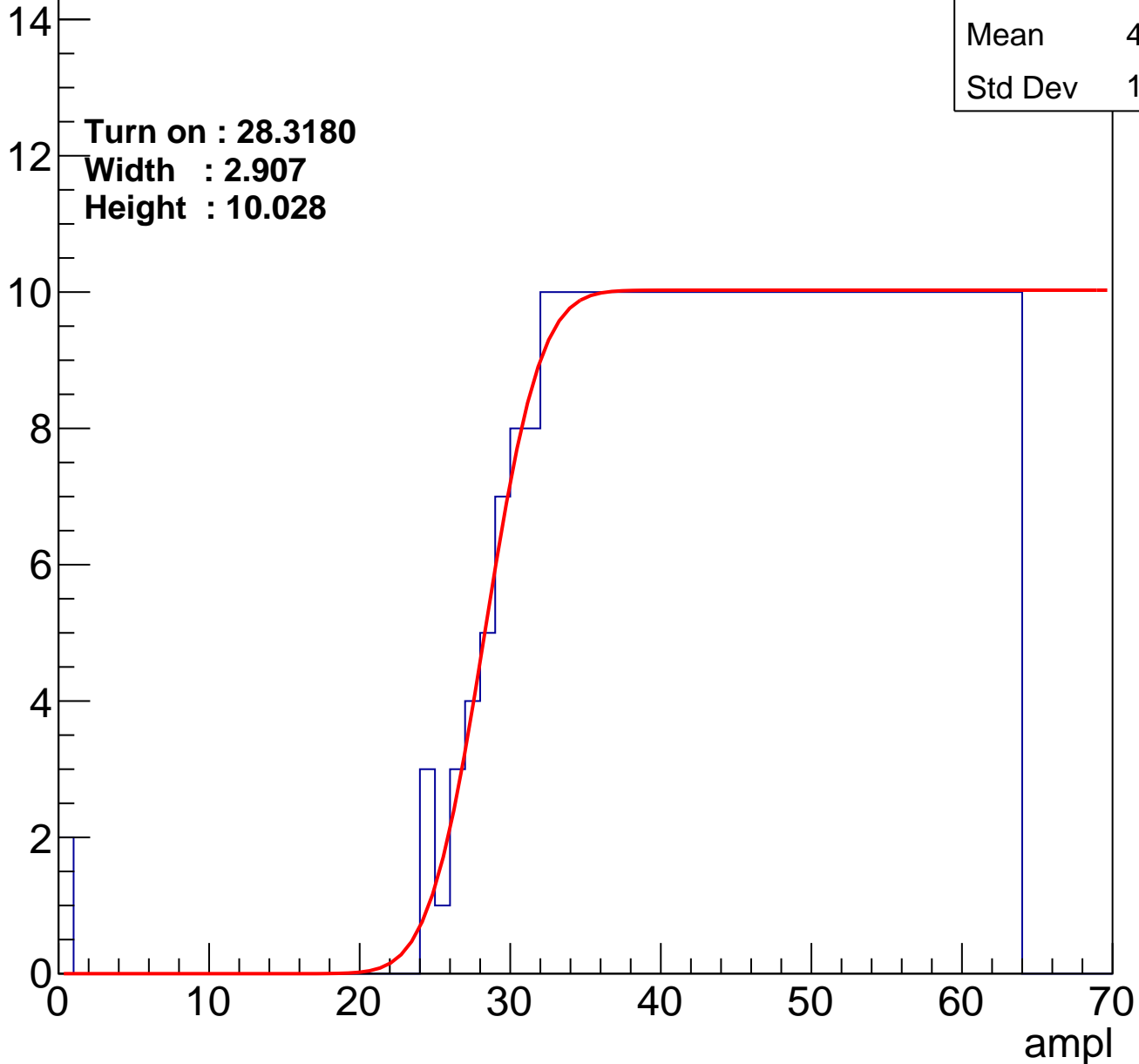
Entries	361
Mean	45.19
Std Dev	11.04

Turn on : 28.3180

Width : 2.907

Height : 10.028

Entry



B0L002S, U12-ch124

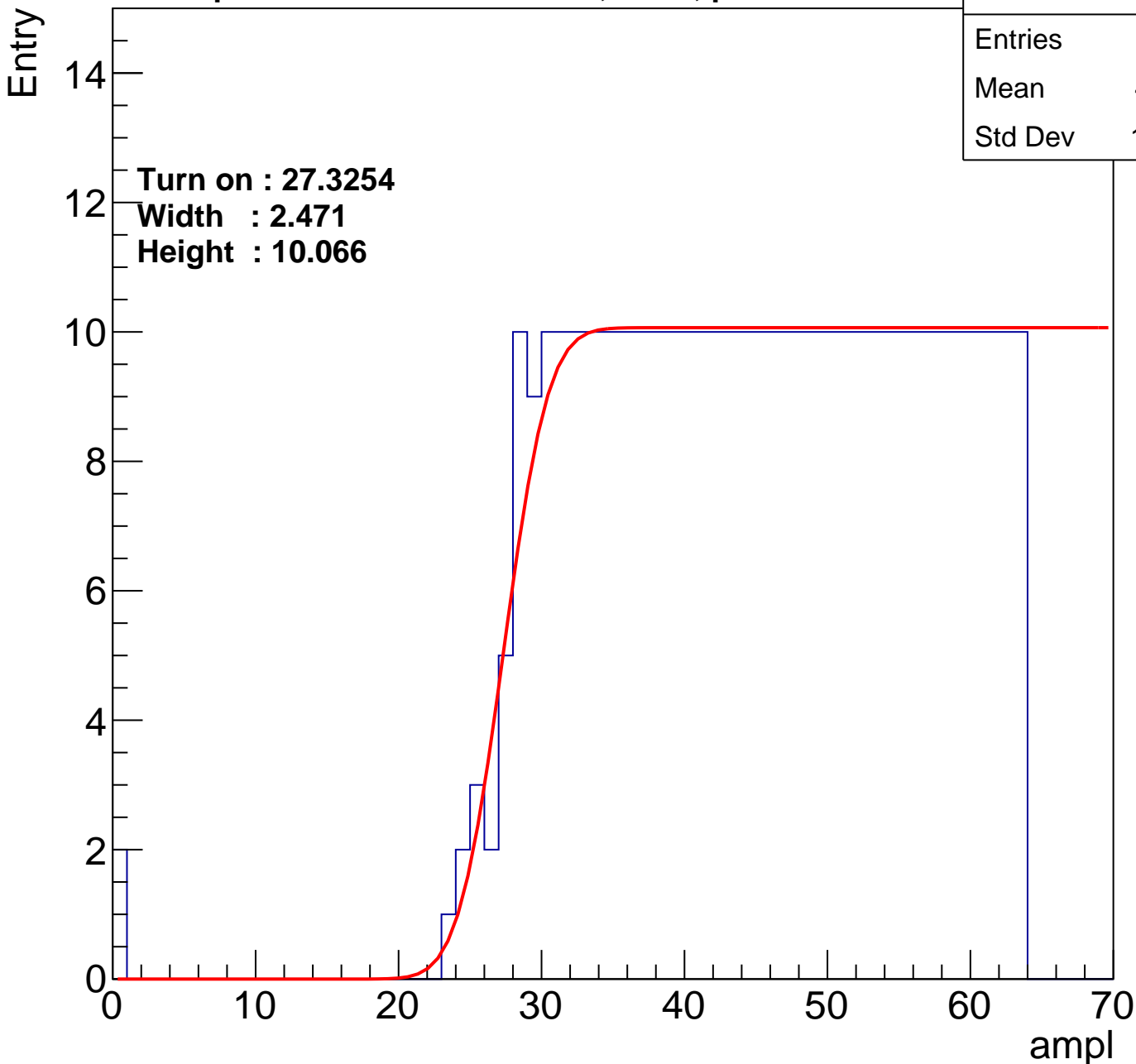
calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.61
Std Dev	11.28

Turn on : 27.3254

Width : 2.471

Height : 10.066



B0L002S, U12-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.83
Std Dev	10.49

Turn on : 29.1392

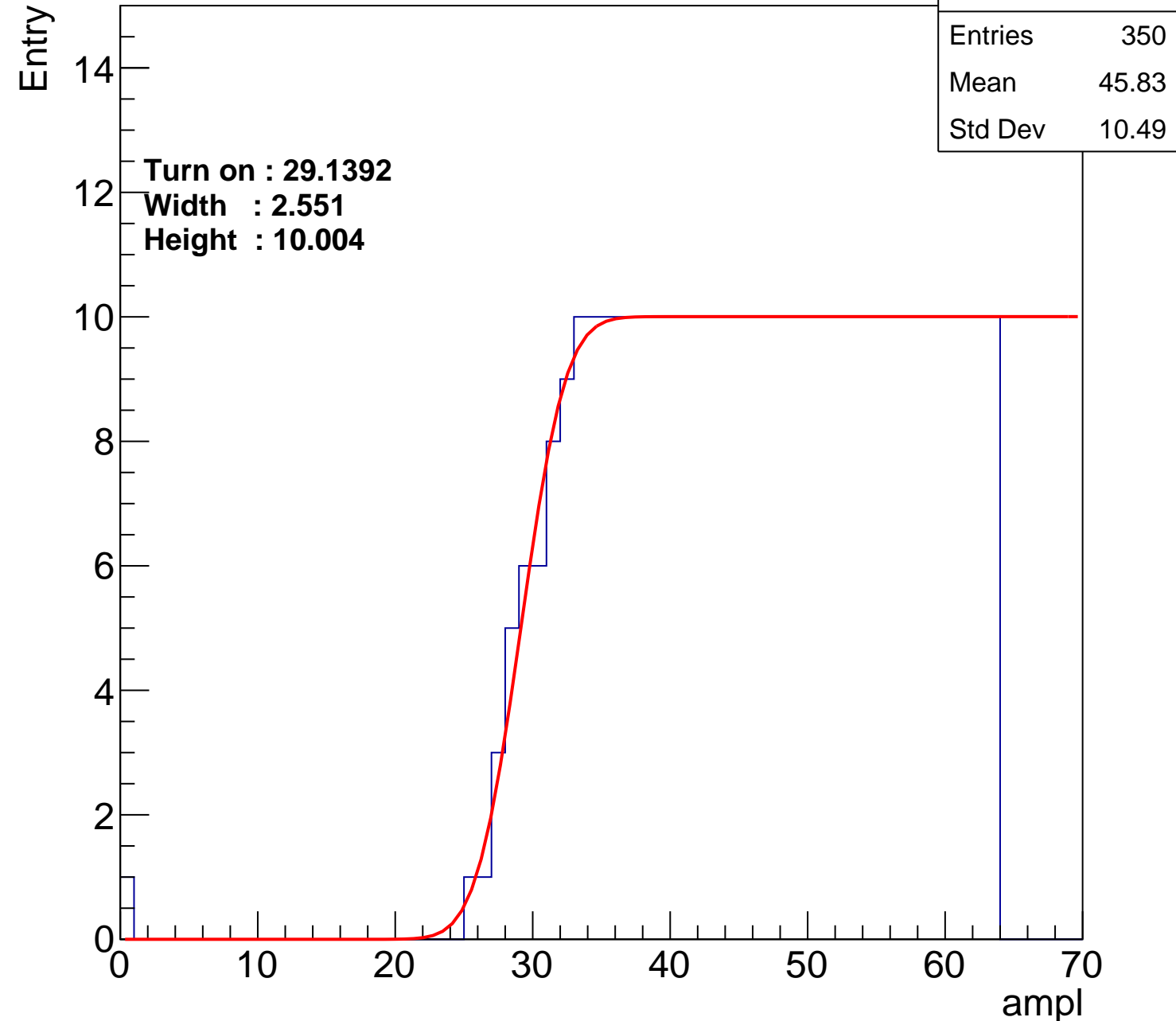
Width : 2.551

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 29.2212

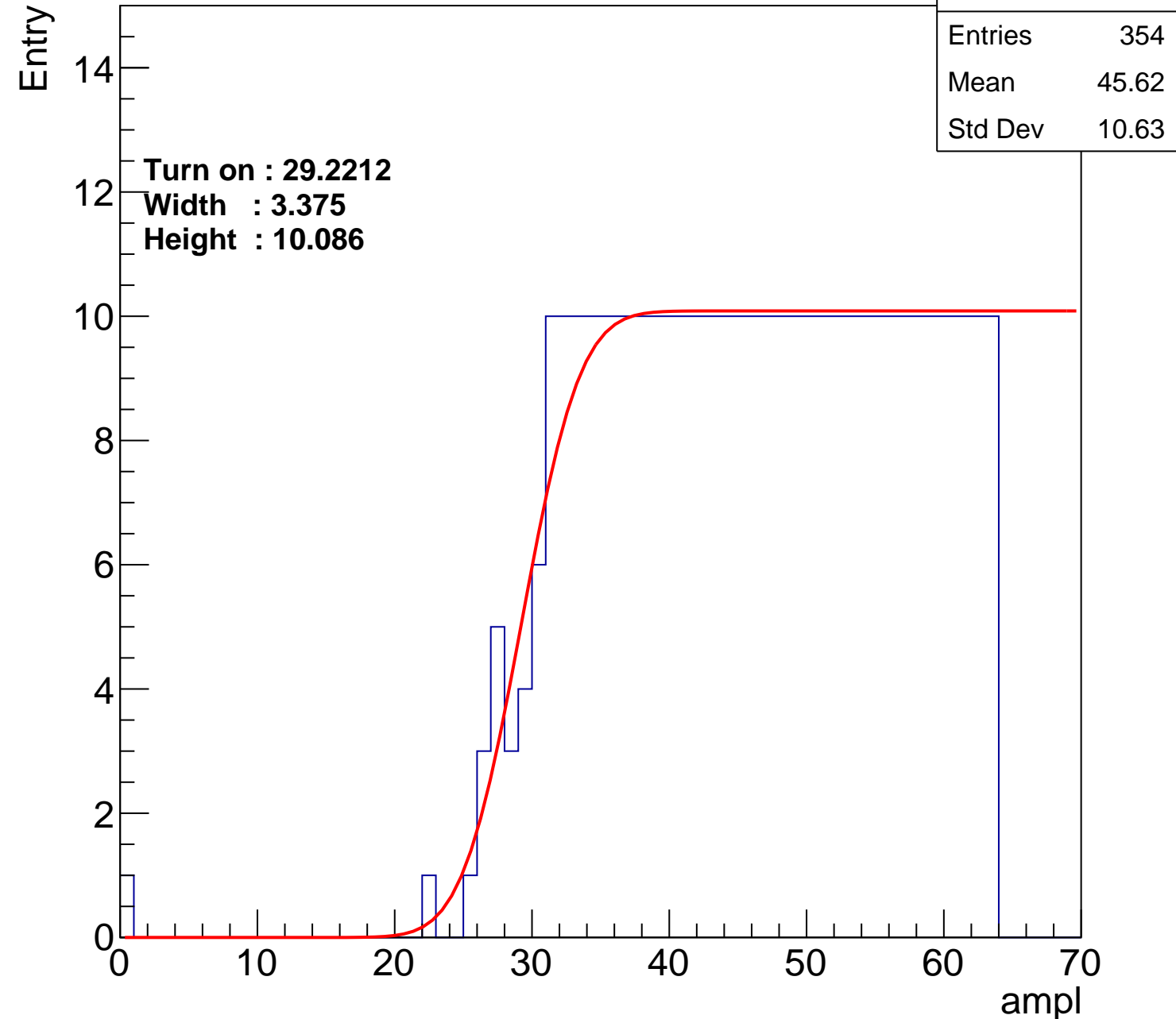
Width : 3.375

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U12-ch127

calib_packv5_042523_0143.root, FC#8, port C1

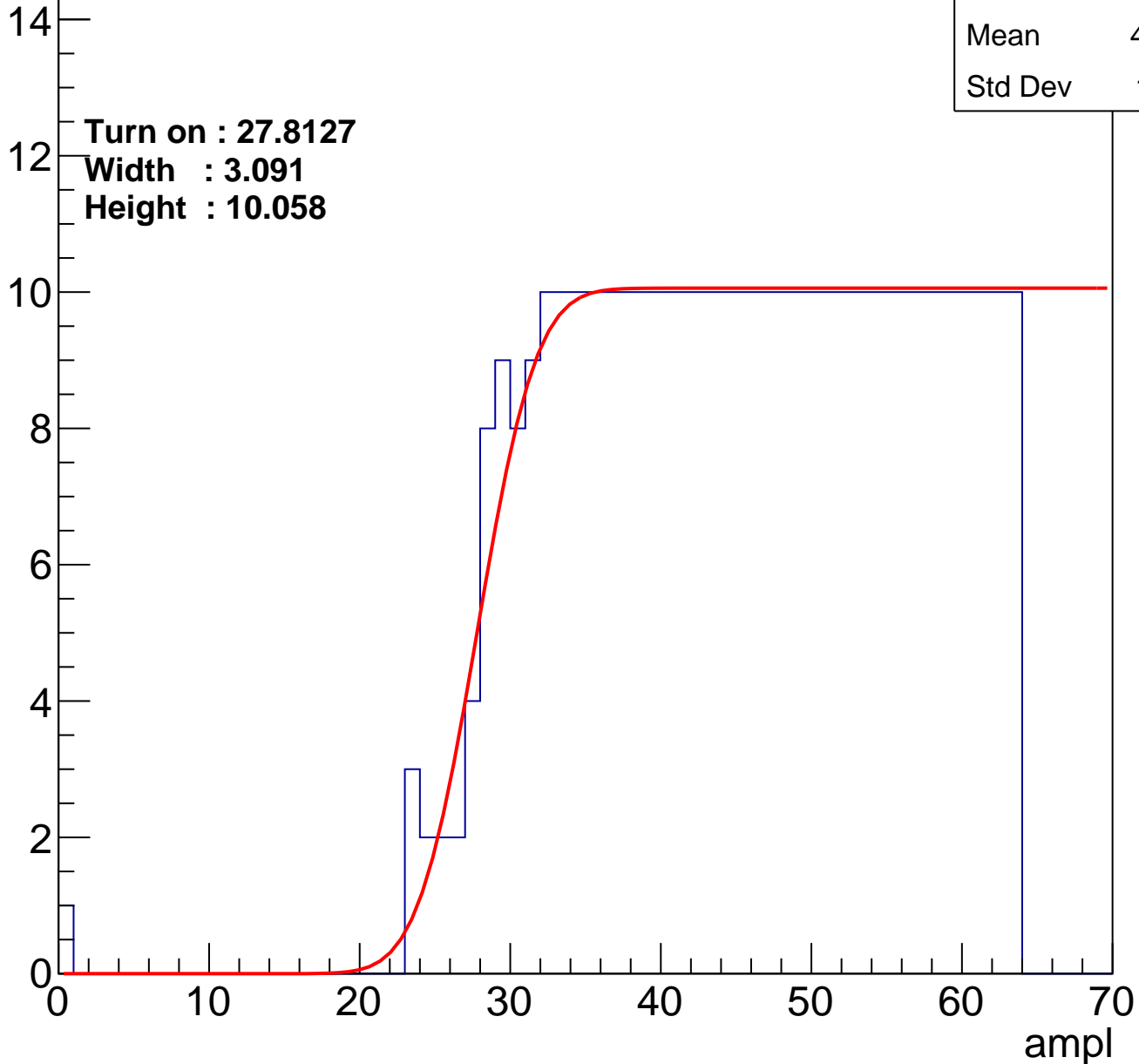
Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.8127

Width : 3.091

Height : 10.058

Entry



B0L002S, U12-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.8127

Width : 3.091

Height : 10.058

Entry

