

B1L100S, U7-ch0, adc0

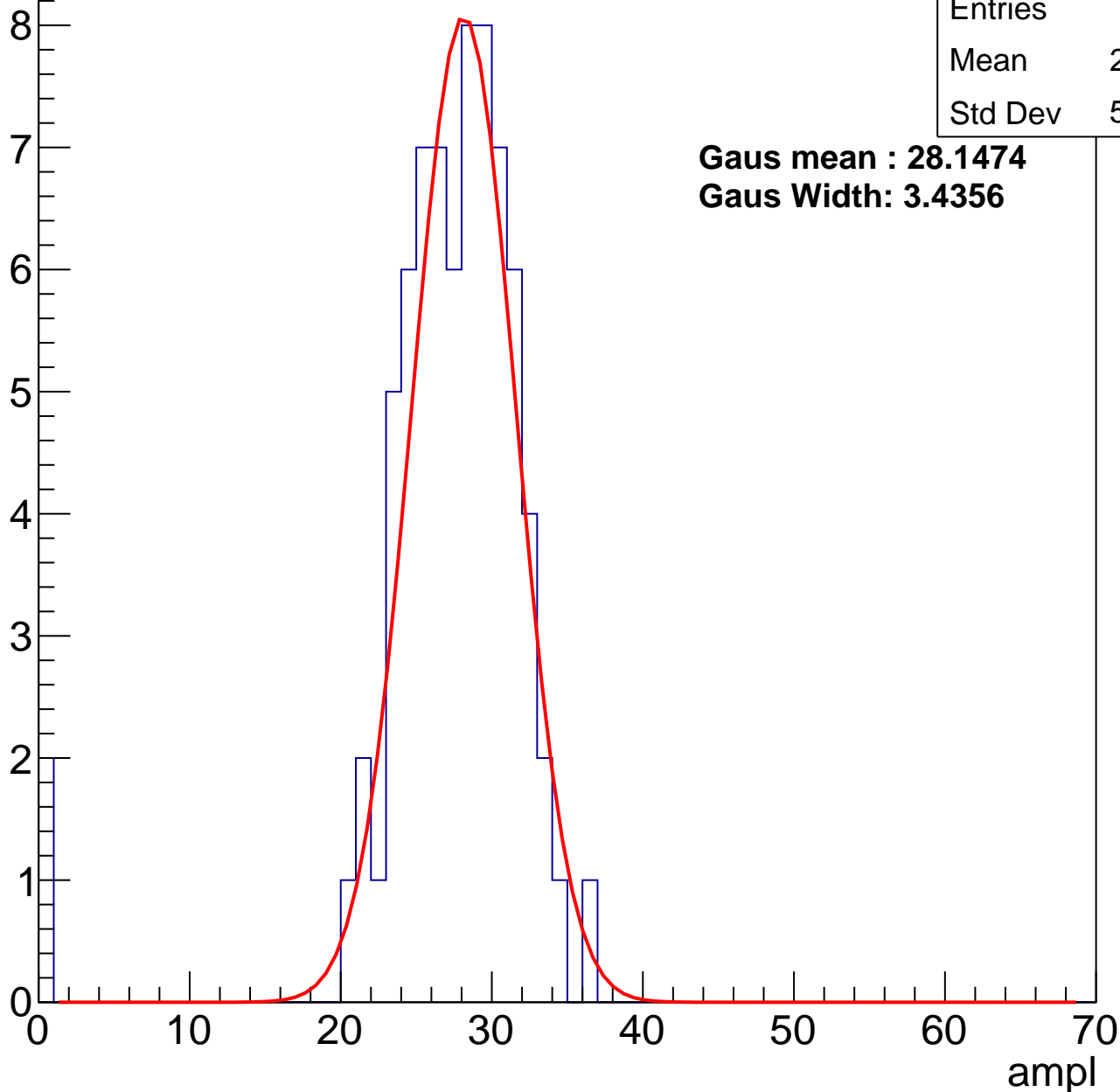
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 26.73 |
| Std Dev | 5.537 |

Gaus mean : 28.1474

Gaus Width: 3.4356



B1L100S, U7-ch0, adc1

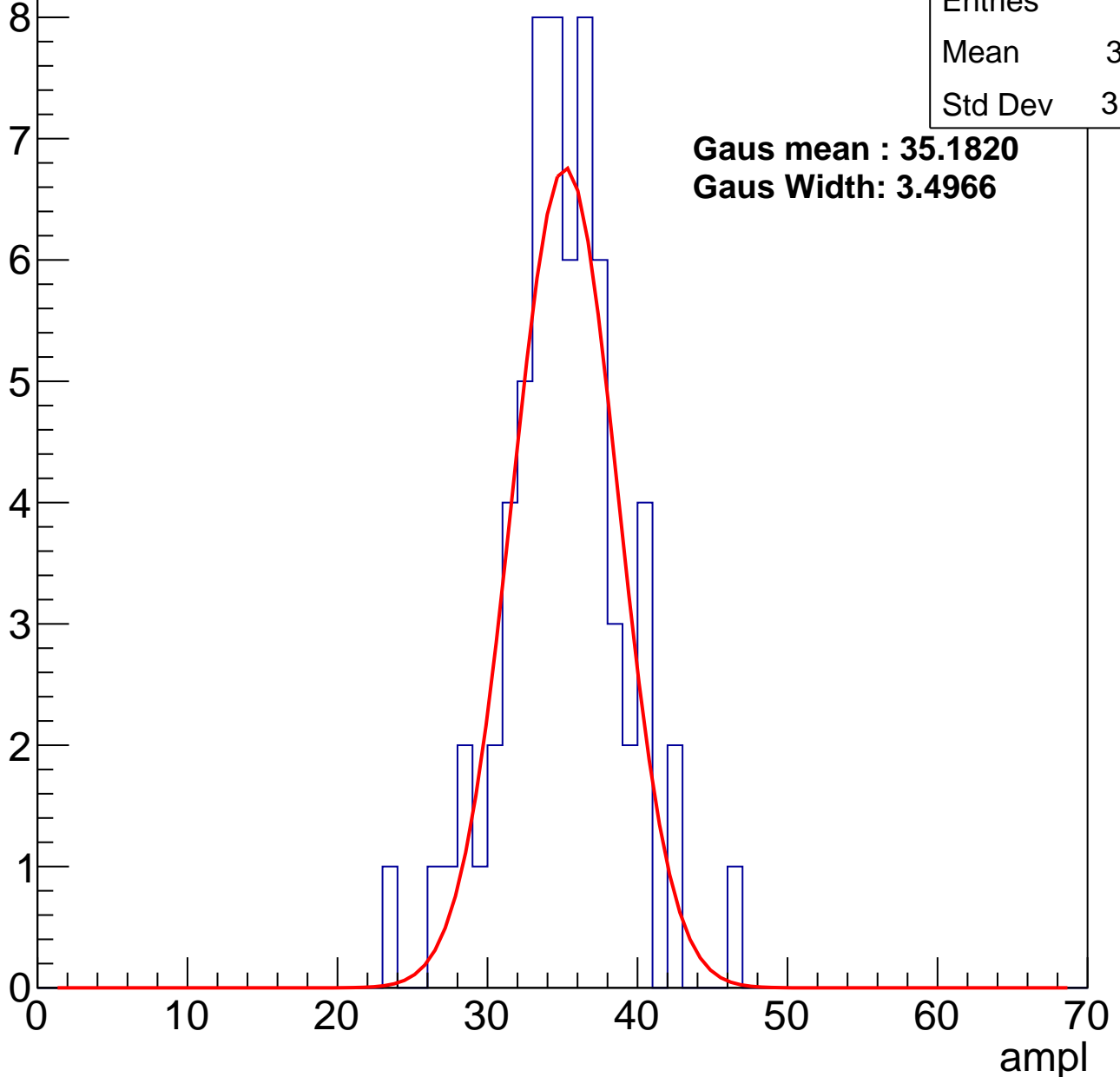
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 34.51 |
| Std Dev | 3.954 |

Gaus mean : 35.1820

Gaus Width: 3.4966



B1L100S, U7-ch0, adc2

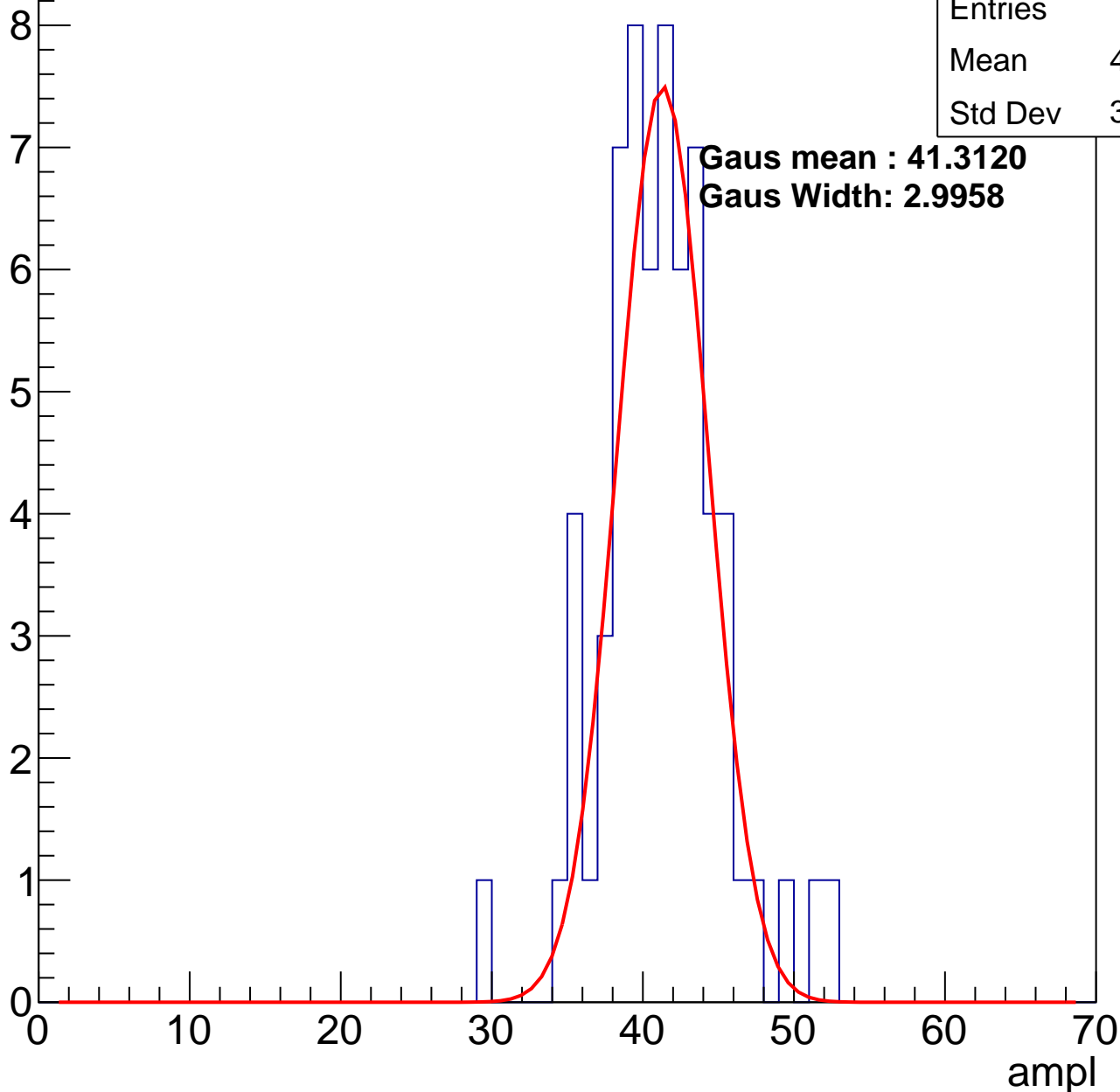
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 40.77 |
| Std Dev | 3.898 |

Gaus mean : 41.3120

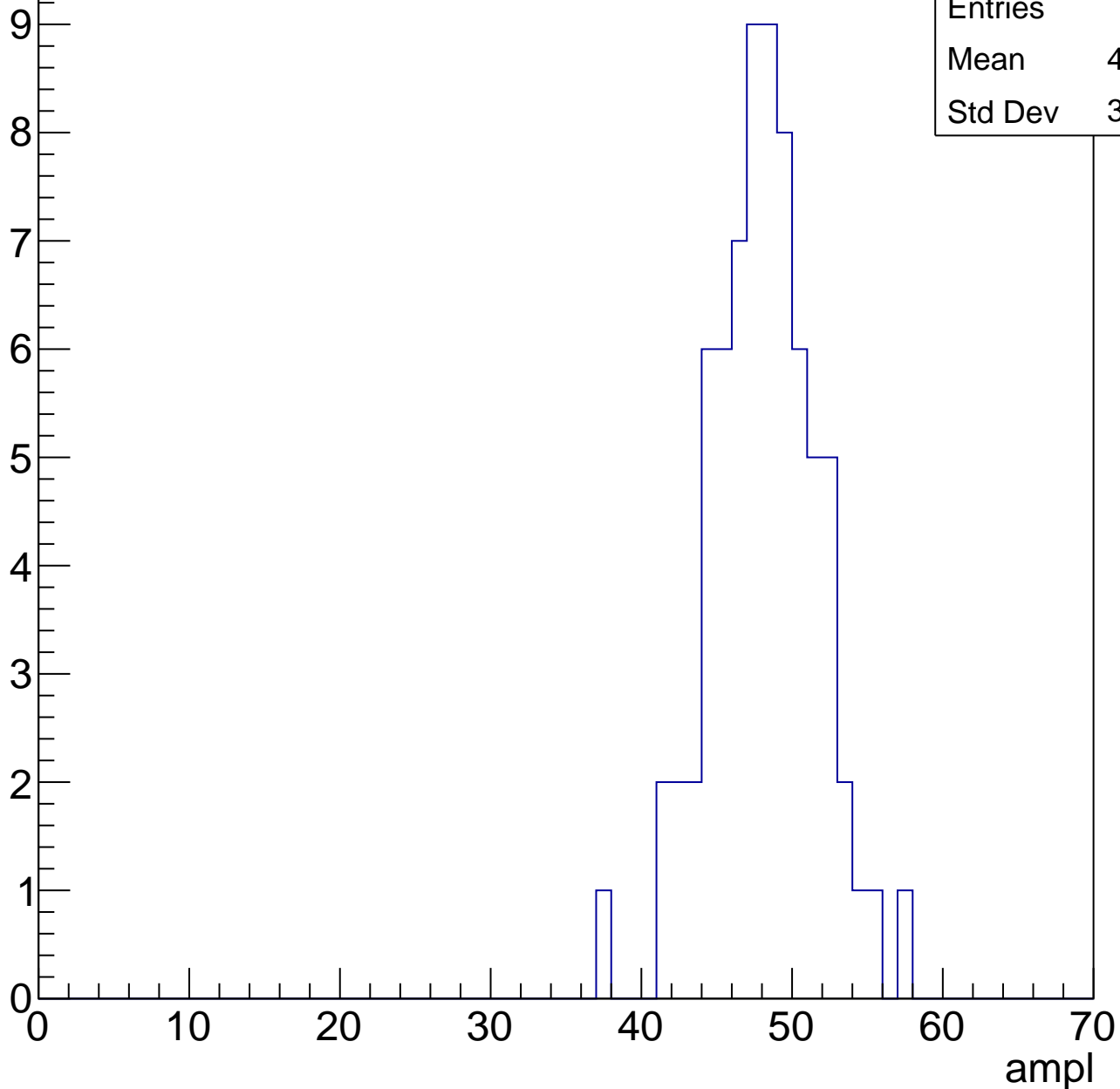
Gaus Width: 2.9958



B1L100S, U7-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

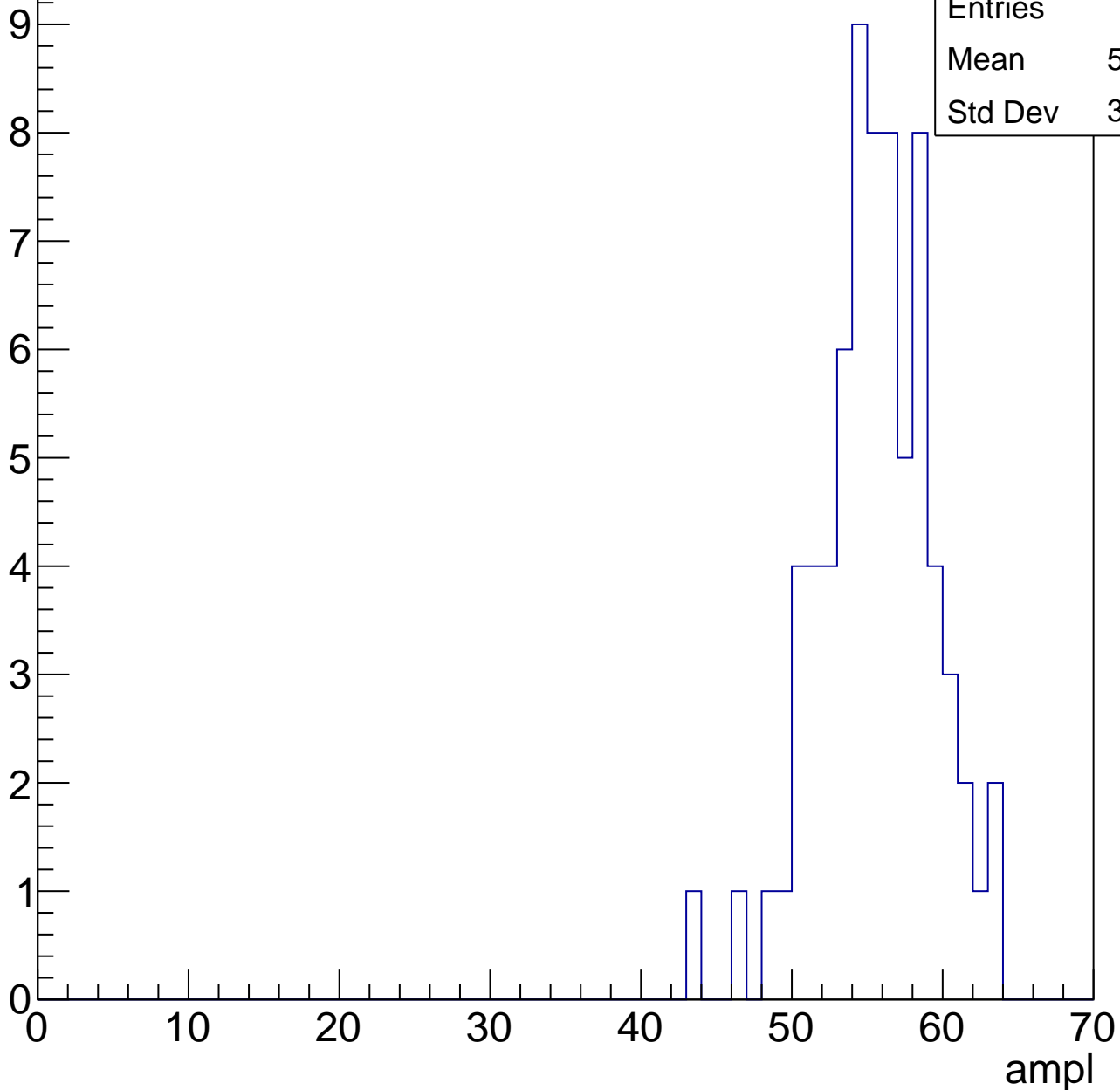


B1L100S, U7-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

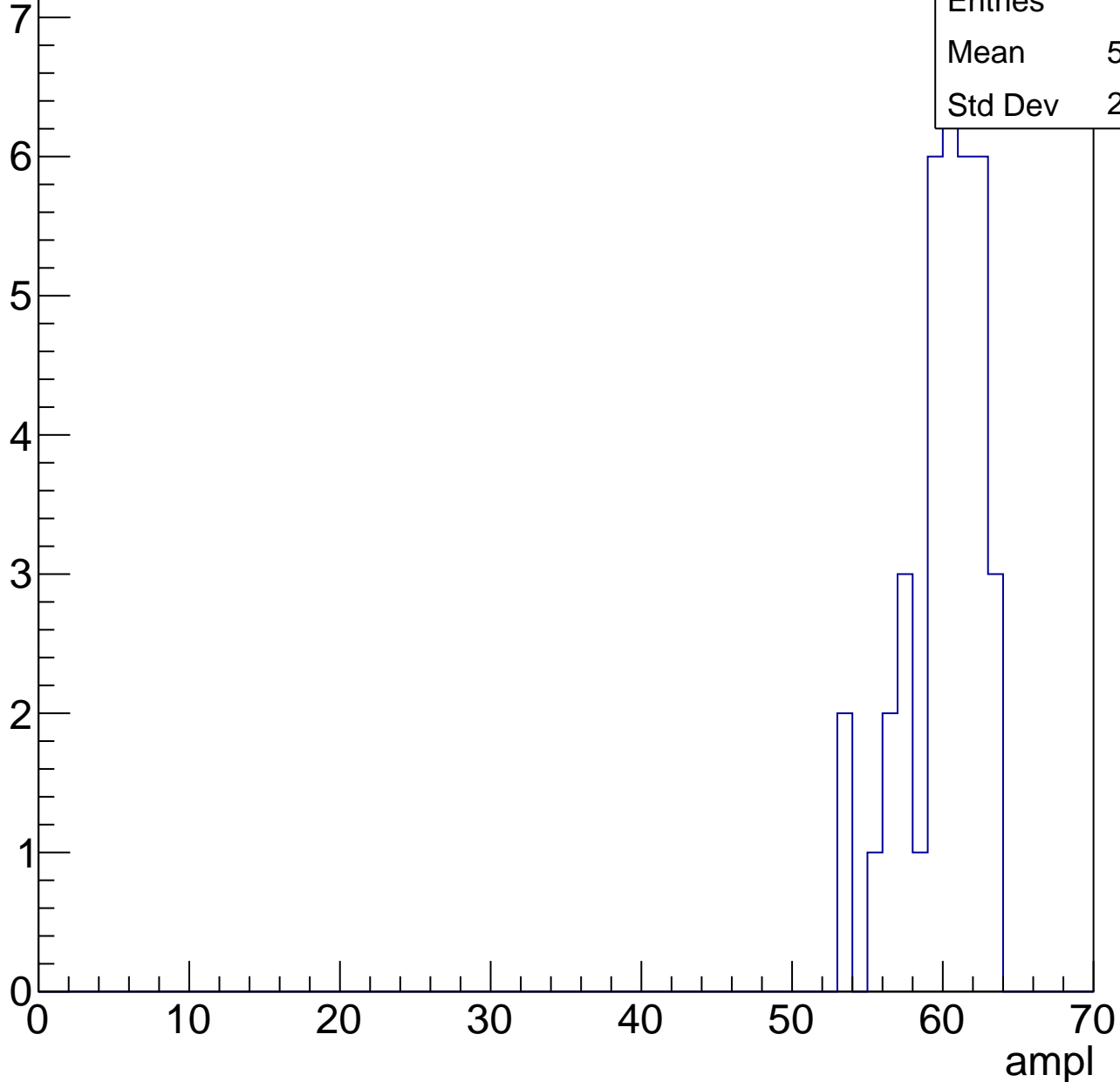
| | |
|---------|-------|
| Entries | 72 |
| Mean | 55.07 |
| Std Dev | 3.783 |



B1L100S, U7-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

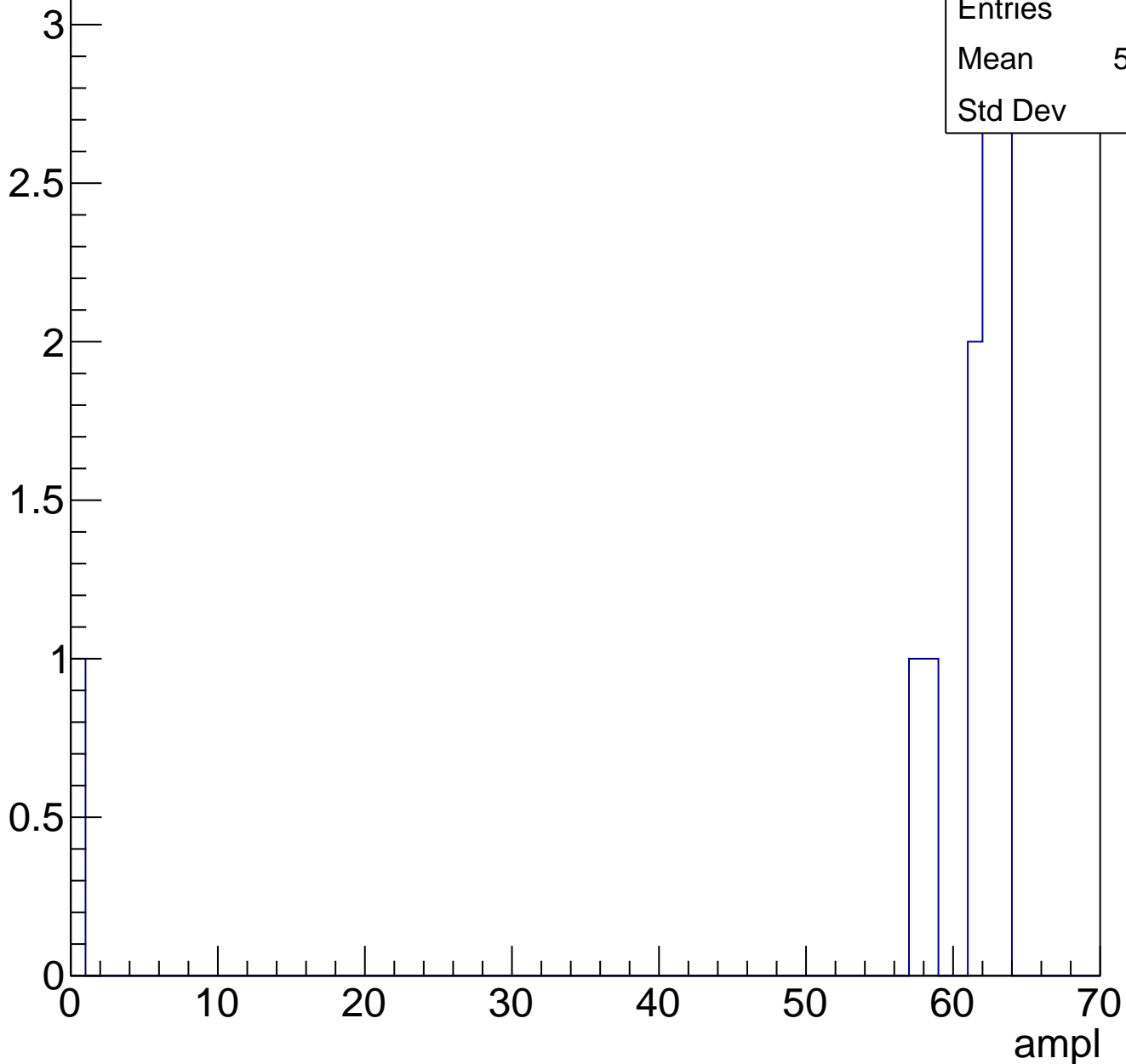
Entry



B1L100S, U7-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch1, adc0

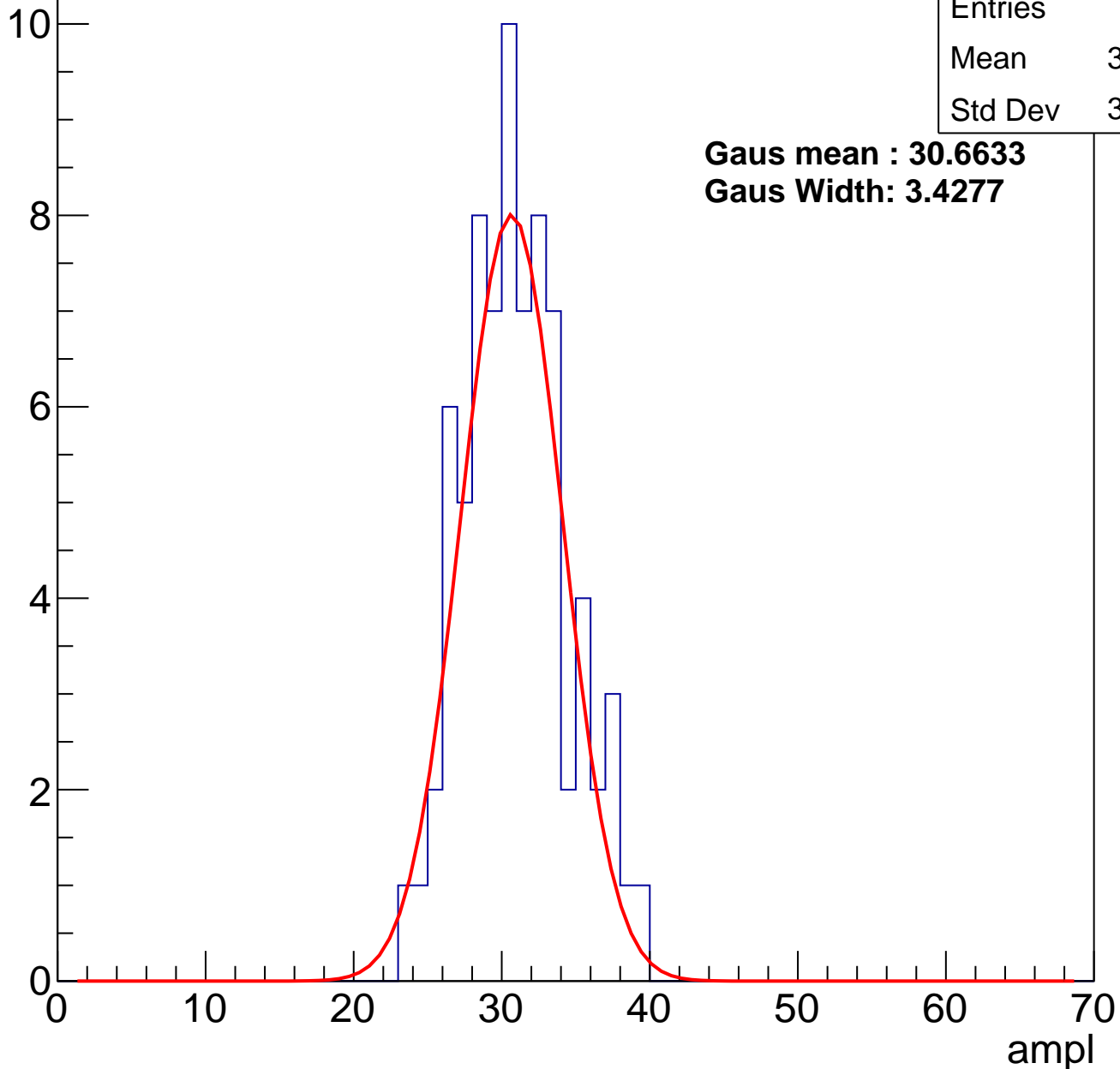
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 30.49 |
| Std Dev | 3.454 |

Gaus mean : 30.6633

Gaus Width: 3.4277

Entry



B1L100S, U7-ch1, adc1

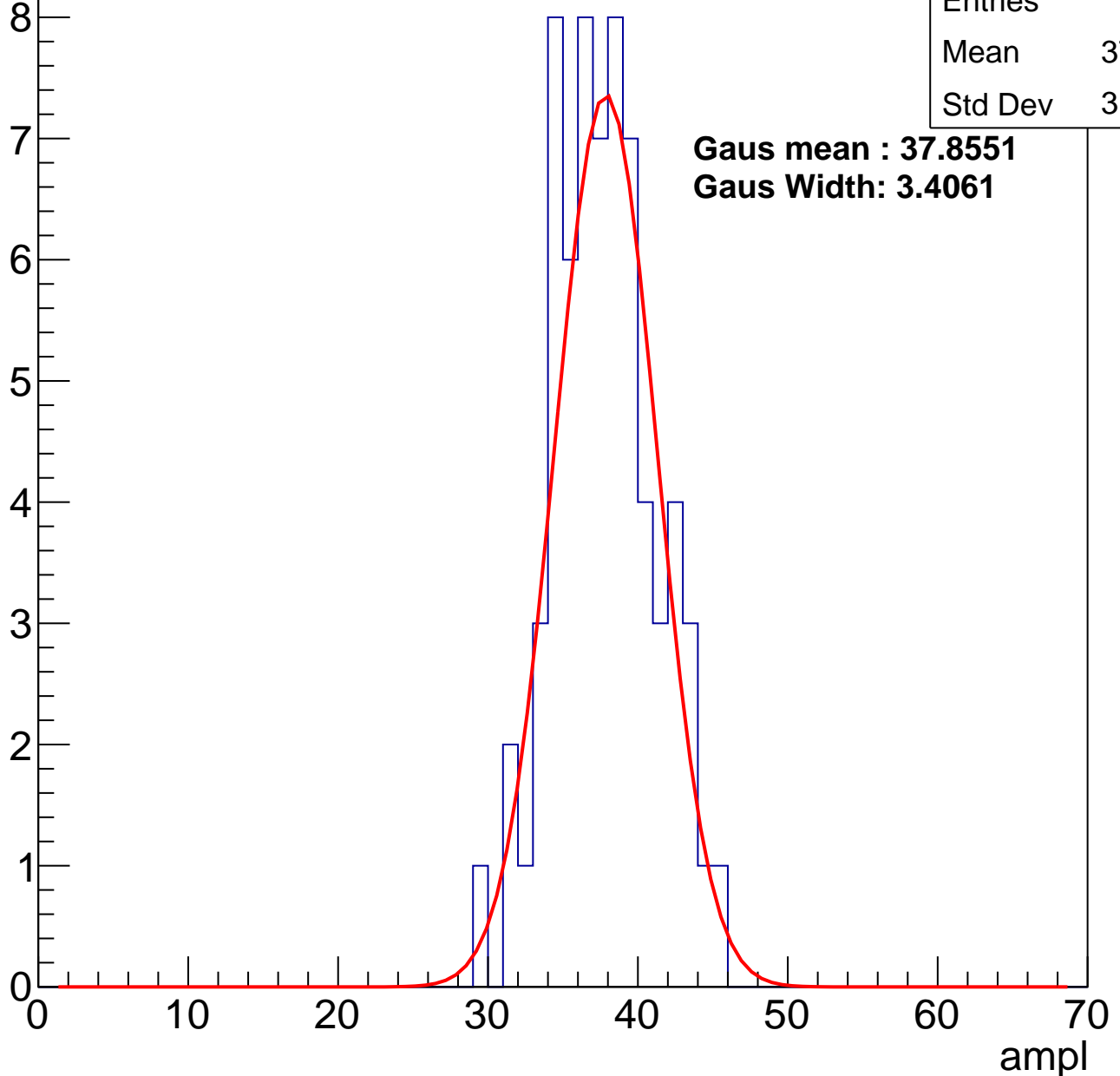
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.27 |
| Std Dev | 3.344 |

Gaus mean : 37.8551

Gaus Width: 3.4061



B1L100S, U7-ch1, adc2

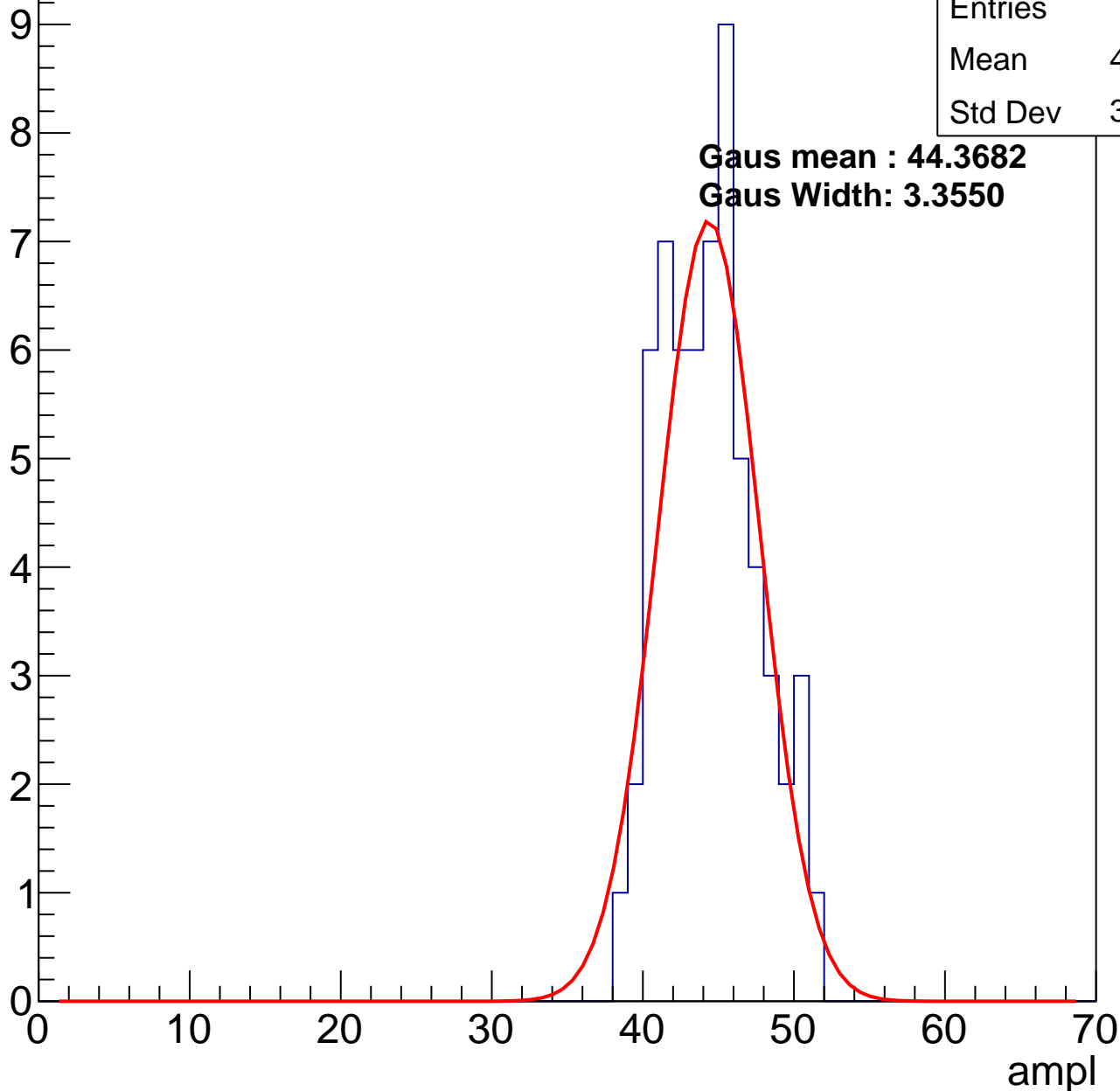
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.98 |
| Std Dev | 3.098 |

Gaus mean : 44.3682

Gaus Width: 3.3550

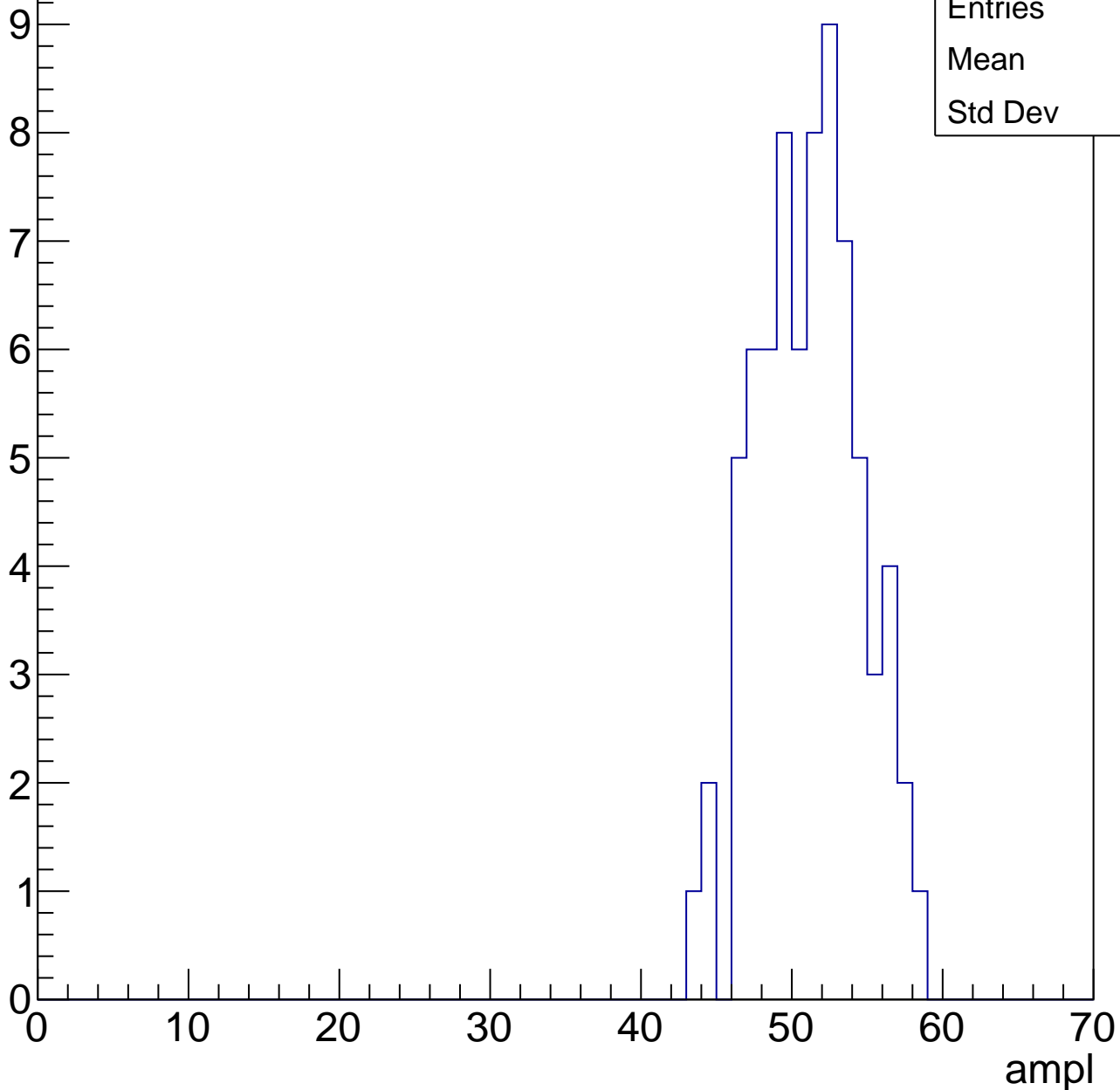


B1L100S, U7-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 73 |
| Mean | 50.7 |
| Std Dev | 3.35 |

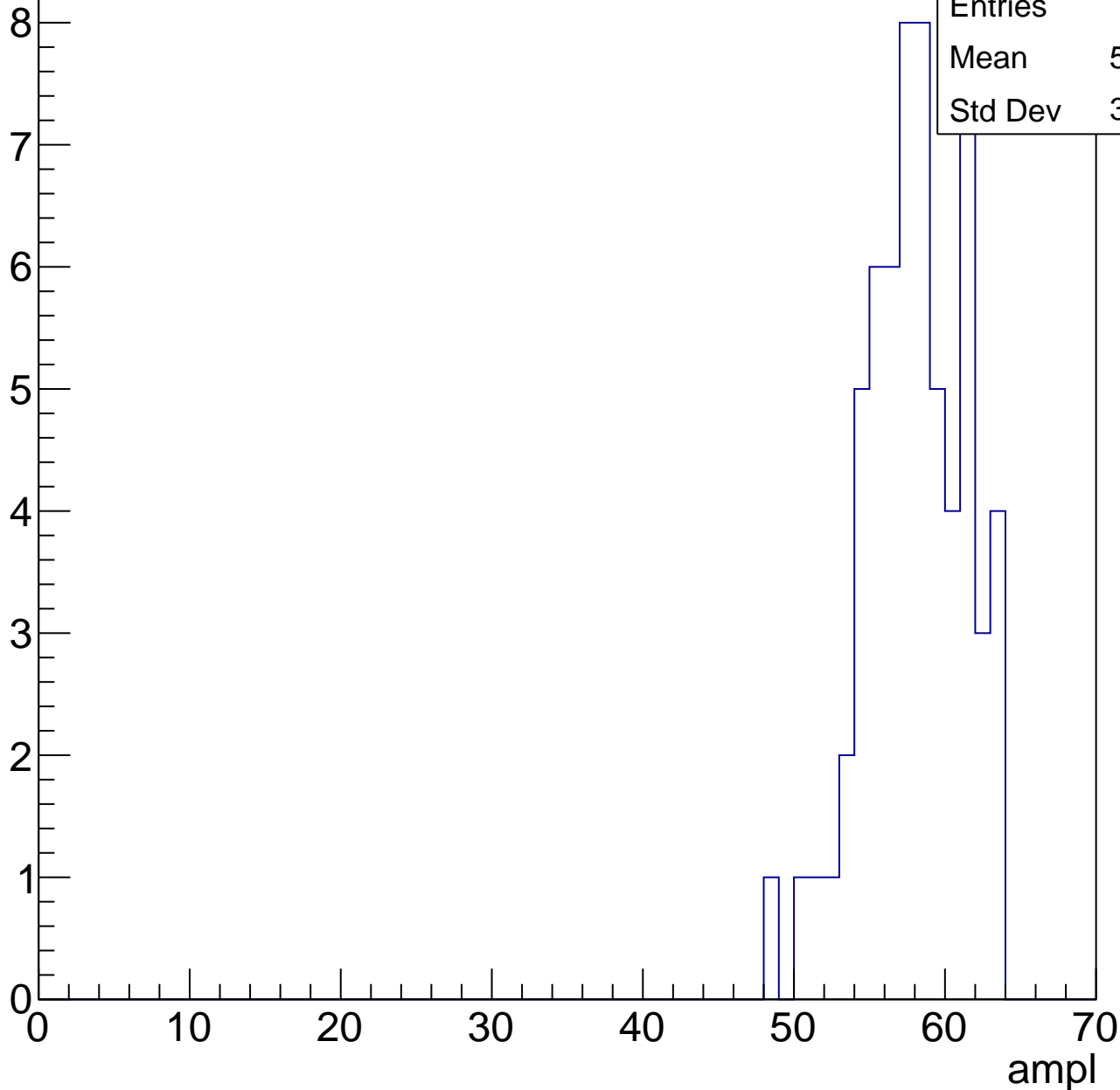


B1L100S, U7-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.52 |
| Std Dev | 3.314 |

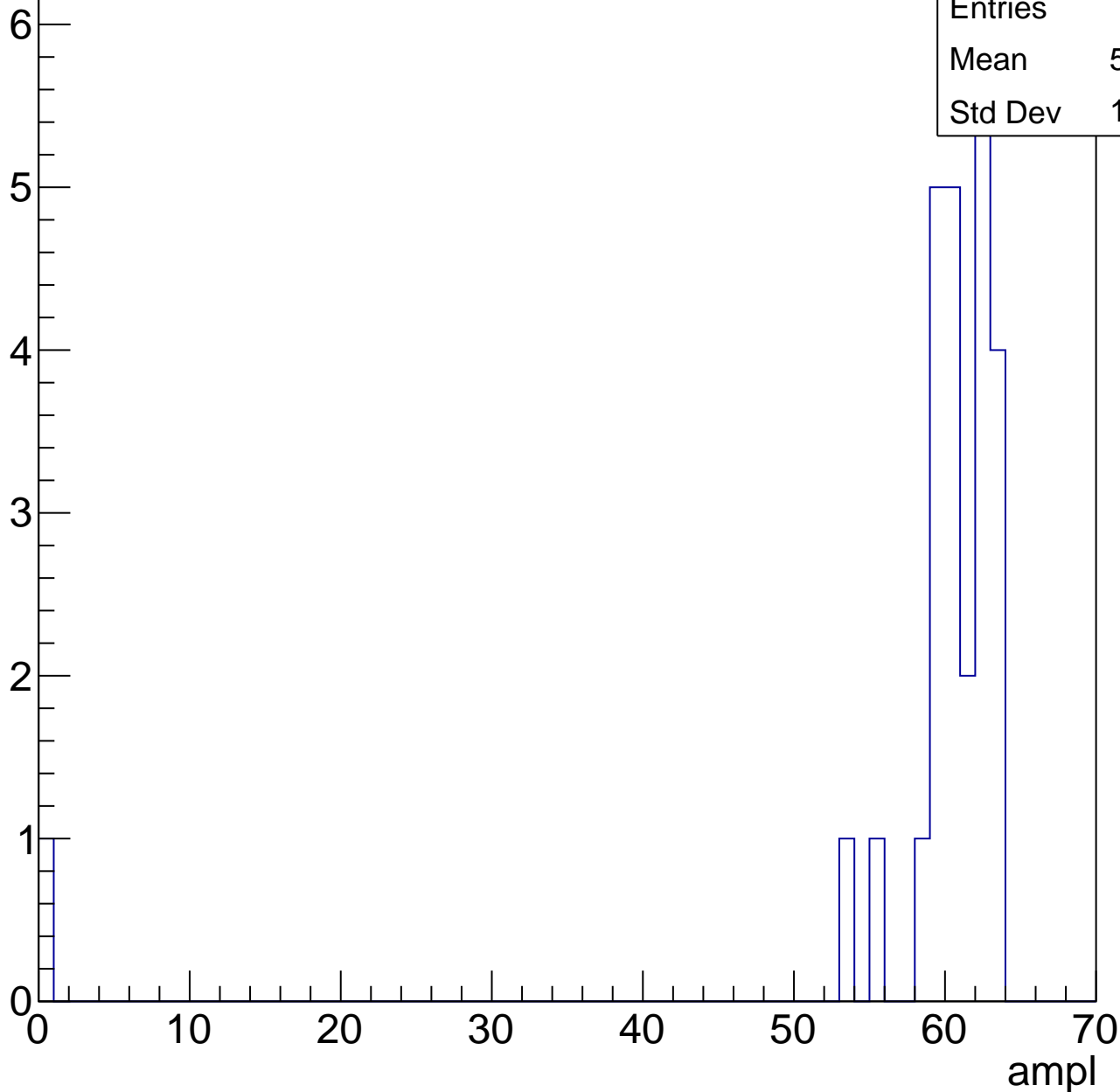


B1L100S, U7-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 57.96 |
| Std Dev | 11.83 |



B1L100S, U7-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 62 |
| Std Dev | 1.225 |

B1L100S, U7-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch2, adc0

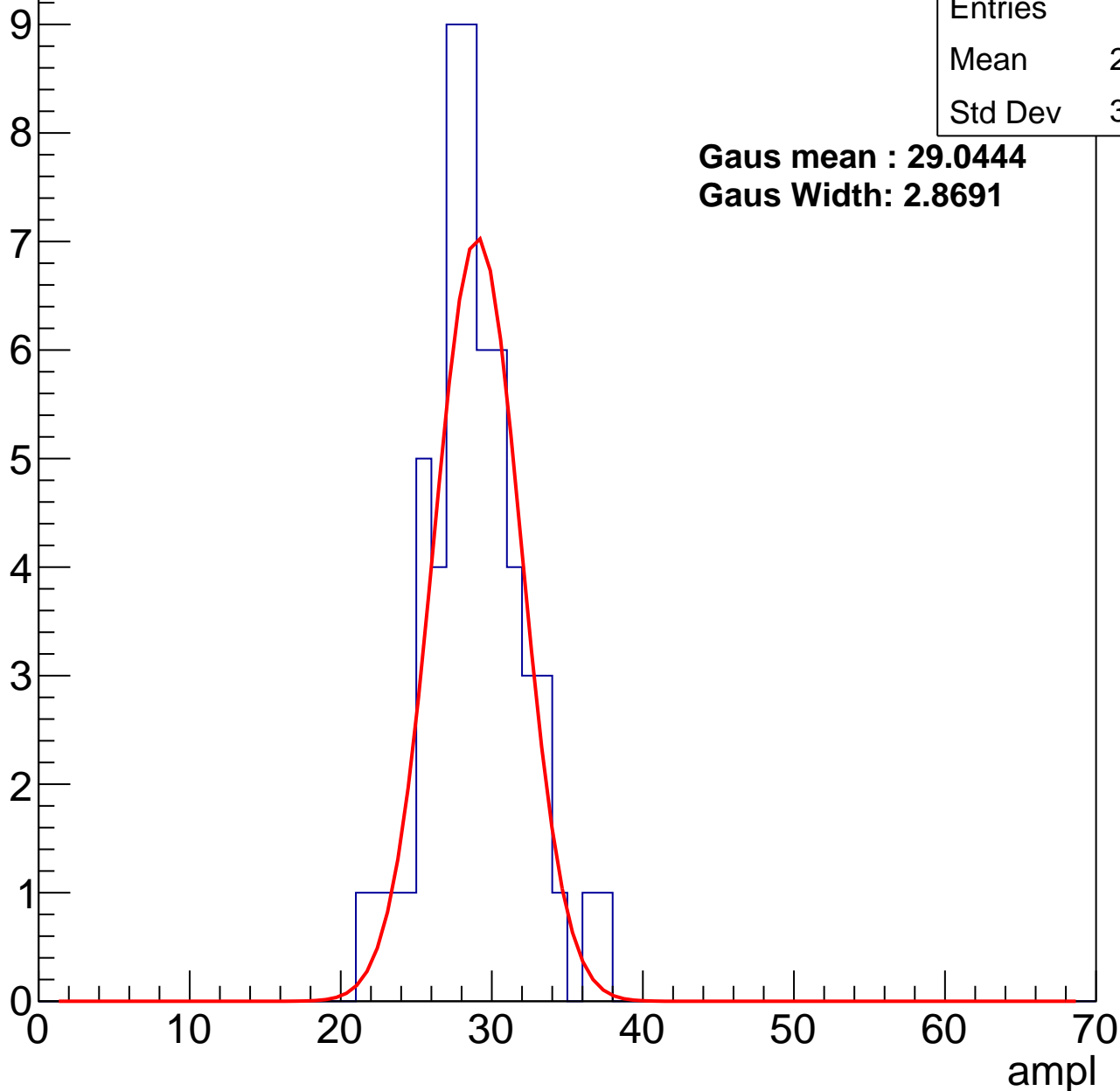
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 28.46 |
| Std Dev | 3.145 |

Gaus mean : 29.0444

Gaus Width: 2.8691



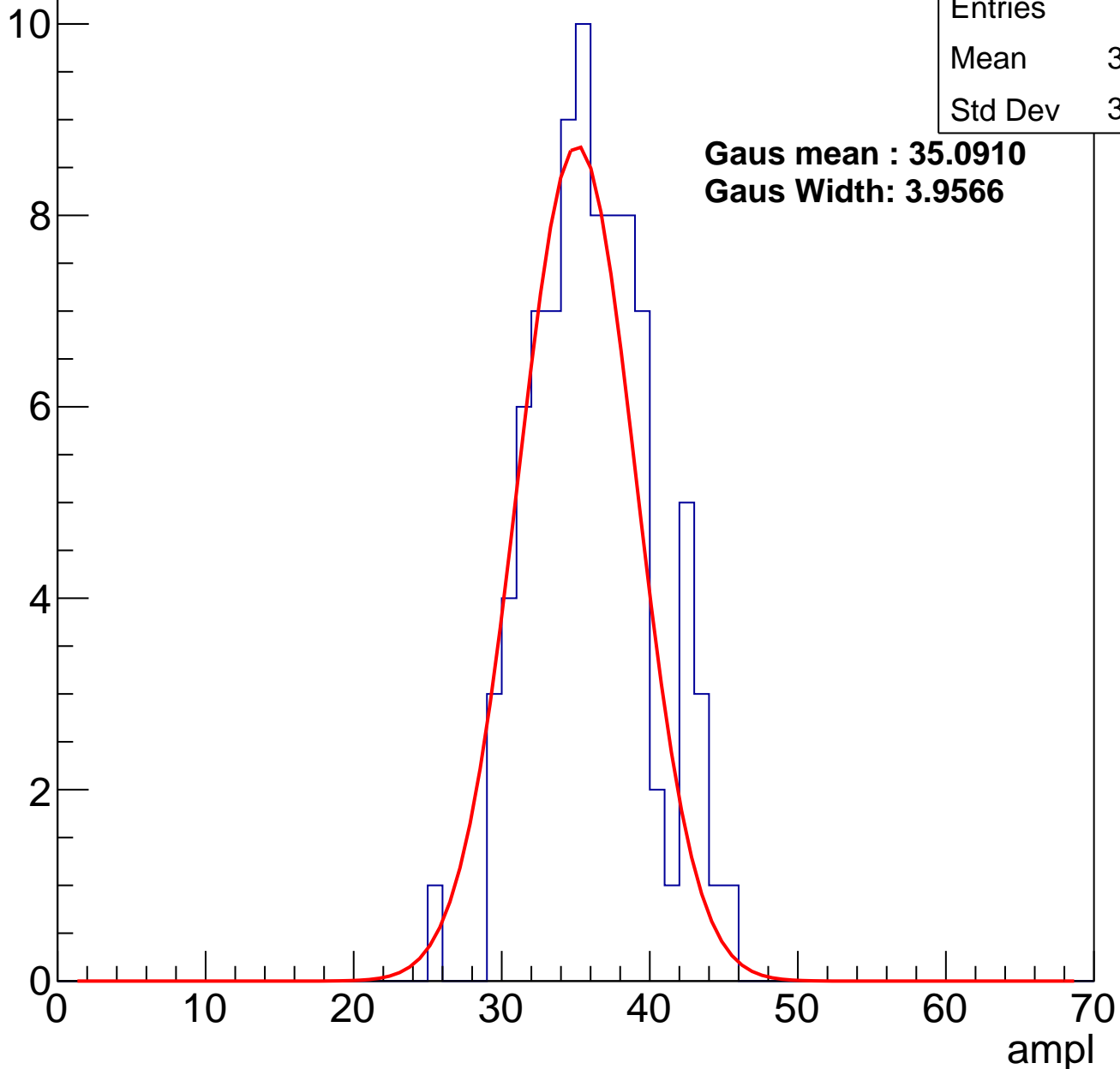
B1L100S, U7-ch2, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 35.59 |
| Std Dev | 3.922 |

Gaus mean : 35.0910
Gaus Width: 3.9566

Entry



B1L100S, U7-ch2, adc2

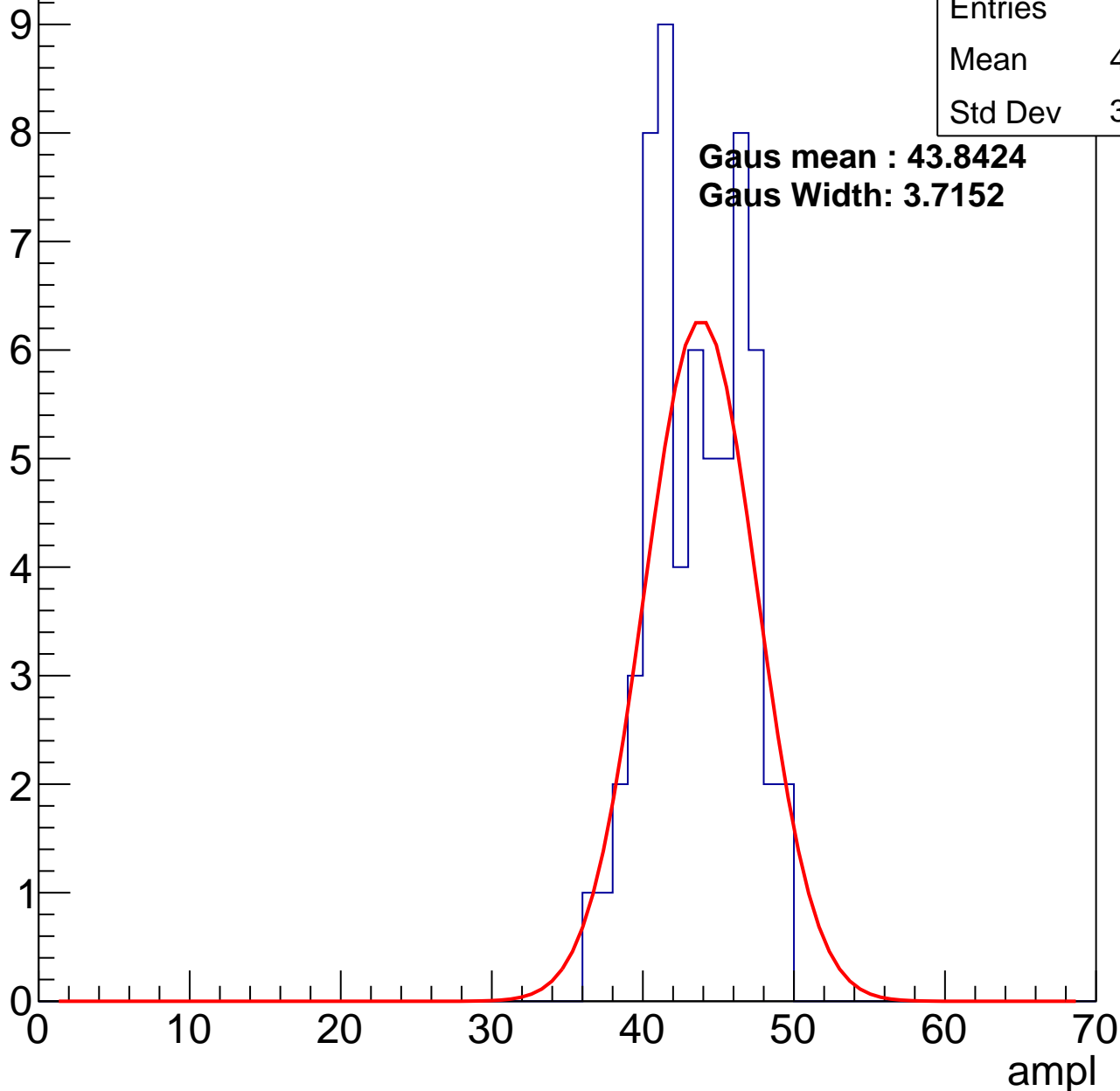
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.06 |
| Std Dev | 3.136 |

Gaus mean : 43.8424

Gaus Width: 3.7152

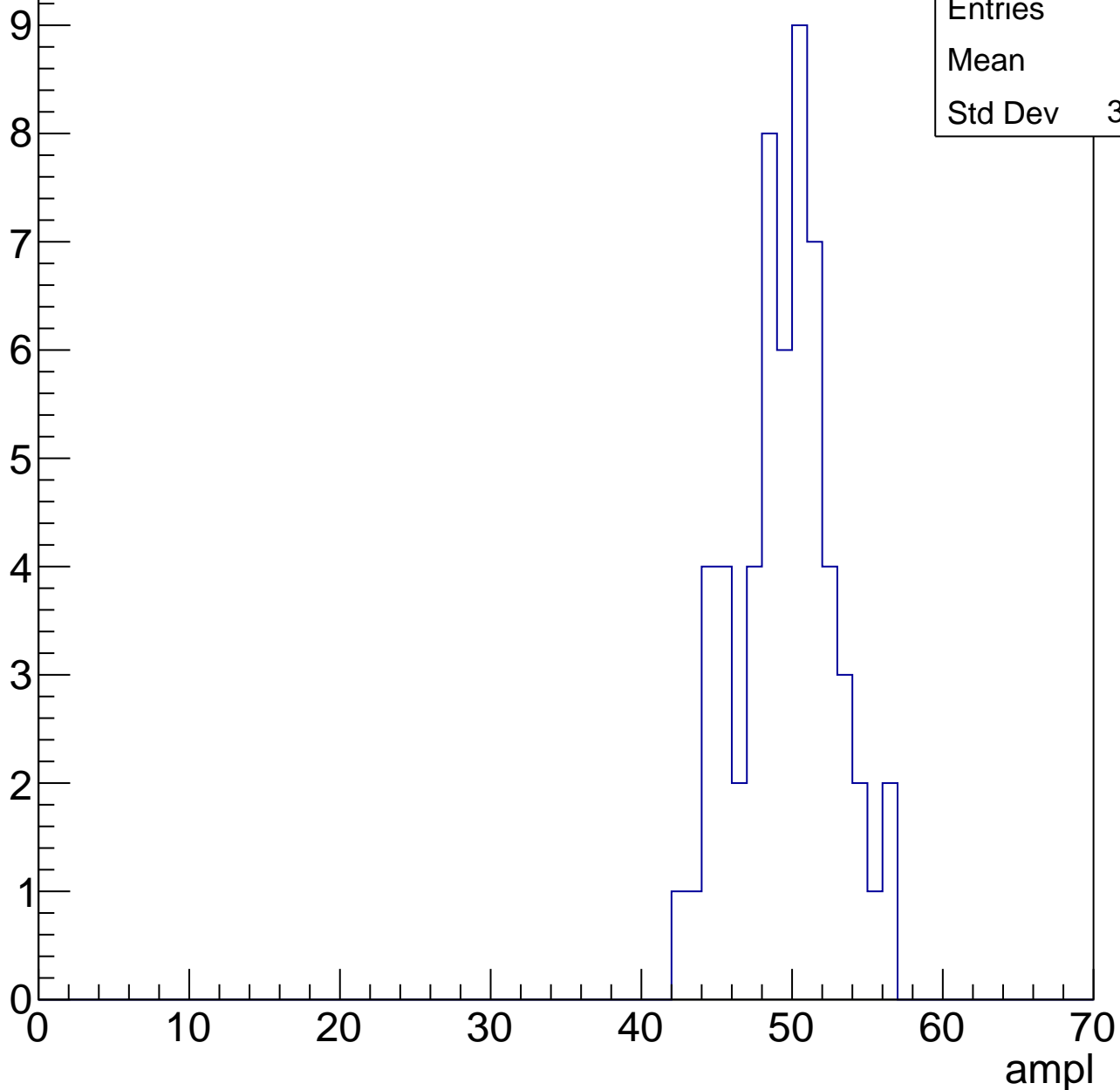


B1L100S, U7-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

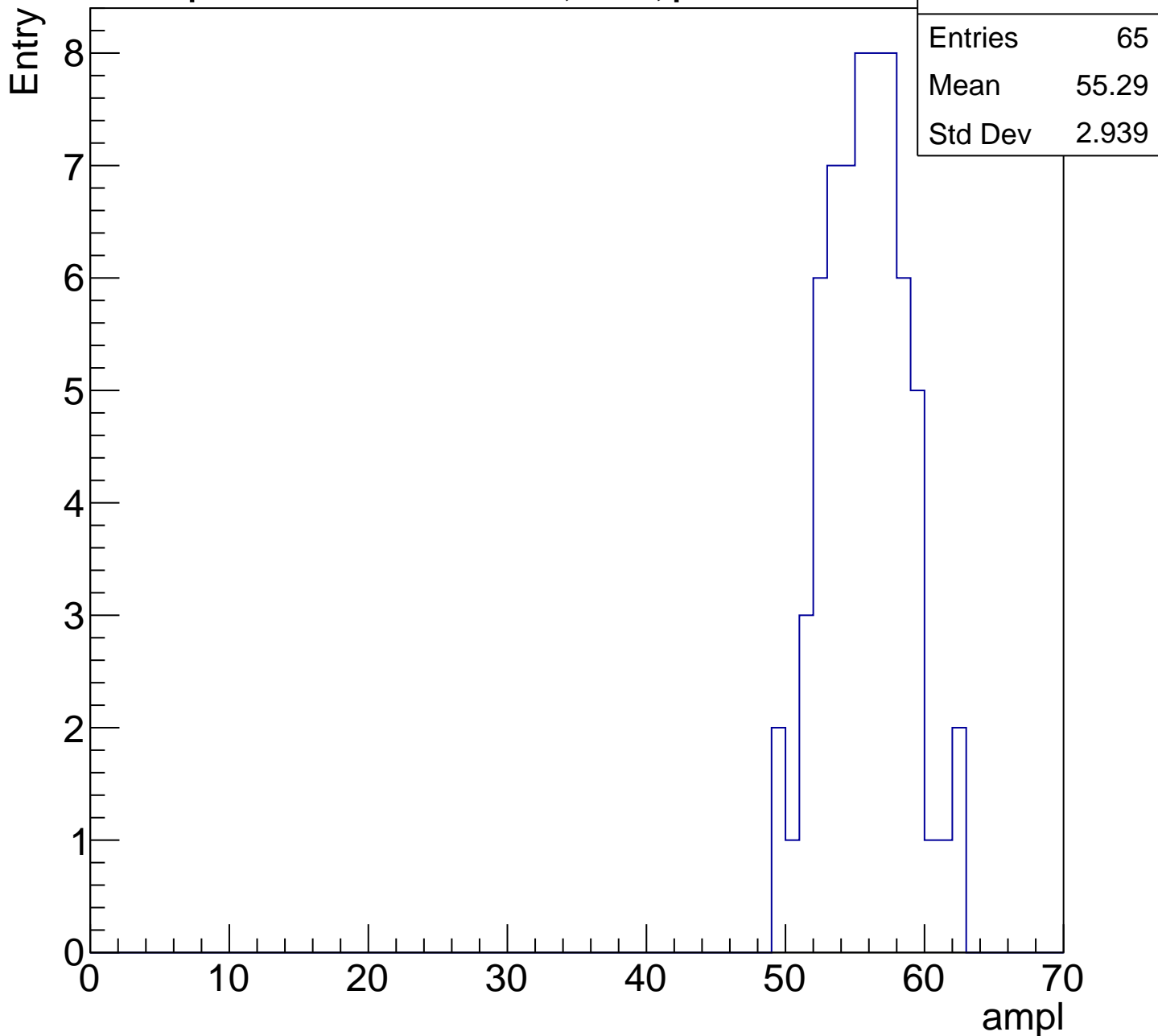
Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 49.1 |
| Std Dev | 3.204 |



B1L100S, U7-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.73 |
| Std Dev | 9.644 |

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

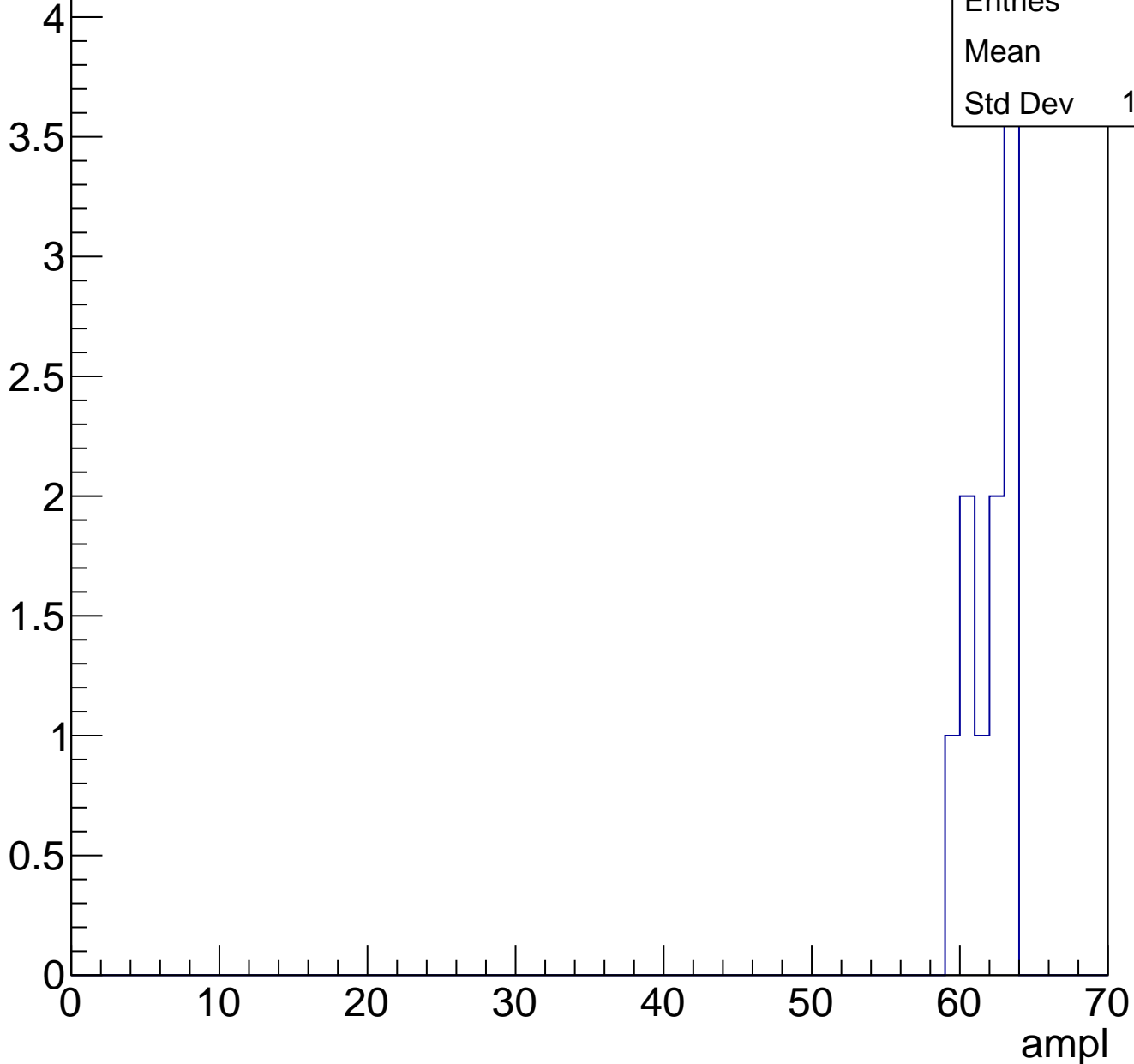
7

8

B1L100S, U7-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 10 |
| Mean | 61.6 |
| Std Dev | 1.428 |

B1L100S, U7-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch3, adc0

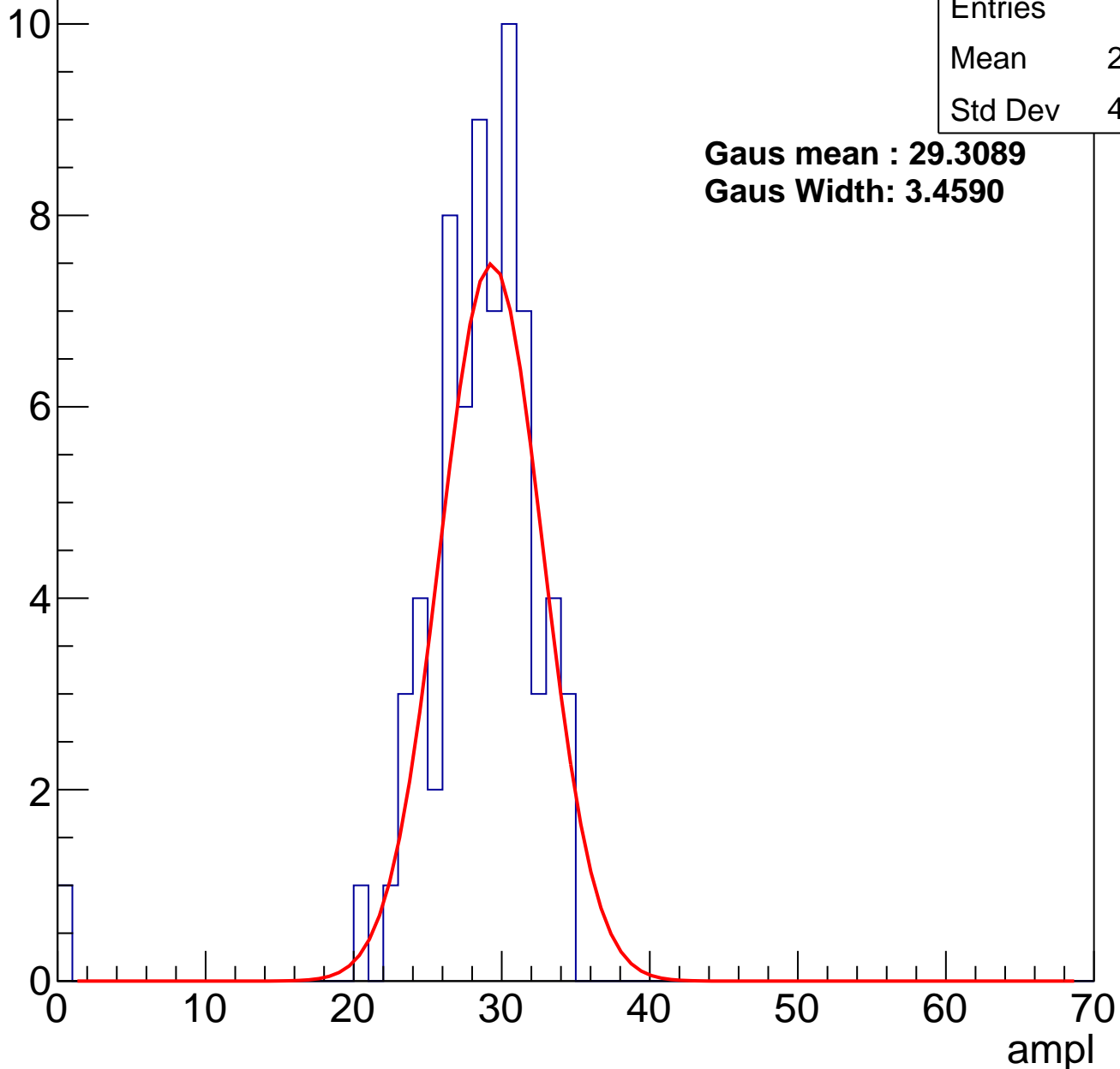
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 27.96 |
| Std Dev | 4.576 |

Gaus mean : 29.3089

Gaus Width: 3.4590

Entry



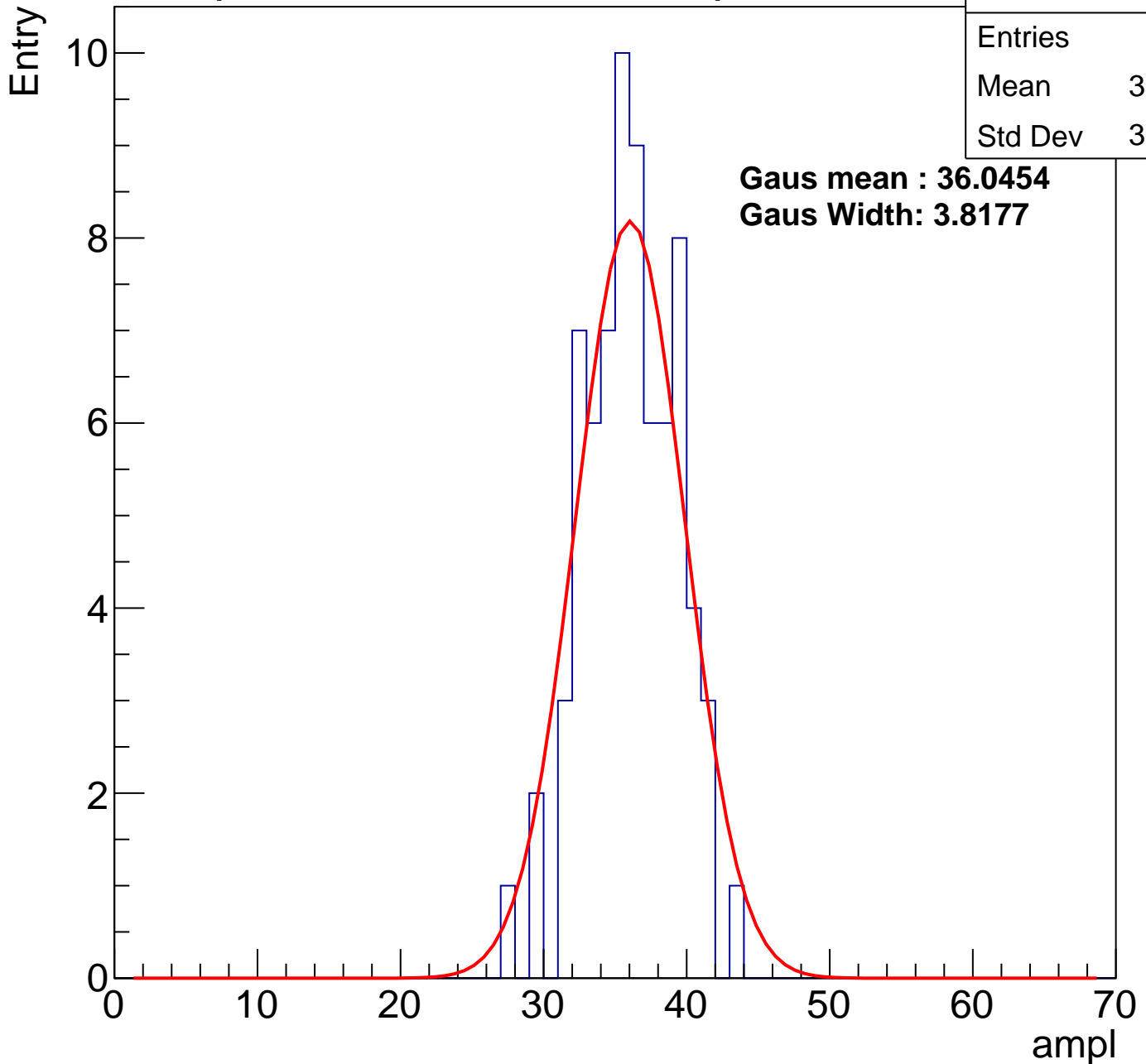
B1L100S, U7-ch3, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 35.62 |
| Std Dev | 3.178 |

Gaus mean : 36.0454

Gaus Width: 3.8177



B1L100S, U7-ch3, adc2

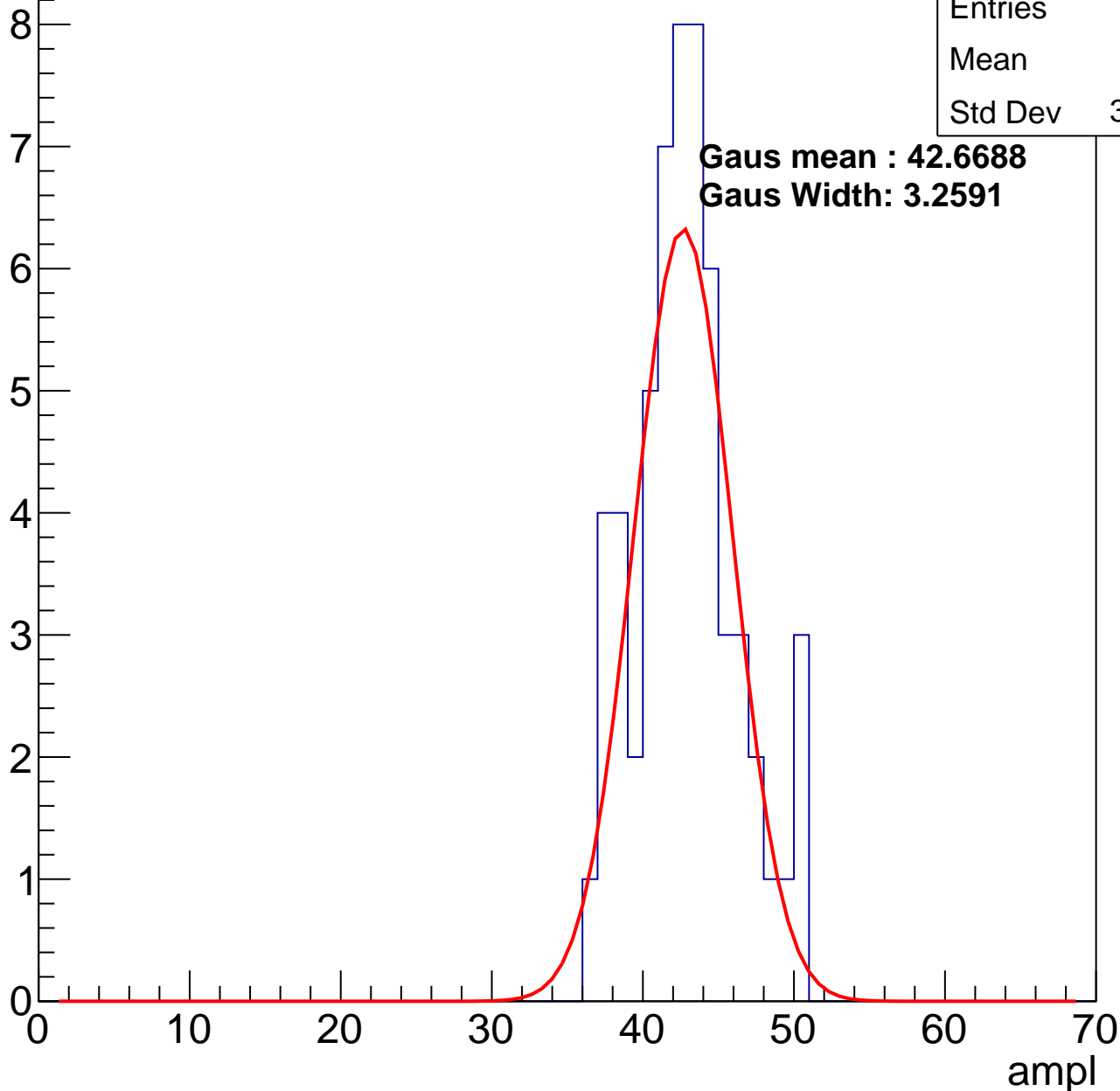
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 42.4 |
| Std Dev | 3.398 |

Gaus mean : 42.6688

Gaus Width: 3.2591

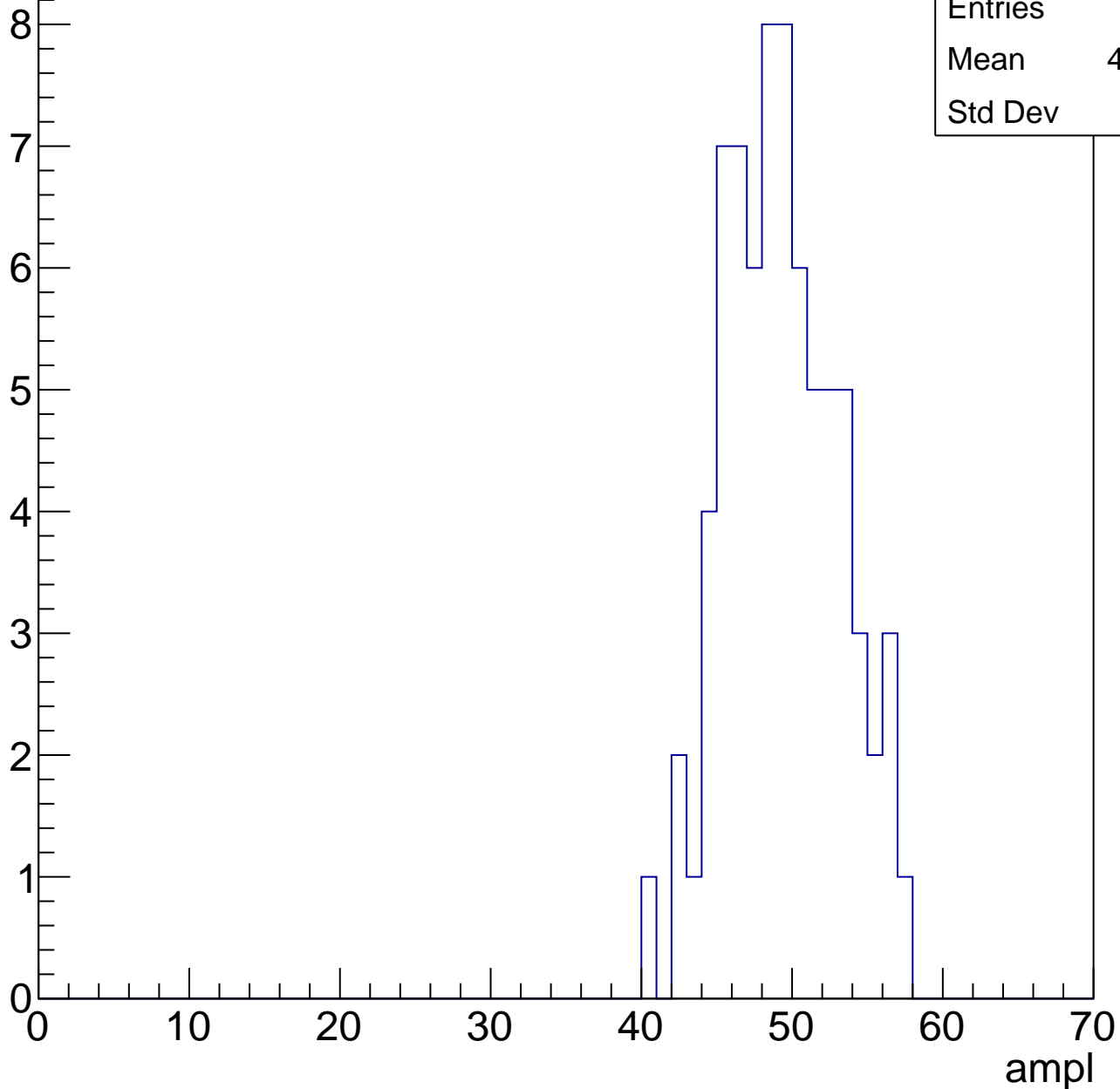


B1L100S, U7-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

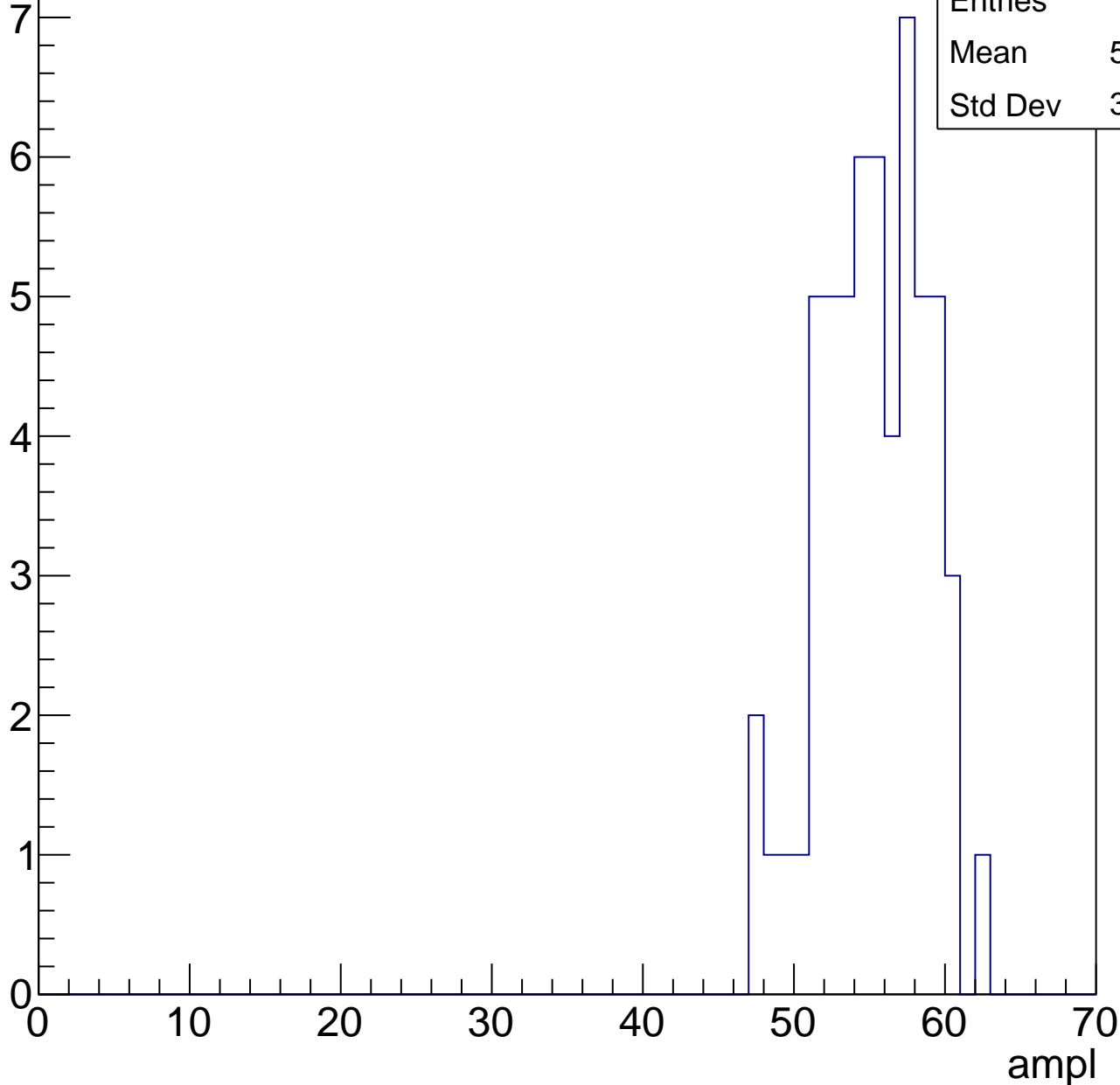
| | |
|---------|-------|
| Entries | 74 |
| Mean | 48.85 |
| Std Dev | 3.73 |



B1L100S, U7-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

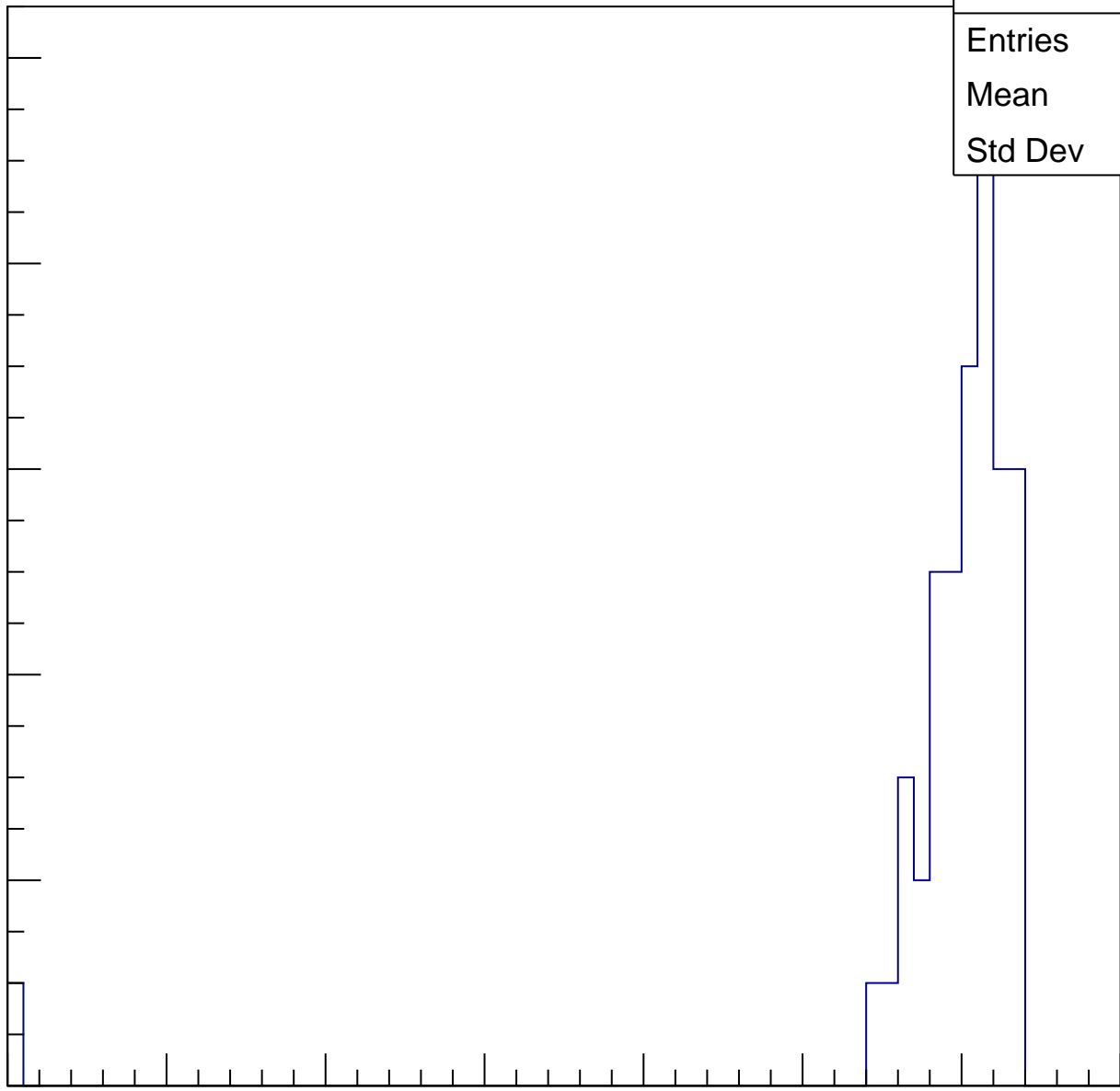
| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.64 |
| Std Dev | 8.936 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

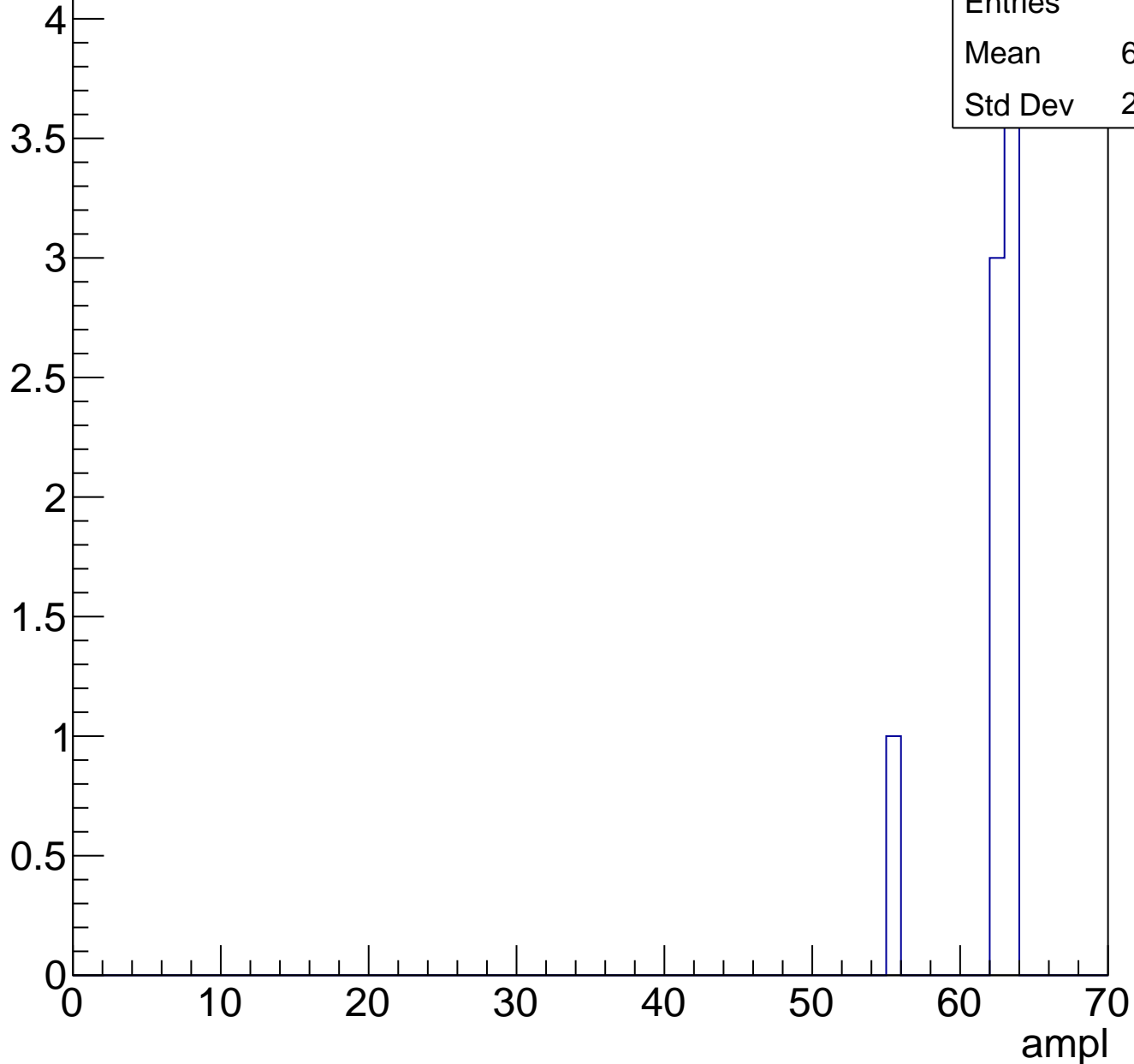
ampl



B1L100S, U7-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

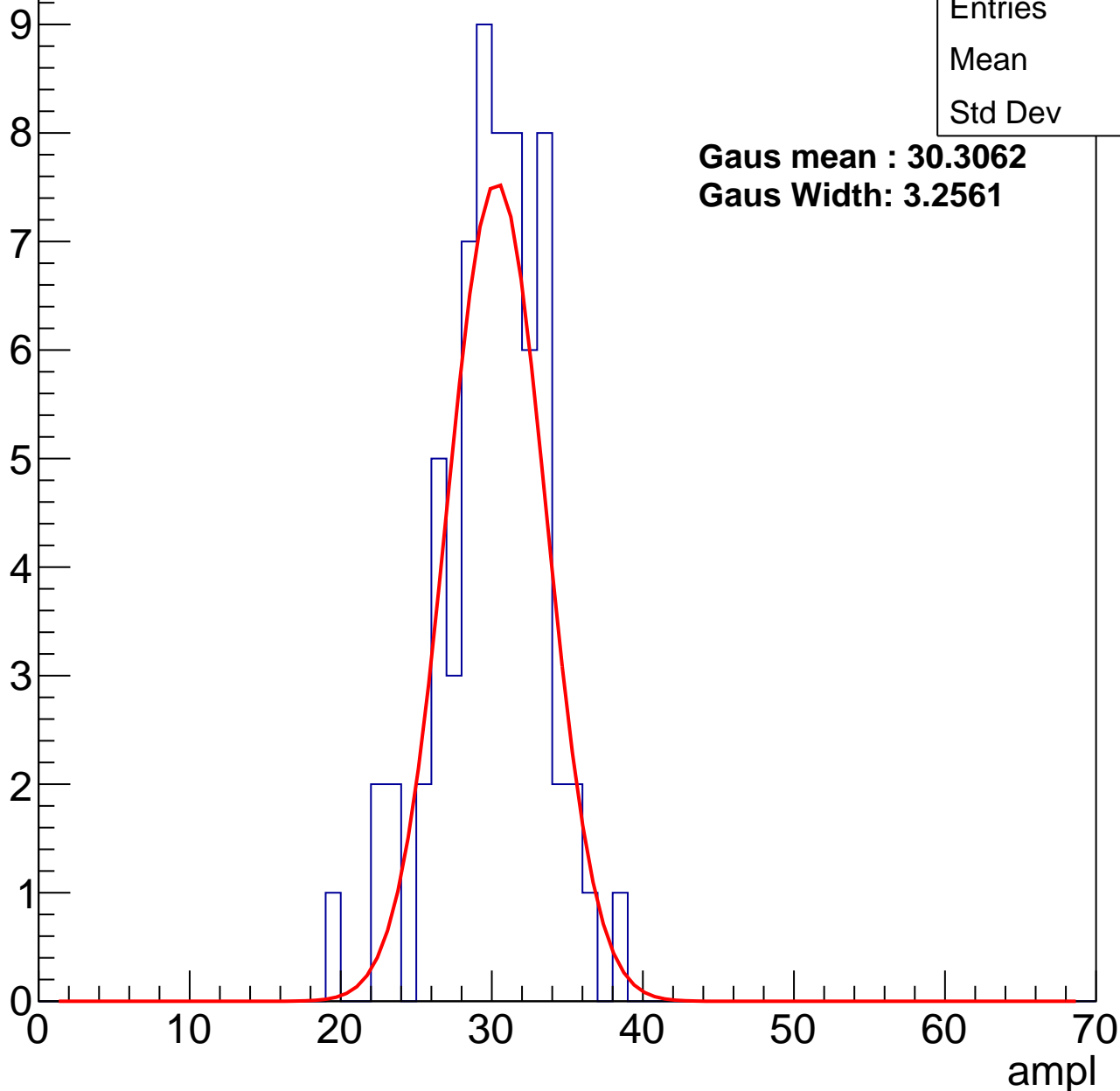


| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch4, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 67 |
| Mean | 29.6 |
| Std Dev | 3.49 |

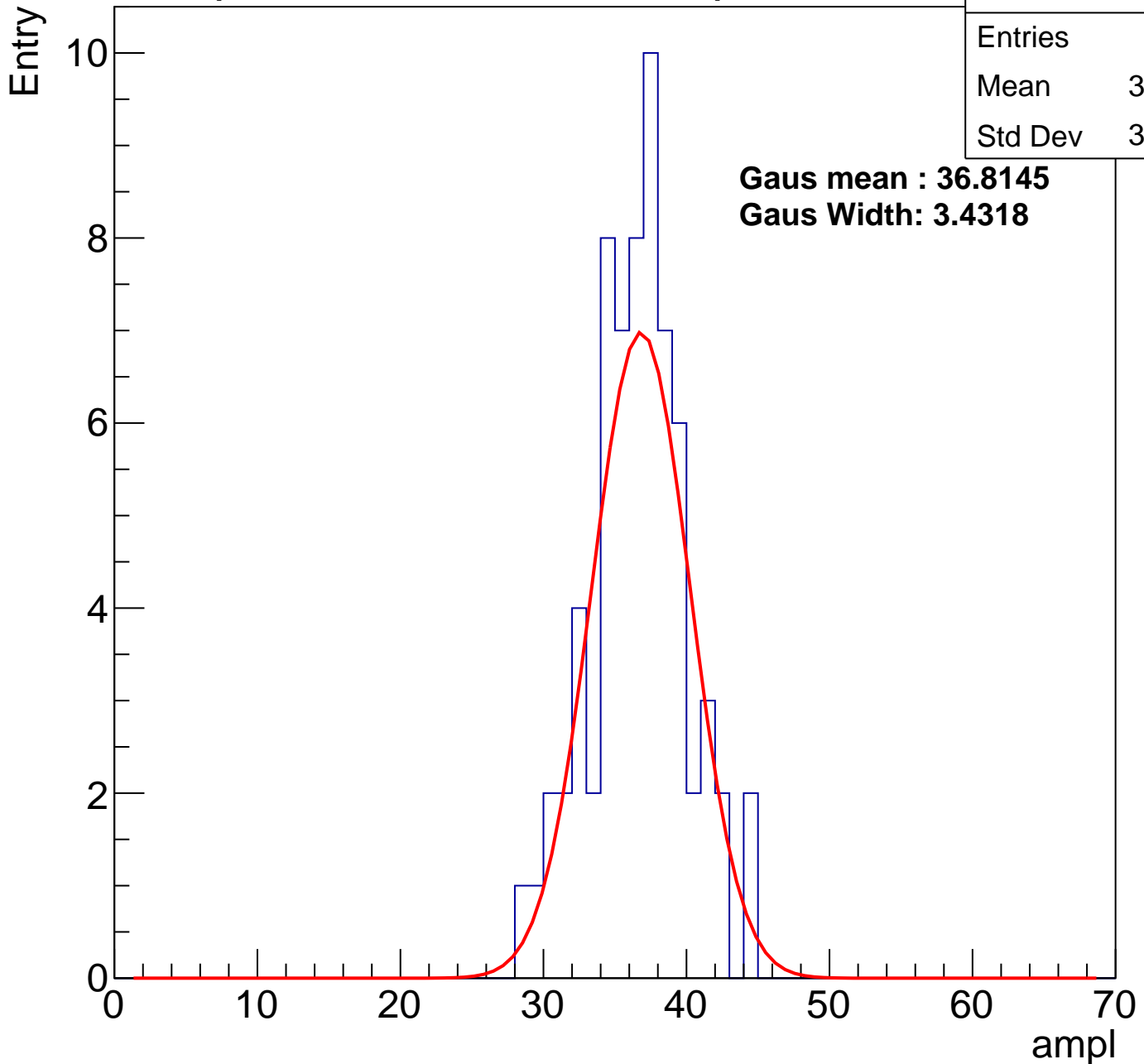
B1L100S, U7-ch4, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.16 |
| Std Dev | 3.353 |

Gaus mean : 36.8145

Gaus Width: 3.4318



B1L100S, U7-ch4, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 41.89 |
| Std Dev | 2.784 |

7

6

5

4

3

2

1

0

Gaus mean : 41.8400

Gaus Width: 2.7231

0

10

20

30

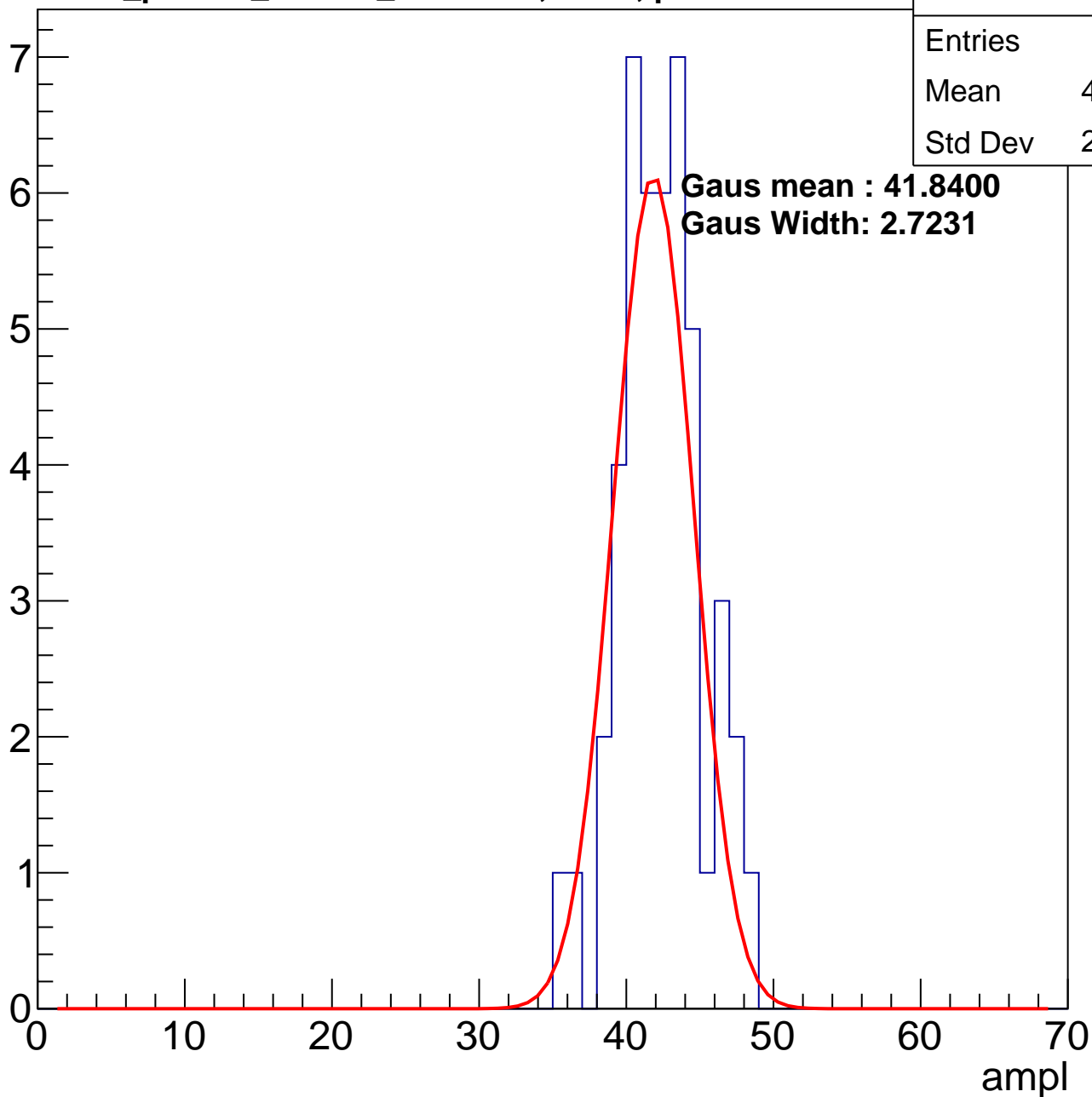
40

50

60

70

ampl

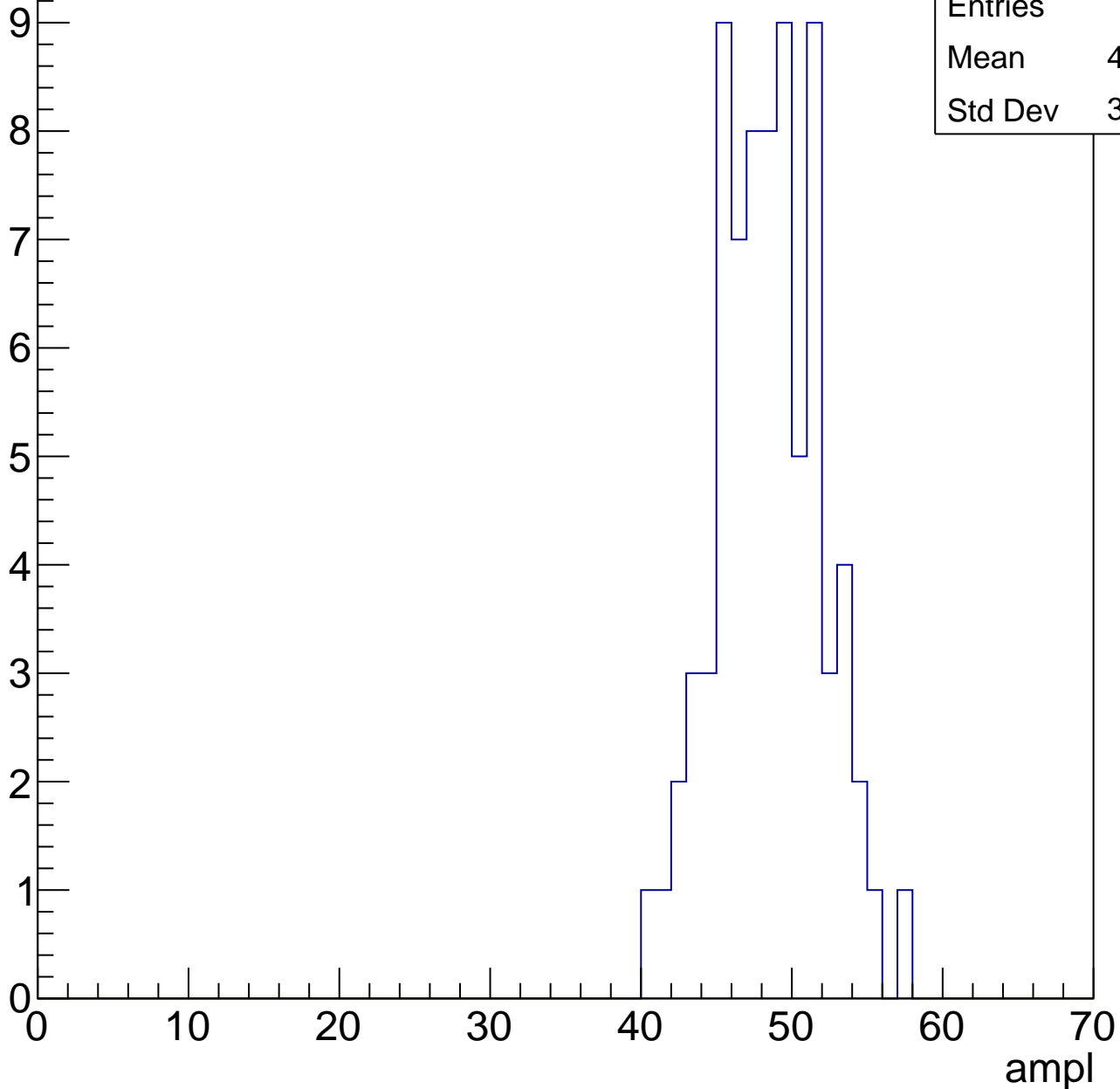


B1L100S, U7-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.04 |
| Std Dev | 3.412 |

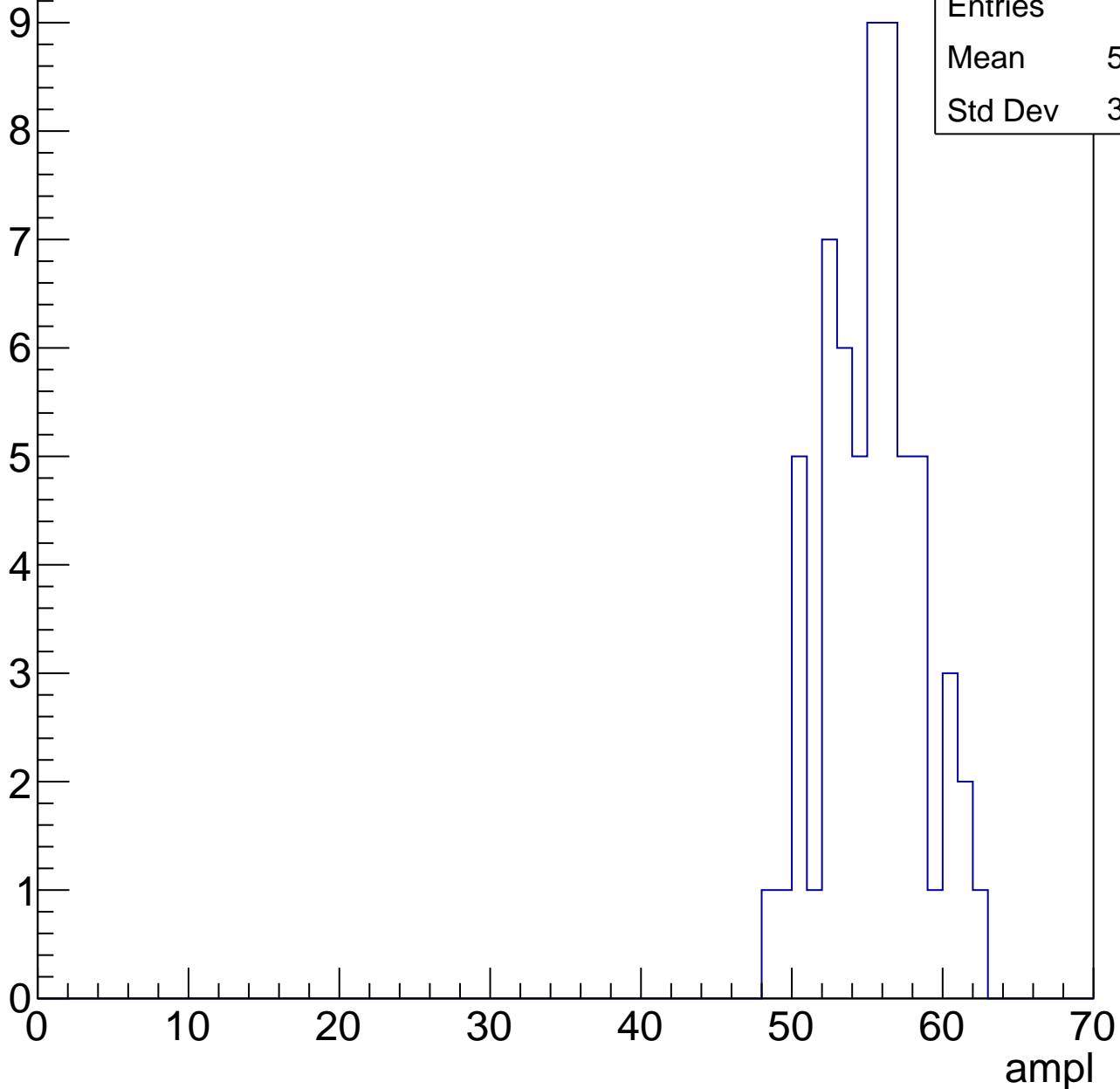


B1L100S, U7-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

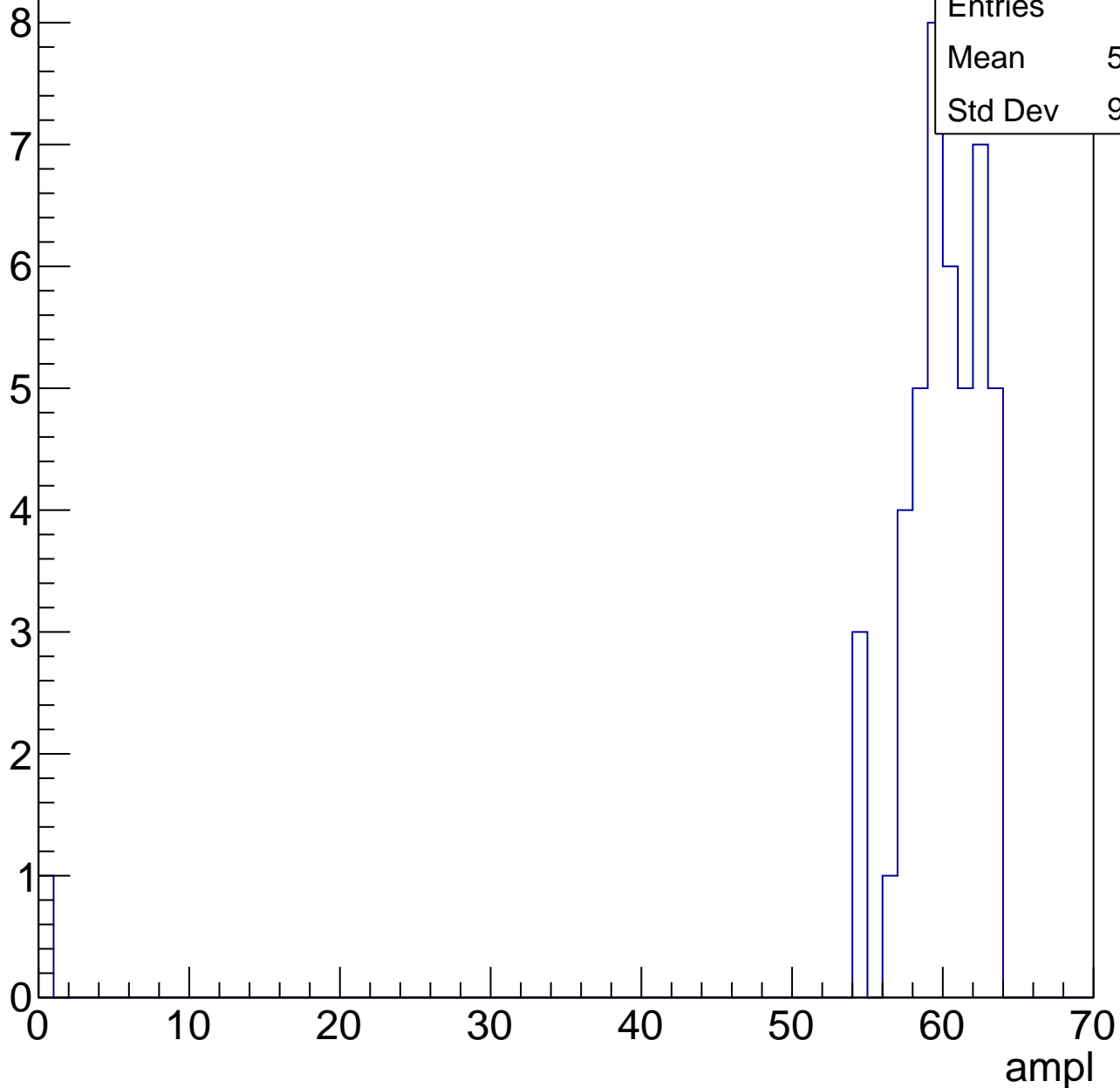
| | |
|---------|-------|
| Entries | 61 |
| Mean | 54.87 |
| Std Dev | 3.144 |



B1L100S, U7-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

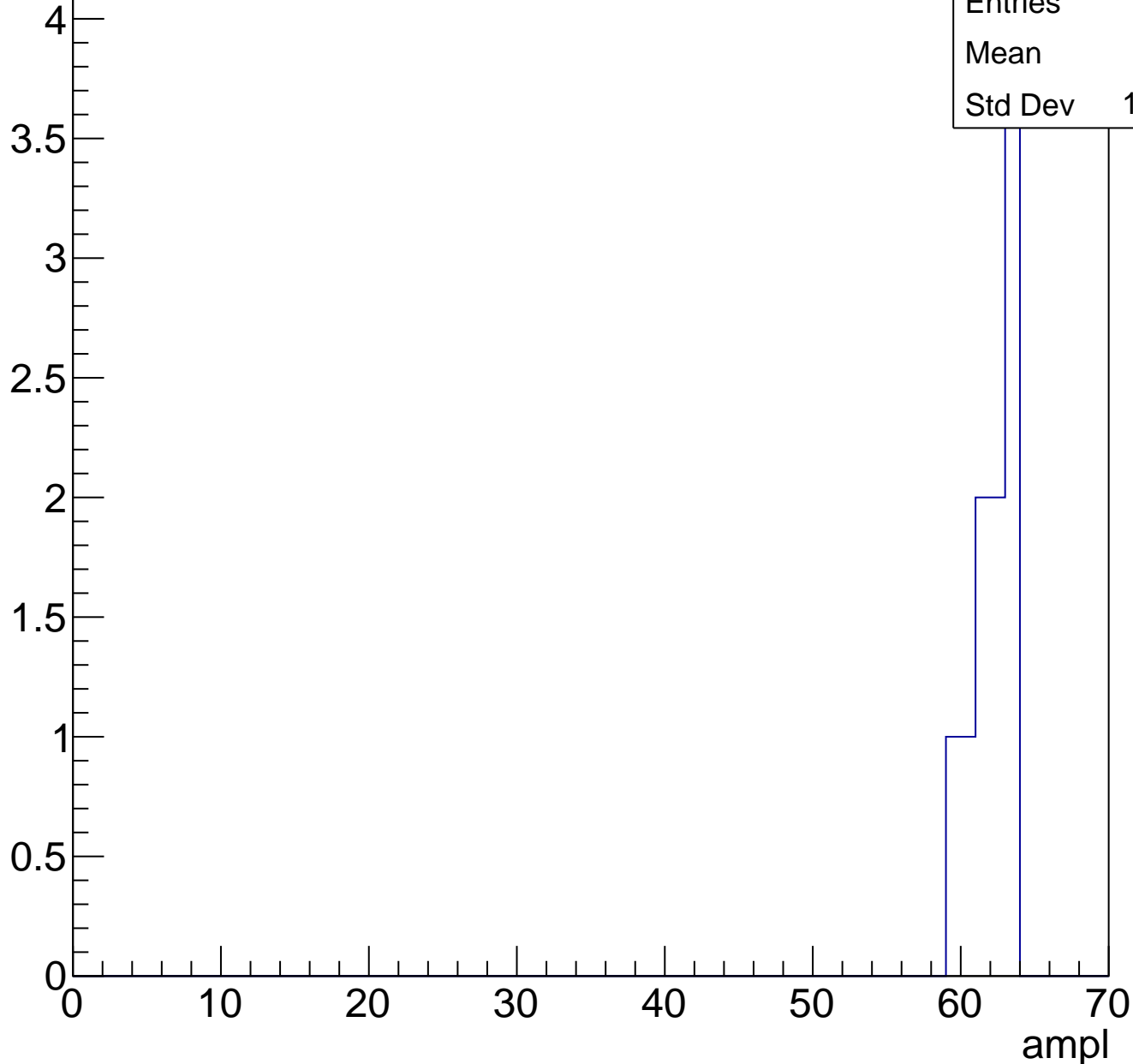
Entry



B1L100S, U7-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

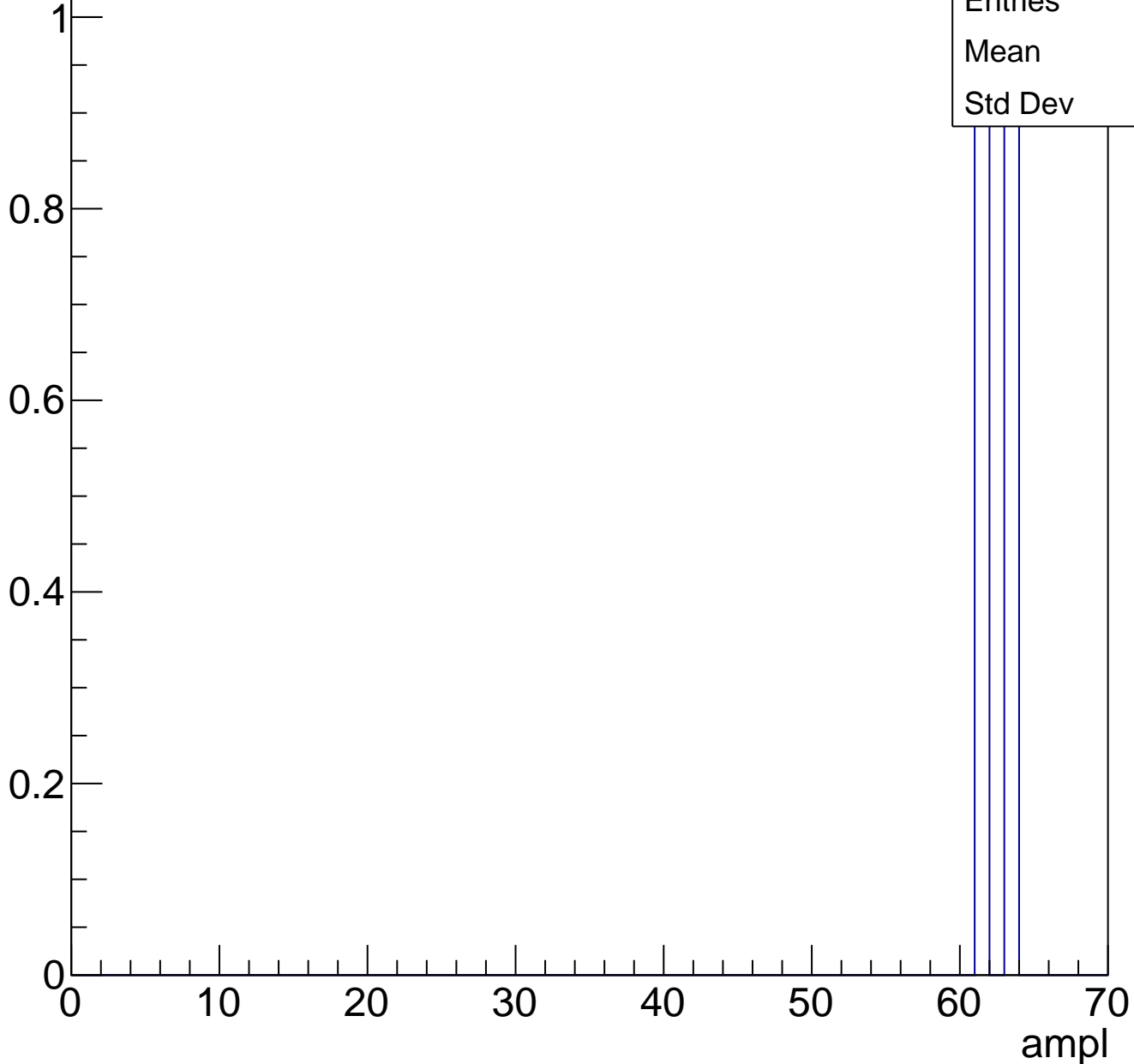
Entry



B1L100S, U7-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch5, adc0

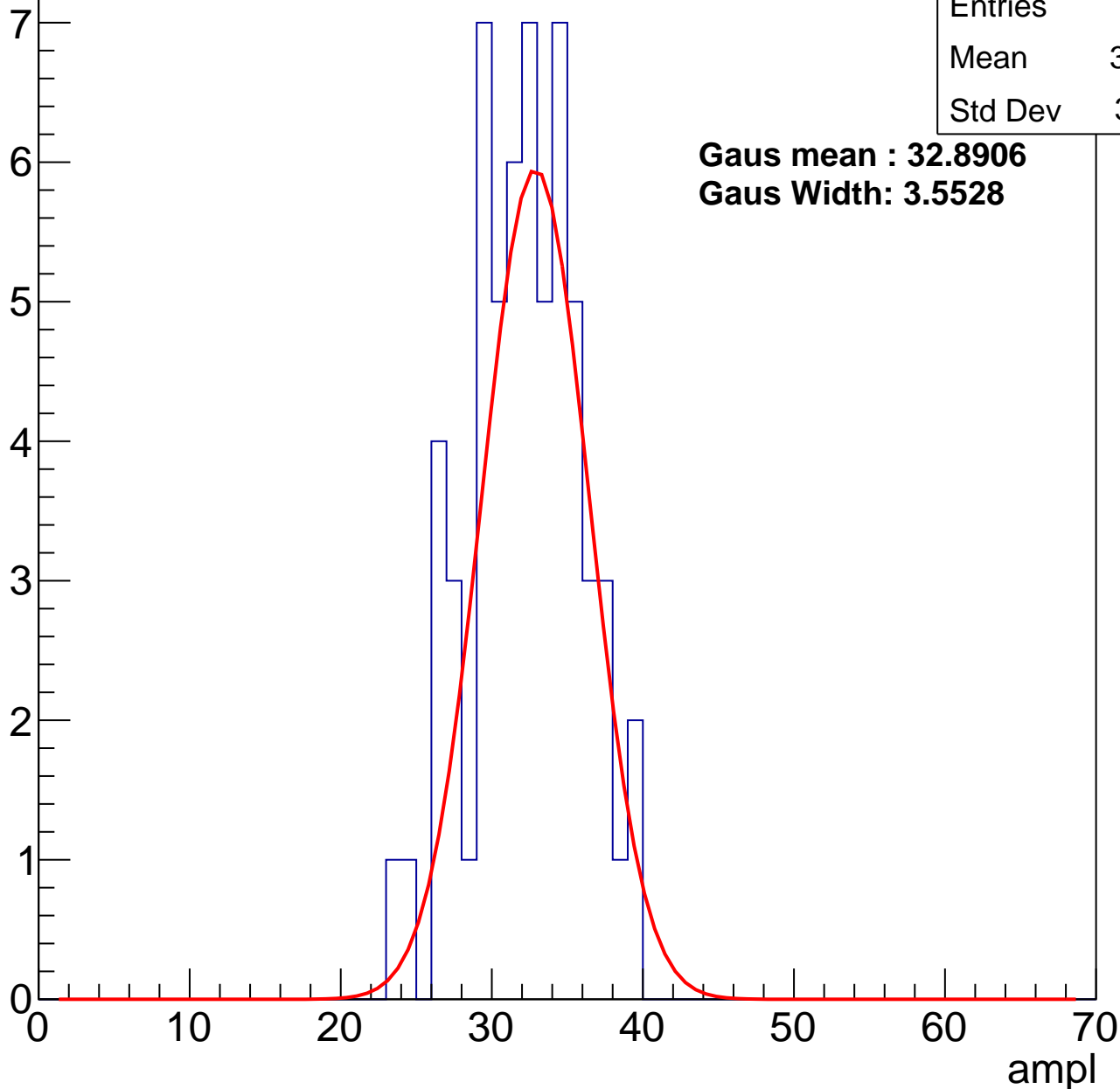
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 31.74 |
| Std Dev | 3.621 |

Gaus mean : 32.8906

Gaus Width: 3.5528



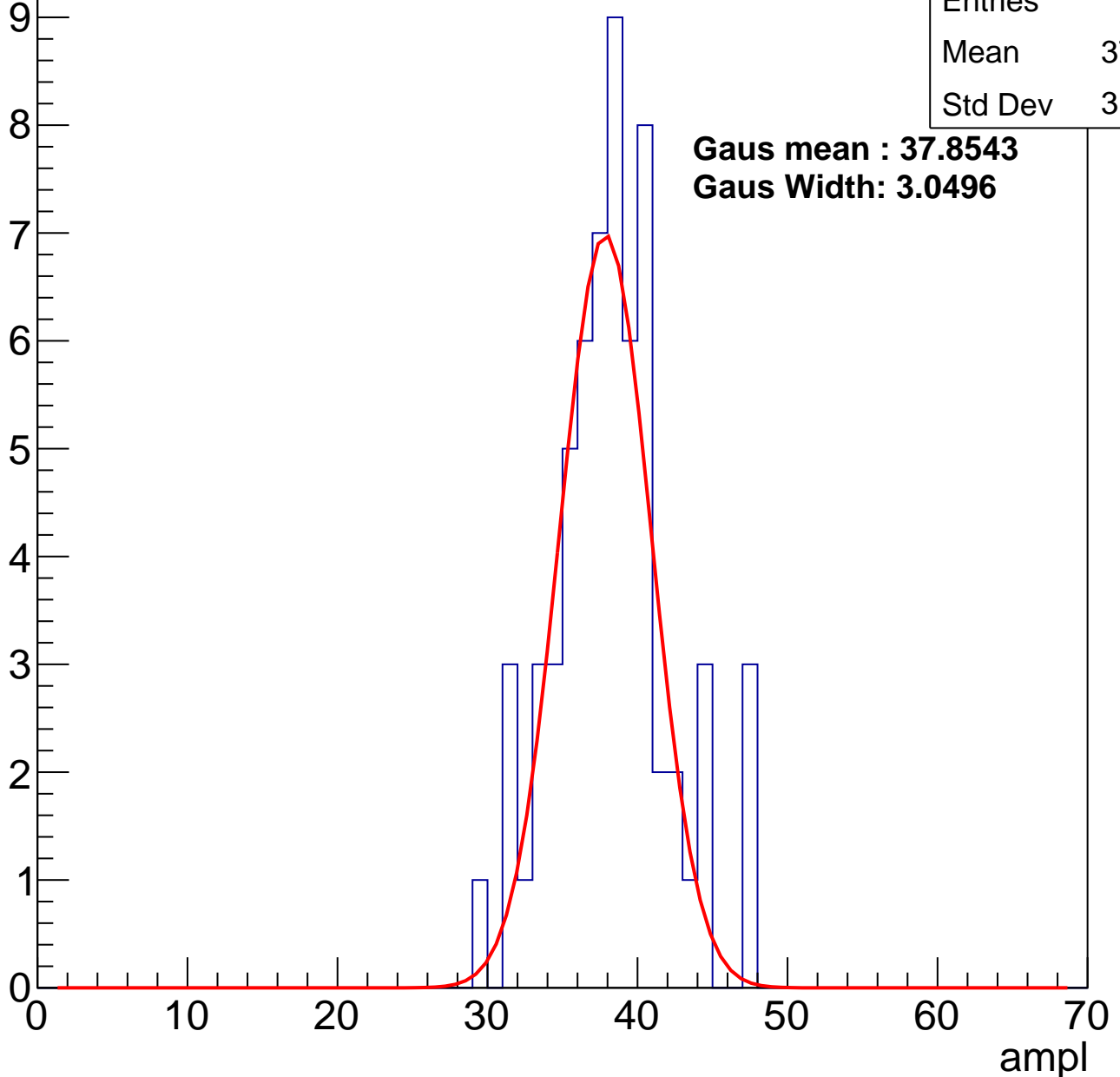
B1L100S, U7-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 37.83 |
| Std Dev | 3.832 |

Gaus mean : 37.8543
Gaus Width: 3.0496



B1L100S, U7-ch5, adc2

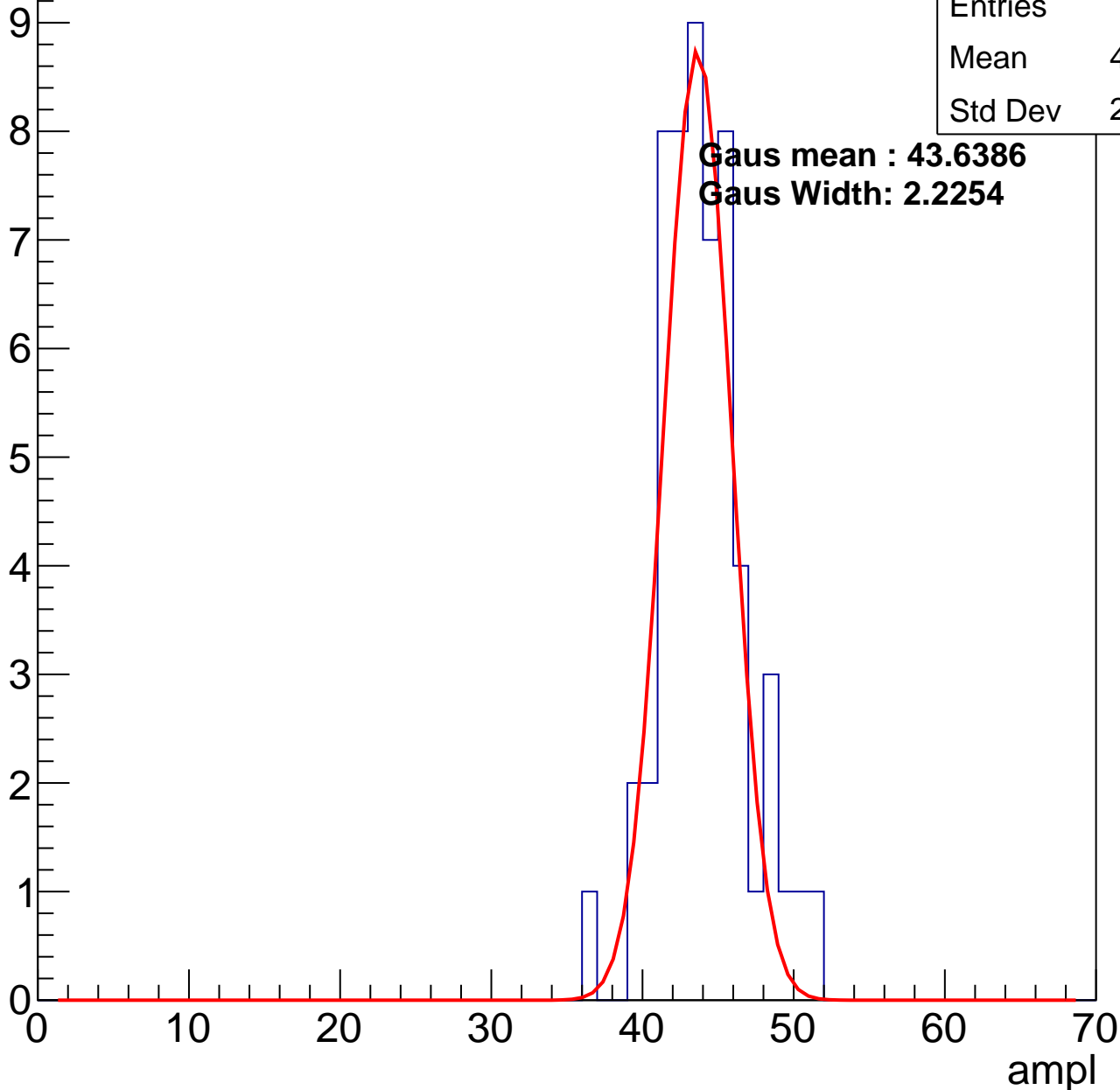
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43.54 |
| Std Dev | 2.796 |

Gaus mean : 43.6386

Gaus Width: 2.2254

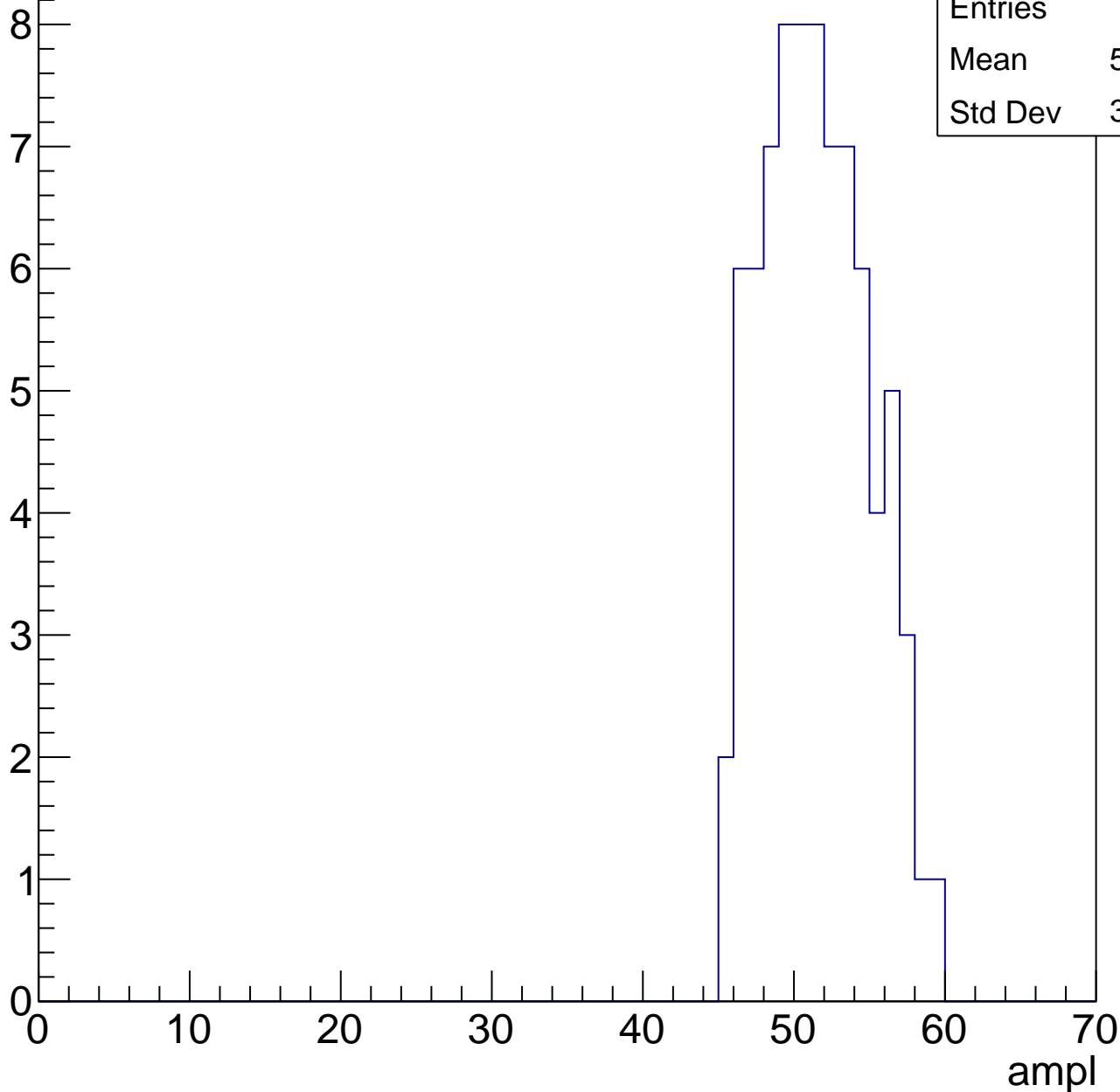


B1L100S, U7-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

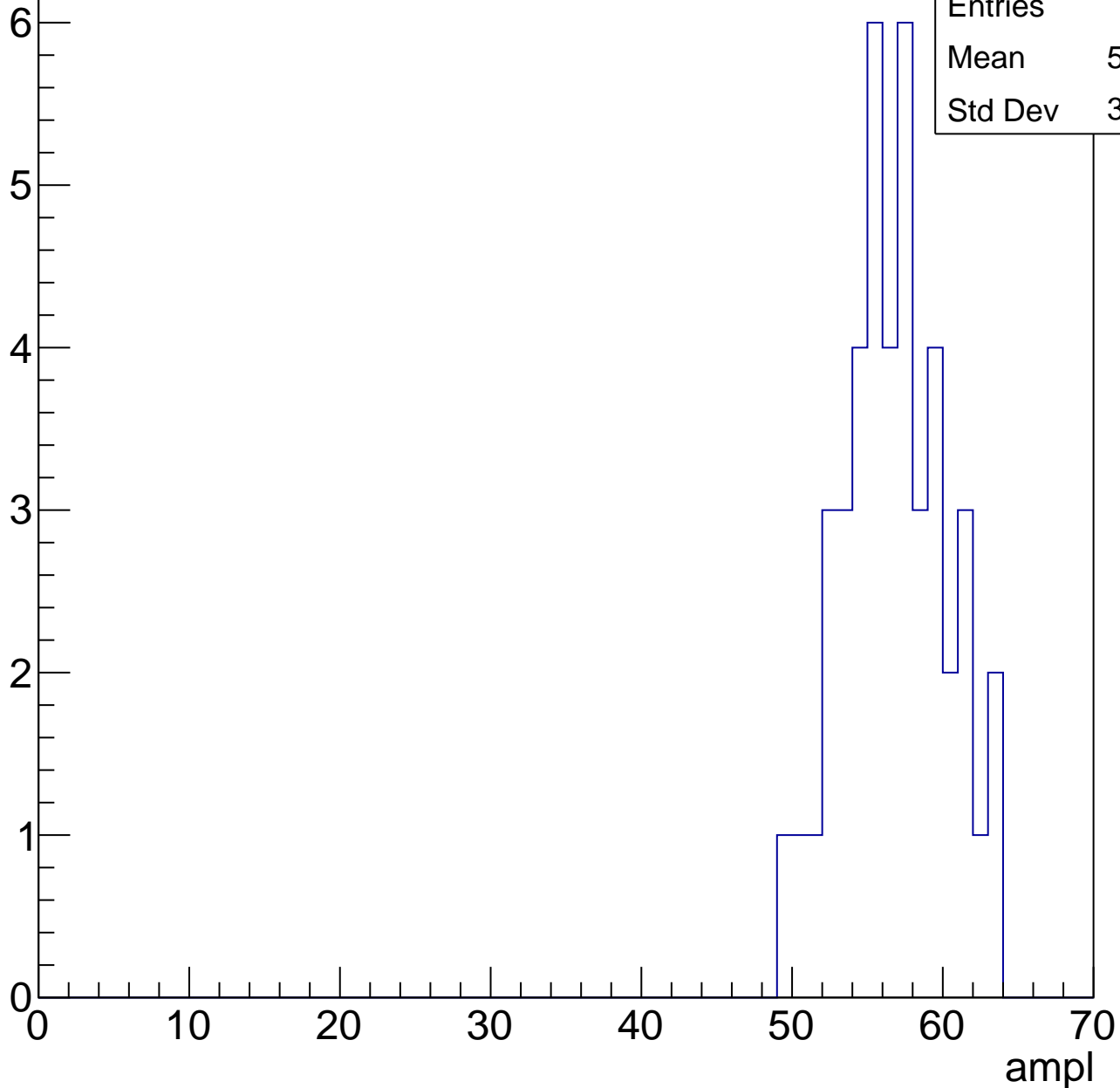
| | |
|---------|-------|
| Entries | 79 |
| Mean | 51.03 |
| Std Dev | 3.412 |



B1L100S, U7-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

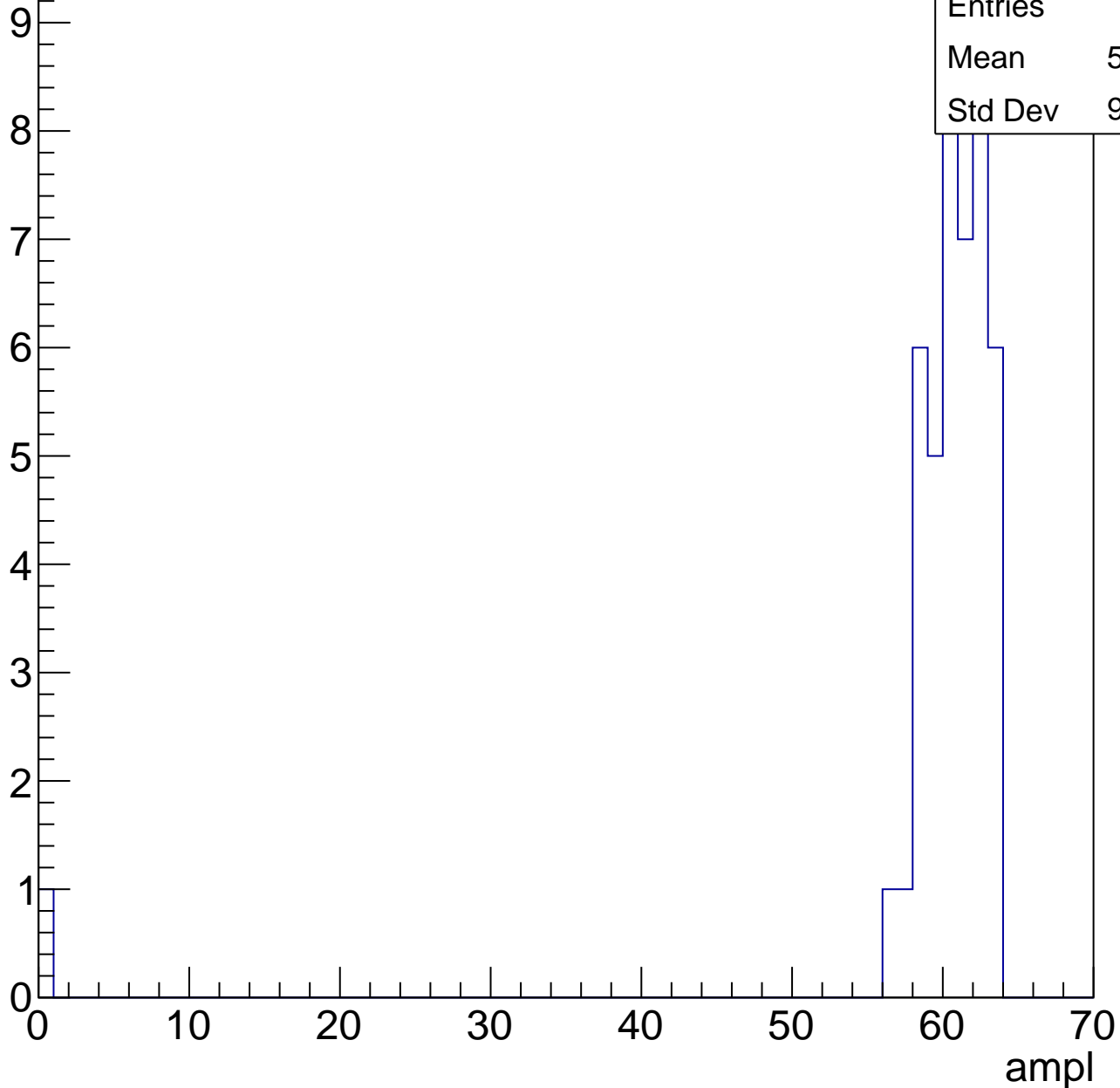


| | |
|---------|-------|
| Entries | 44 |
| Mean | 56.32 |
| Std Dev | 3.363 |

B1L100S, U7-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

0 10 20 30 40 50 60 70

ampl

B1L100S, U7-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch6, adc0

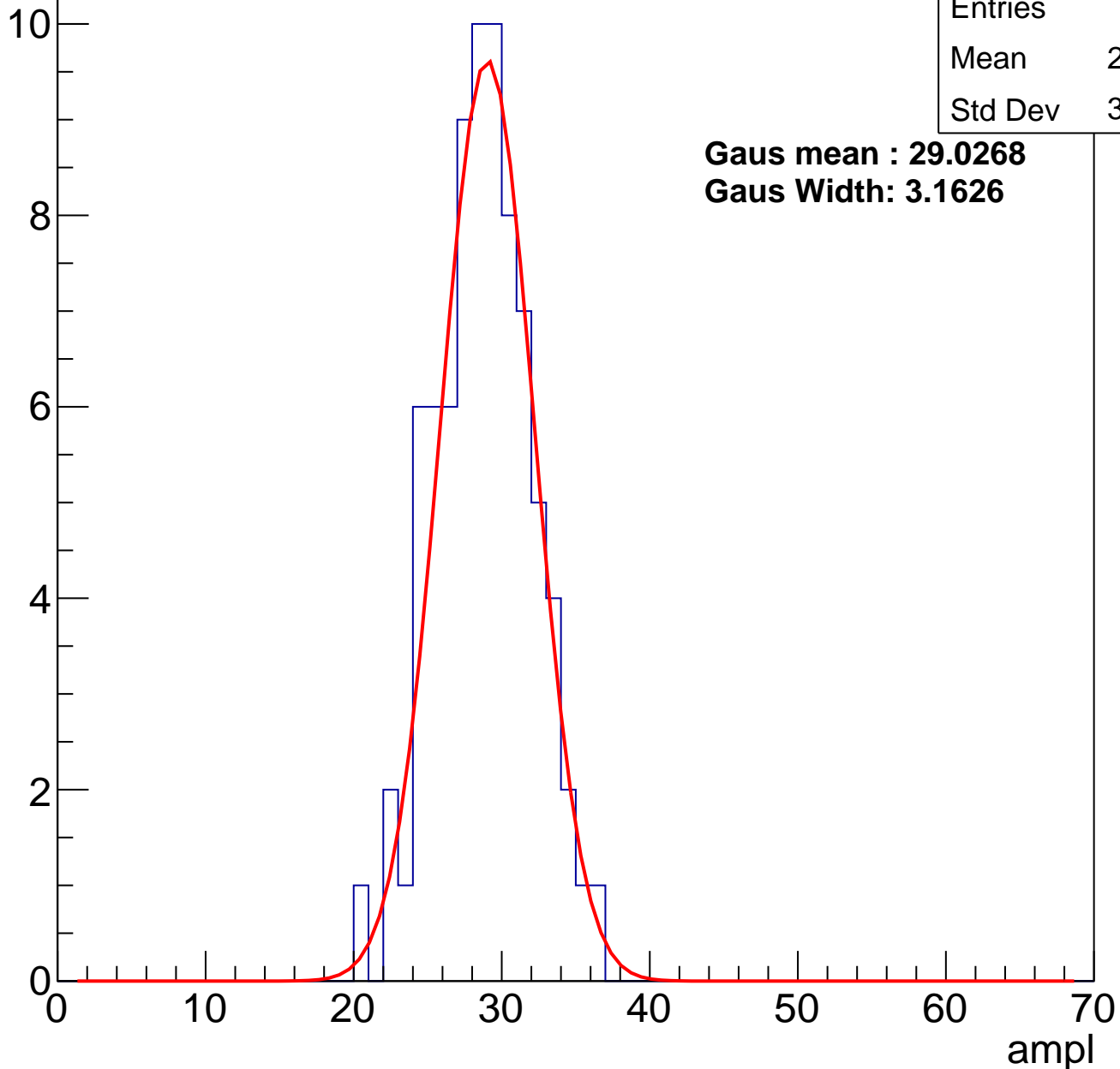
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.33 |
| Std Dev | 3.193 |

Gaus mean : 29.0268

Gaus Width: 3.1626

Entry



B1L100S, U7-ch6, adc1

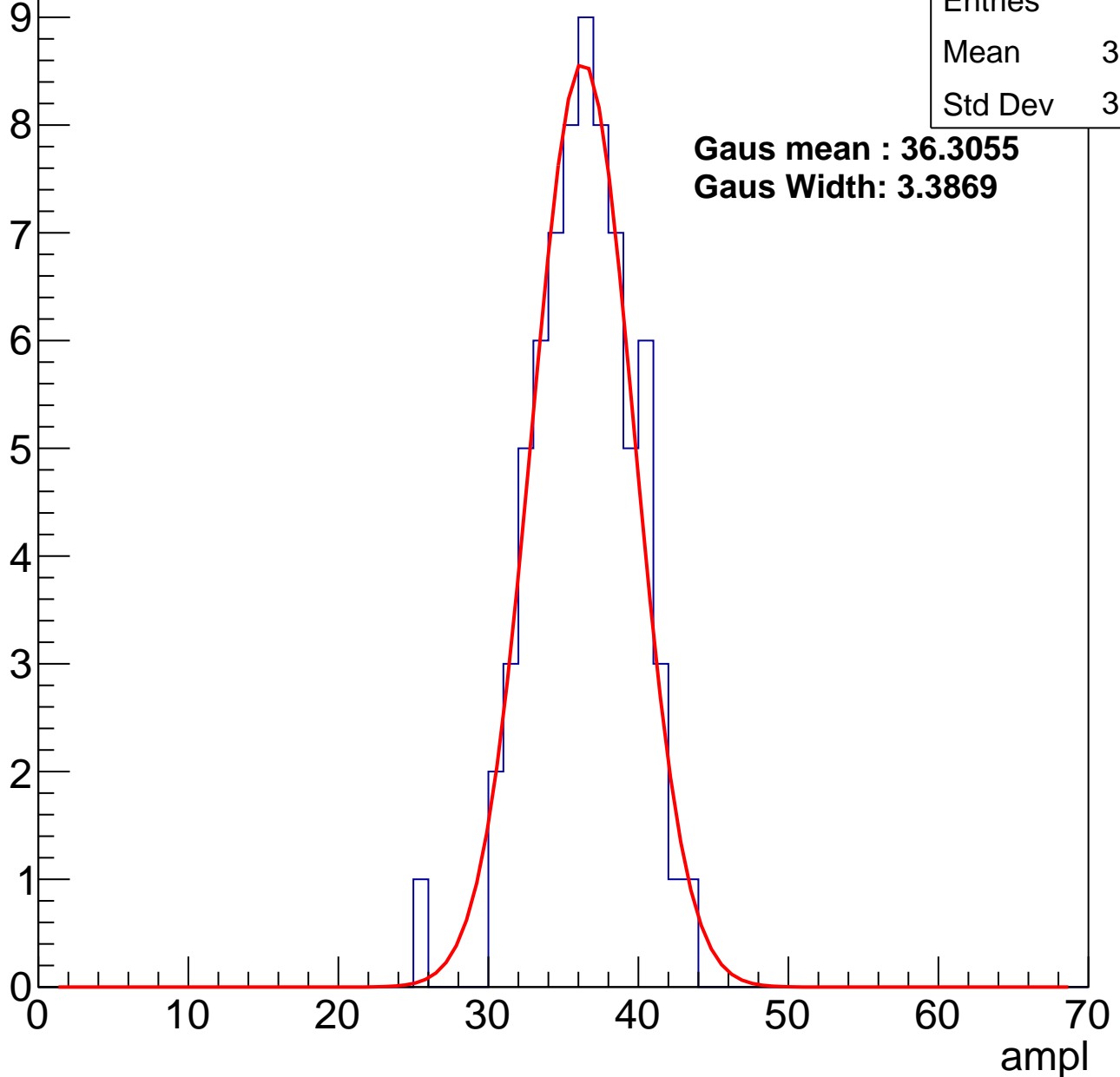
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.88 |
| Std Dev | 3.278 |

Gaus mean : 36.3055

Gaus Width: 3.3869



B1L100S, U7-ch6, adc2

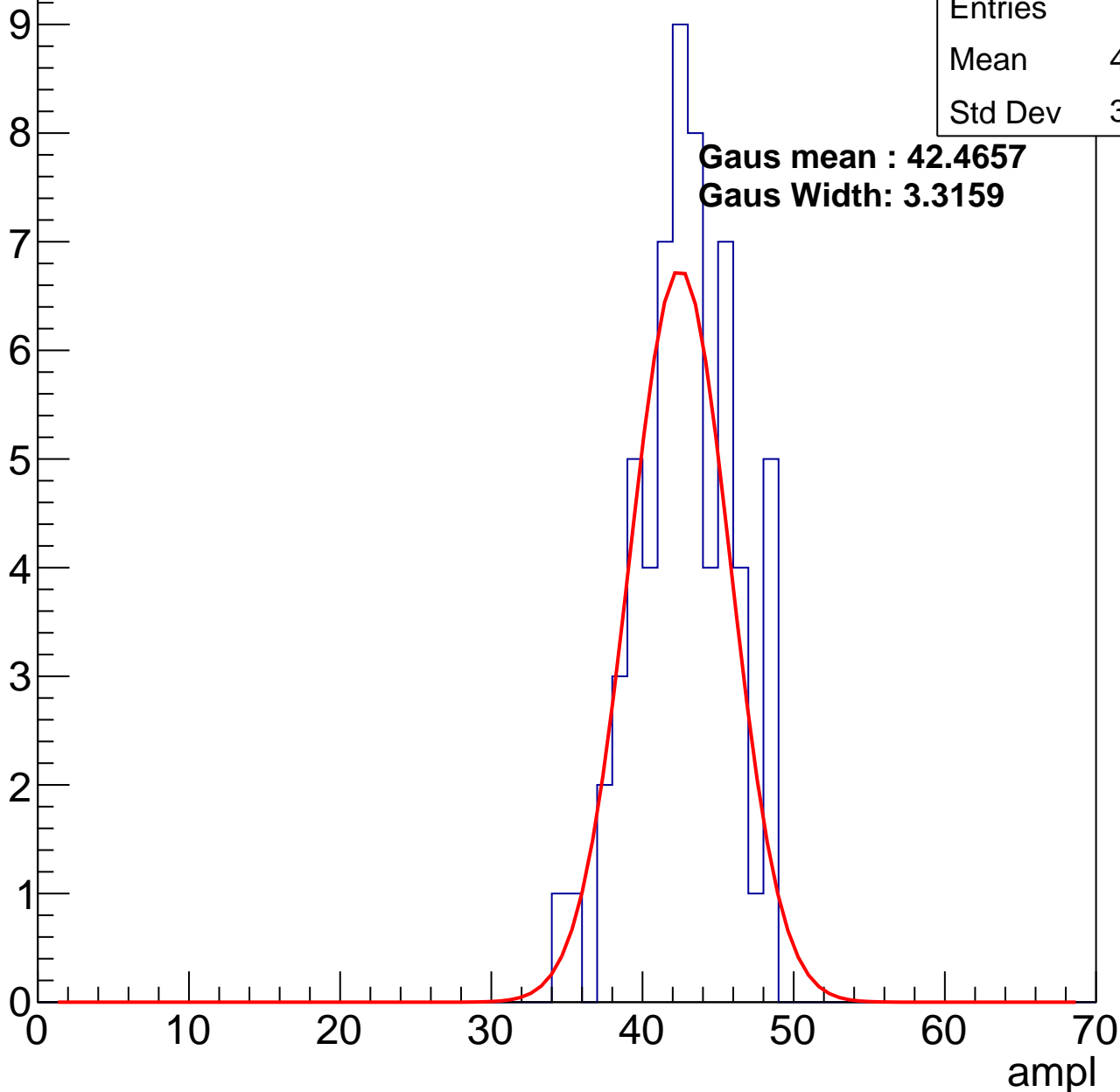
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.34 |
| Std Dev | 3.208 |

Gaus mean : 42.4657

Gaus Width: 3.3159

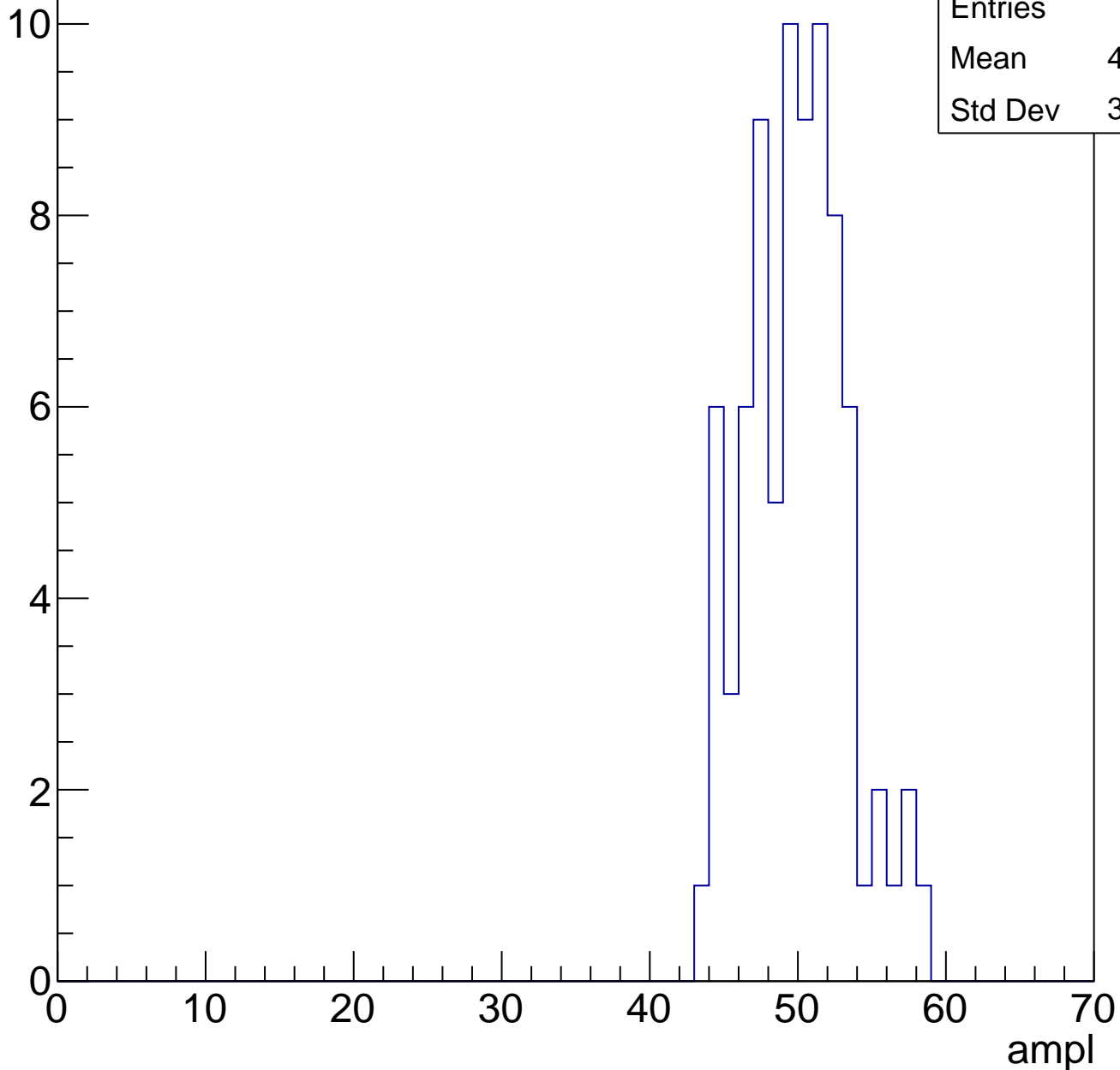


B1L100S, U7-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 49.46 |
| Std Dev | 3.324 |

Entry

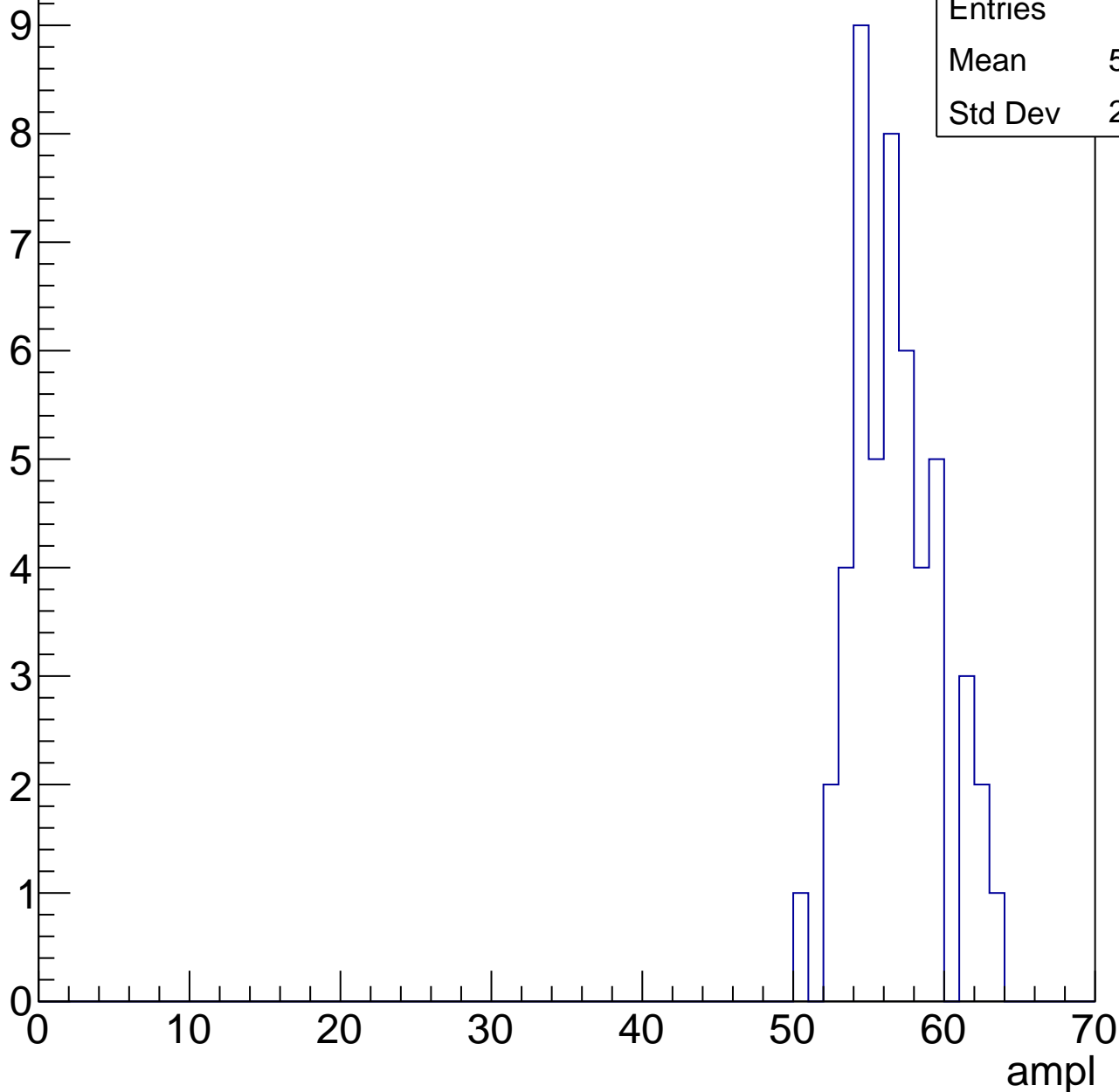


B1L100S, U7-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

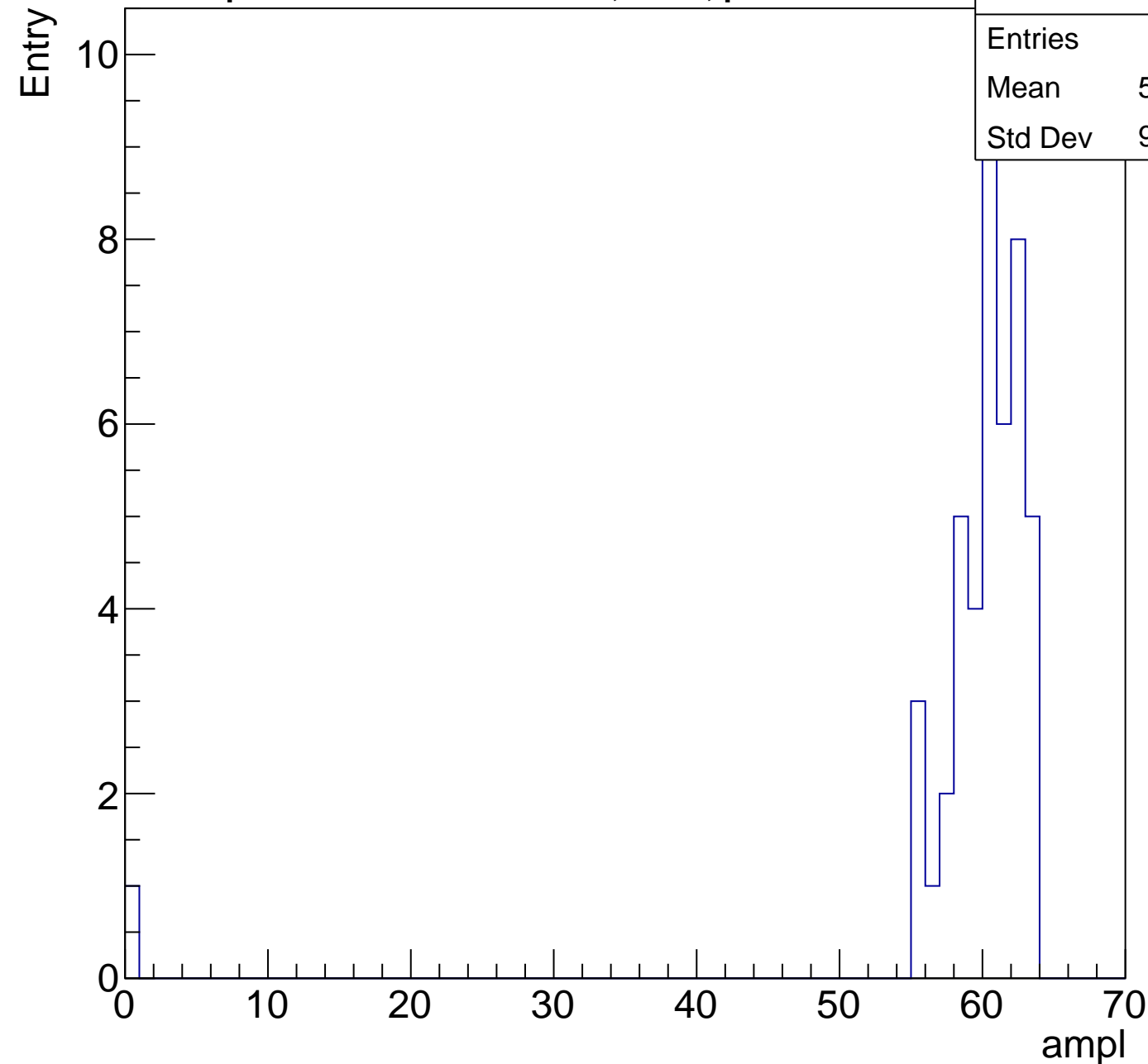
| | |
|---------|-------|
| Entries | 50 |
| Mean | 56.28 |
| Std Dev | 2.843 |



B1L100S, U7-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

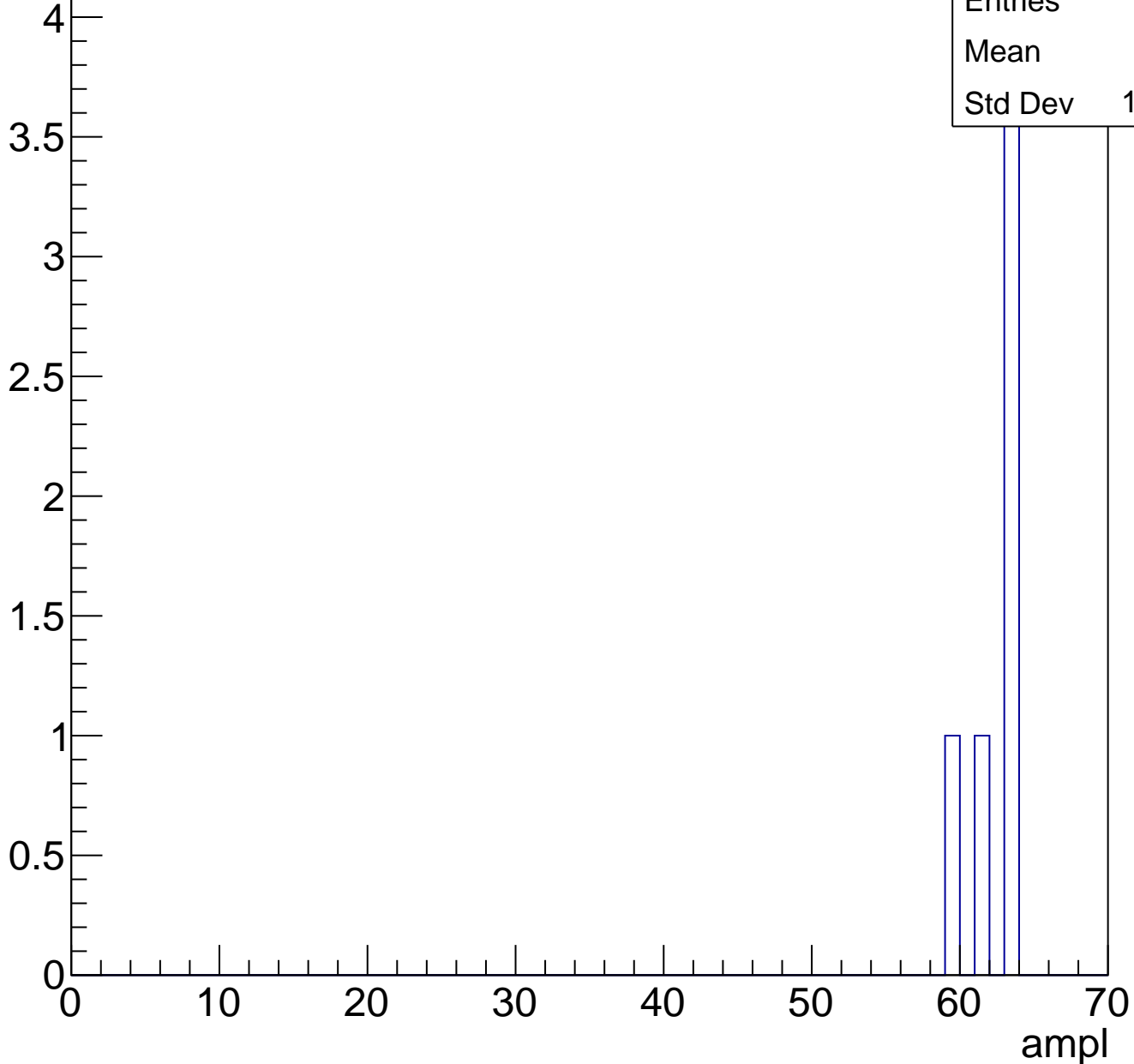
| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.62 |
| Std Dev | 9.105 |



B1L100S, U7-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch7, adc0

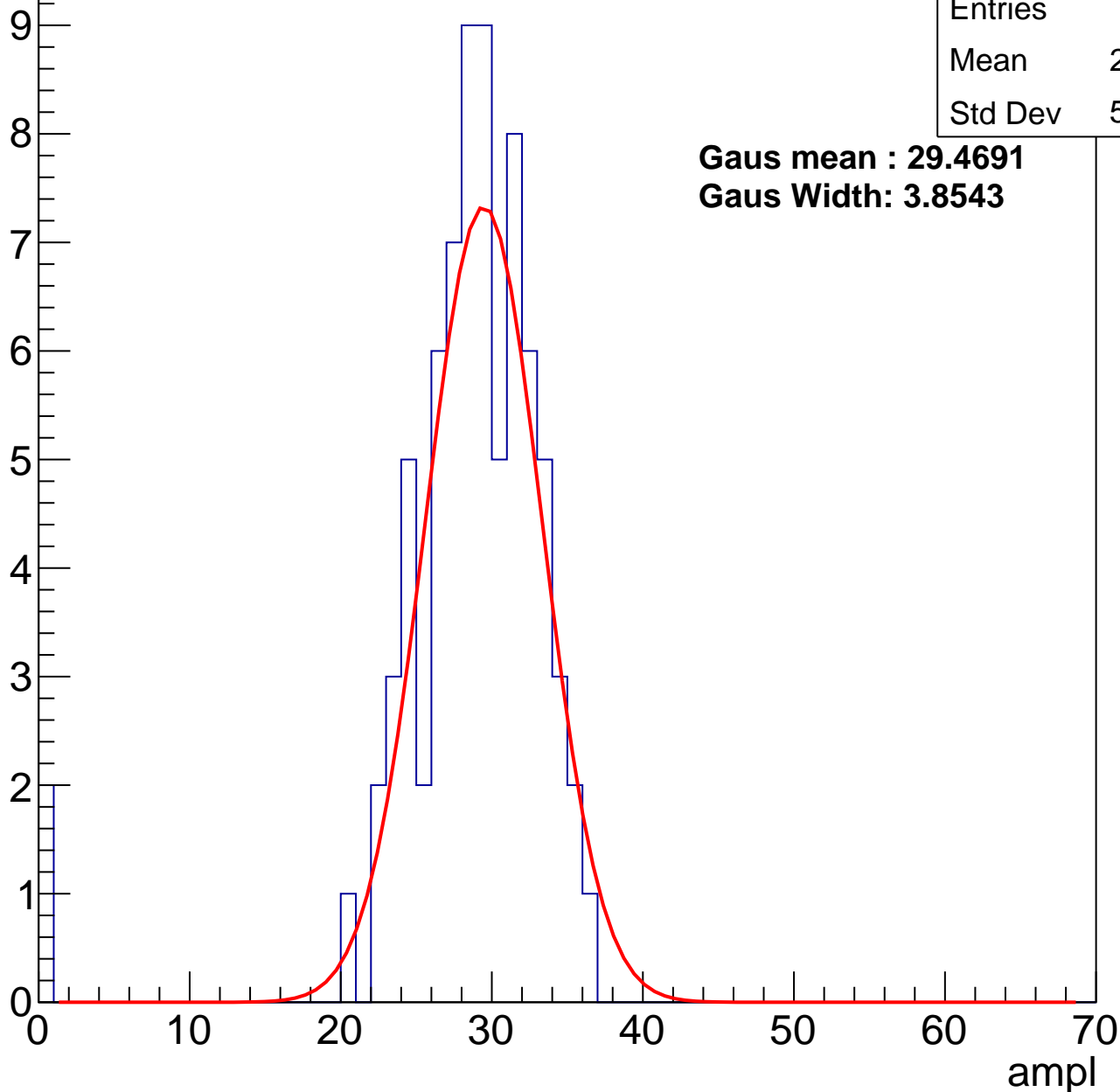
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 27.95 |
| Std Dev | 5.735 |

Gaus mean : 29.4691

Gaus Width: 3.8543



B1L100S, U7-ch7, adc1

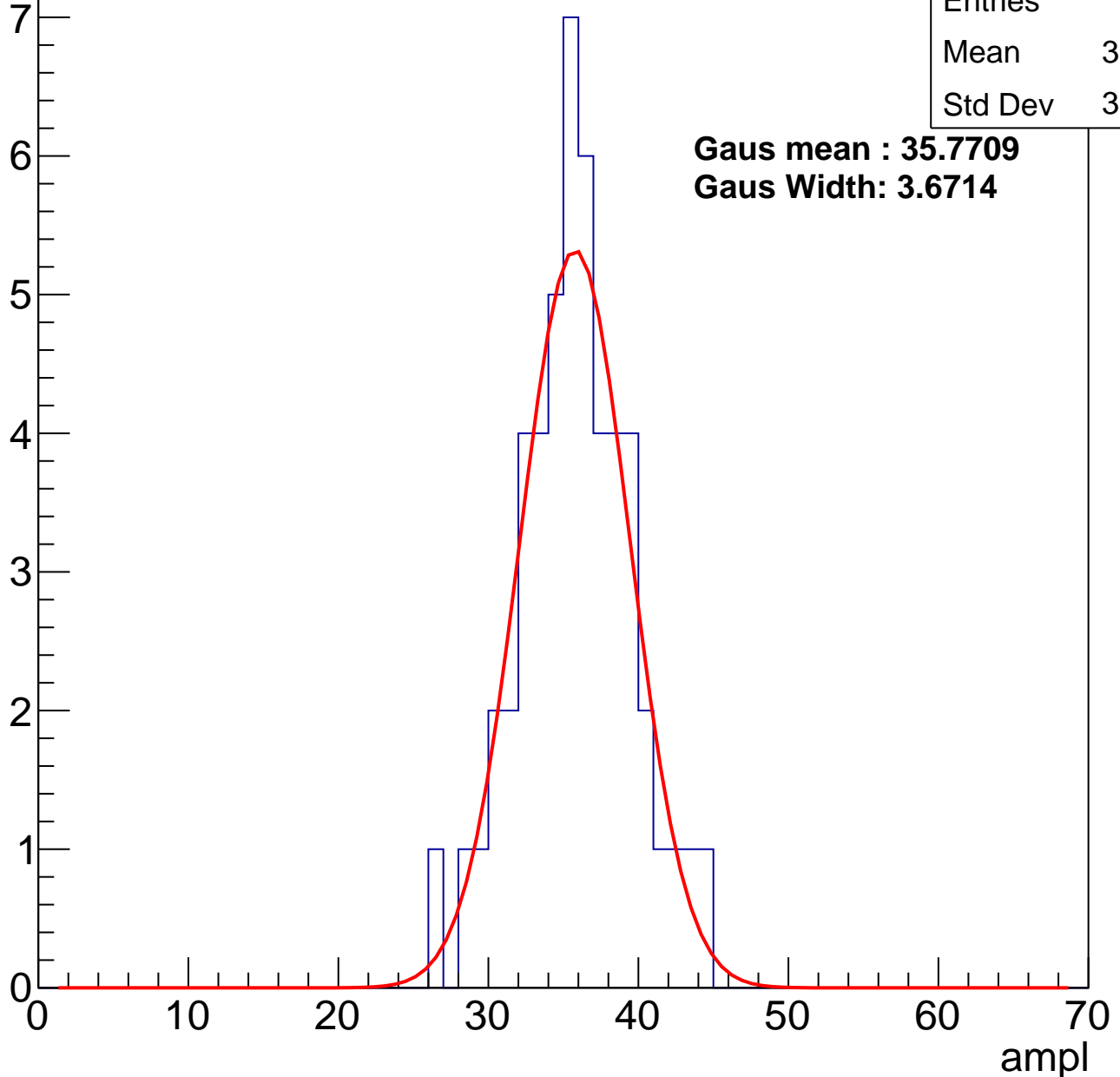
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 35.33 |
| Std Dev | 3.708 |

Gaus mean : 35.7709

Gaus Width: 3.6714



B1L100S, U7-ch7, adc2

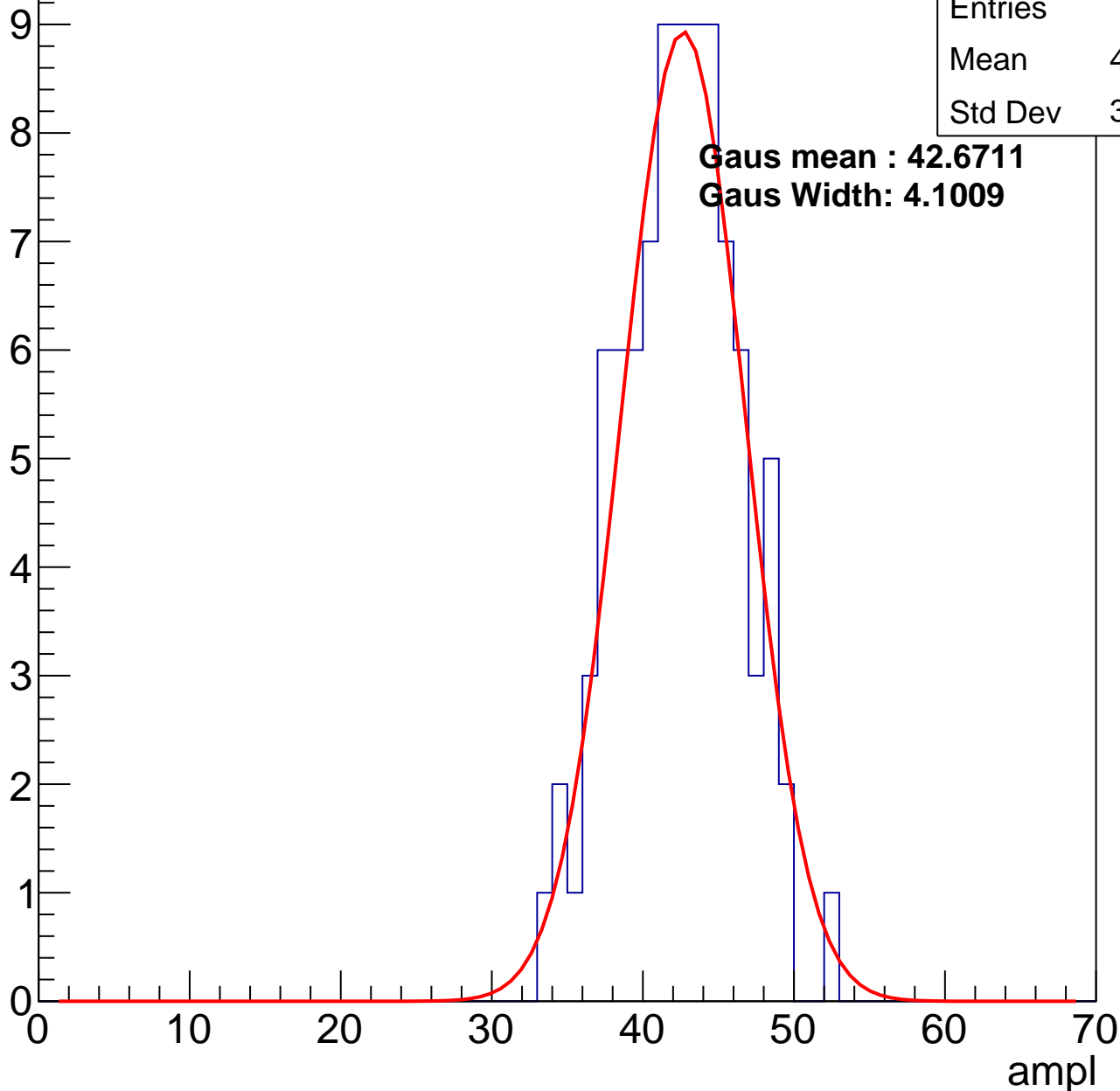
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 92 |
| Mean | 41.96 |
| Std Dev | 3.845 |

Gaus mean : 42.6711

Gaus Width: 4.1009

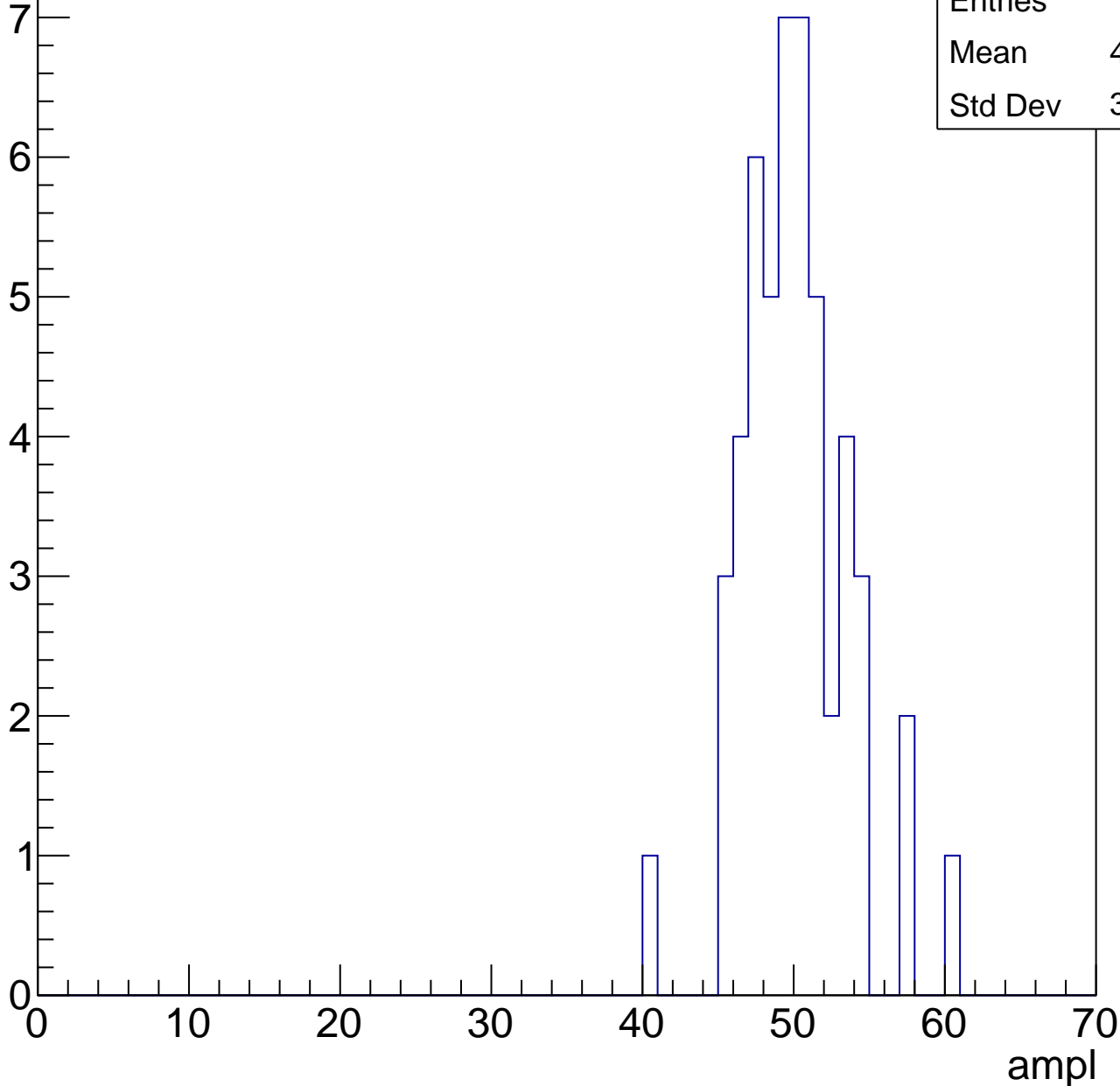


B1L100S, U7-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

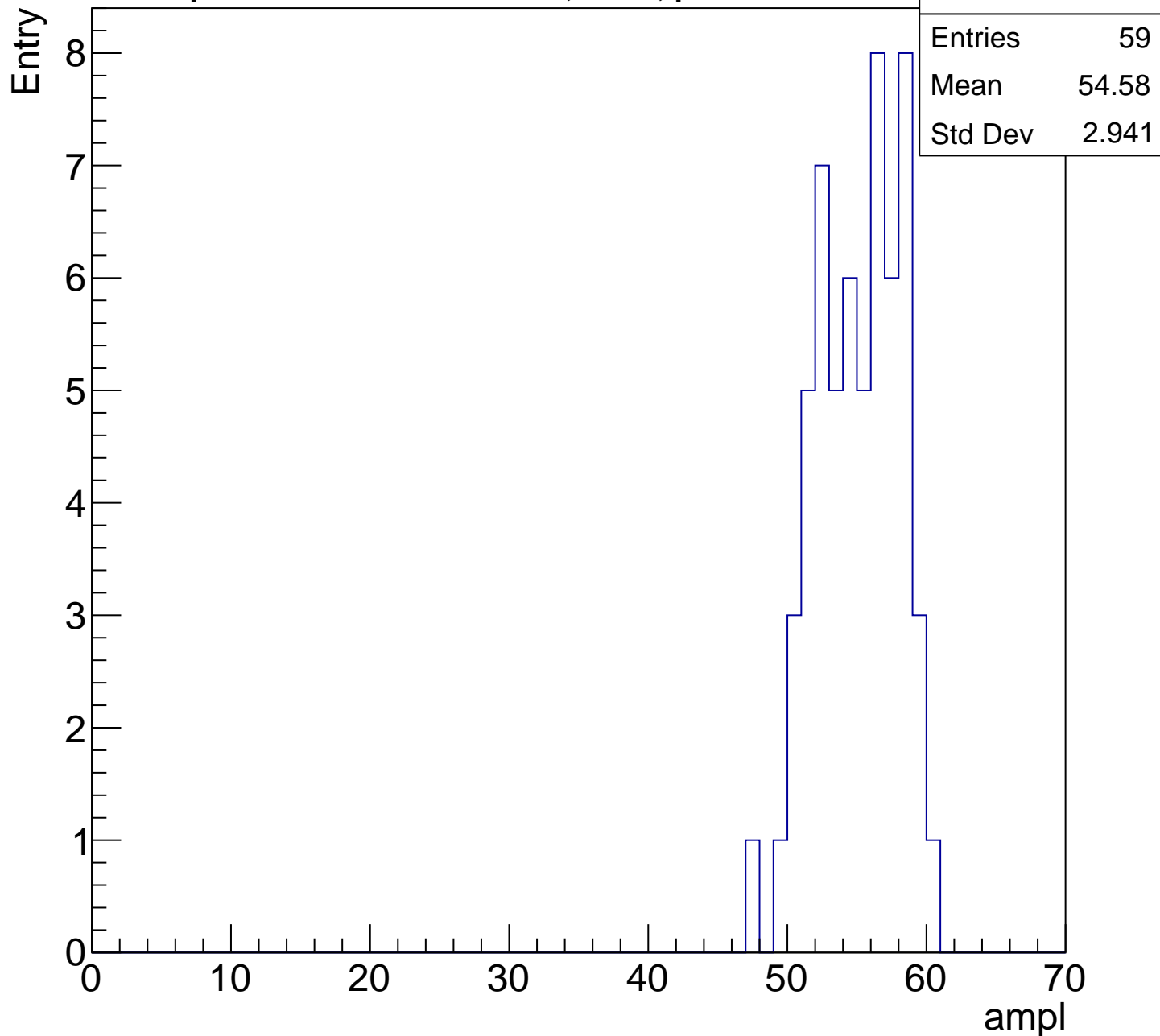
Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 49.62 |
| Std Dev | 3.486 |



B1L100S, U7-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.61 |
| Std Dev | 8.477 |

Entry

10

8

6

4

2

0

0

10

20

30

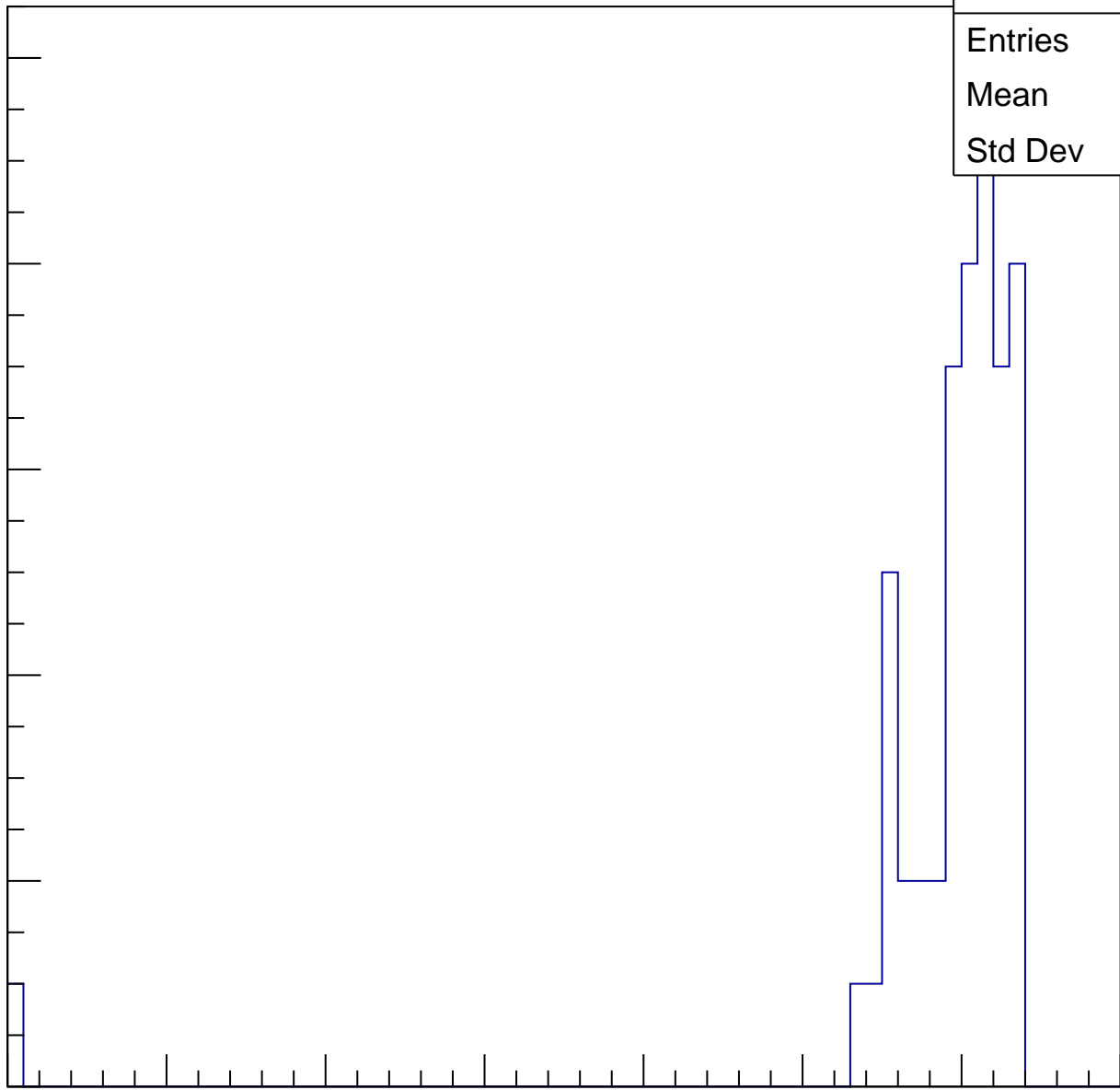
40

50

60

70

ampl



B1L100S, U7-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

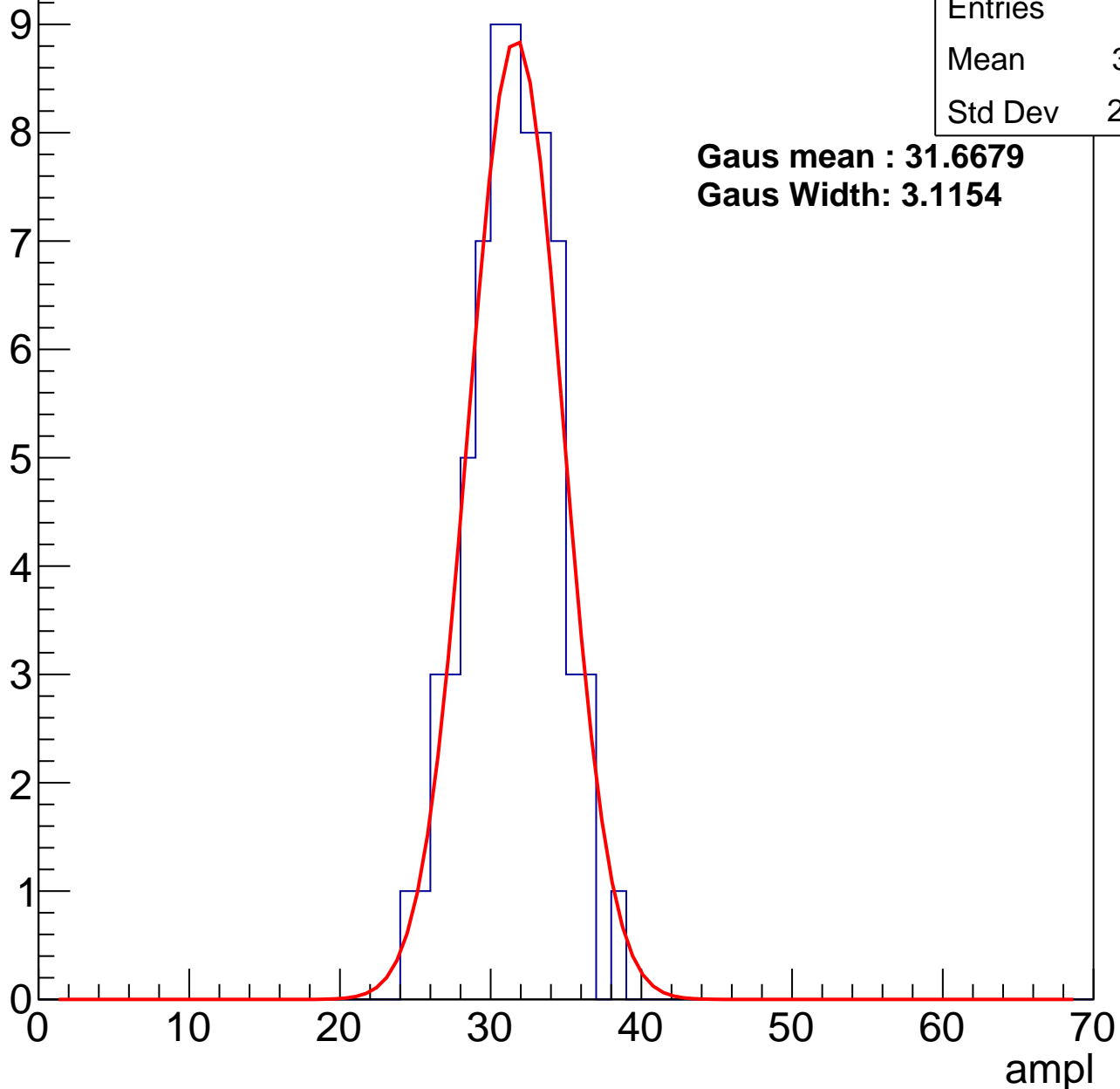
Entry



B1L100S, U7-ch8, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

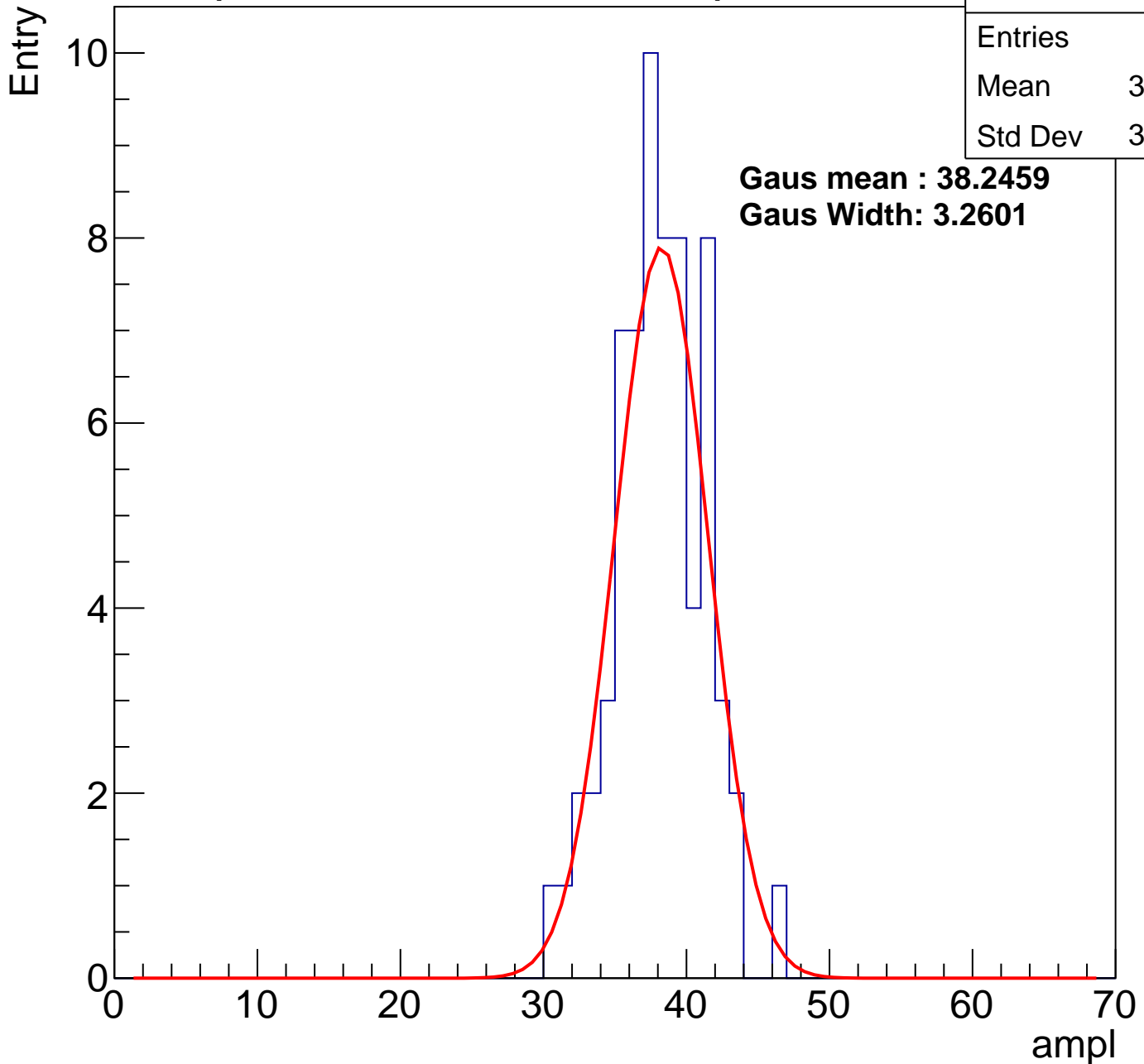


B1L100S, U7-ch8, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.64 |
| Std Dev | 3.075 |

Gaus mean : 38.2459
Gaus Width: 3.2601



B1L100S, U7-ch8, adc2

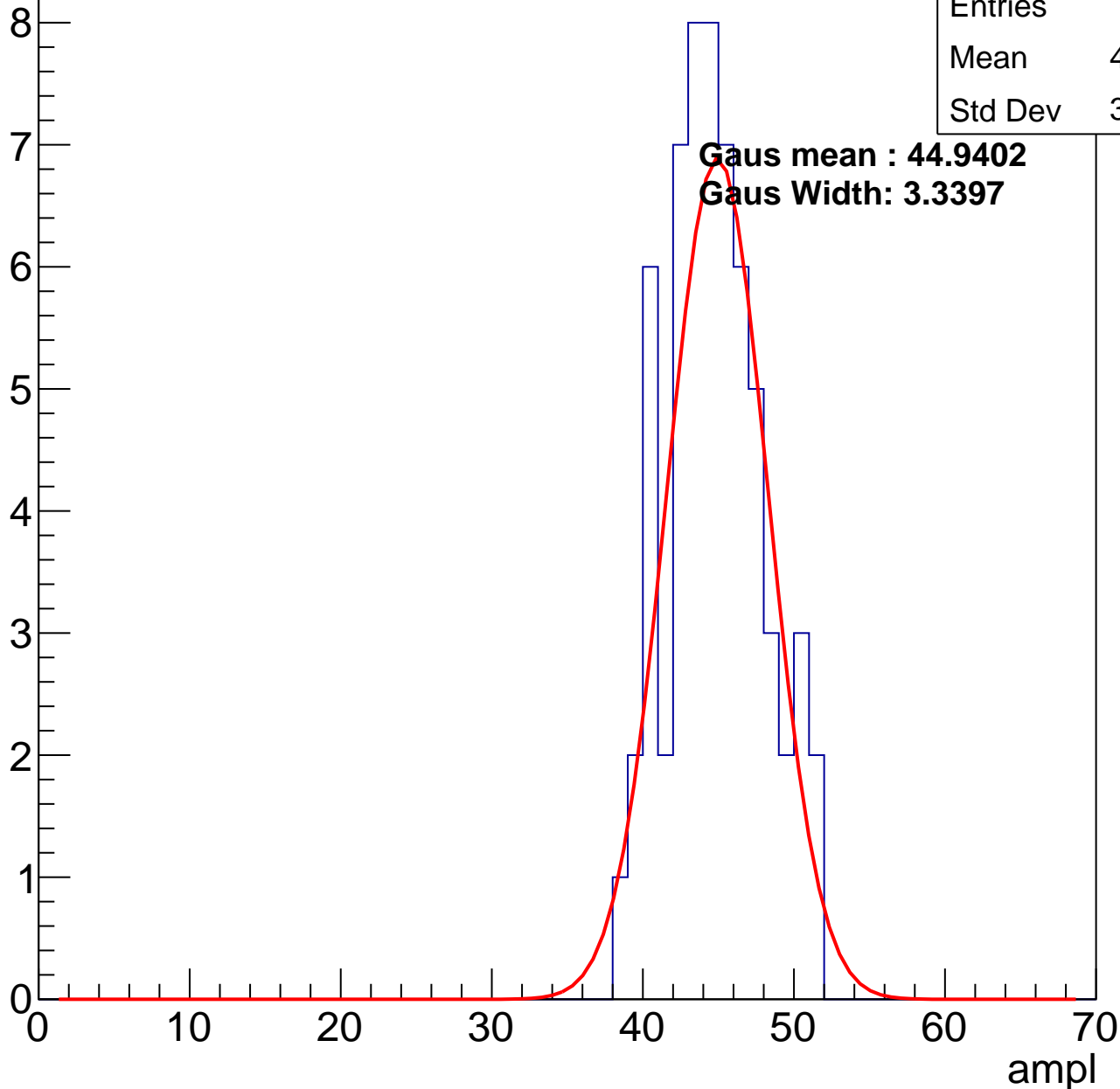
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 44.32 |
| Std Dev | 3.136 |

Gaus mean : 44.9402

Gaus Width: 3.3397

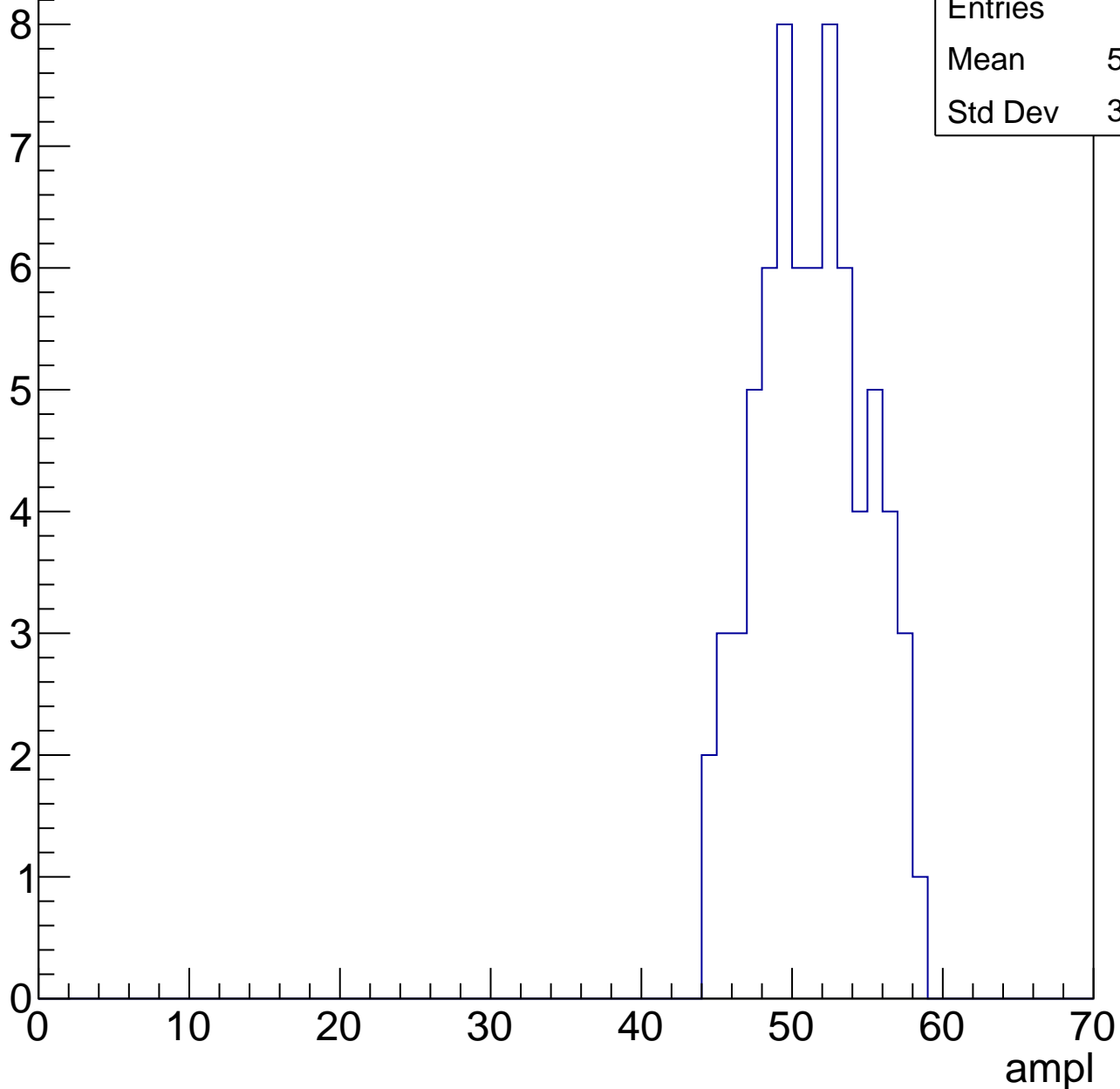


B1L100S, U7-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 50.86 |
| Std Dev | 3.498 |

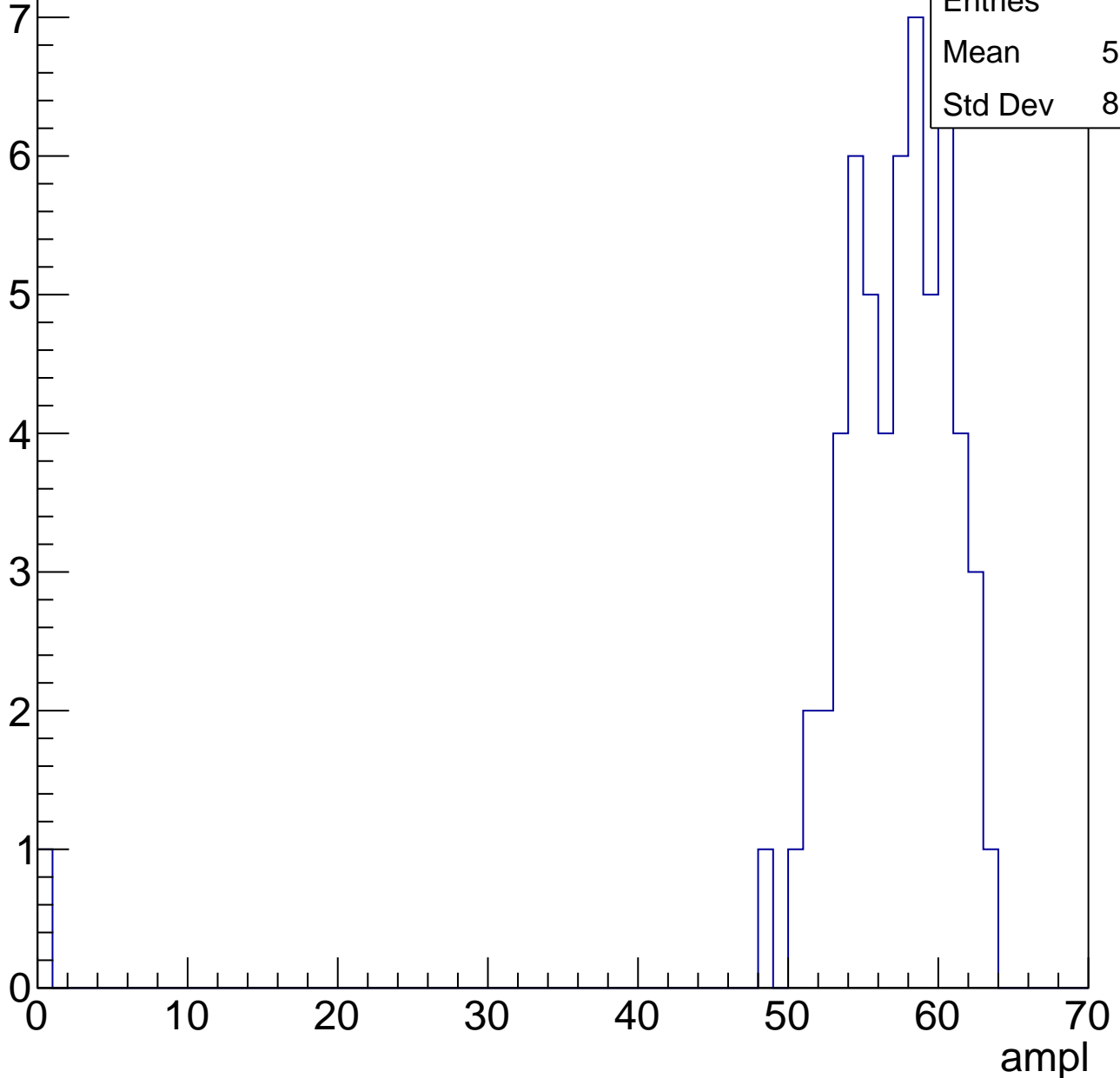


B1L100S, U7-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 55.85 |
| Std Dev | 8.057 |



B1L100S, U7-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

32

Mean

60.56

Std Dev

2.03

ampl

0

10

20

30

40

50

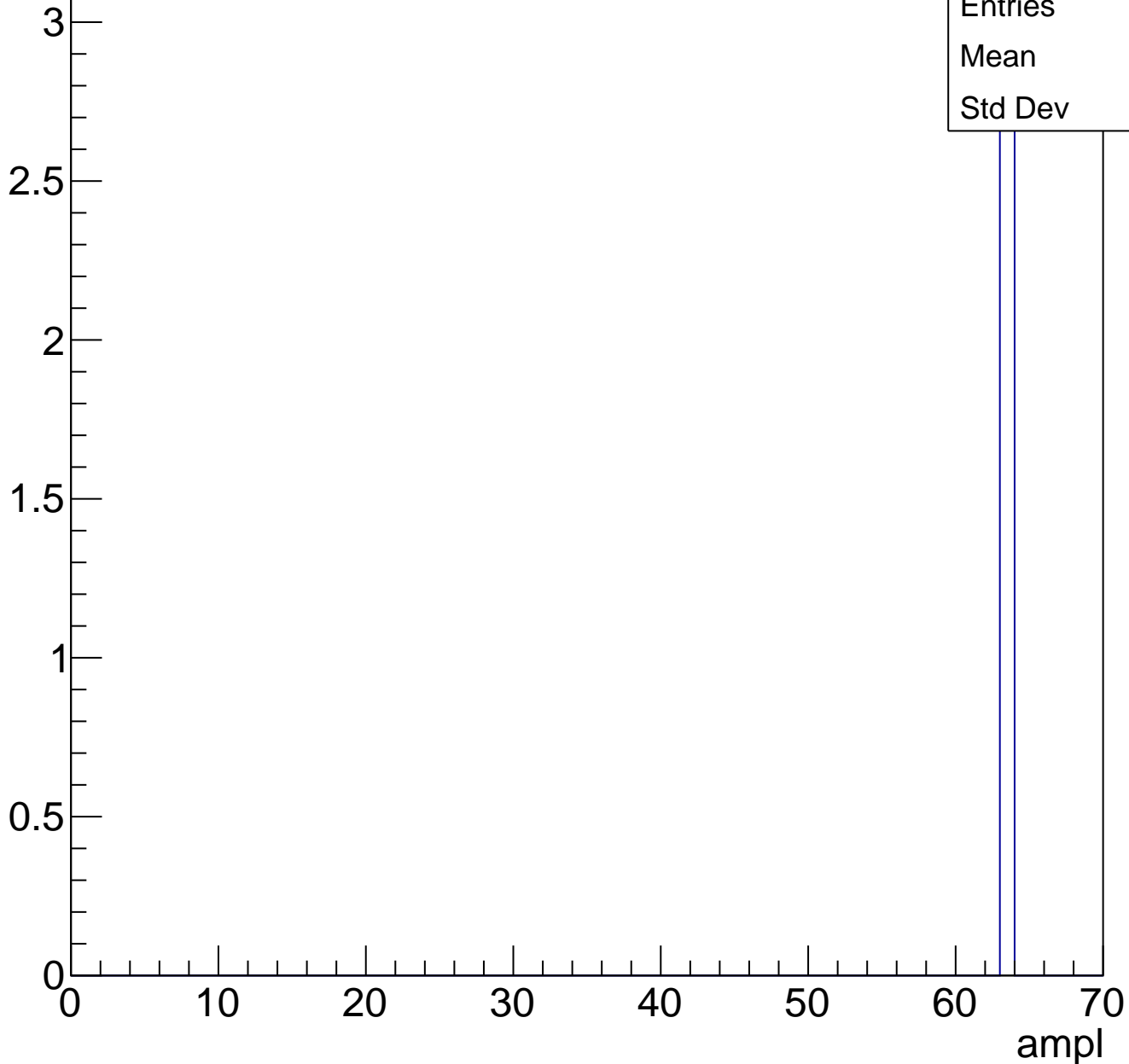
60

70

B1L100S, U7-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch9, adc0

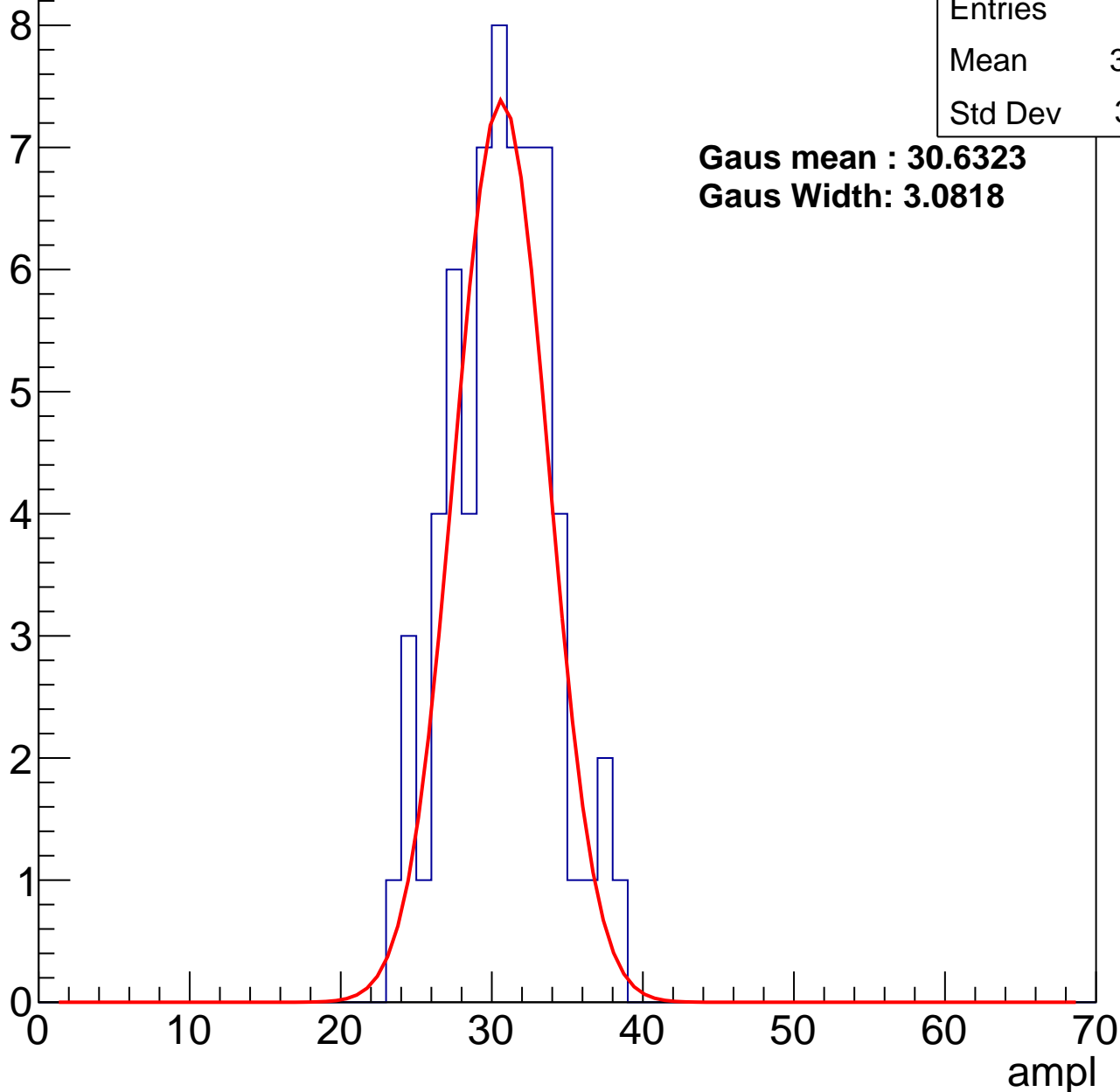
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.19 |
| Std Dev | 3.321 |

Gaus mean : 30.6323

Gaus Width: 3.0818



B1L100S, U7-ch9, adc1

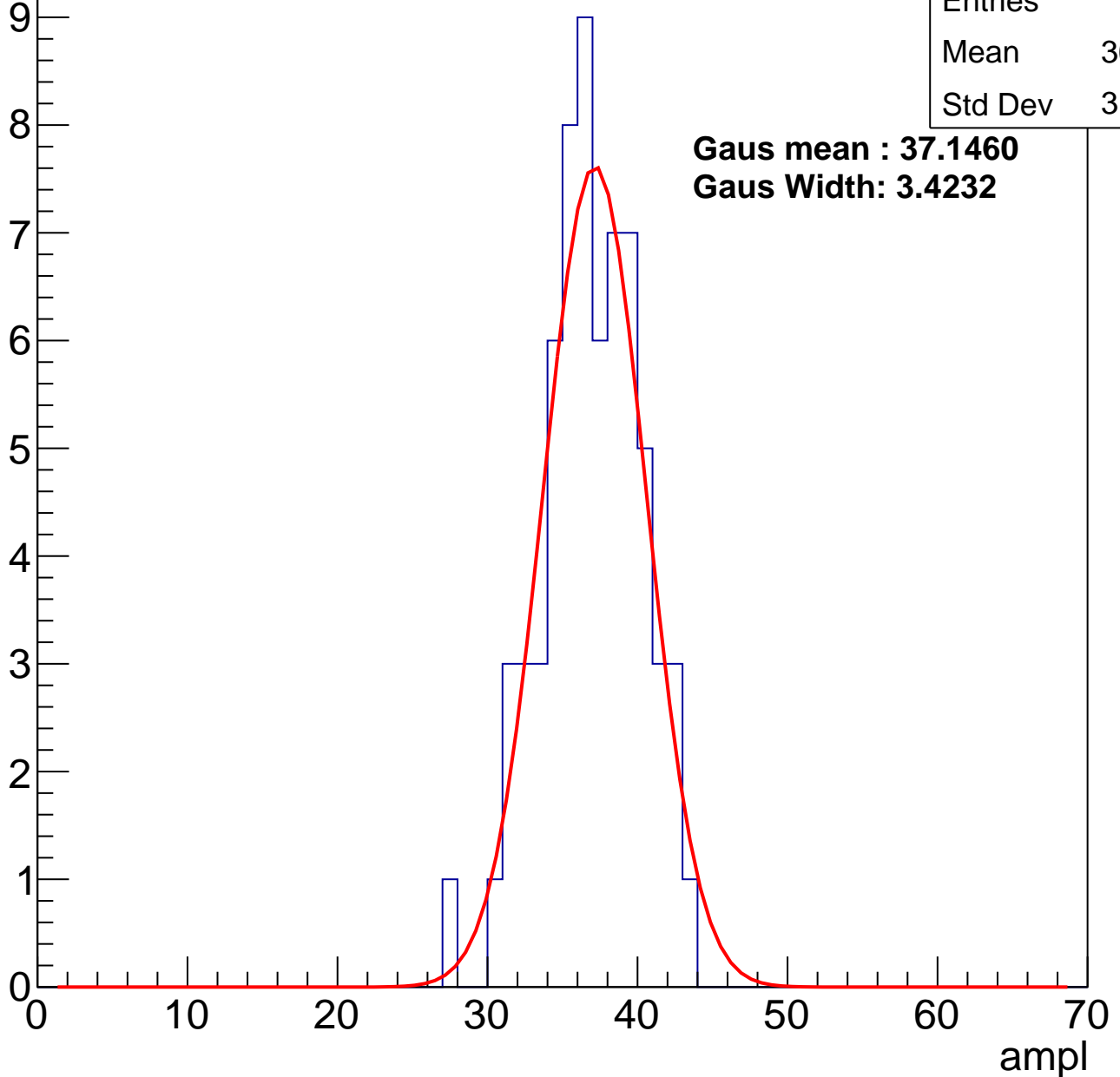
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.45 |
| Std Dev | 3.244 |

Gaus mean : 37.1460

Gaus Width: 3.4232



B1L100S, U7-ch9, adc2

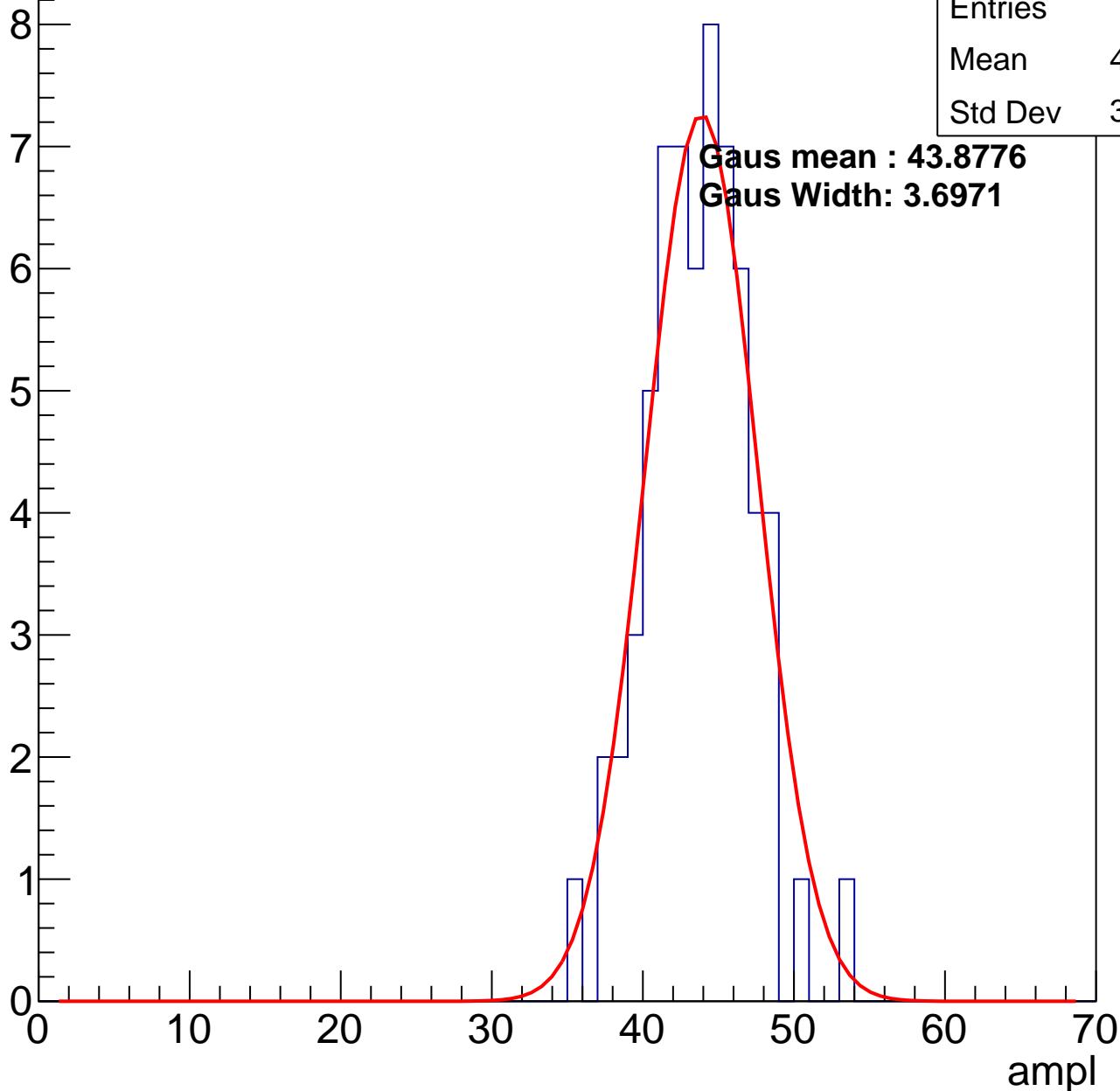
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.23 |
| Std Dev | 3.339 |

Gaus mean : 43.8776

Gaus Width: 3.6971



B1L100S, U7-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 74 |
| Mean | 49.84 |
| Std Dev | 3.373 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 55 |
| Mean | 56.31 |
| Std Dev | 2.55 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B1L100S, U7-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.33 |
| Std Dev | 9.436 |

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L100S, U7-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

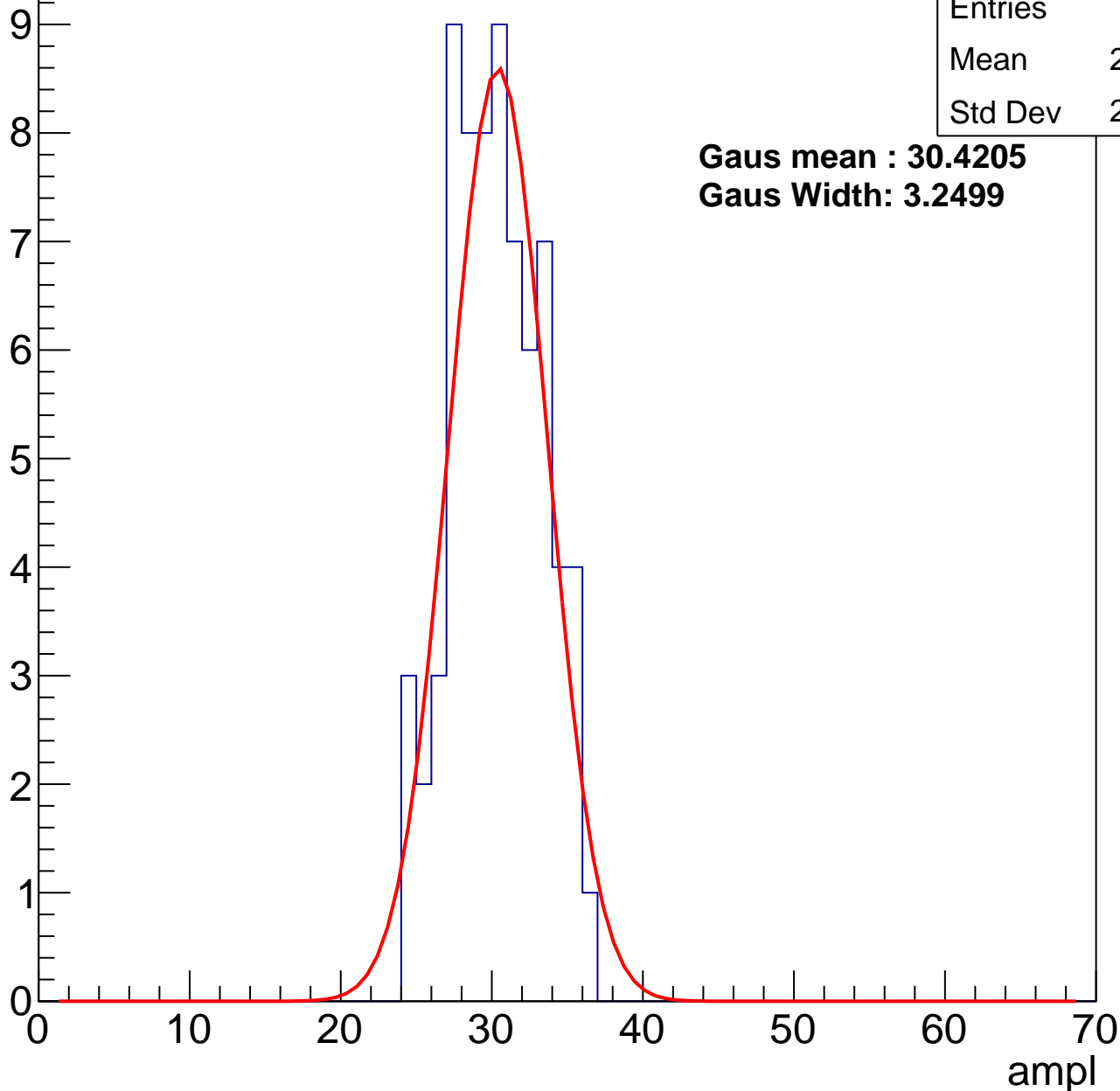
Entry



B1L100S, U7-ch10, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch10, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.58 |
| Std Dev | 3.454 |

Gaus mean : 36.5628

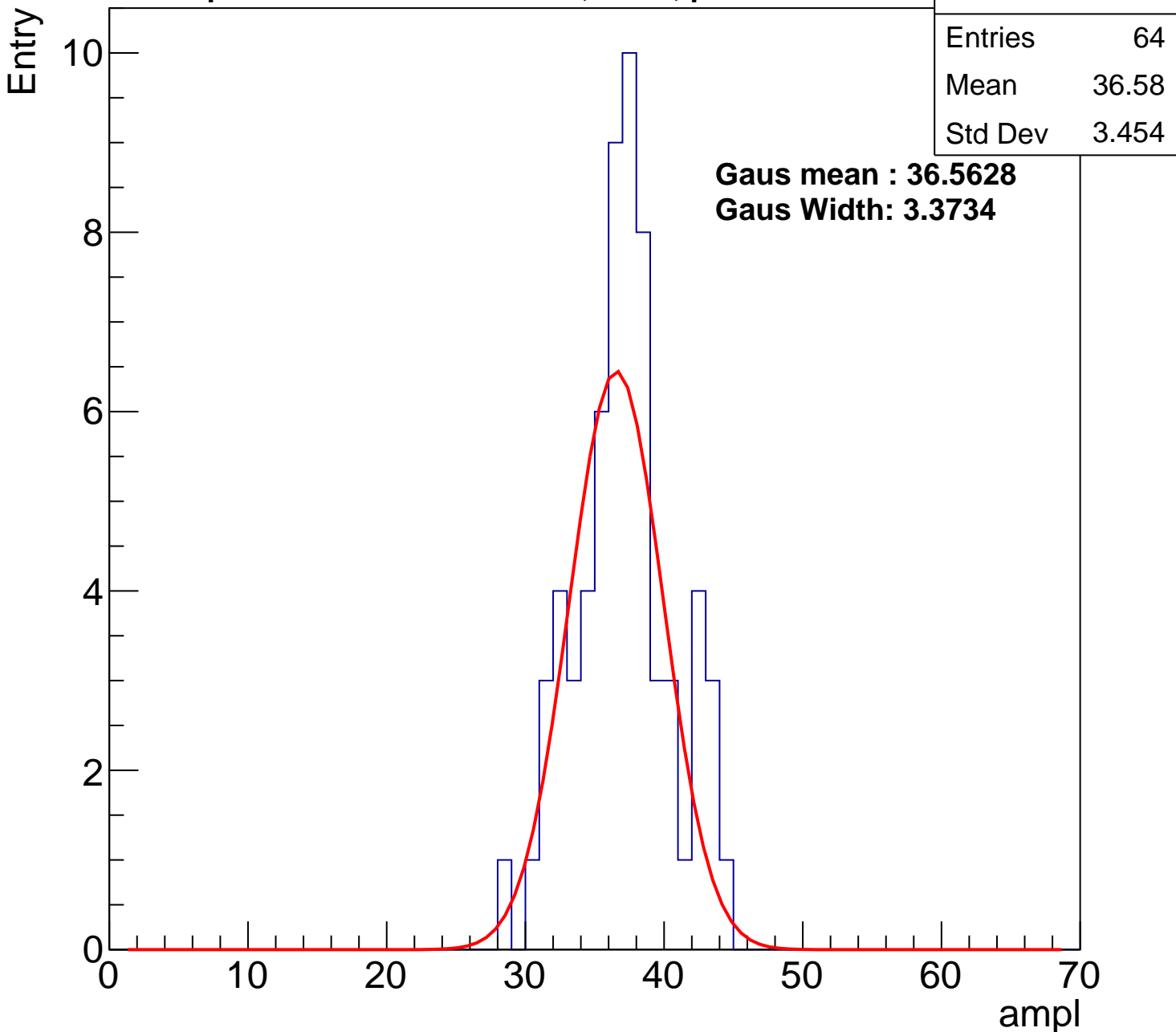
Gaus Width: 3.3734

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch10, adc2

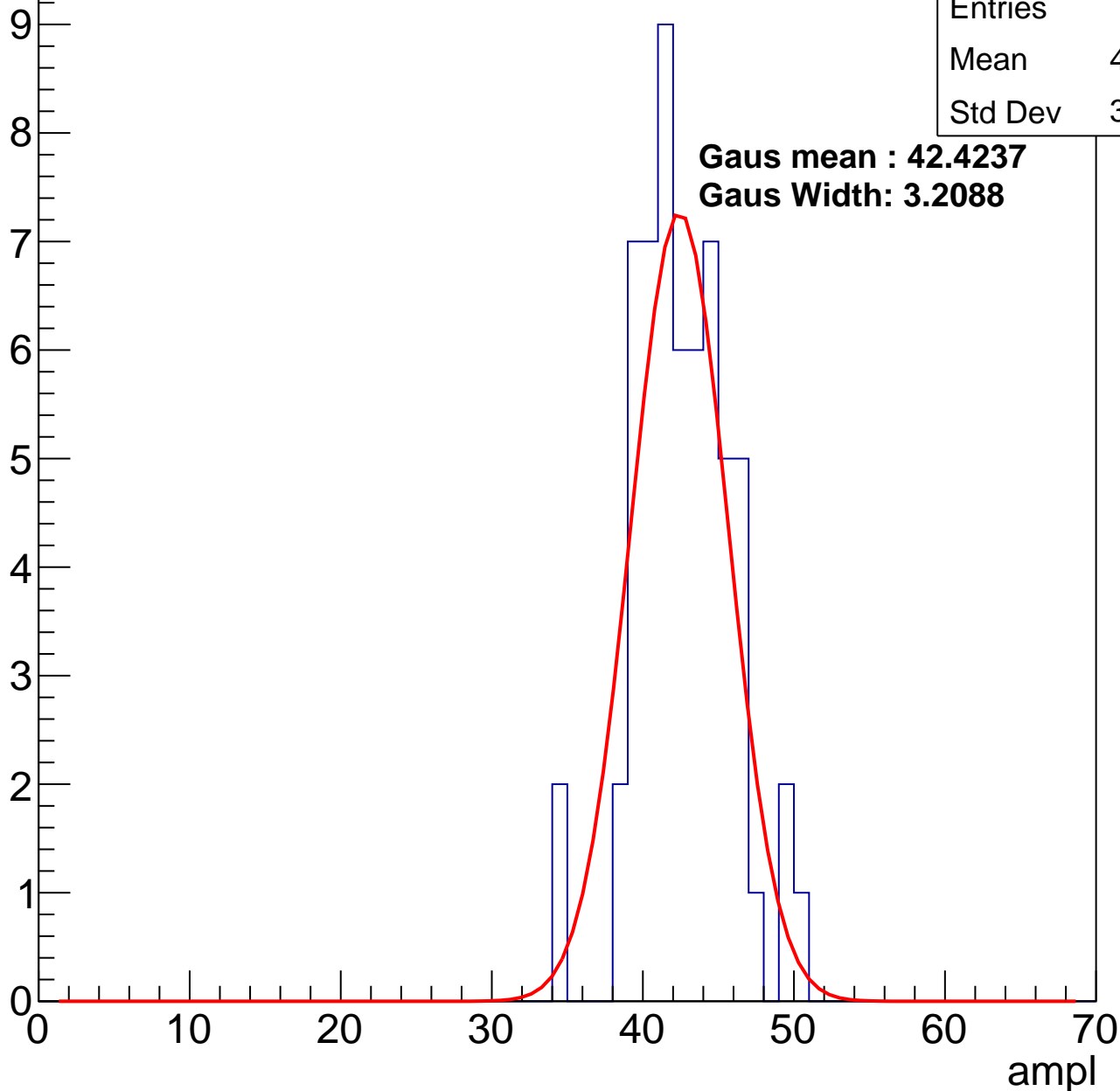
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.23 |
| Std Dev | 3.169 |

Gaus mean : 42.4237

Gaus Width: 3.2088

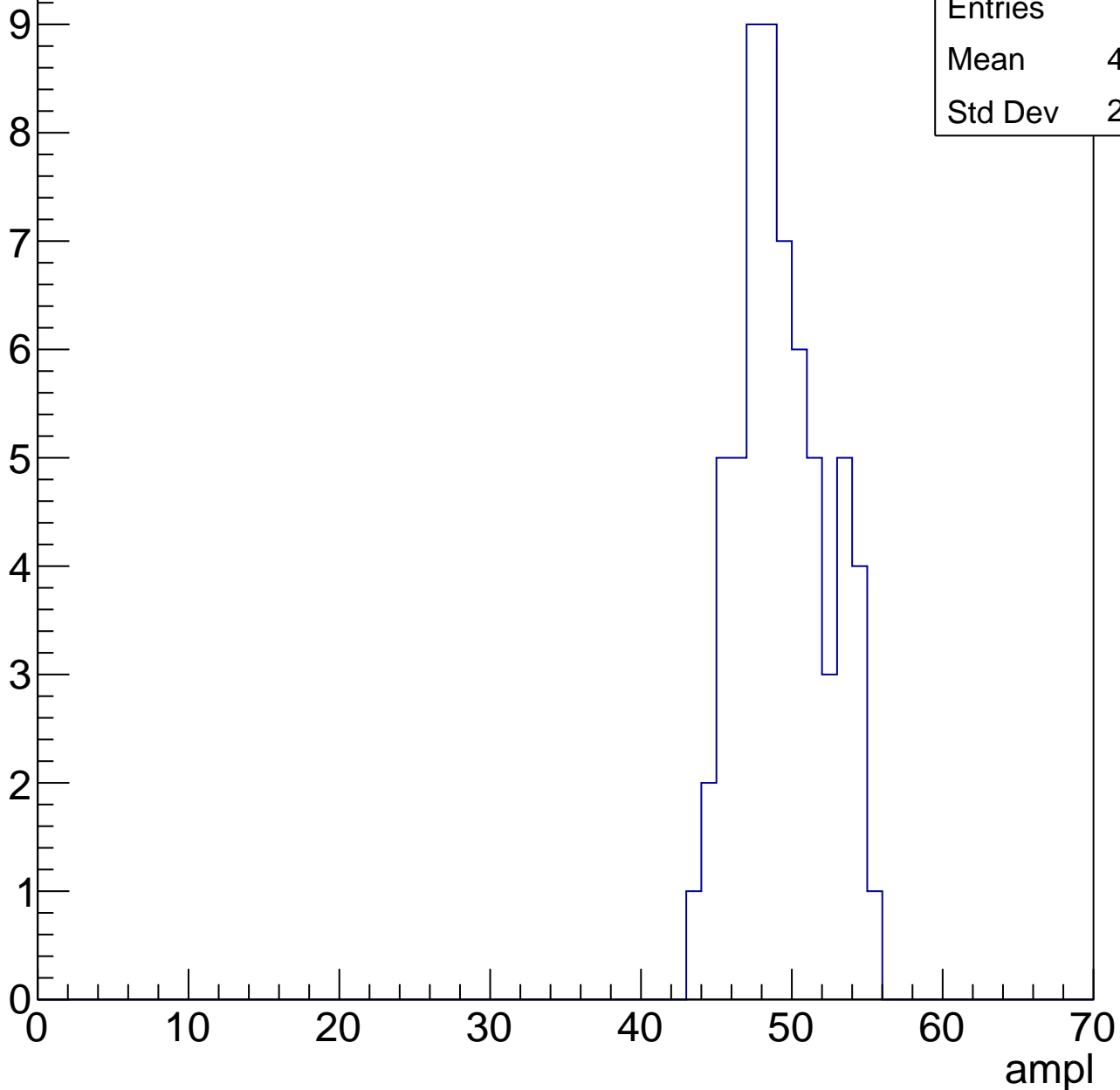


B1L100S, U7-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 48.89 |
| Std Dev | 2.908 |

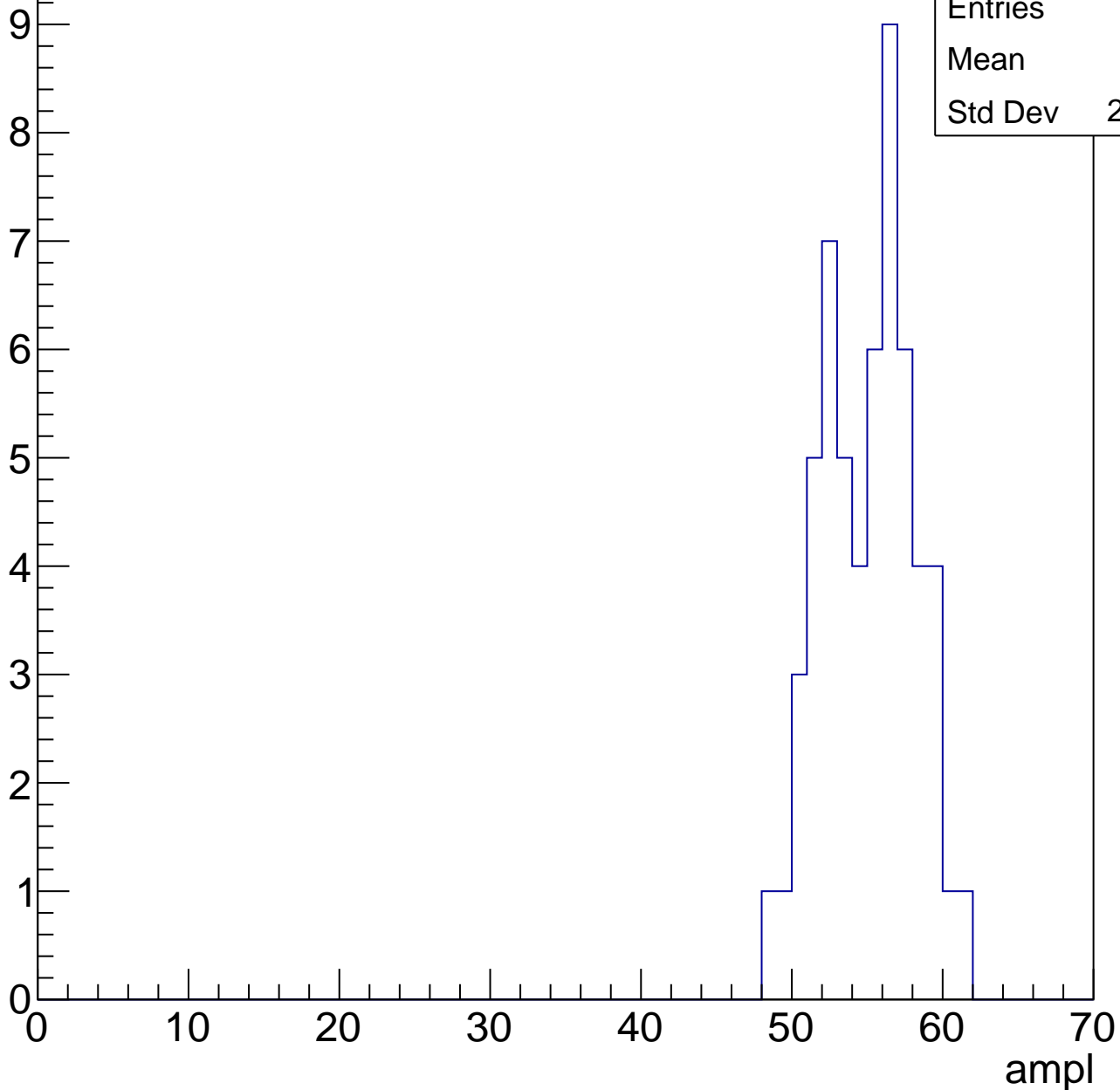


B1L100S, U7-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.6 |
| Std Dev | 2.996 |

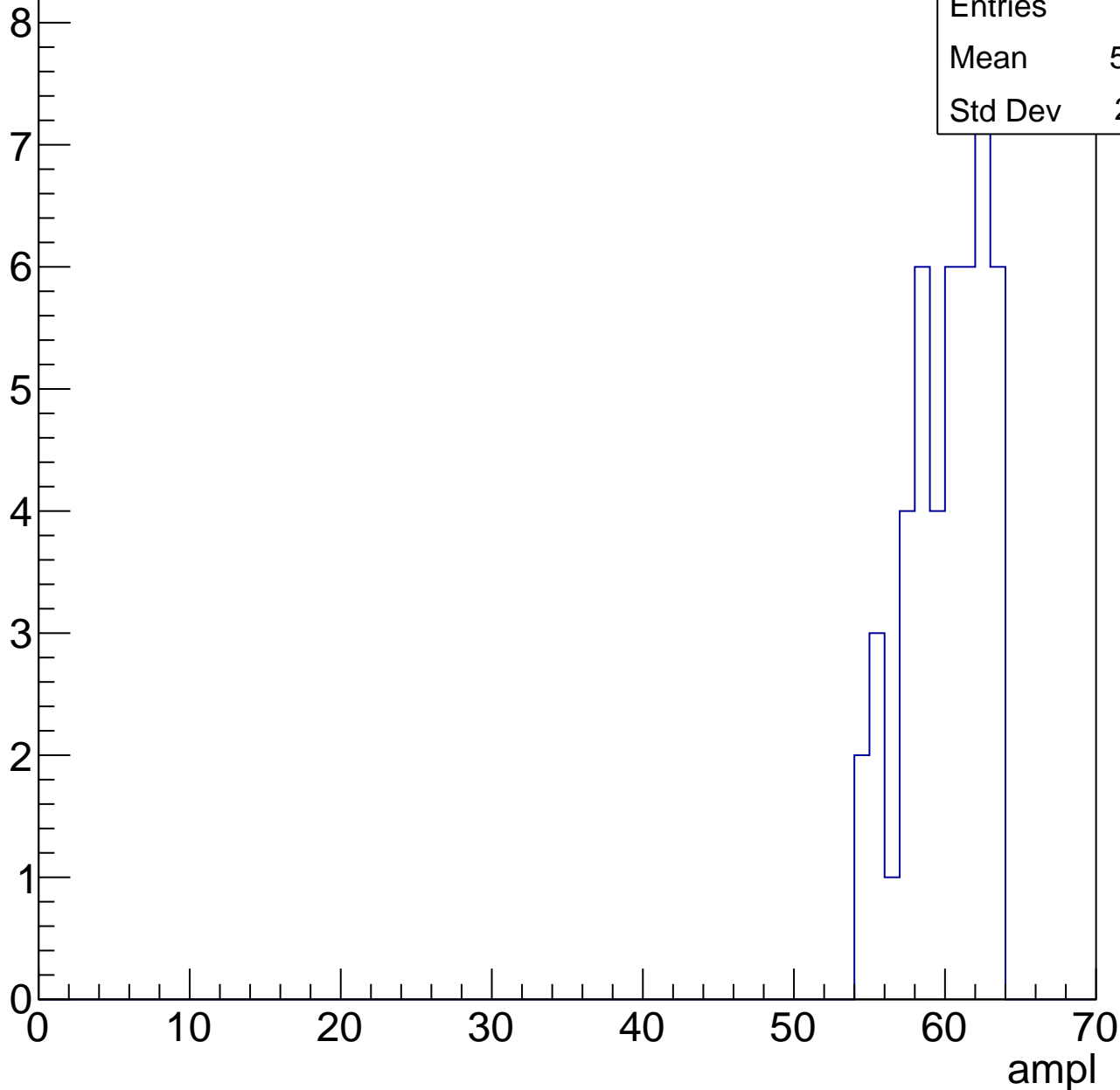


B1L100S, U7-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

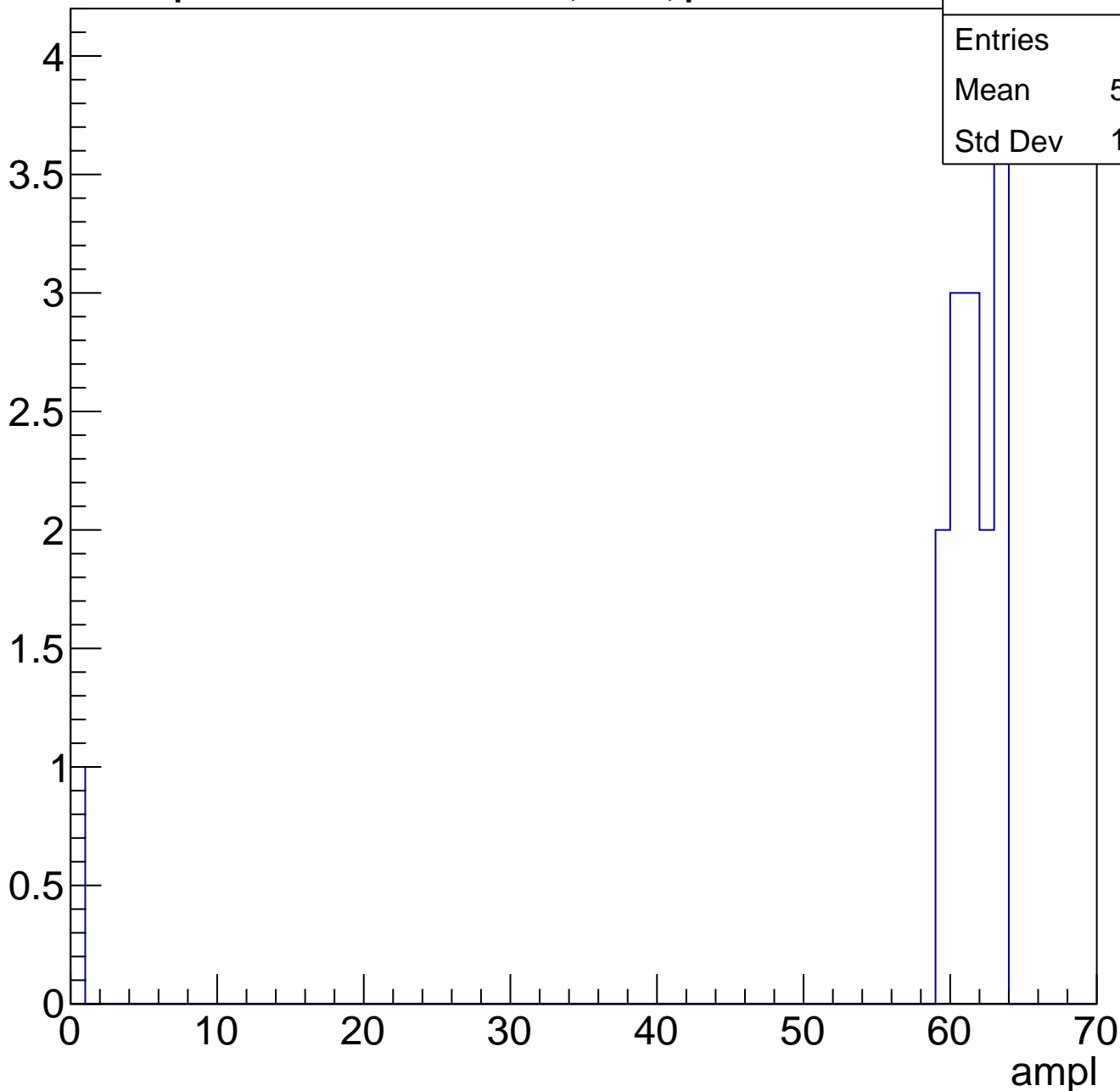
| | |
|---------|-------|
| Entries | 46 |
| Mean | 59.59 |
| Std Dev | 2.601 |



B1L100S, U7-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 15 |
| Mean | 57.13 |
| Std Dev | 15.33 |

B1L100S, U7-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch11, adc0

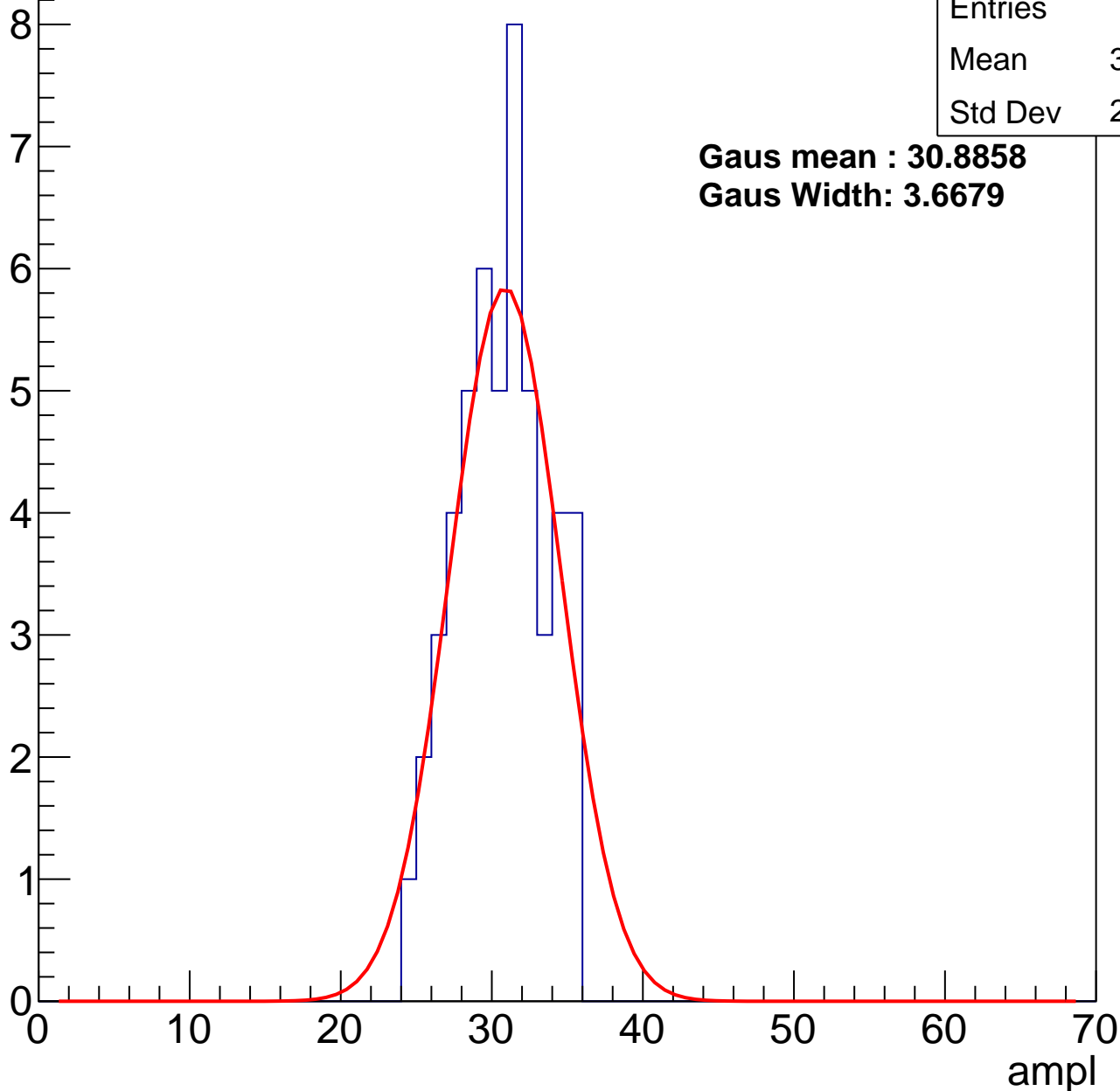
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 30.14 |
| Std Dev | 2.878 |

Gaus mean : 30.8858

Gaus Width: 3.6679



B1L100S, U7-ch11, adc1

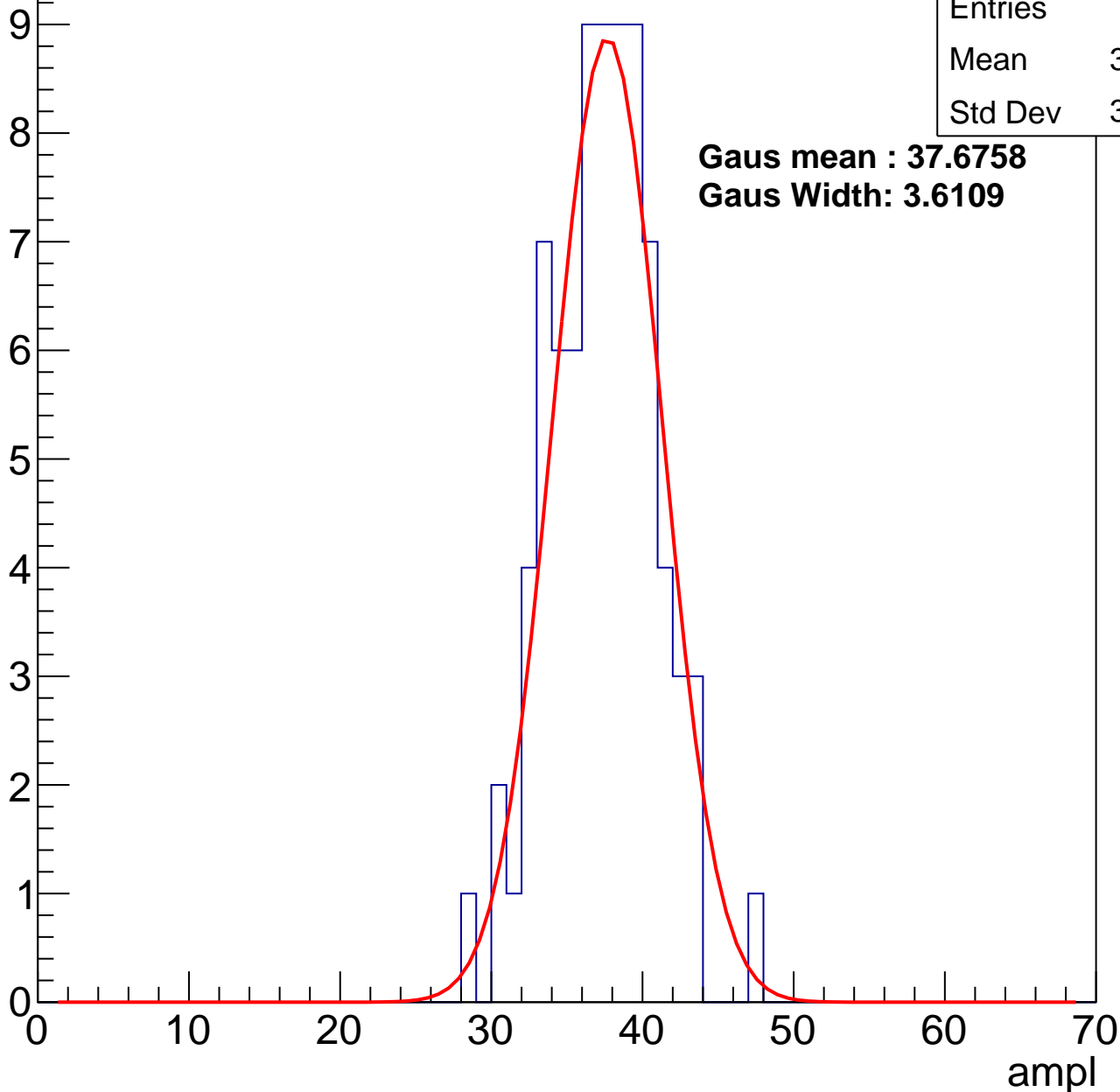
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 36.89 |
| Std Dev | 3.457 |

Gaus mean : 37.6758

Gaus Width: 3.6109



B1L100S, U7-ch11, adc2

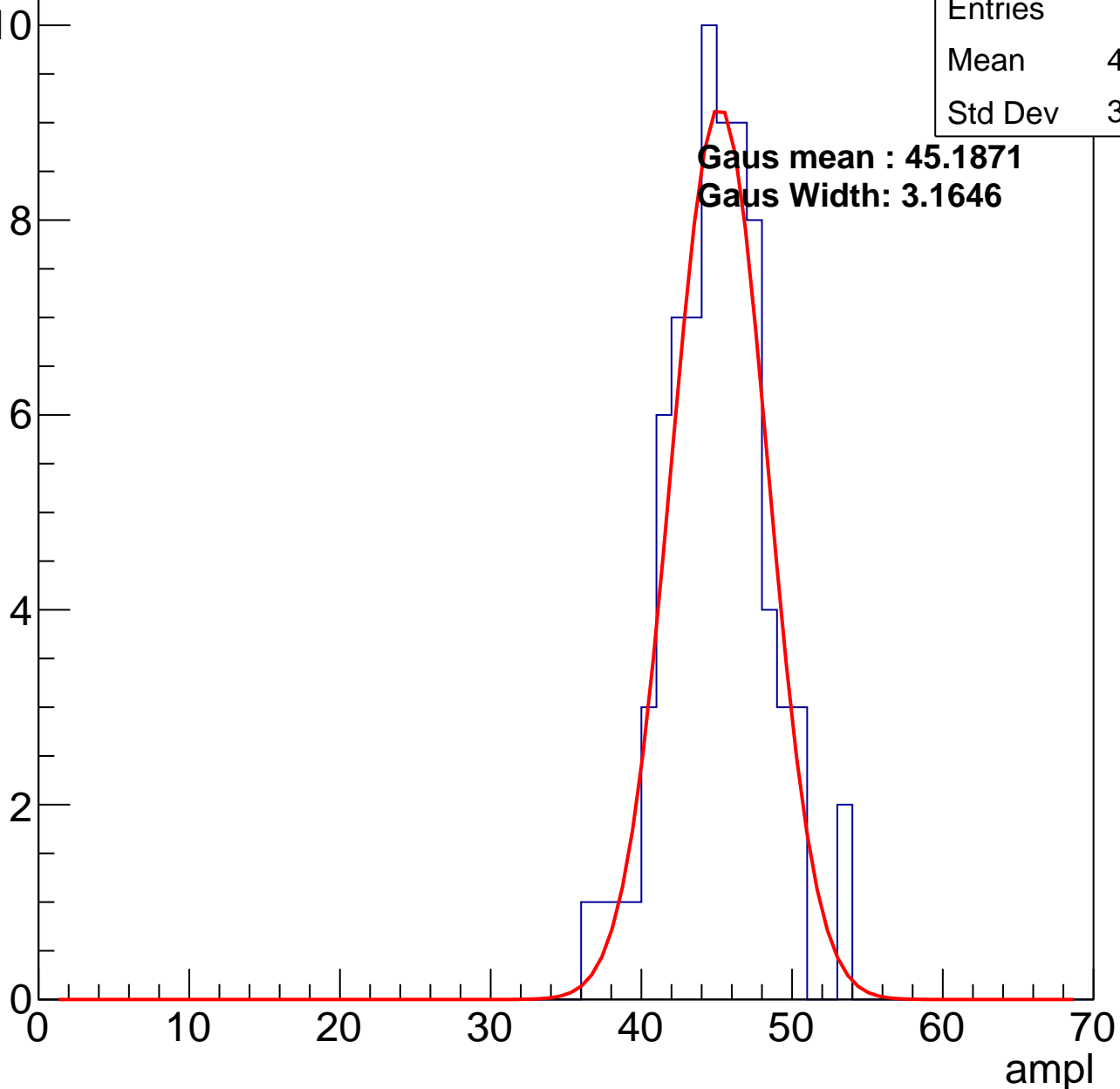
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 44.55 |
| Std Dev | 3.296 |

Gaus mean : 45.1871

Gaus Width: 3.1646

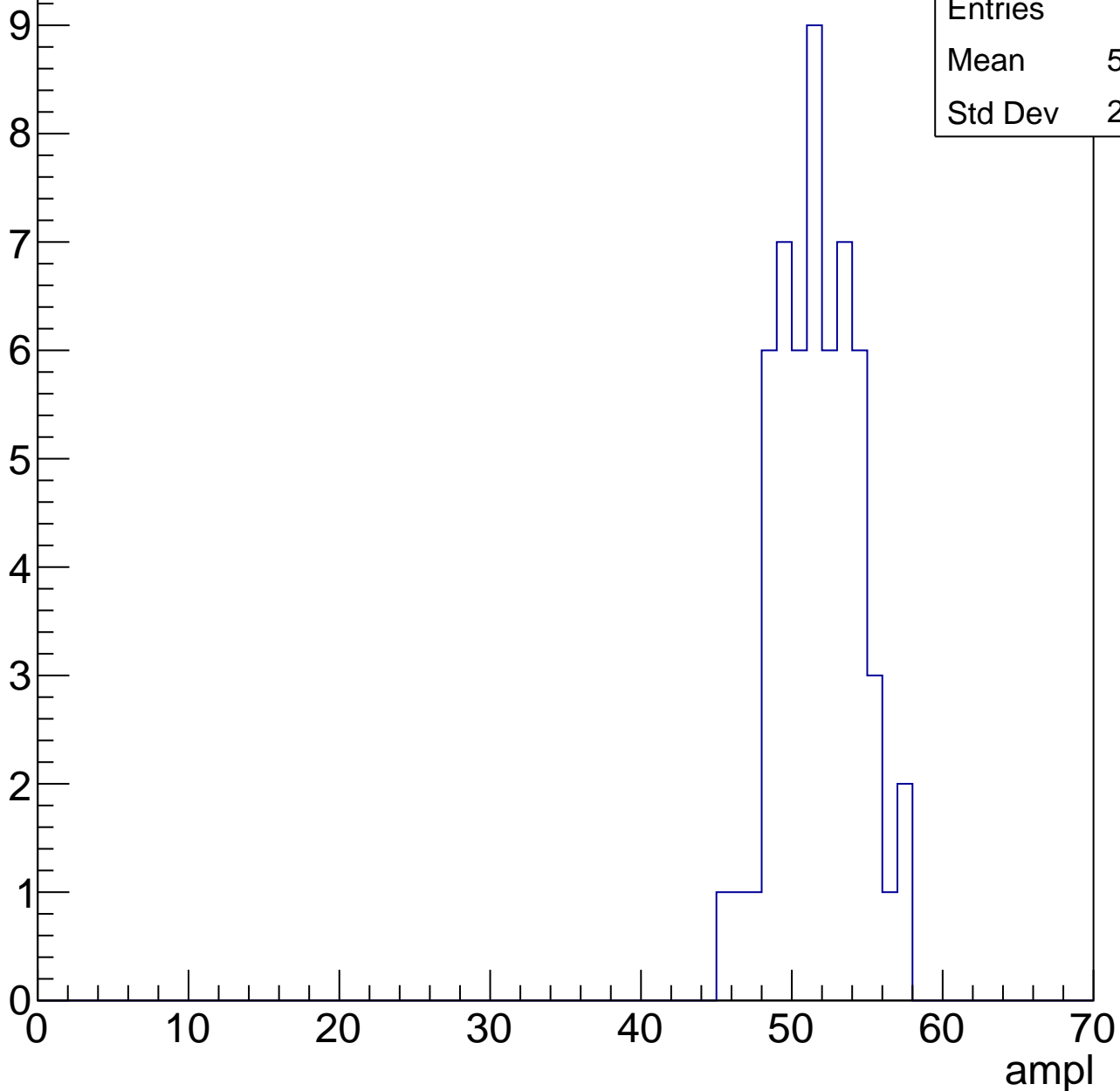


B1L100S, U7-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 51.25 |
| Std Dev | 2.654 |



B1L100S, U7-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

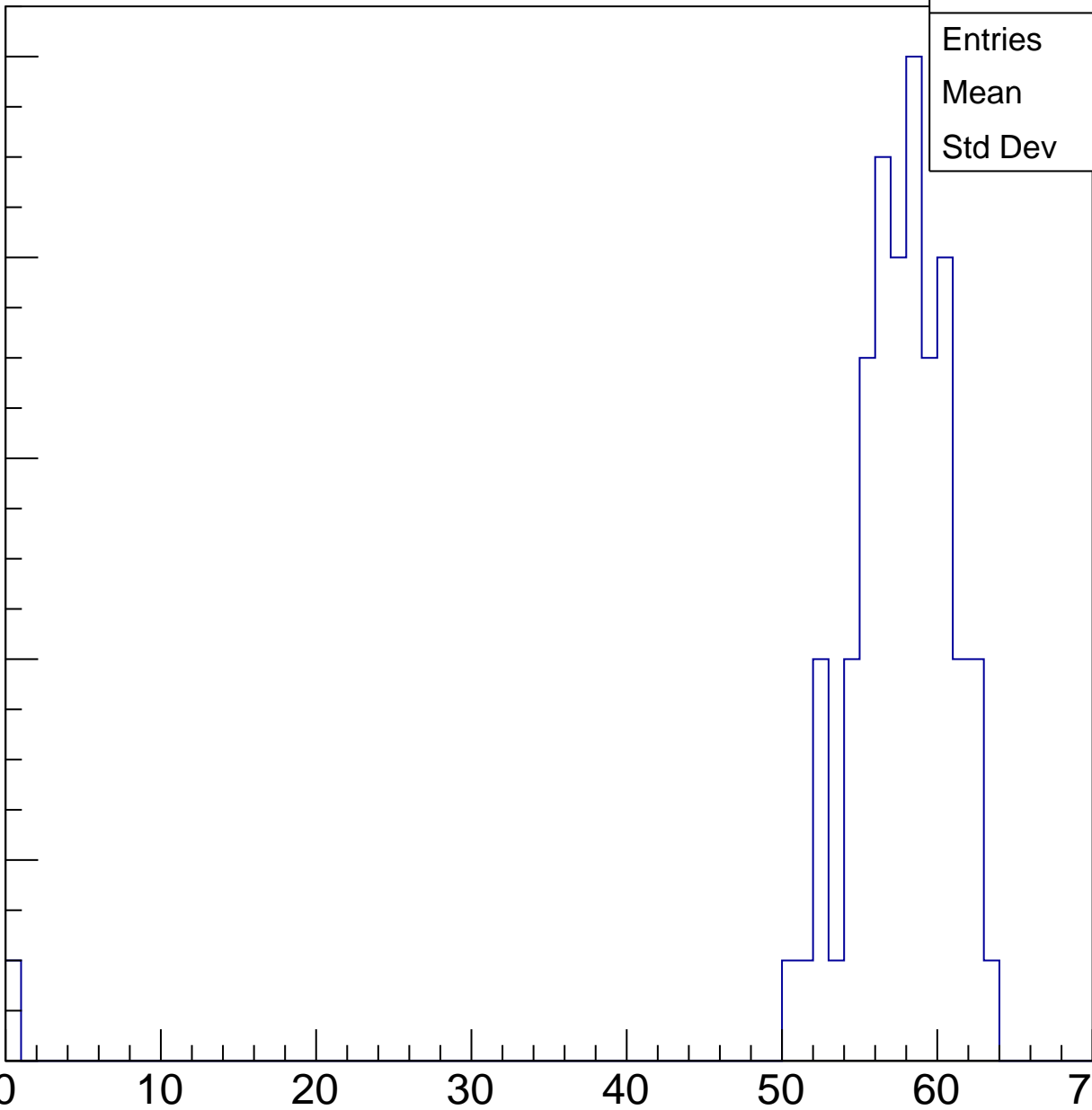
| | |
|---------|-------|
| Entries | 70 |
| Mean | 56.44 |
| Std Dev | 7.379 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

24

Mean

61.5

Std Dev

1.323

ampl

0

10

20

30

40

50

60

70

B1L100S, U7-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch12, adc0

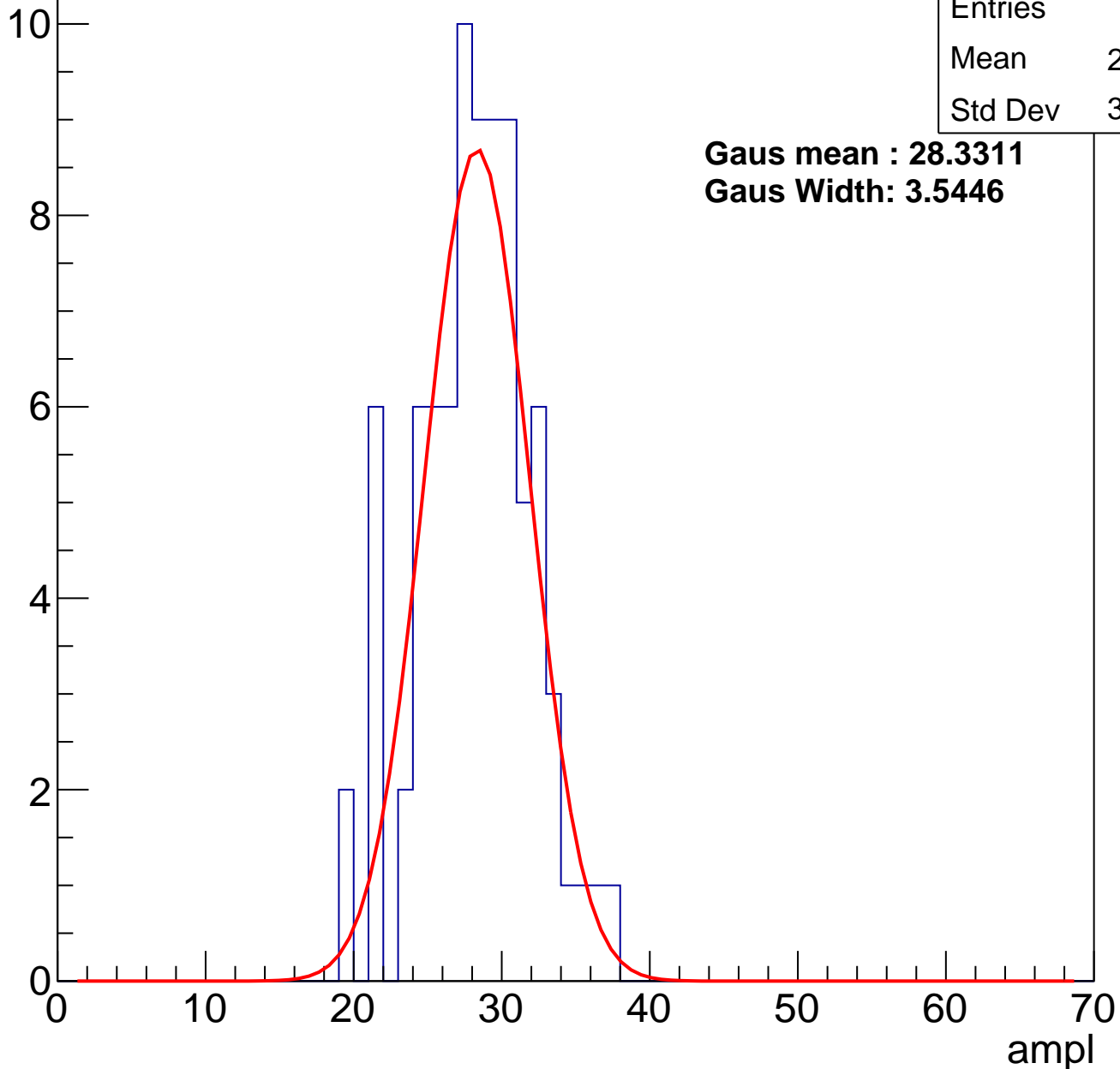
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 27.72 |
| Std Dev | 3.762 |

Gaus mean : 28.3311

Gaus Width: 3.5446

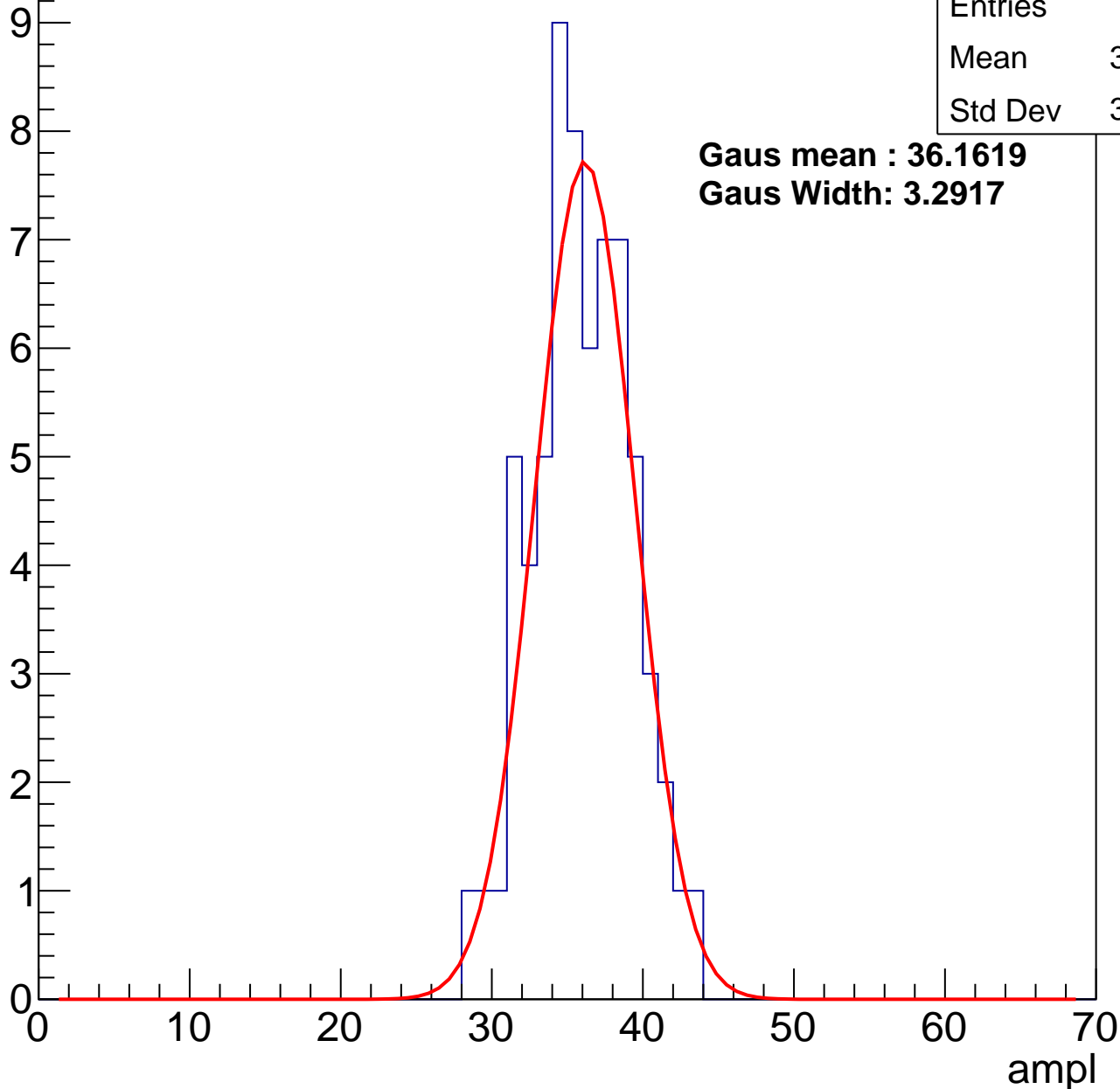
Entry



B1L100S, U7-ch12, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch12, adc2

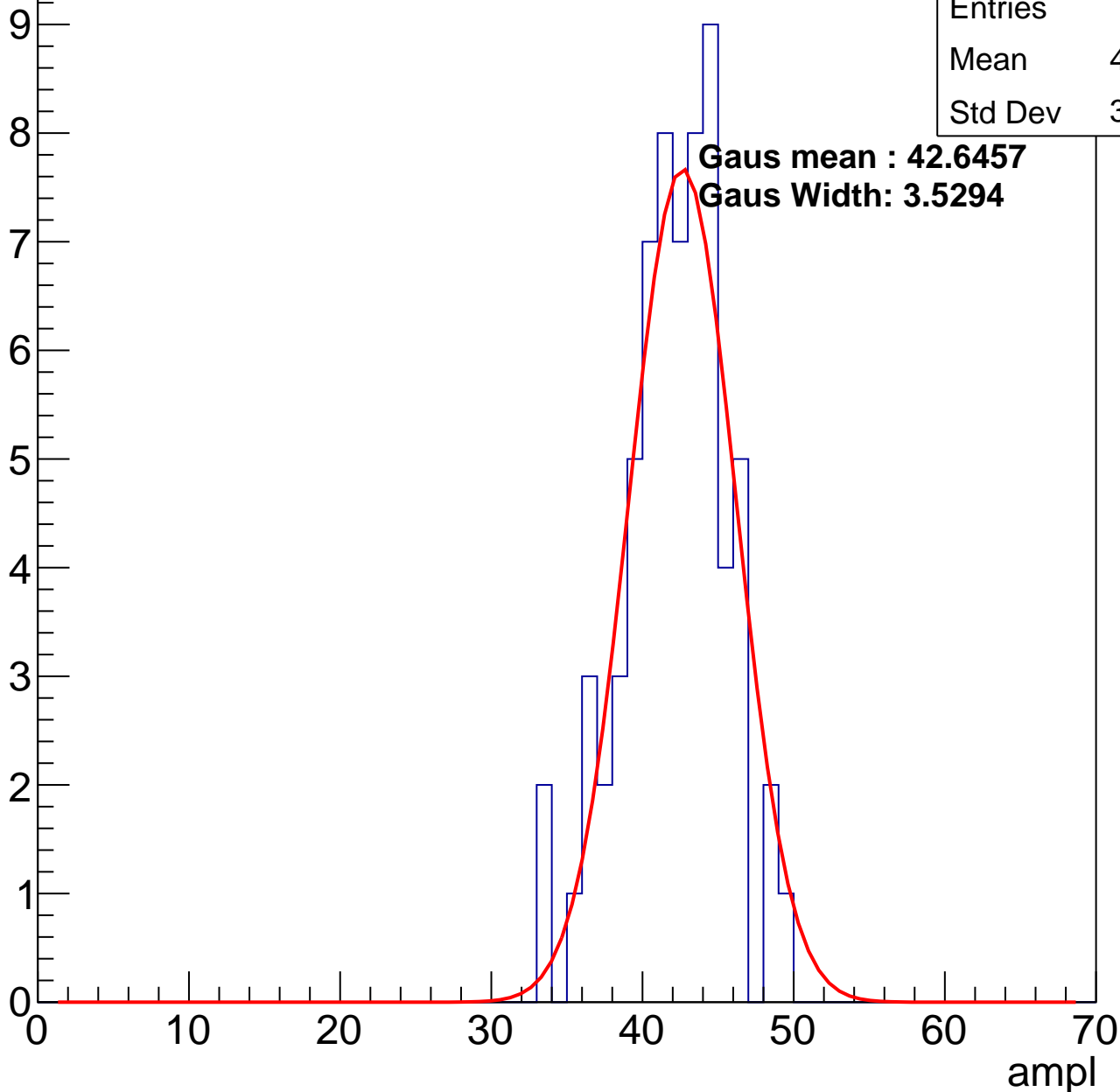
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 41.63 |
| Std Dev | 3.389 |

Gaus mean : 42.6457

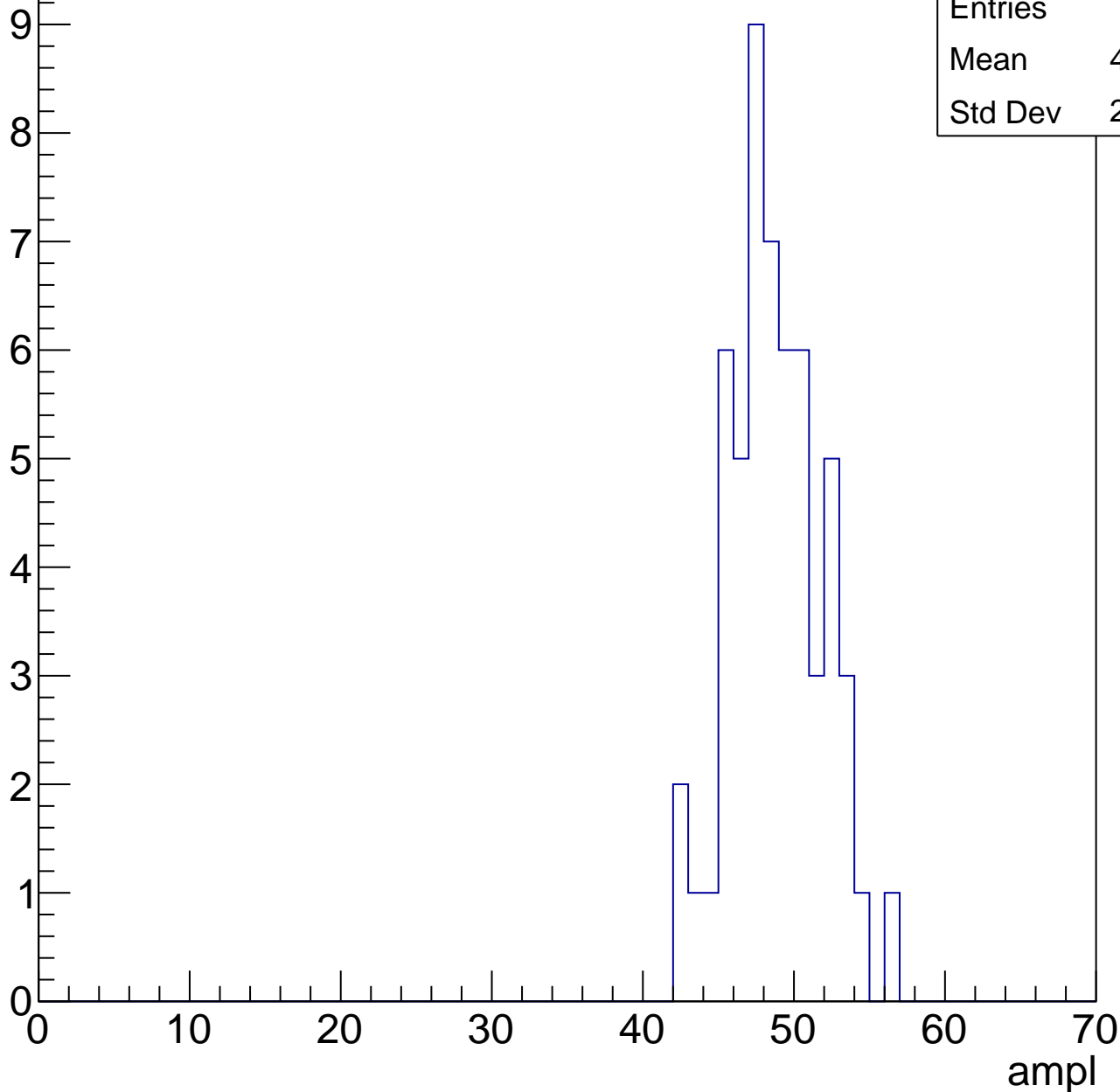
Gaus Width: 3.5294



B1L100S, U7-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



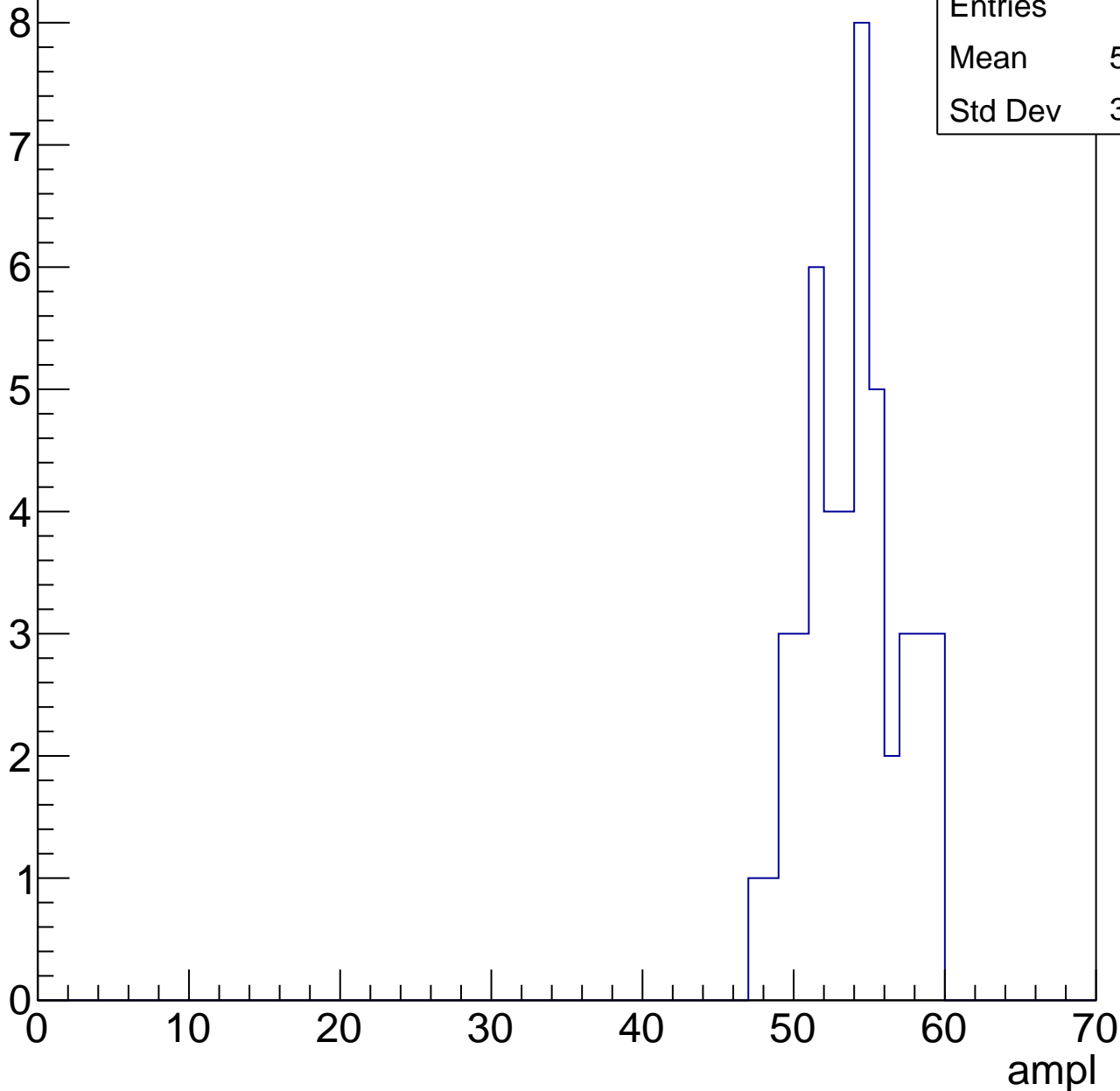
| | |
|---------|-------|
| Entries | 56 |
| Mean | 48.32 |
| Std Dev | 2.995 |

B1L100S, U7-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 53.46 |
| Std Dev | 3.062 |

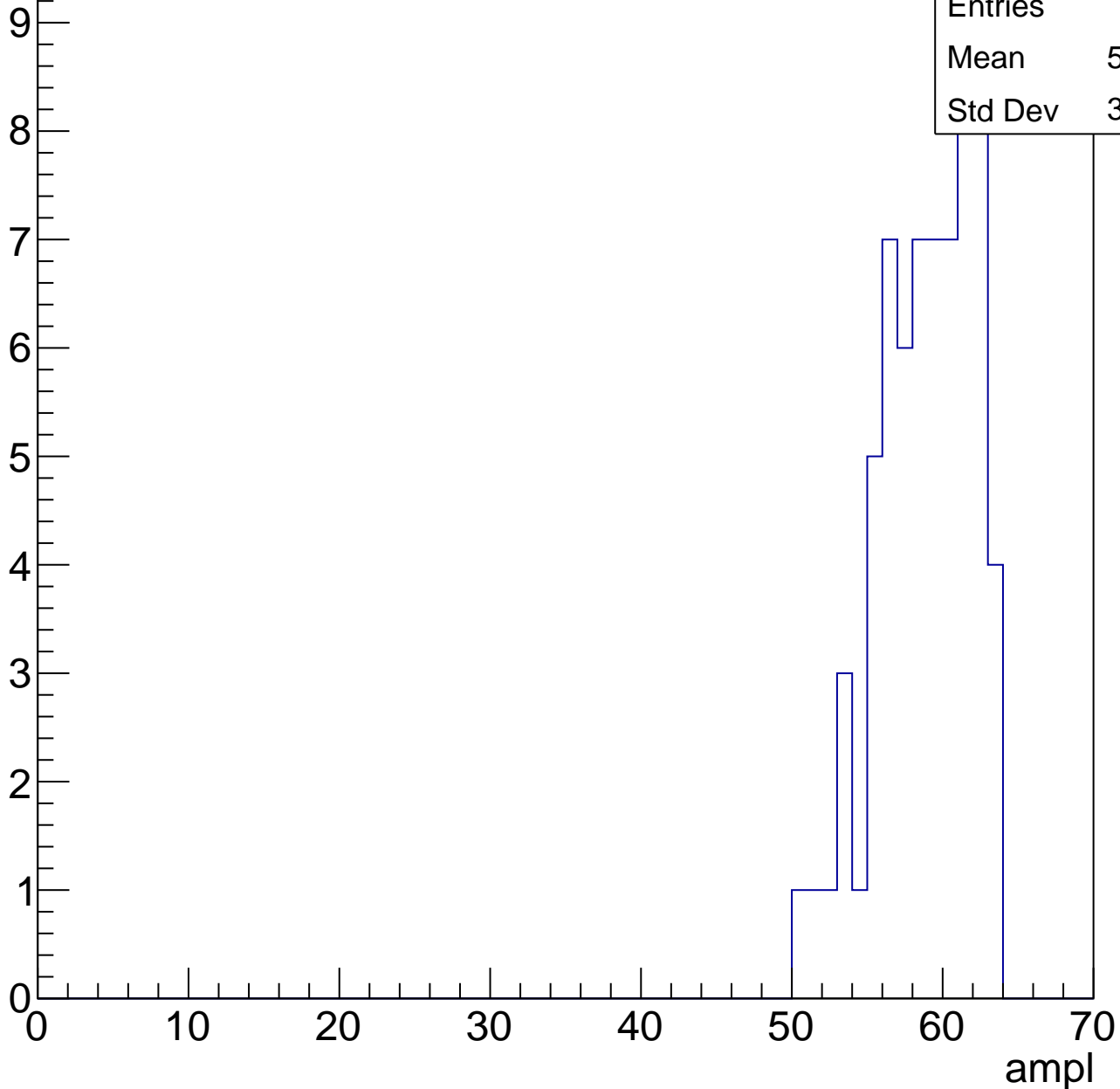


B1L100S, U7-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 58.37 |
| Std Dev | 3.124 |

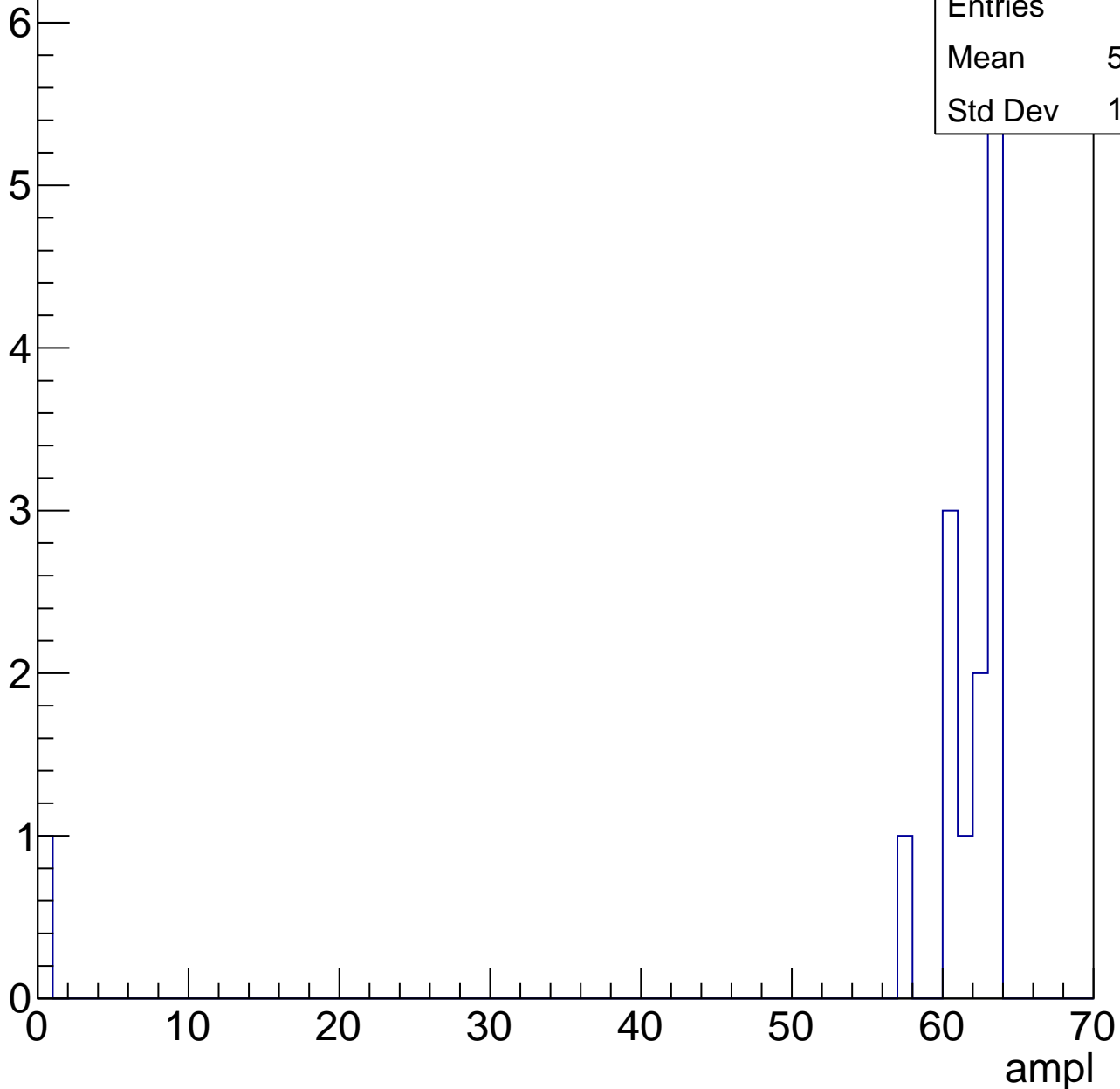


B1L100S, U7-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.14 |
| Std Dev | 15.94 |



B1L100S, U7-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch13, adc0

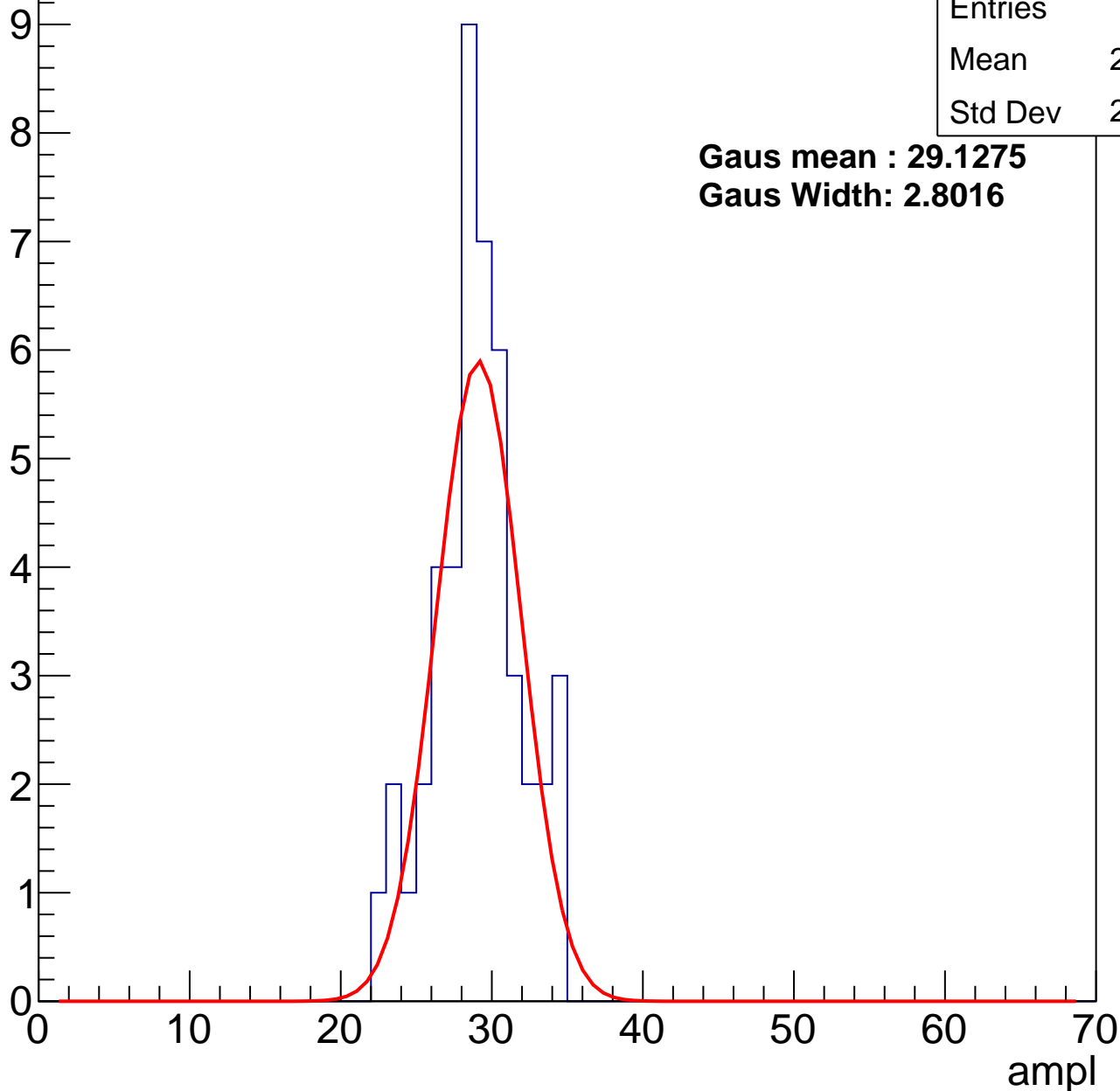
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 28.57 |
| Std Dev | 2.849 |

Gaus mean : 29.1275

Gaus Width: 2.8016



B1L100S, U7-ch13, adc1

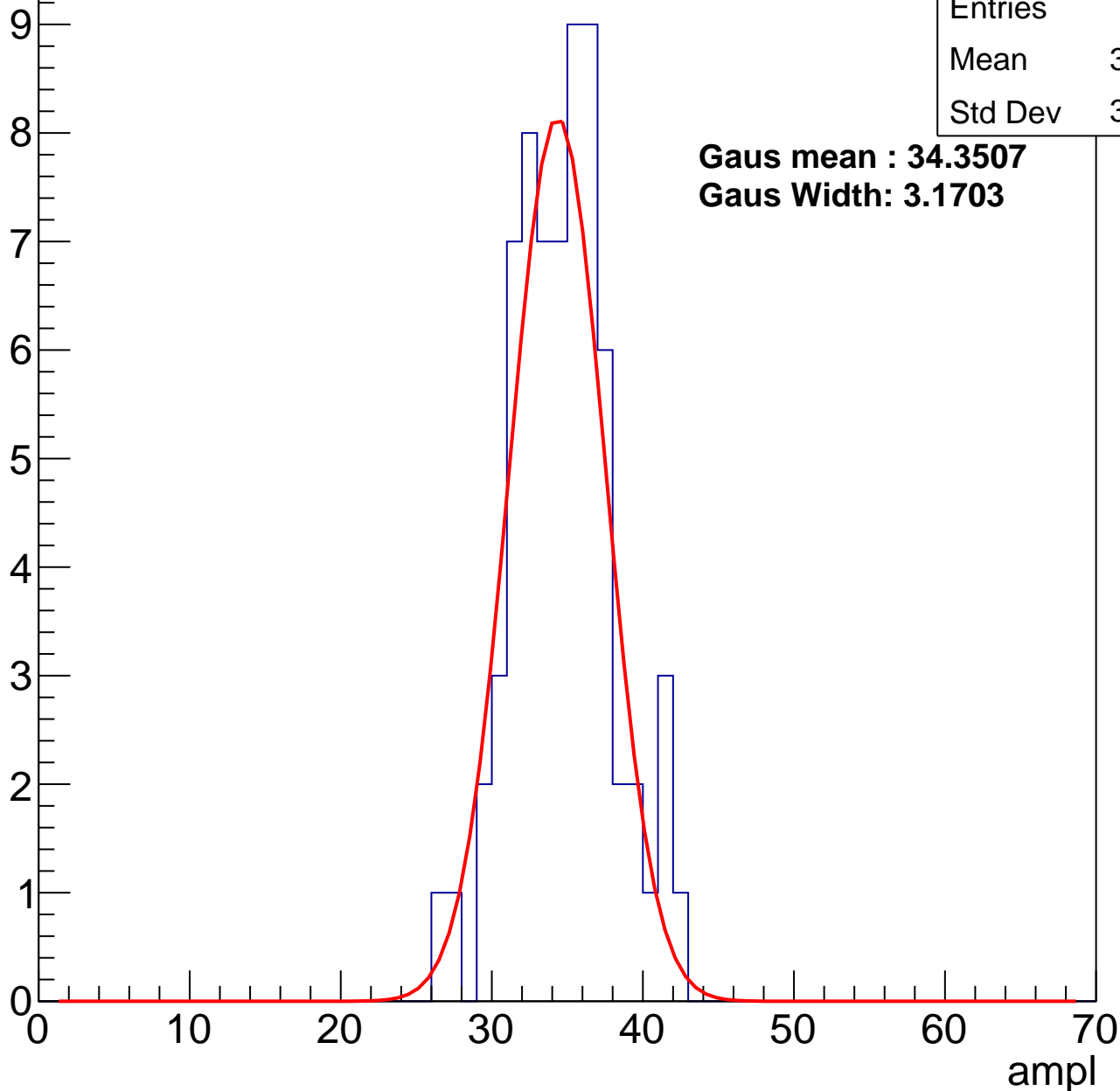
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.25 |
| Std Dev | 3.268 |

Gaus mean : 34.3507

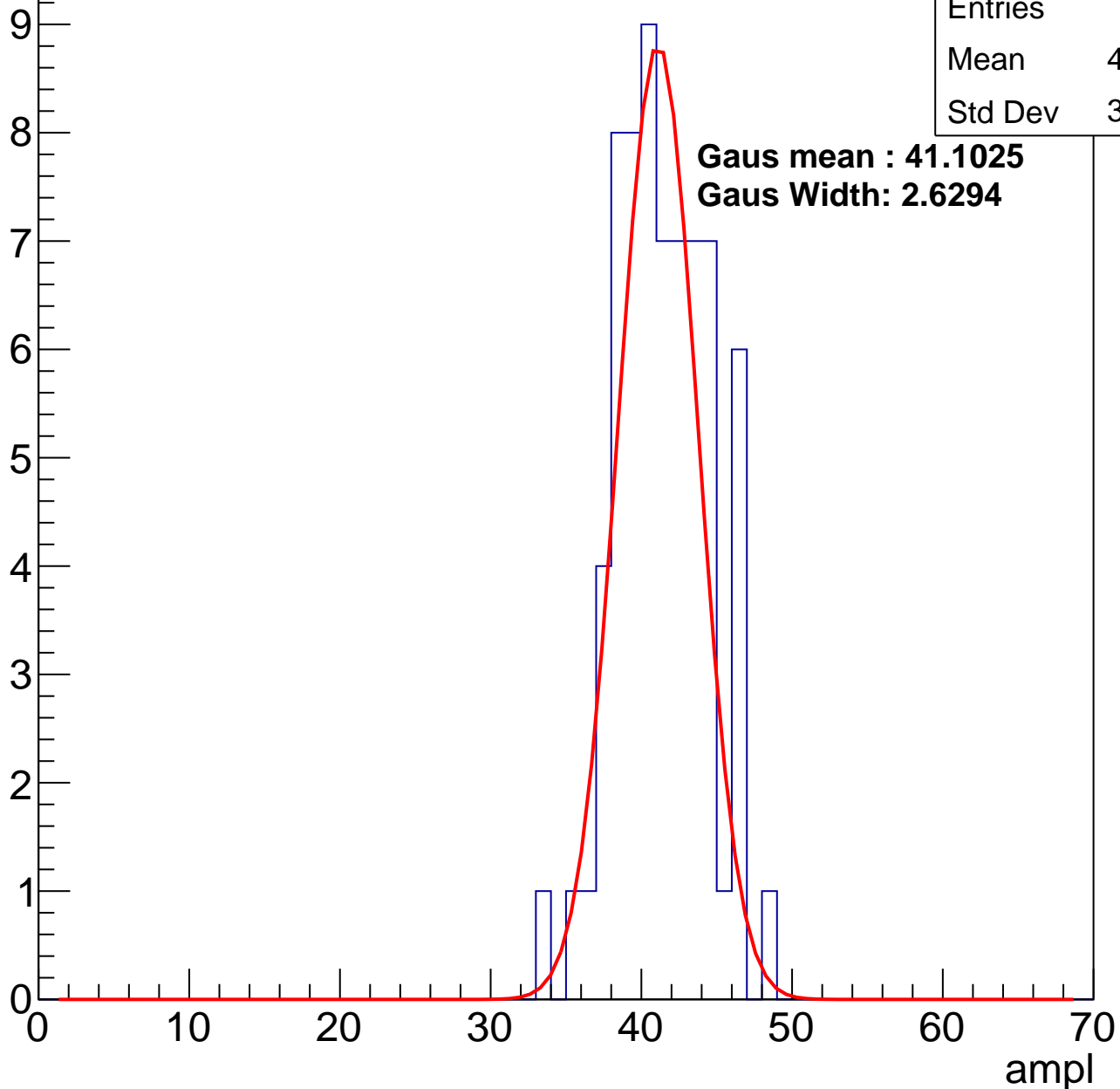
Gaus Width: 3.1703



B1L100S, U7-ch13, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

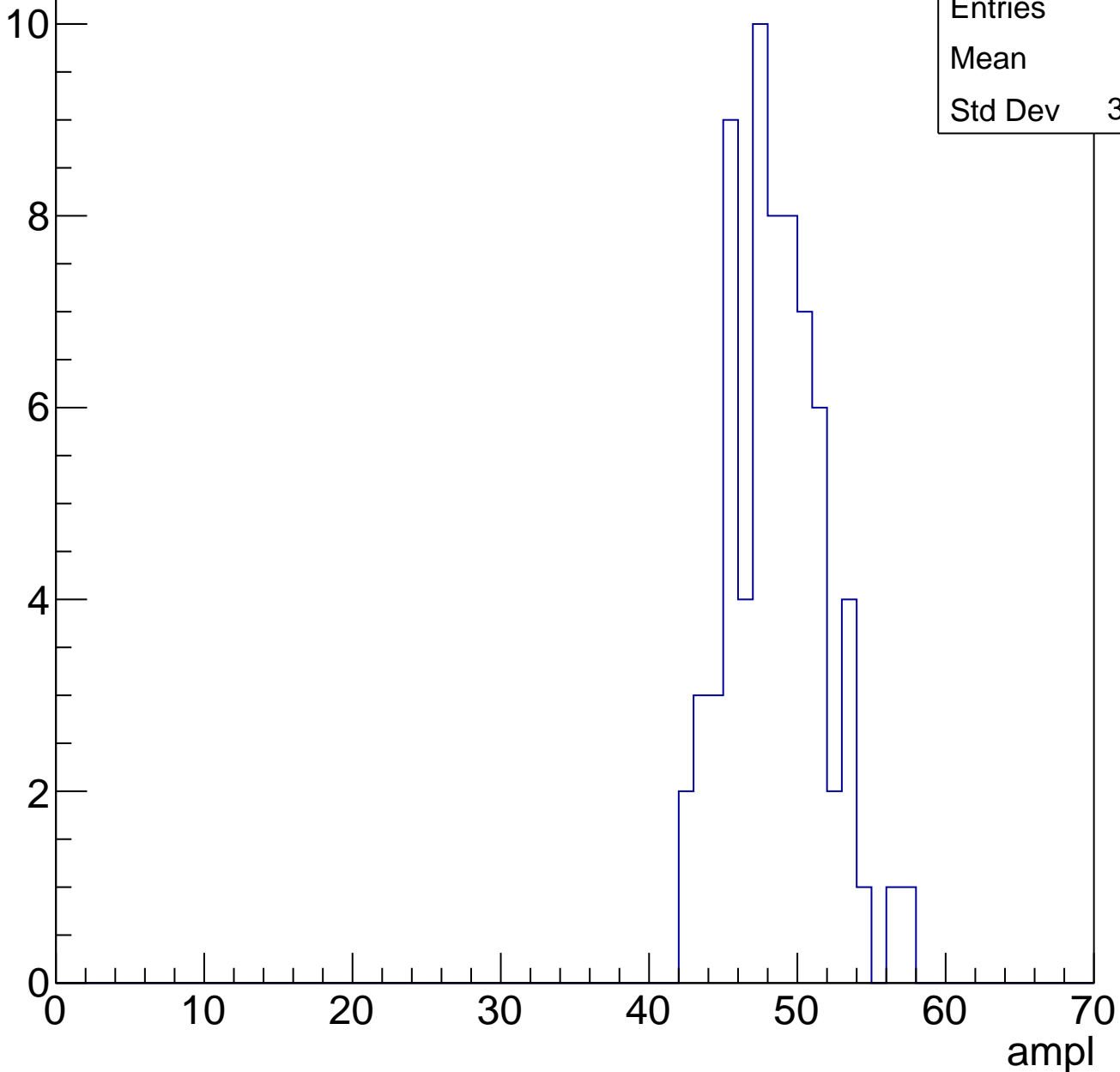


B1L100S, U7-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

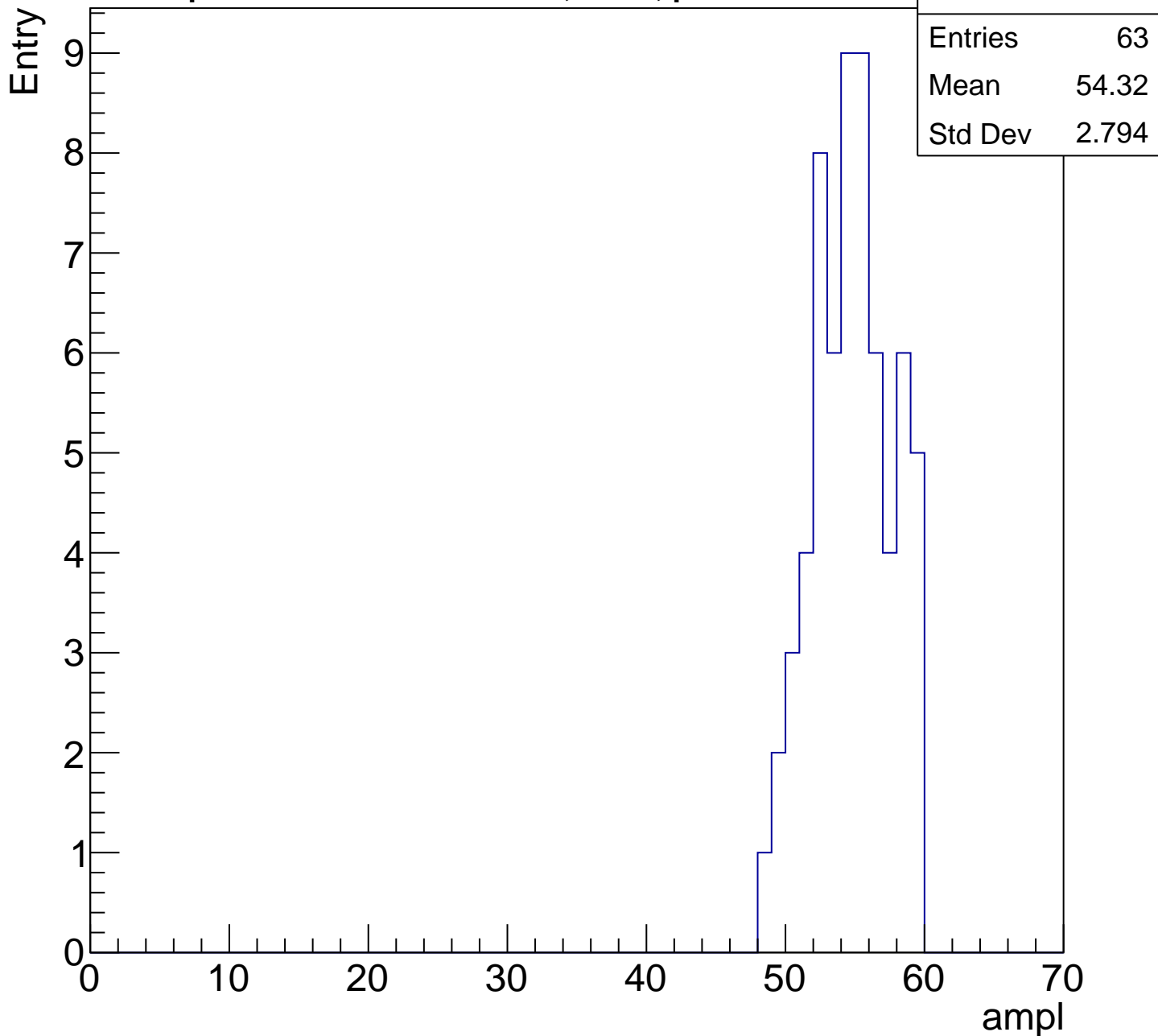
| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.1 |
| Std Dev | 3.195 |

Entry



B1L100S, U7-ch13, adc4

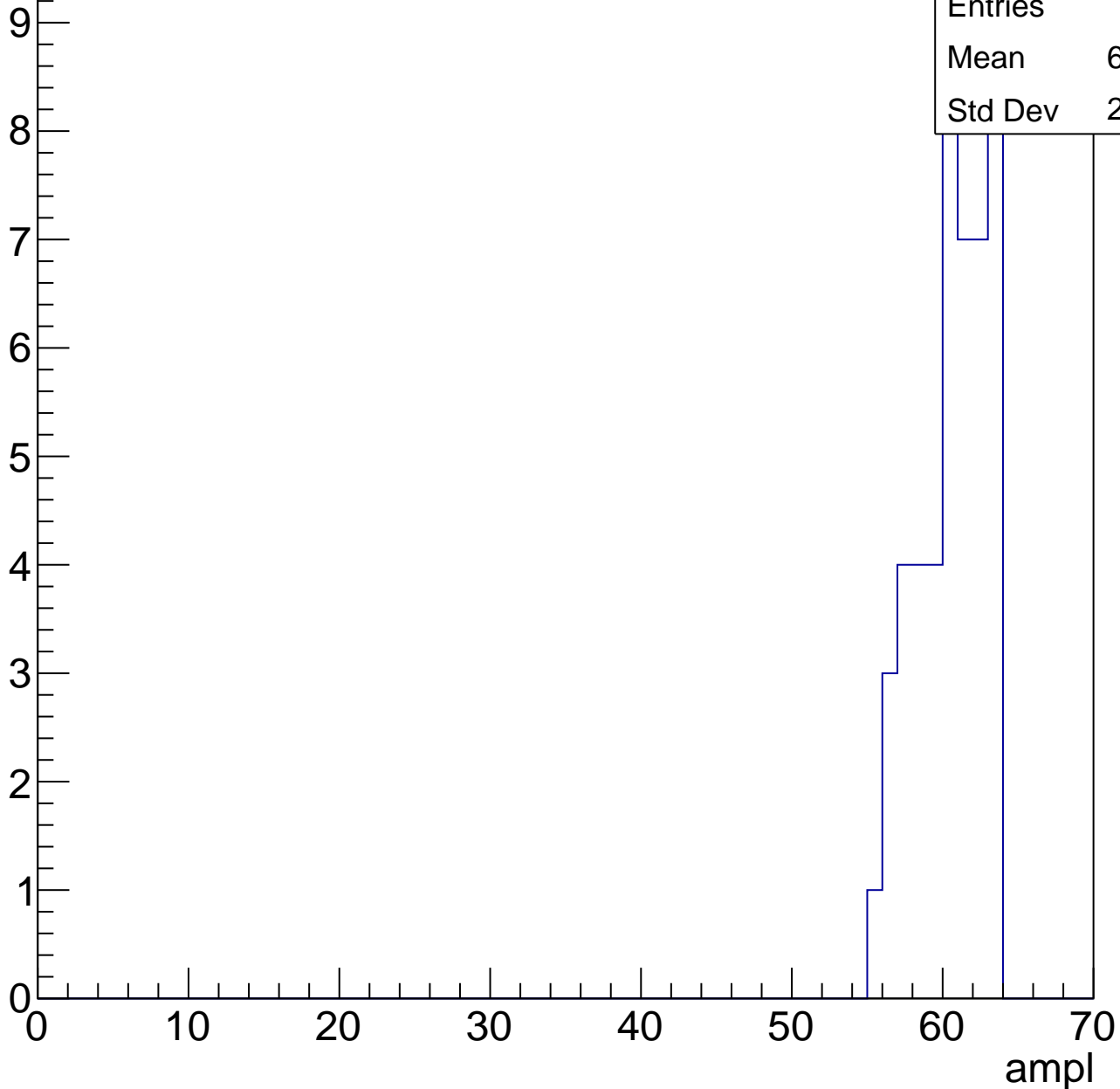
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

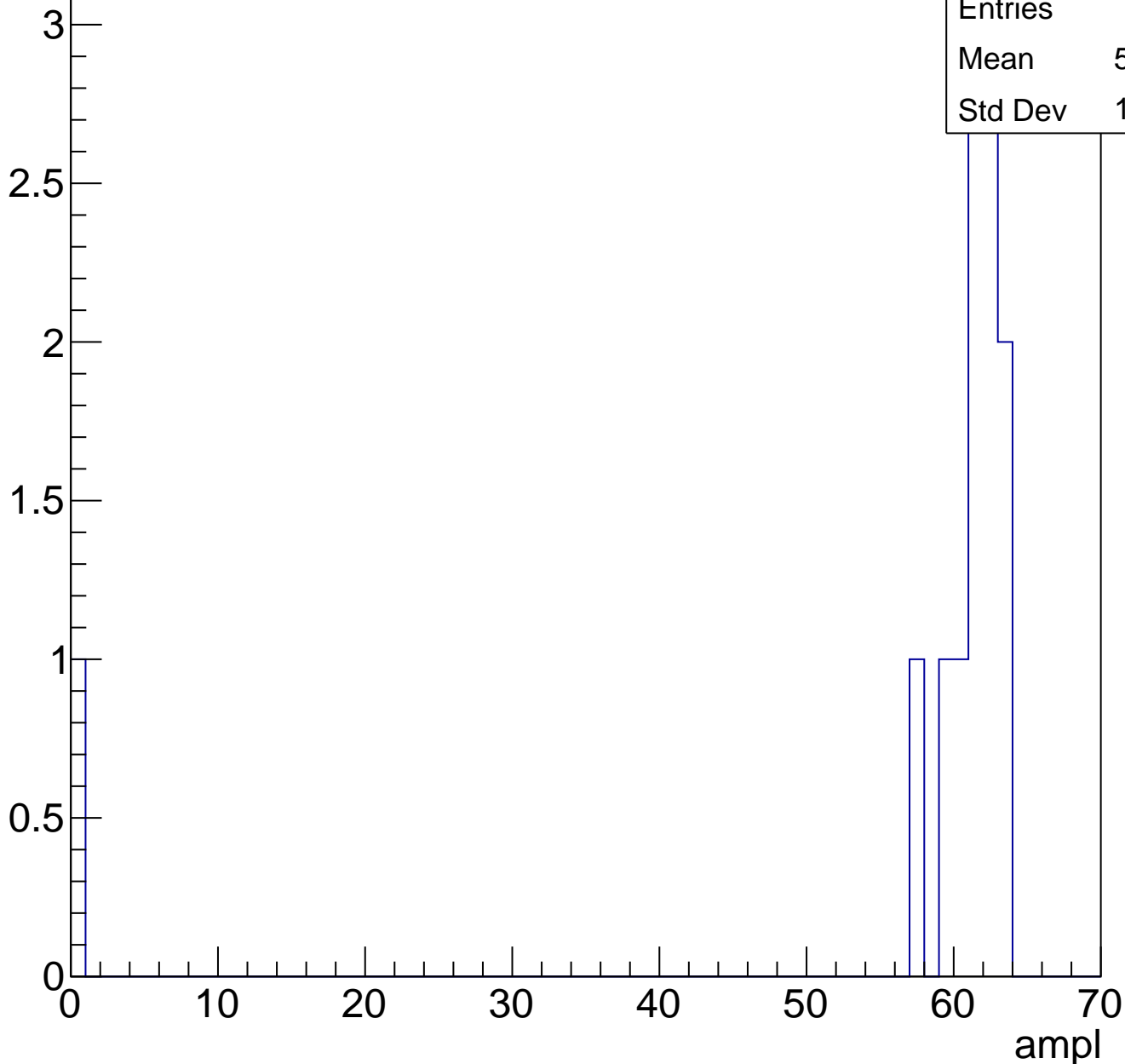
Entry



B1L100S, U7-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch14, adc0

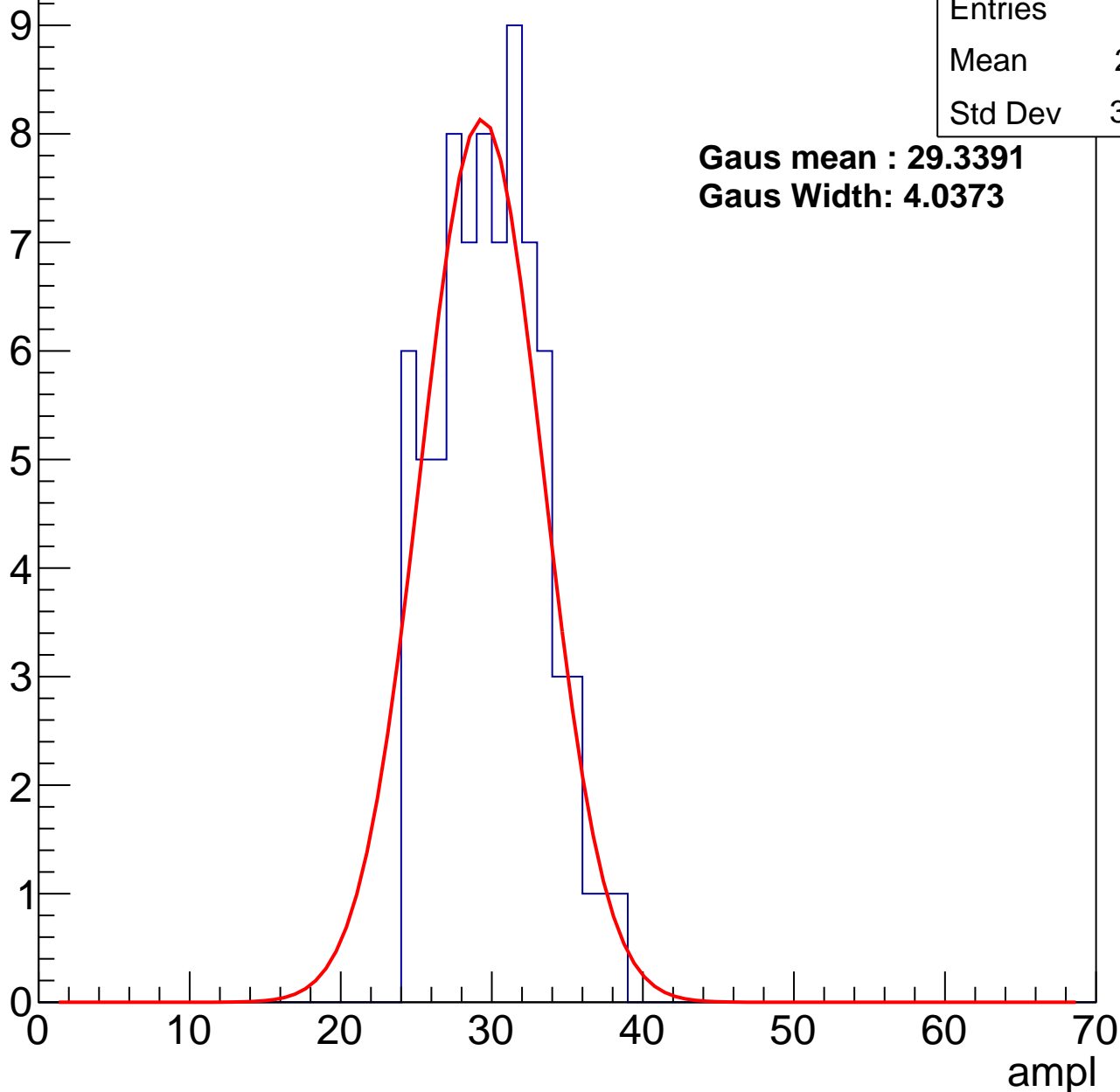
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 29.51 |
| Std Dev | 3.368 |

Gaus mean : 29.3391

Gaus Width: 4.0373



B1L100S, U7-ch14, adc1

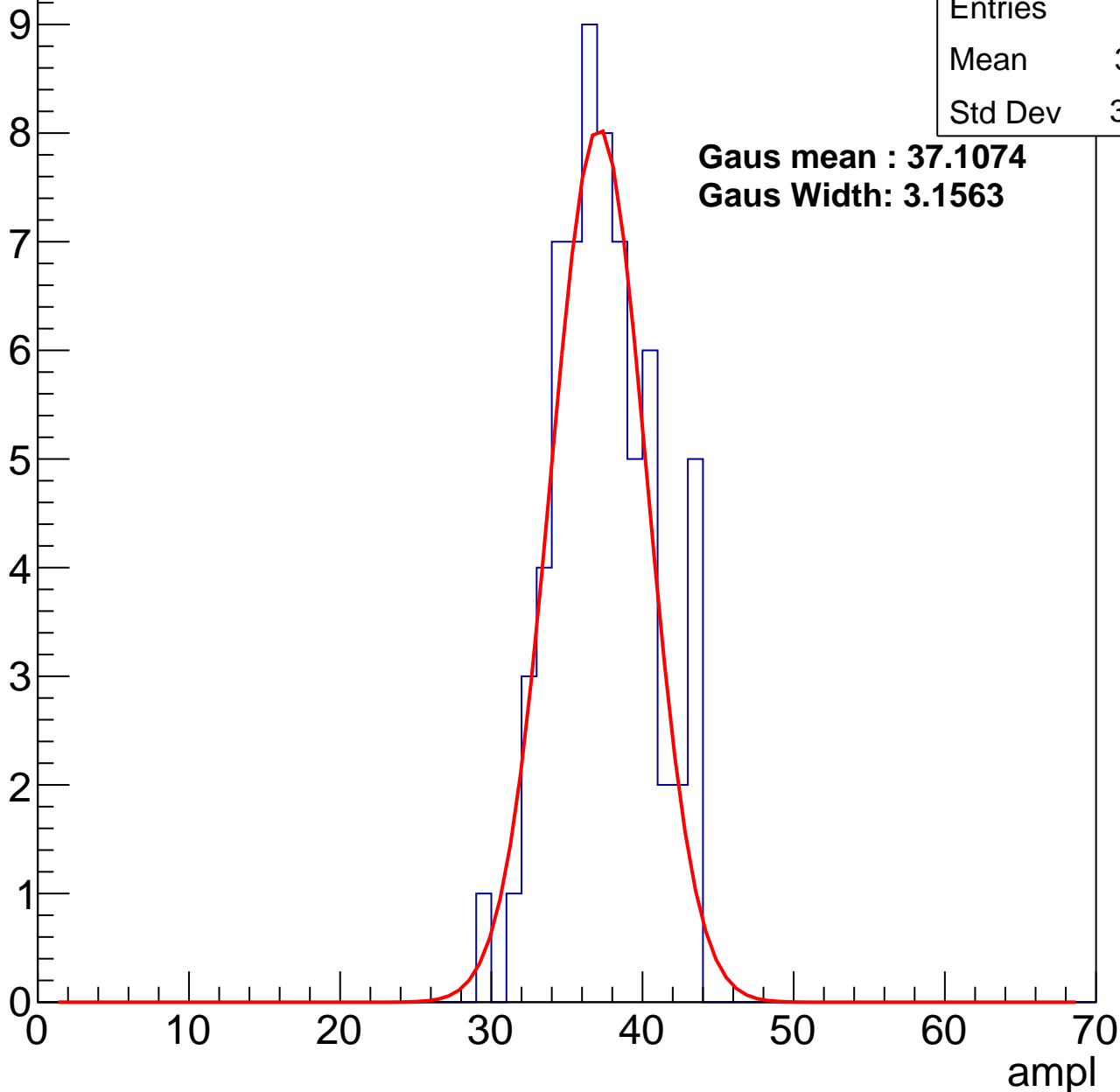
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.91 |
| Std Dev | 3.189 |

Gaus mean : 37.1074

Gaus Width: 3.1563



B1L100S, U7-ch14, adc2

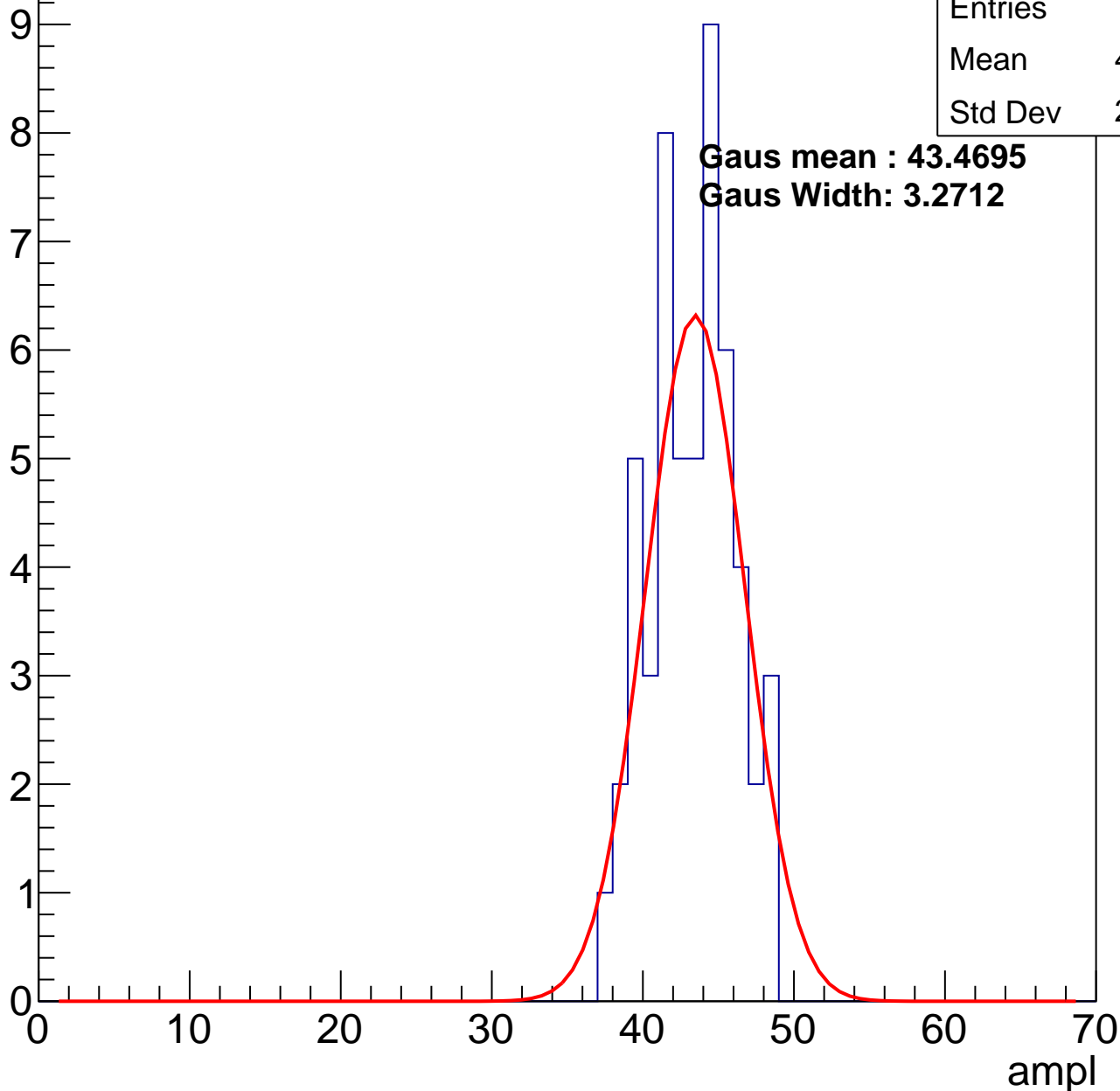
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 42.81 |
| Std Dev | 2.761 |

Gaus mean : 43.4695

Gaus Width: 3.2712

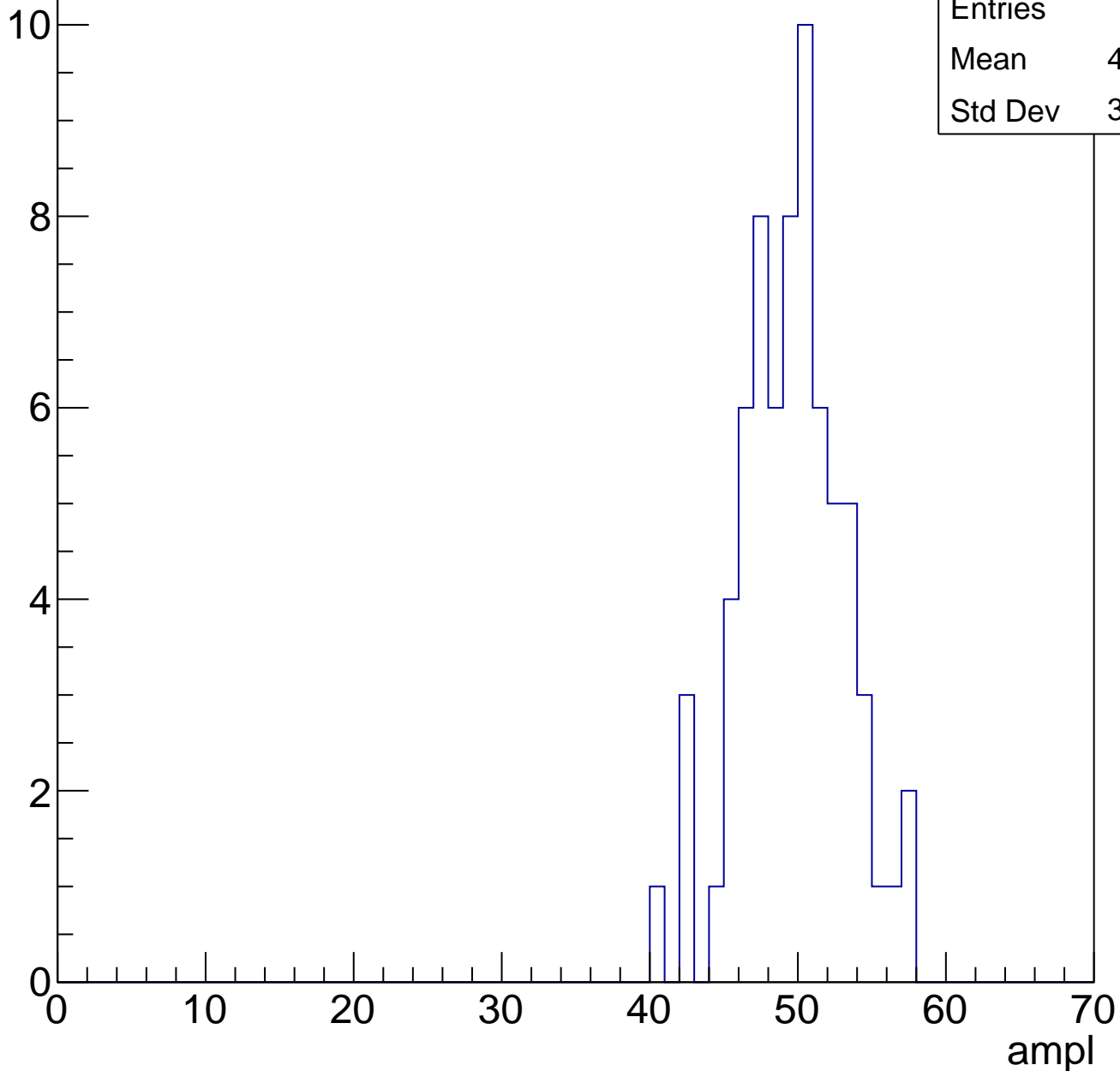


B1L100S, U7-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.14 |
| Std Dev | 3.494 |

Entry

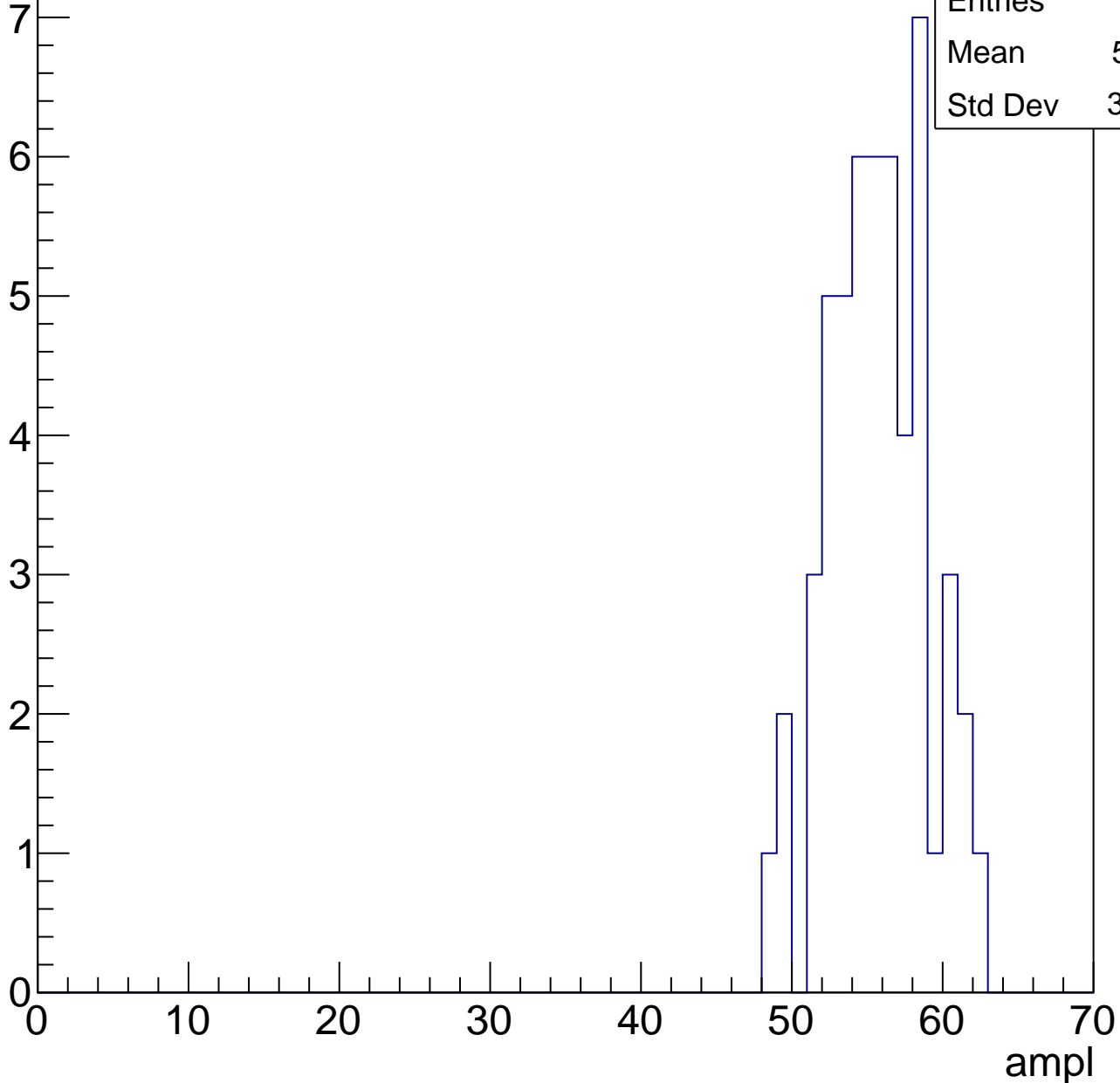


B1L100S, U7-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 55.21 |
| Std Dev | 3.207 |



B1L100S, U7-ch14, adc5

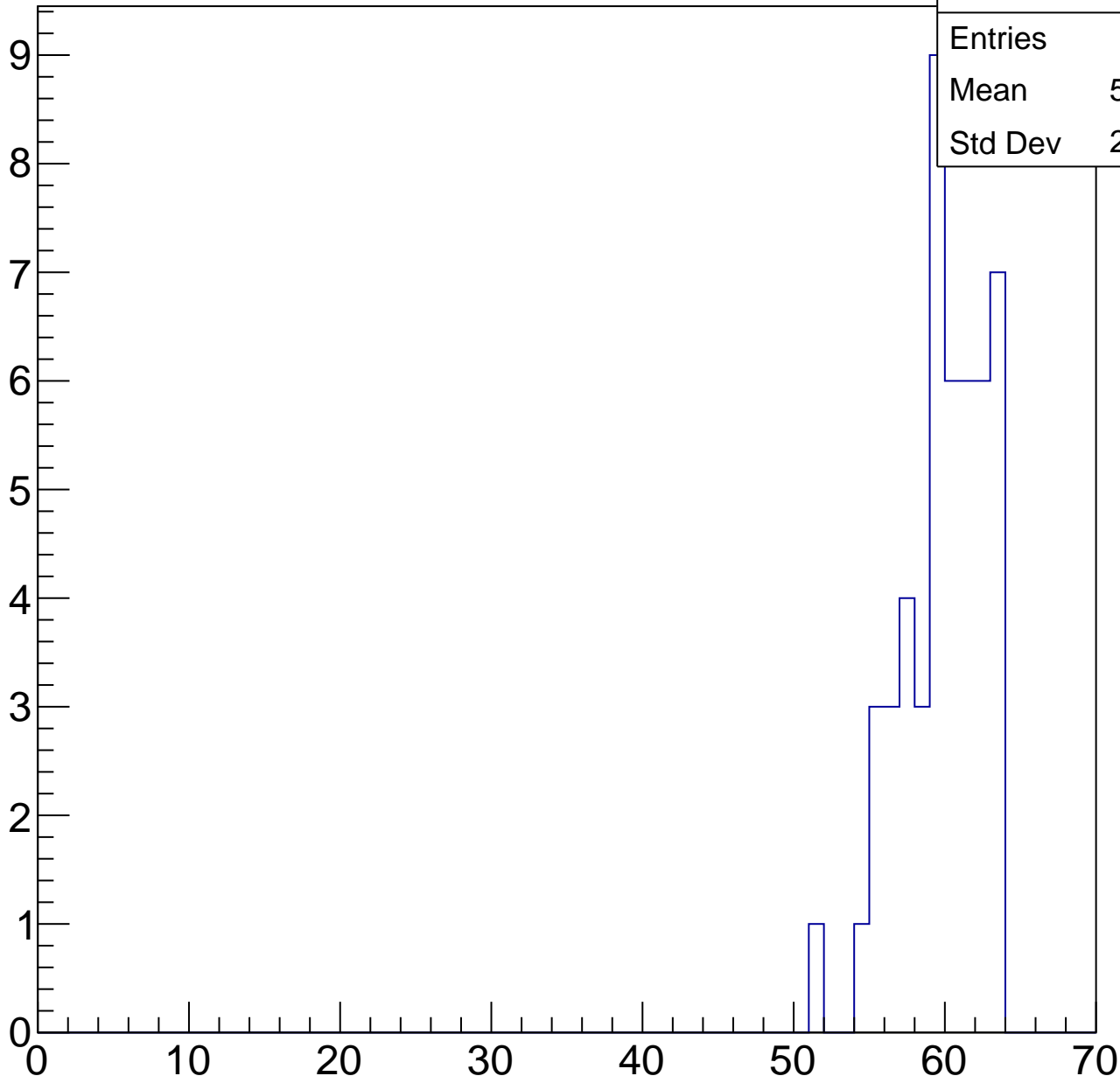
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 59.39 |
| Std Dev | 2.754 |

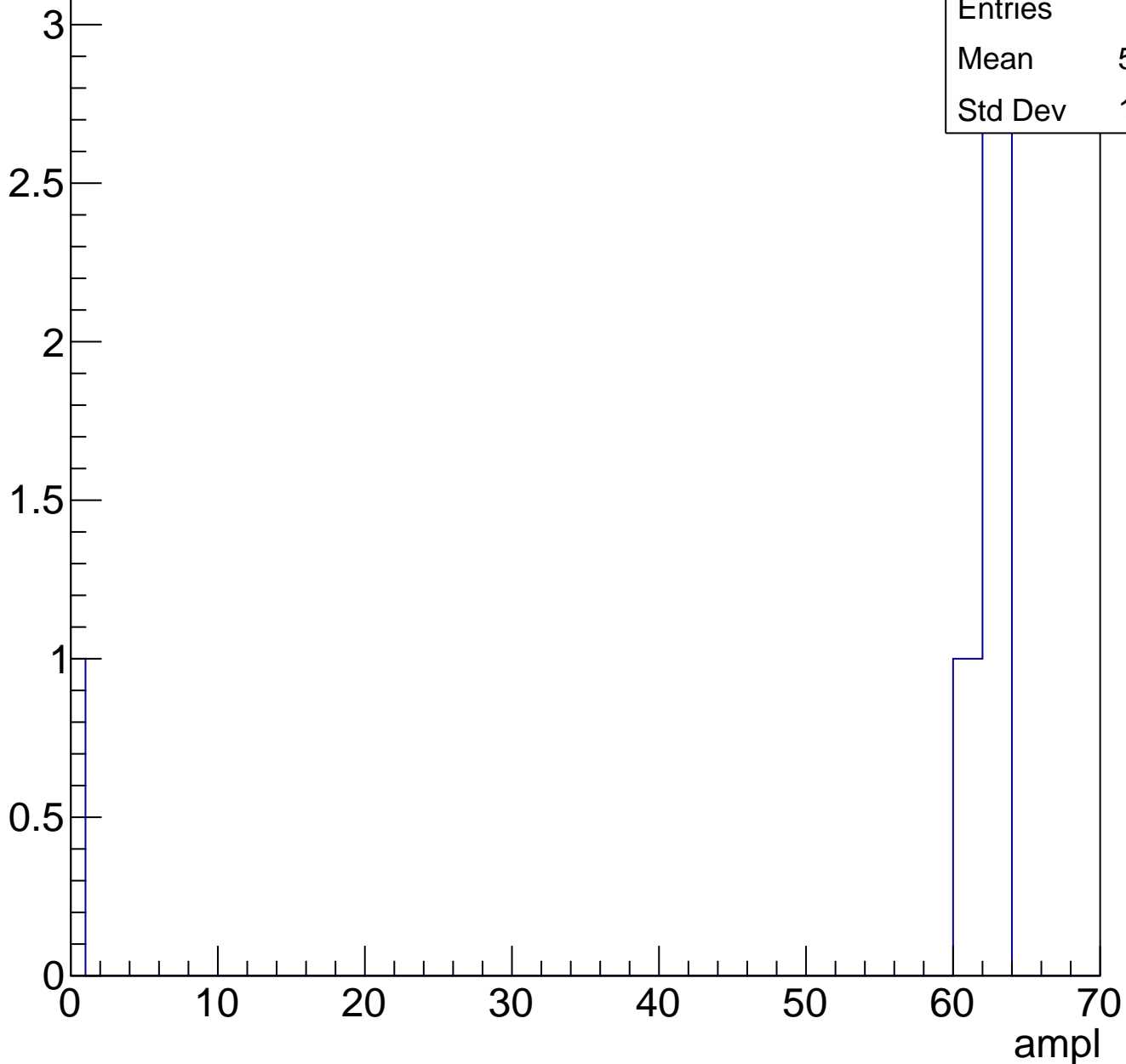
ampl



B1L100S, U7-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

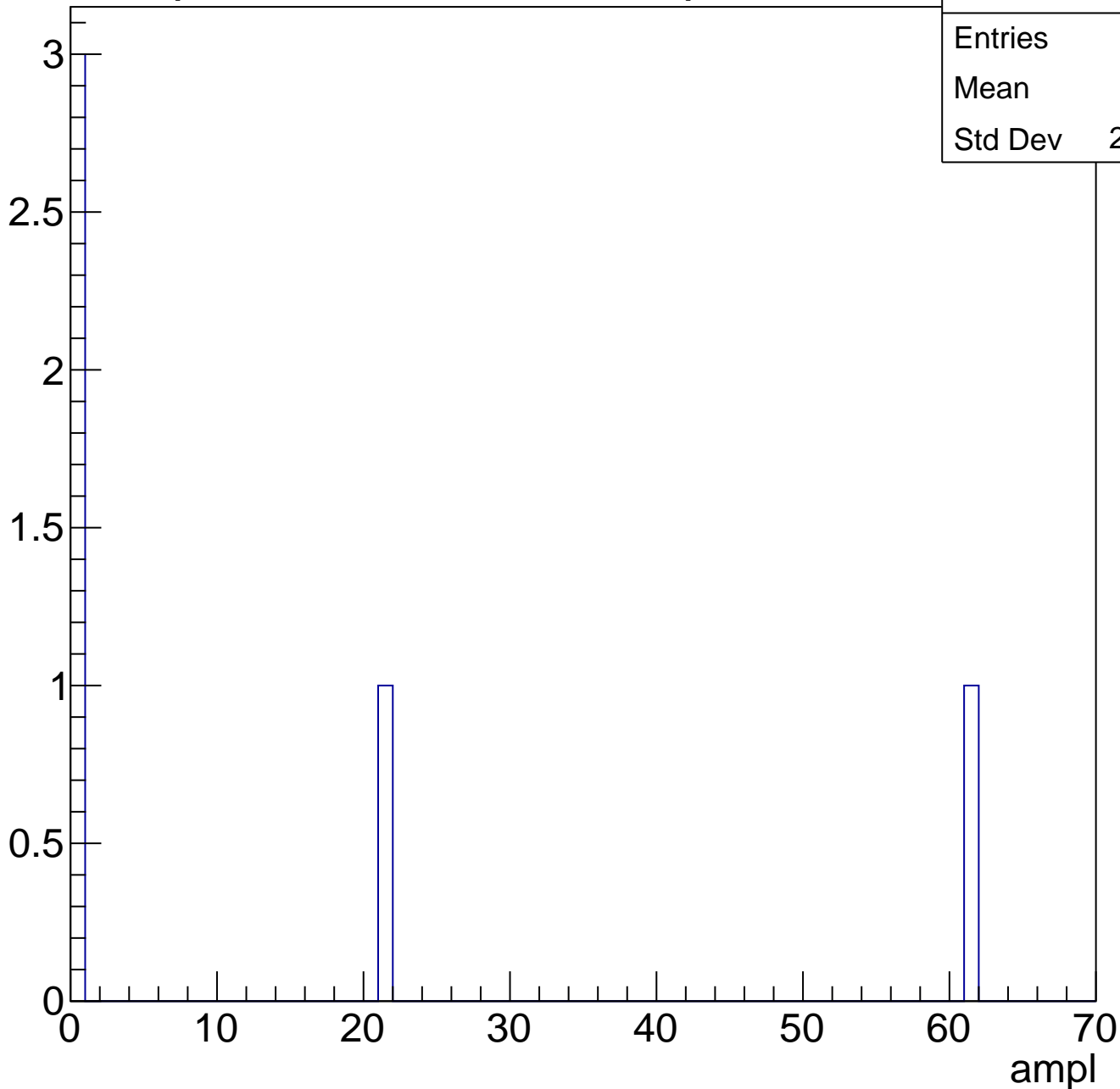
Entry



B1L100S, U7-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 5 |
| Mean | 16.4 |
| Std Dev | 23.74 |

B1L100S, U7-ch15, adc0

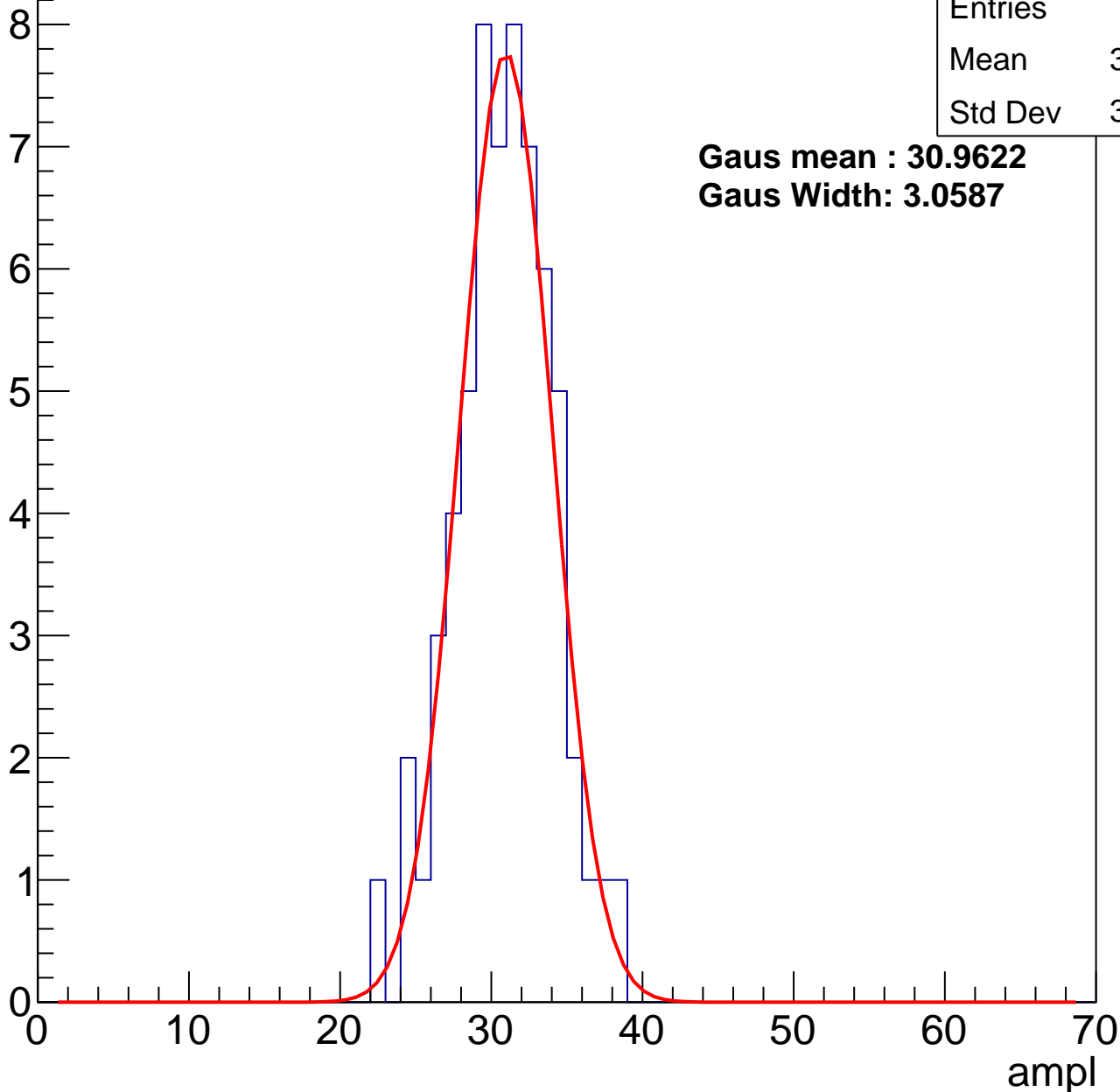
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.39 |
| Std Dev | 3.195 |

Gaus mean : 30.9622

Gaus Width: 3.0587



B1L100S, U7-ch15, adc1

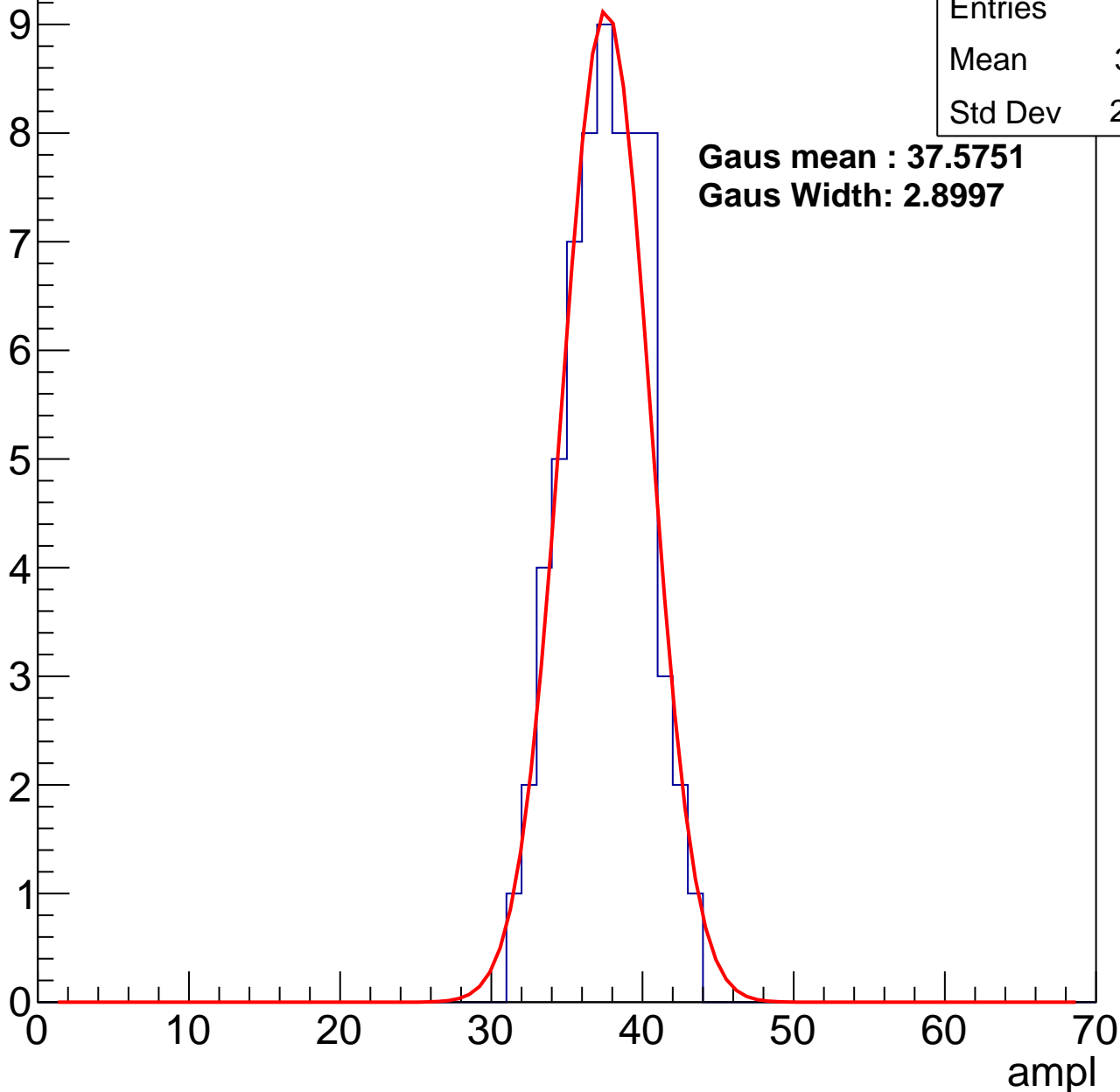
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 37.11 |
| Std Dev | 2.686 |

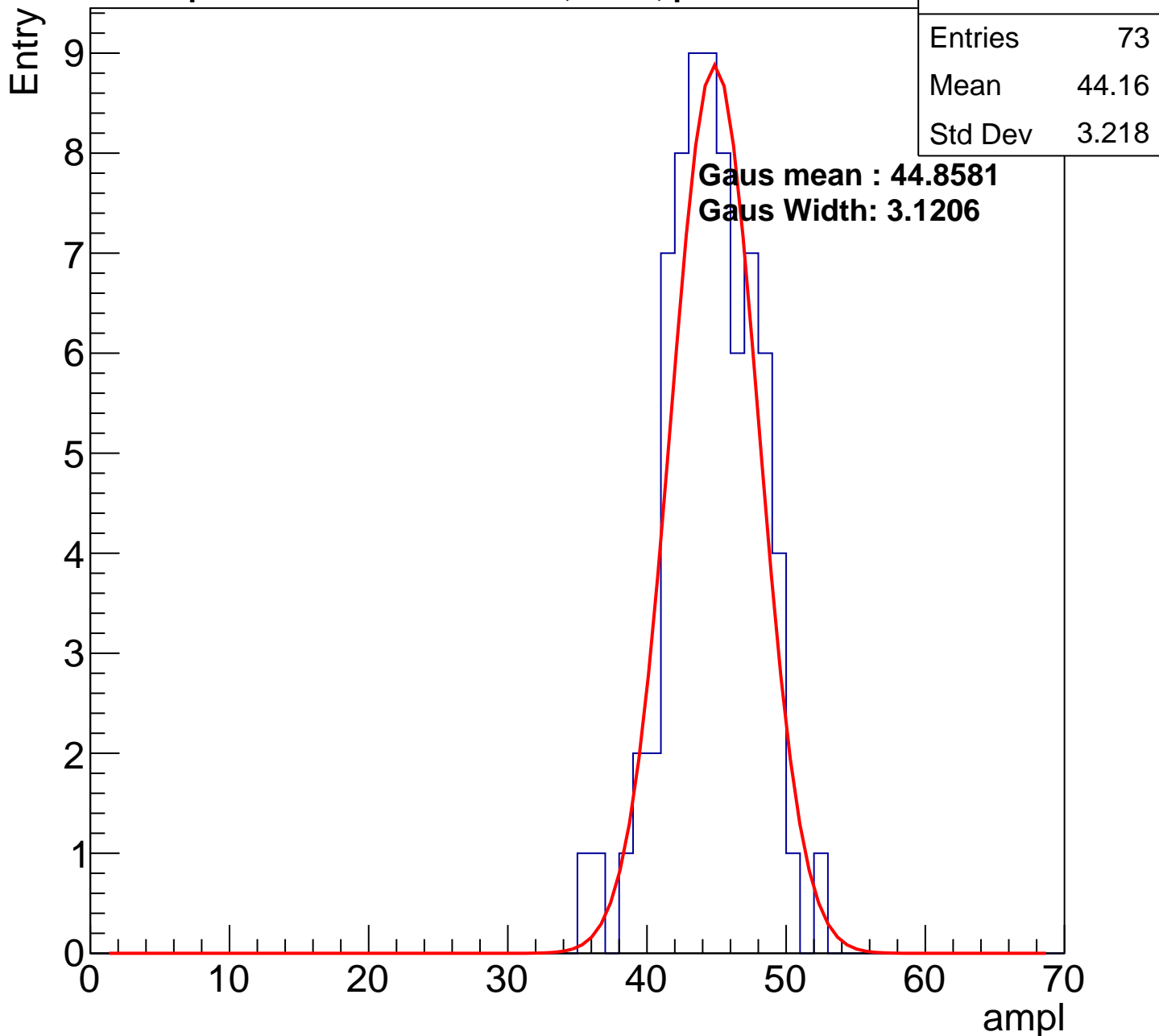
Gaus mean : 37.5751

Gaus Width: 2.8997



B1L100S, U7-ch15, adc2

calib_packv5_042523_0143.root, FC#4, port A2

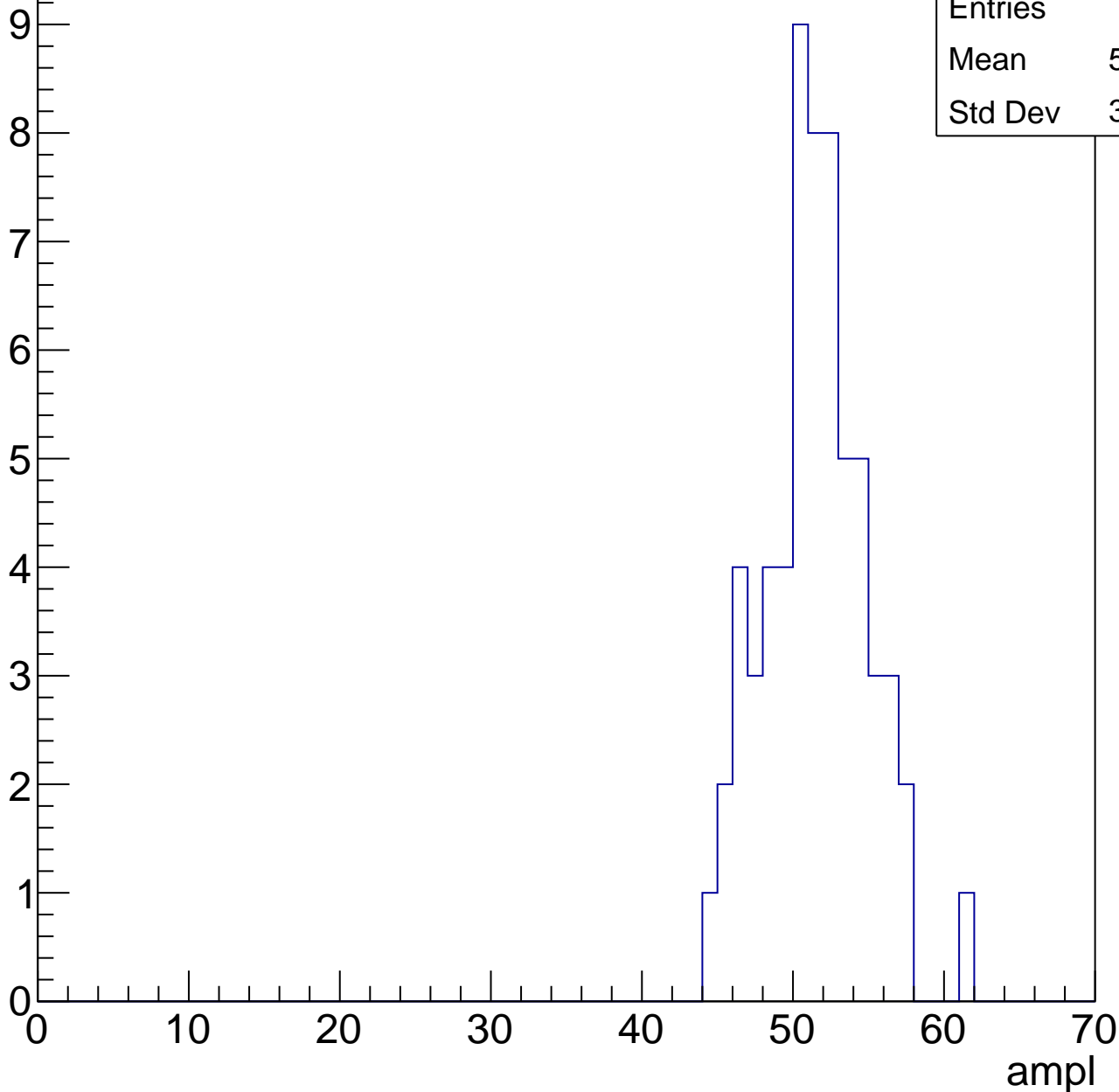


B1L100S, U7-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

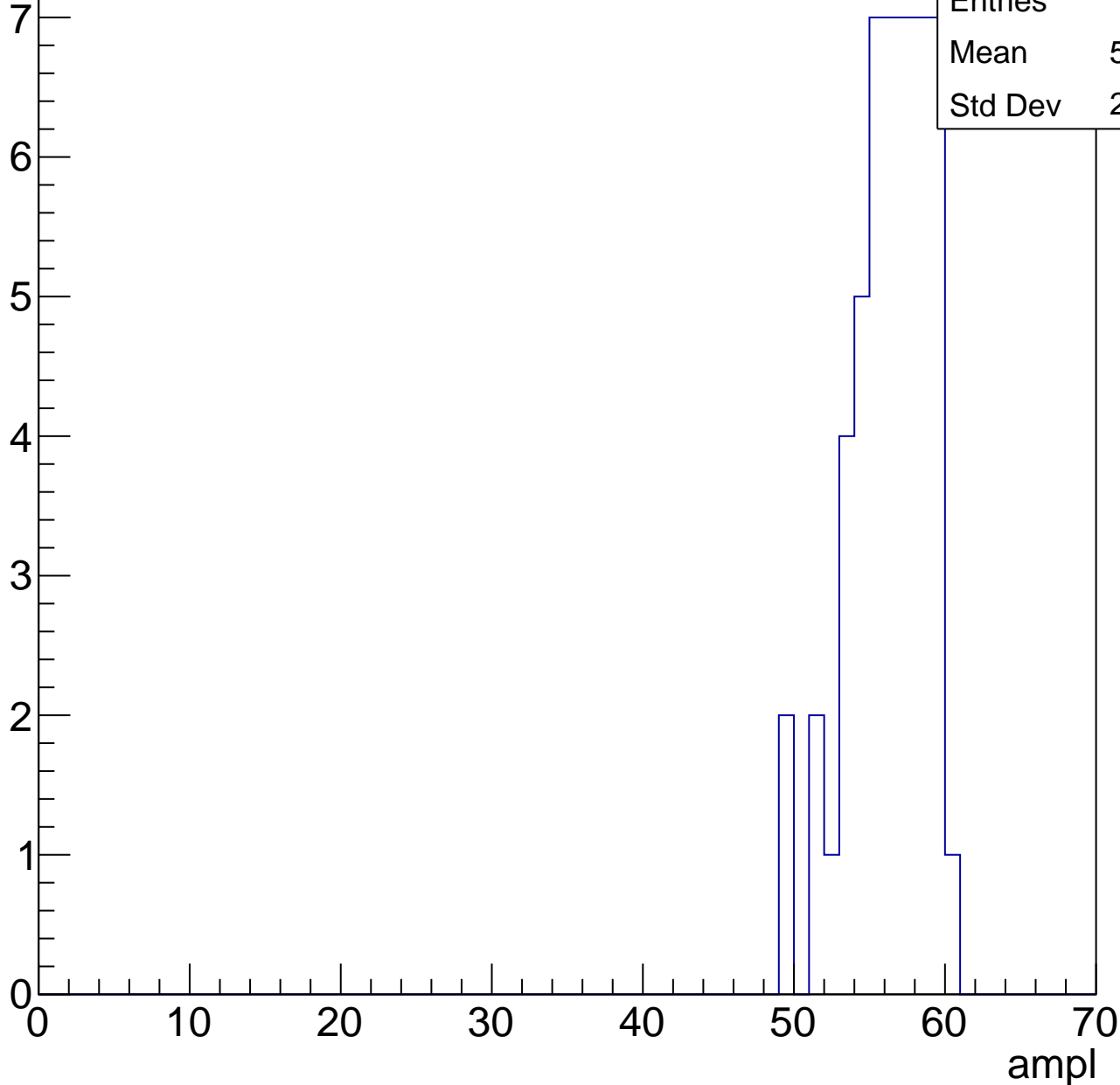
| | |
|---------|-------|
| Entries | 62 |
| Mean | 51.03 |
| Std Dev | 3.355 |



B1L100S, U7-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.33 |
| Std Dev | 9.363 |

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L100S, U7-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch16, adc0

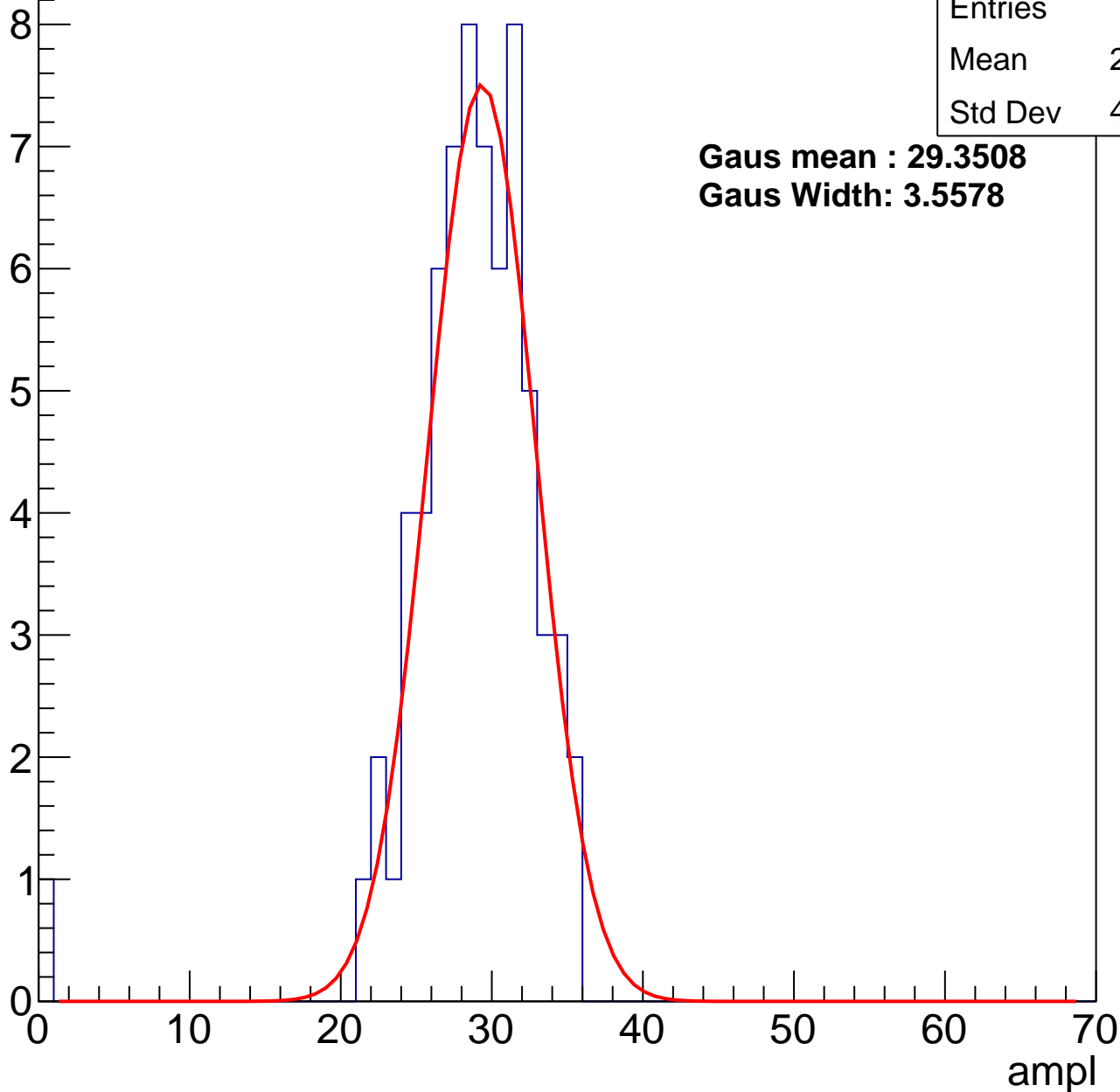
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.16 |
| Std Dev | 4.733 |

Gaus mean : 29.3508

Gaus Width: 3.5578



B1L100S, U7-ch16, adc1

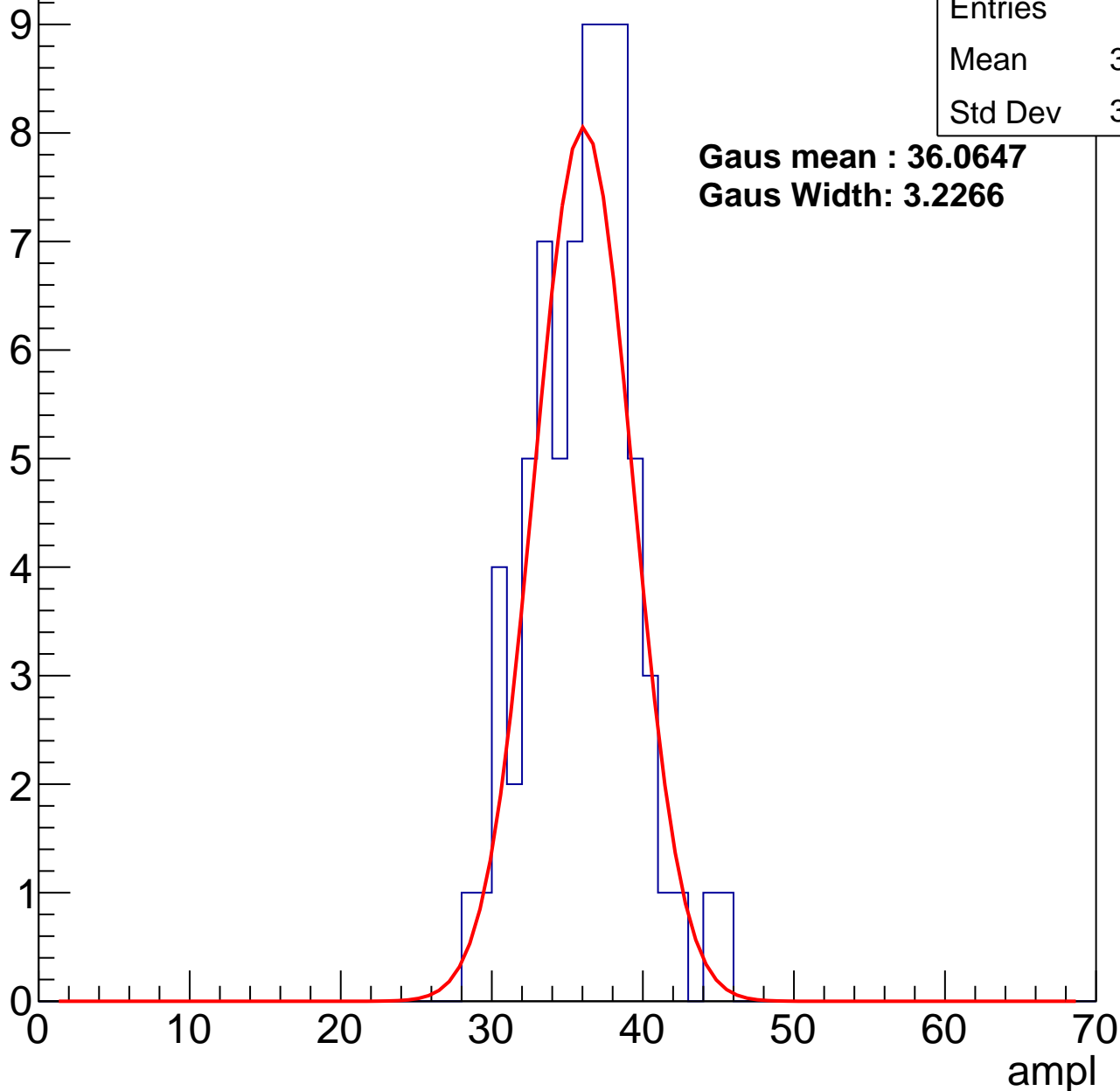
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.65 |
| Std Dev | 3.382 |

Gaus mean : 36.0647

Gaus Width: 3.2266



B1L100S, U7-ch16, adc2

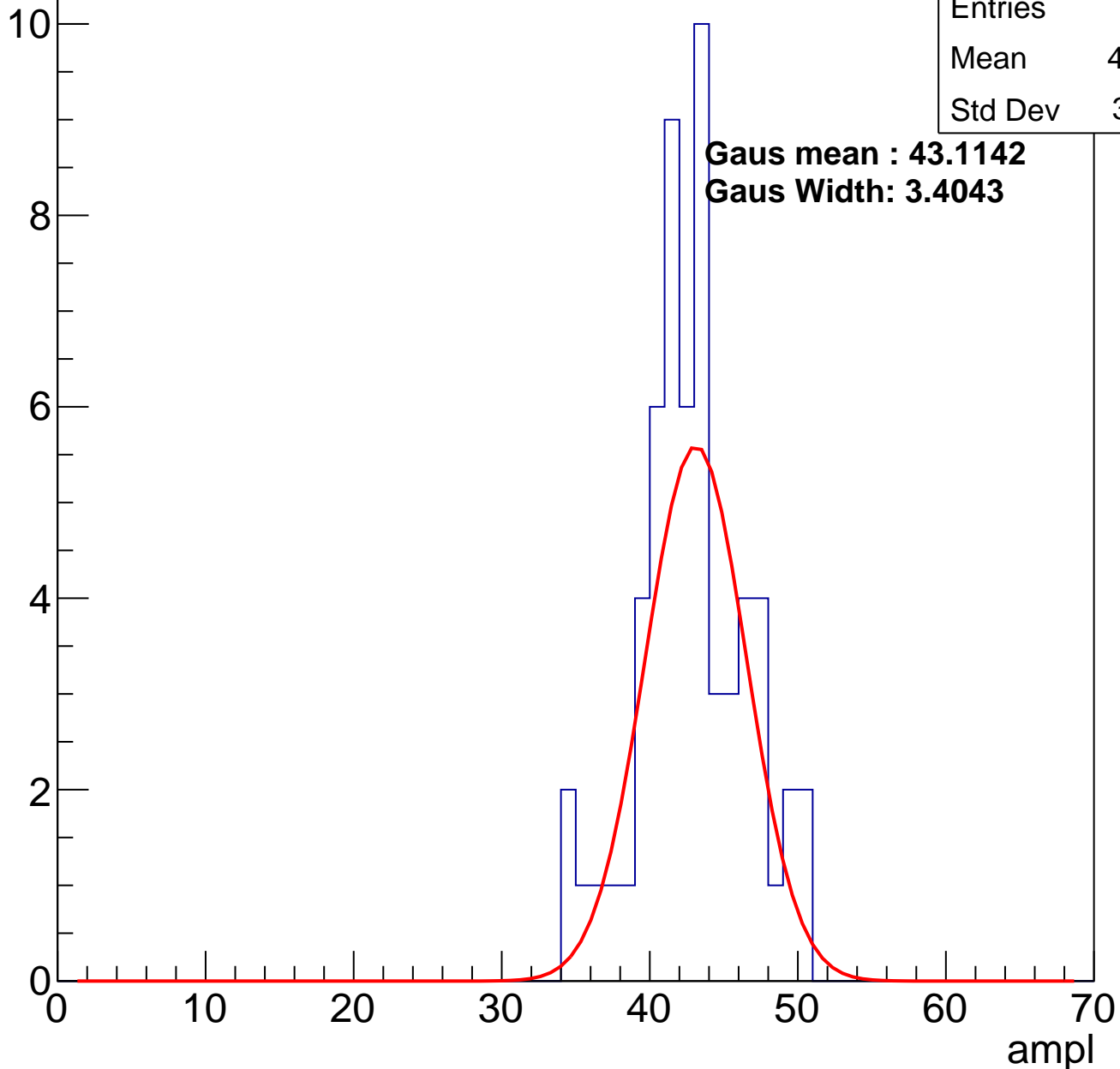
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.43 |
| Std Dev | 3.621 |

Gaus mean : 43.1142

Gaus Width: 3.4043

Entry

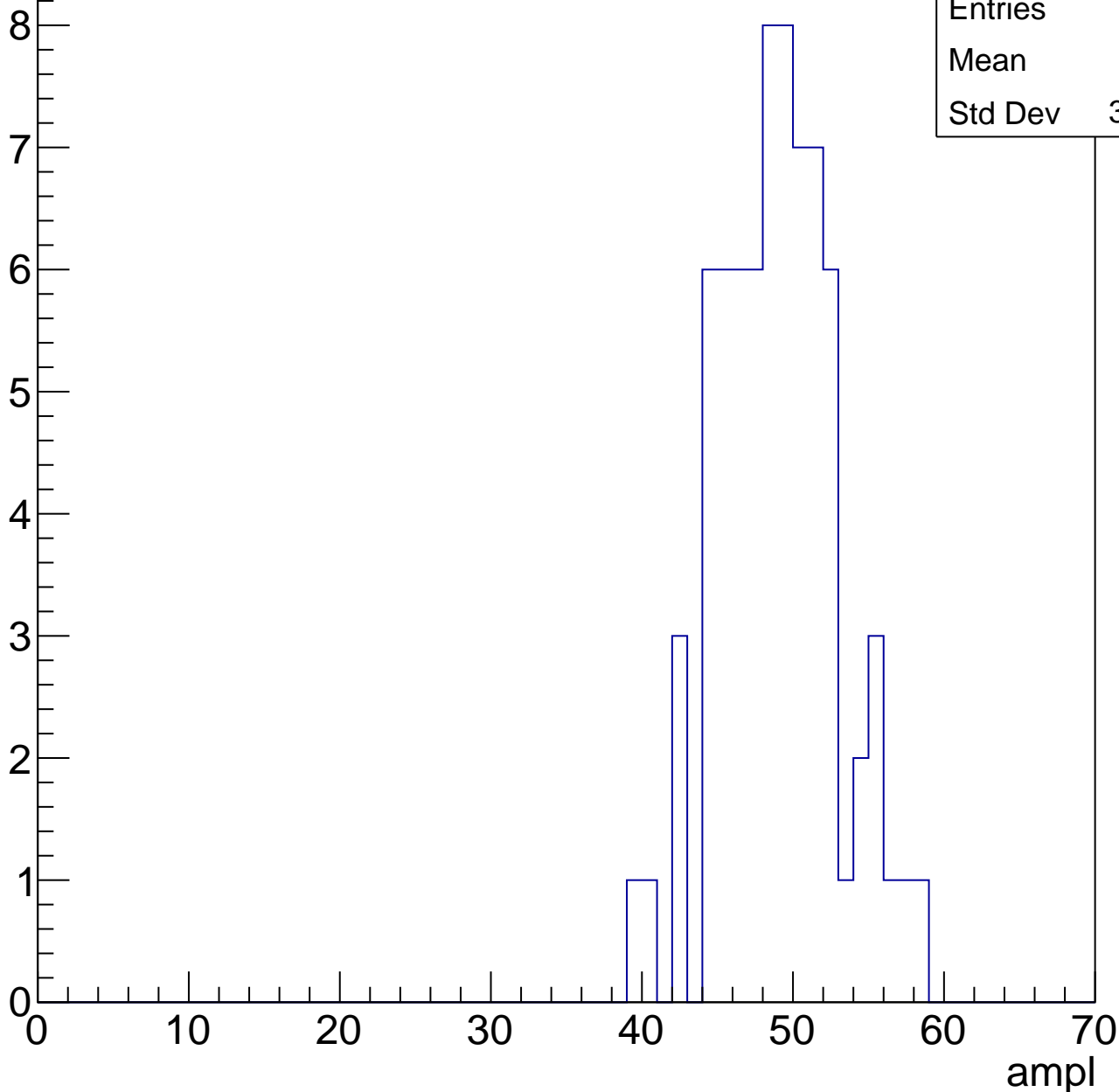


B1L100S, U7-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 48.5 |
| Std Dev | 3.853 |

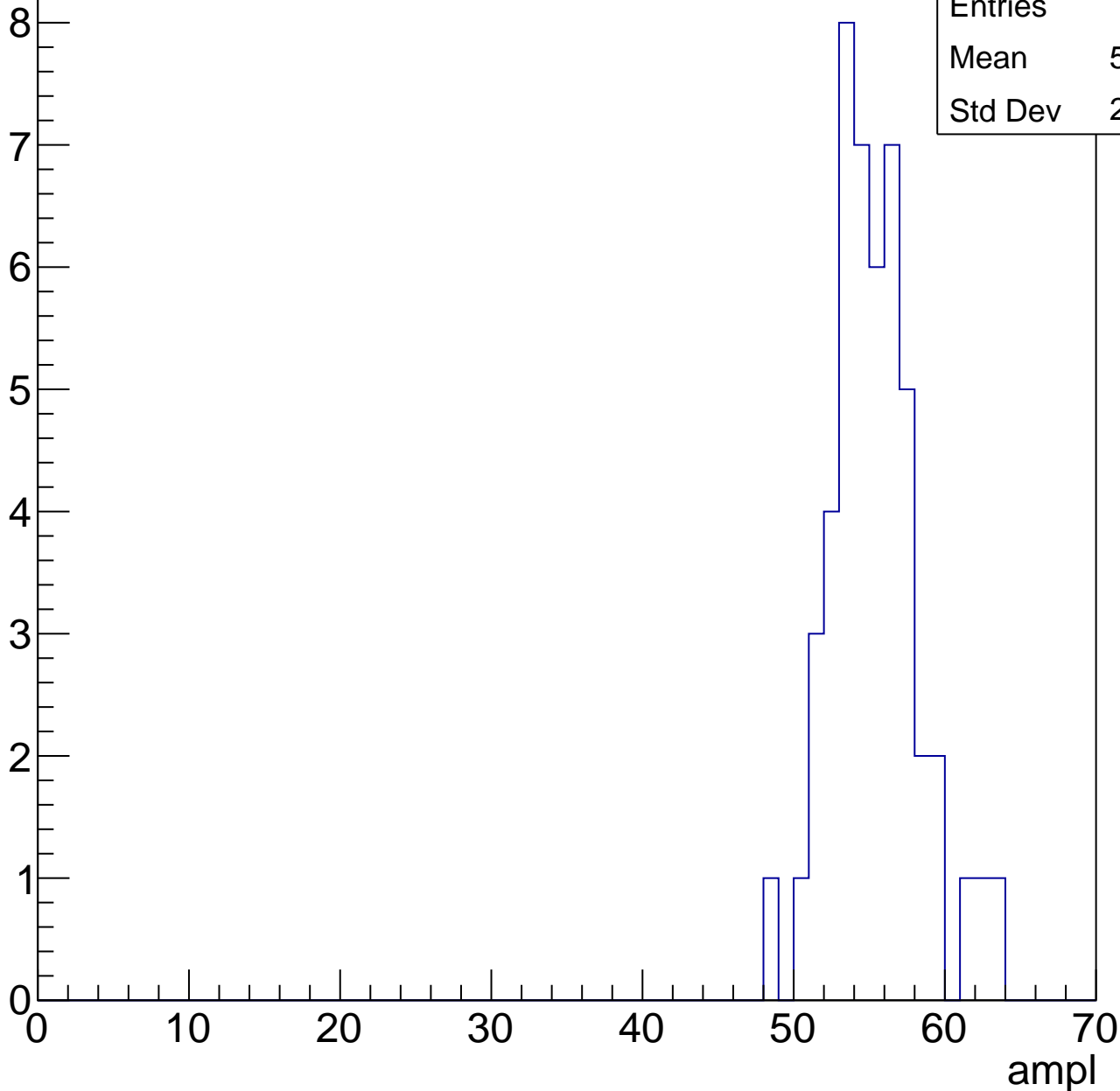


B1L100S, U7-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 54.86 |
| Std Dev | 2.935 |



B1L100S, U7-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

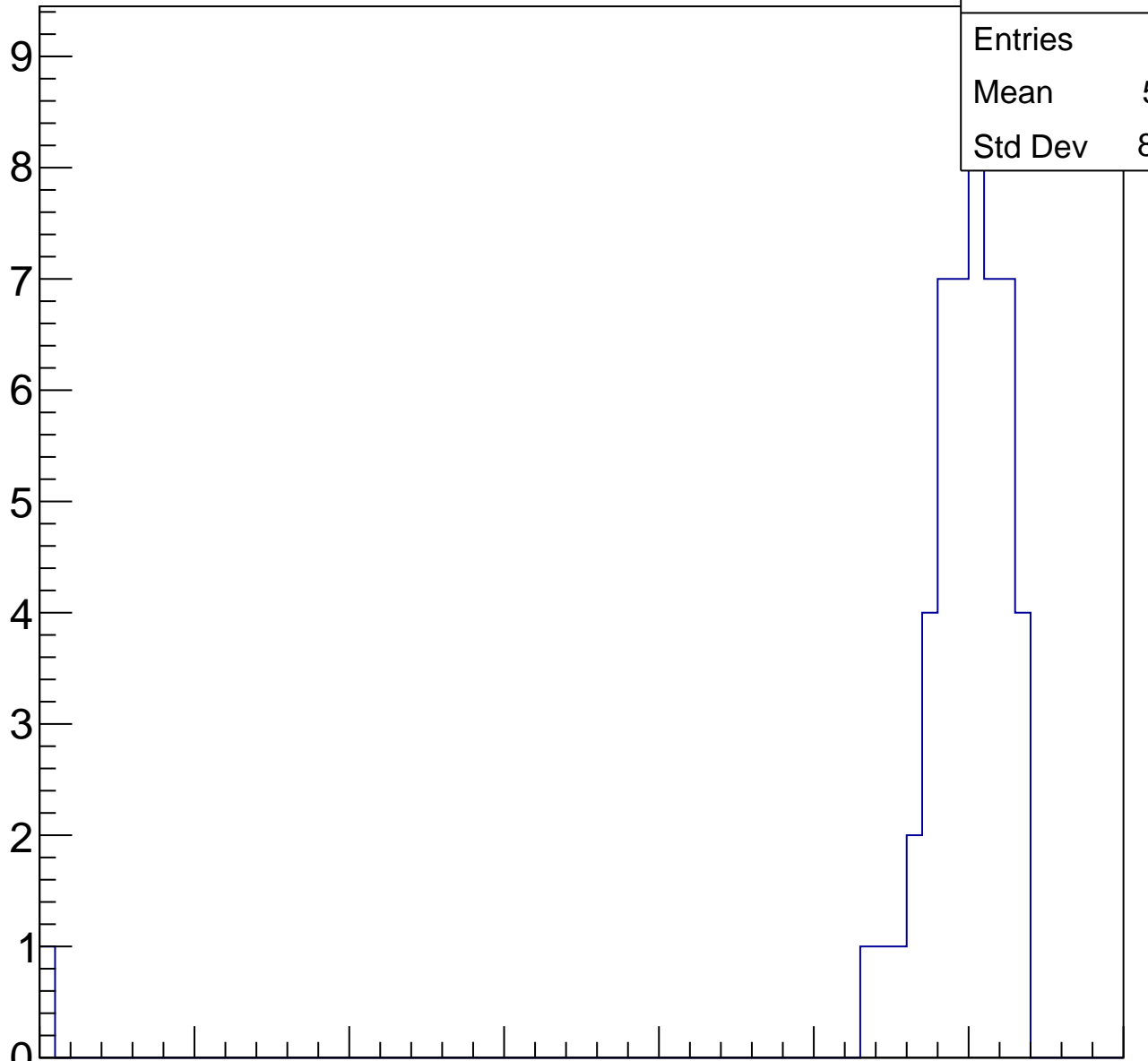
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.31 |
| Std Dev | 8.562 |

ampl

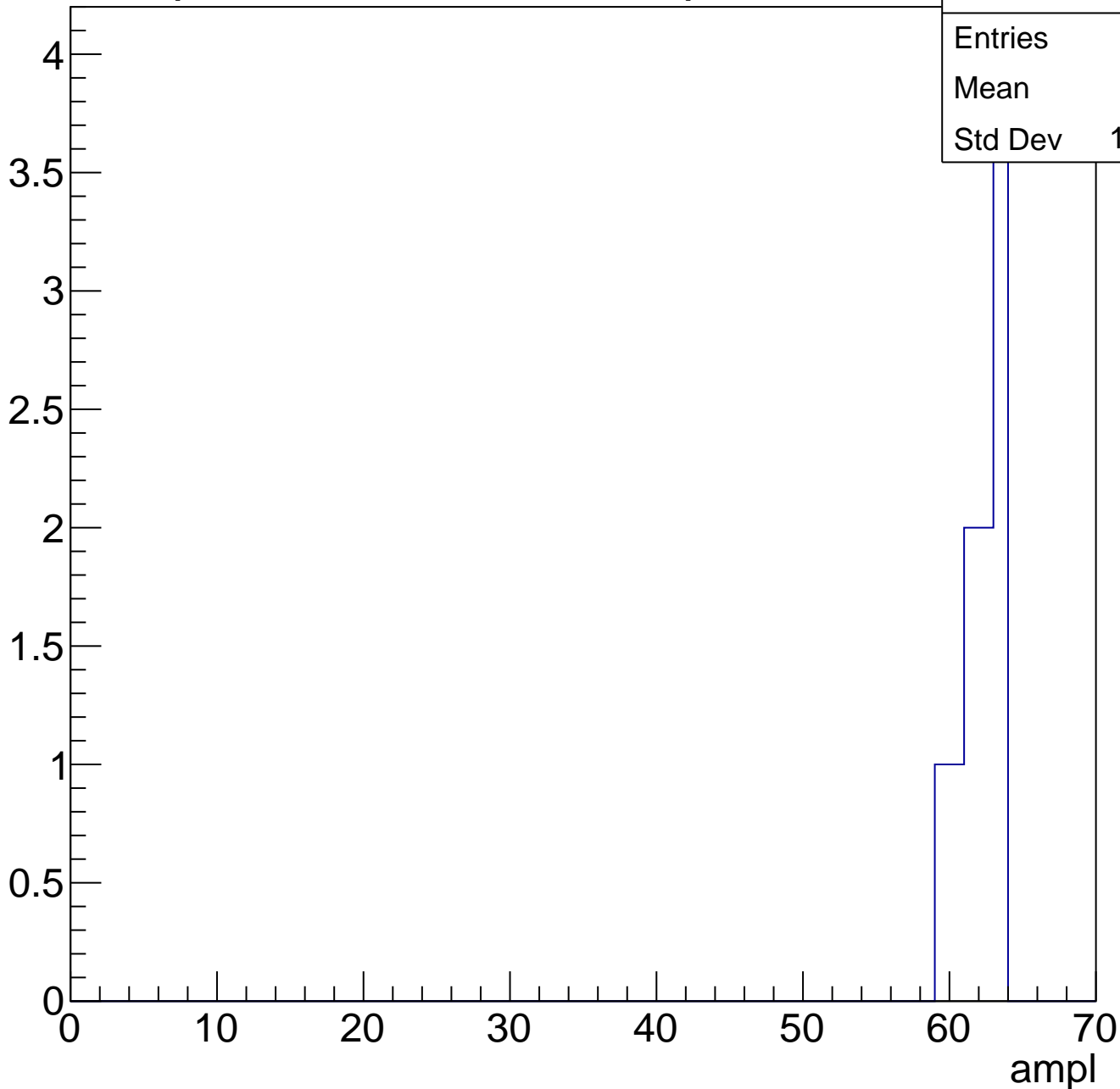
0 10 20 30 40 50 60 70



B1L100S, U7-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 10 |
| Mean | 61.7 |
| Std Dev | 1.345 |

B1L100S, U7-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch17, adc0

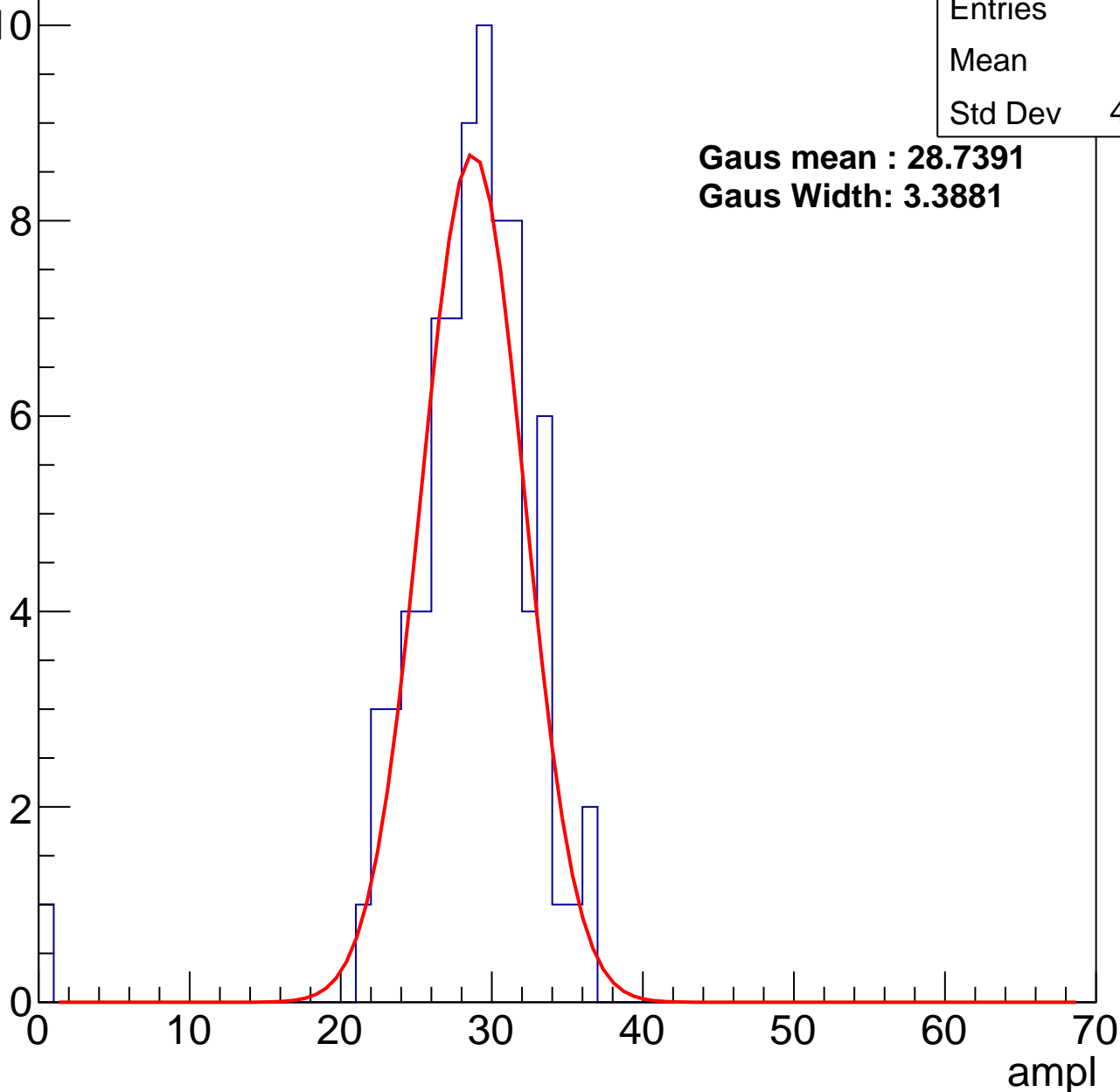
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.1 |
| Std Dev | 4.627 |

Gaus mean : 28.7391

Gaus Width: 3.3881



B1L100S, U7-ch17, adc1

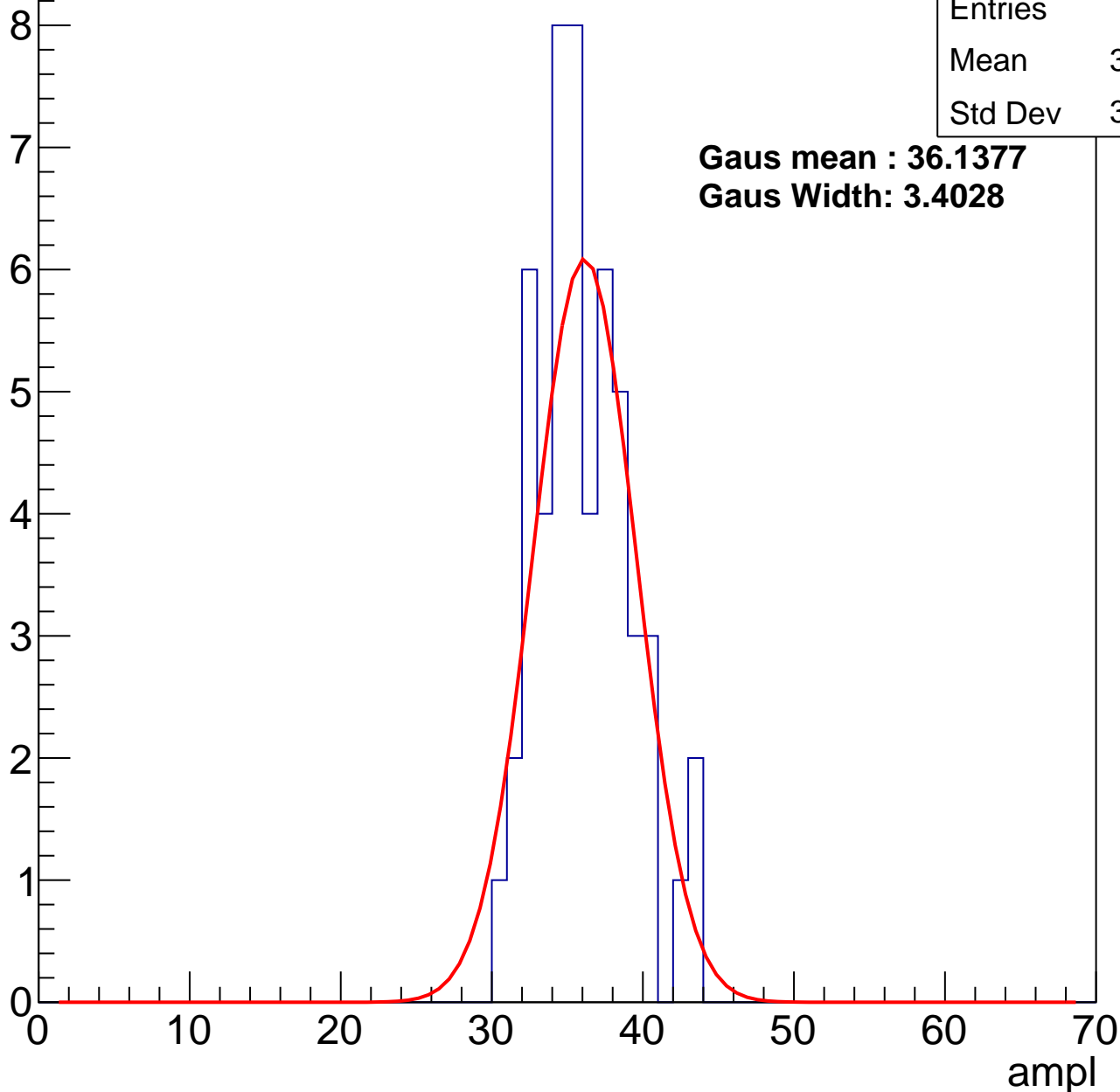
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 35.64 |
| Std Dev | 3.029 |

Gaus mean : 36.1377

Gaus Width: 3.4028



B1L100S, U7-ch17, adc2

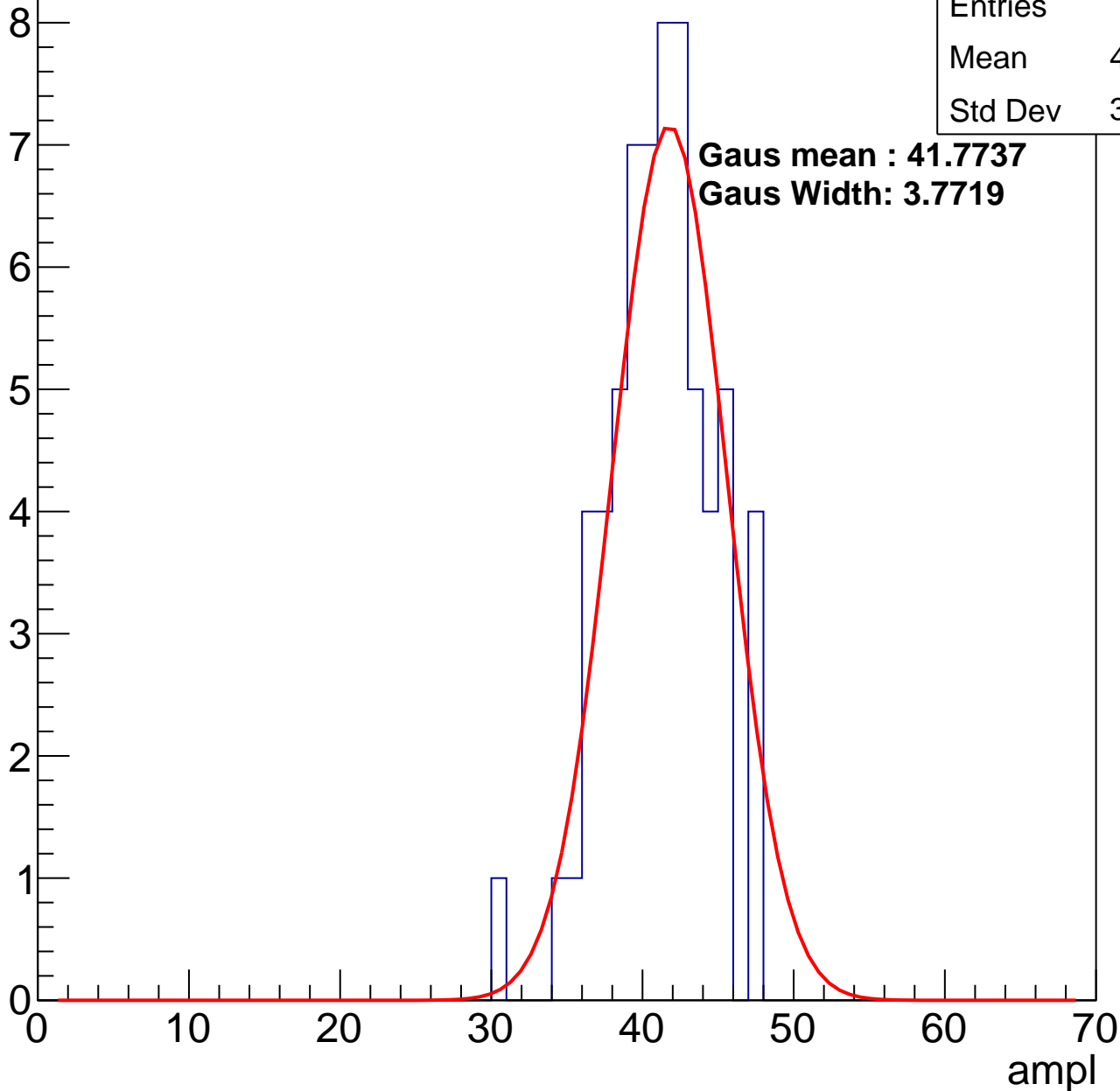
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 40.66 |
| Std Dev | 3.378 |

Gaus mean : 41.7737

Gaus Width: 3.7719



B1L100S, U7-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

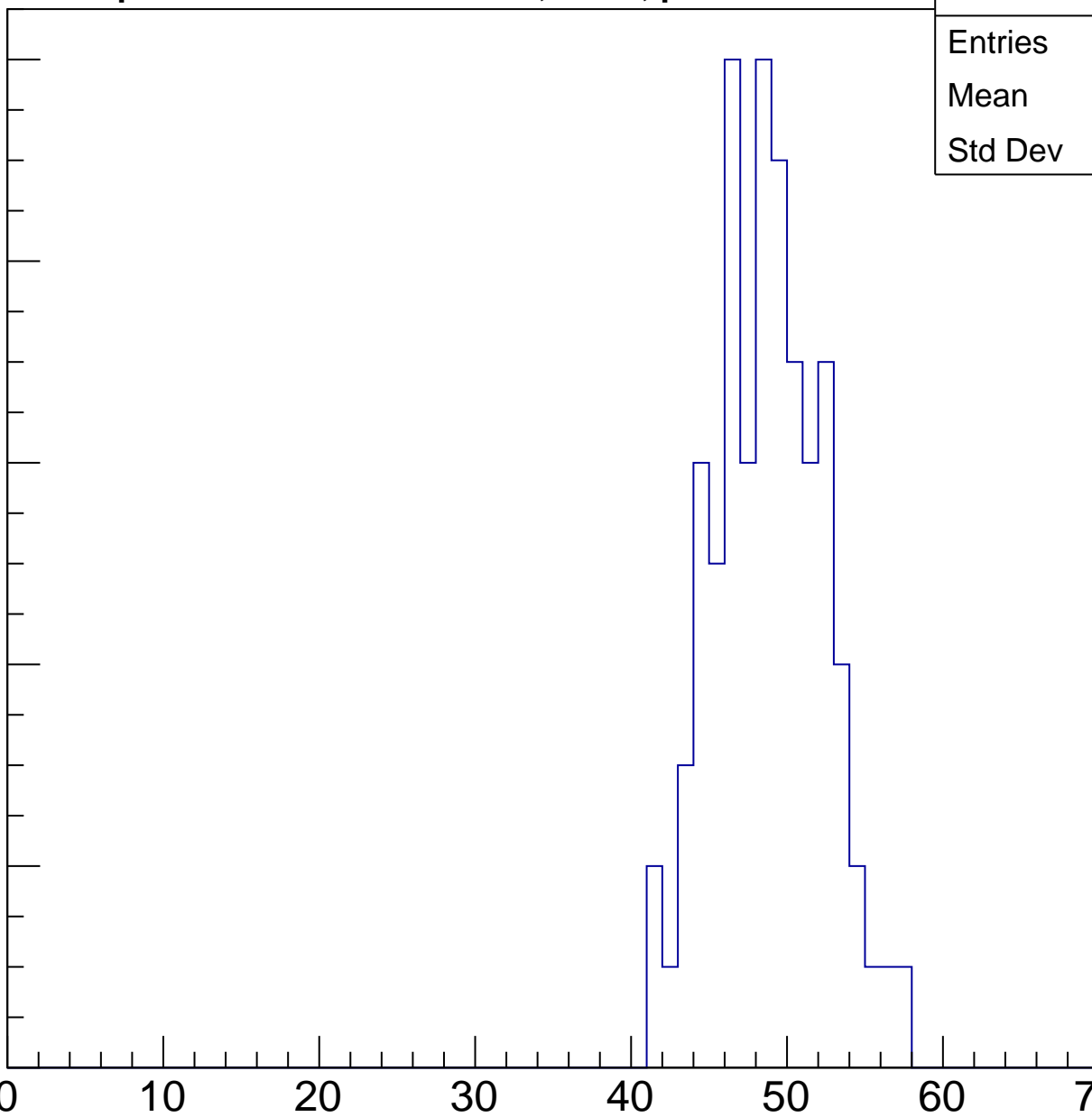
| | |
|---------|-------|
| Entries | 81 |
| Mean | 48.31 |
| Std Dev | 3.438 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 55

Mean 55.22

Std Dev 3.251

0

1

2

3

4

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323

324</

B1L100S, U7-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

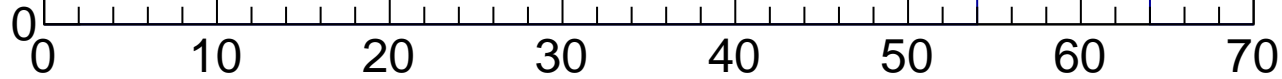
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.65 |
| Std Dev | 2.35 |

ampl

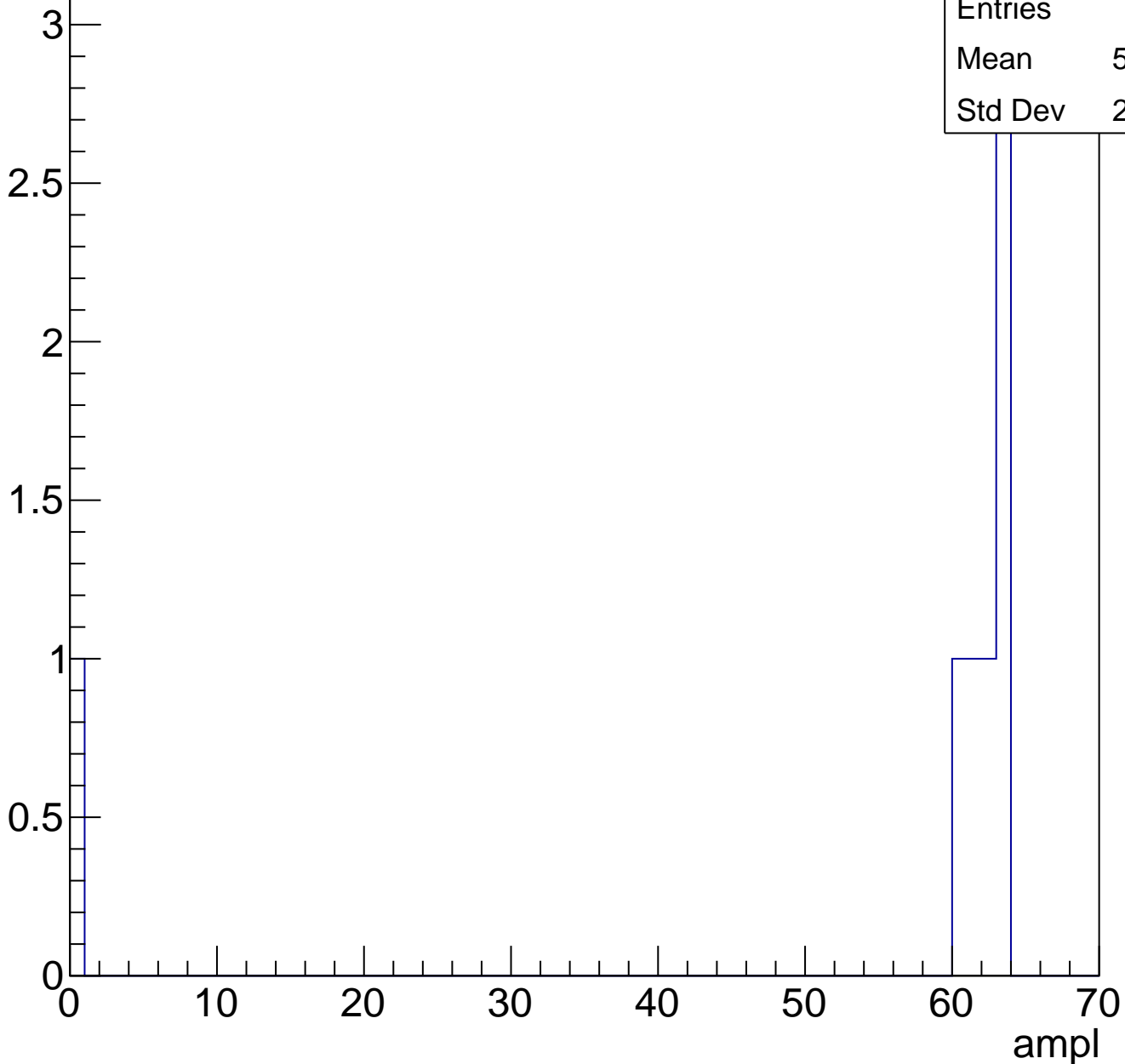
0 10 20 30 40 50 60 70



B1L100S, U7-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

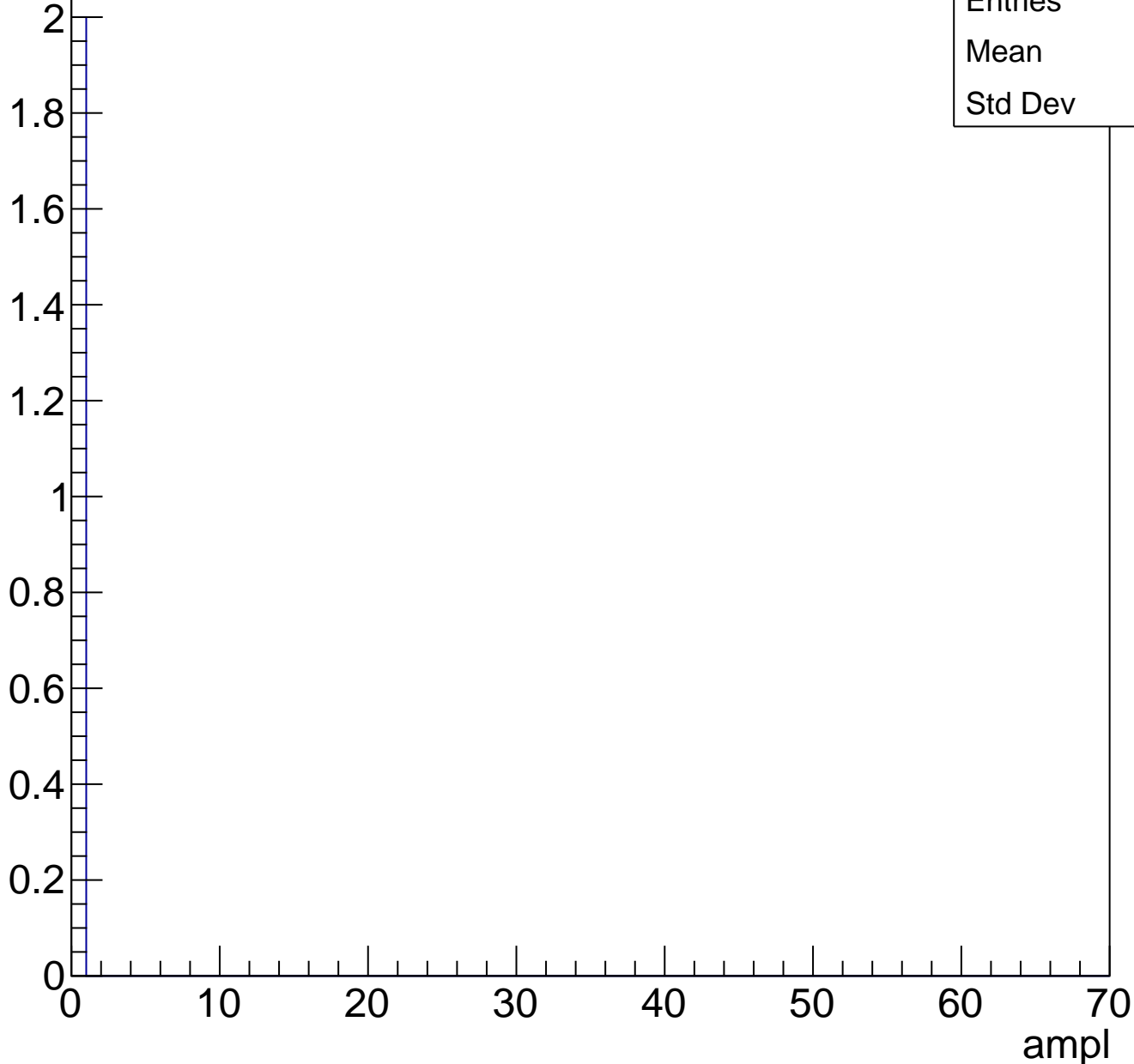
Entry



B1L100S, U7-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch18, adc0

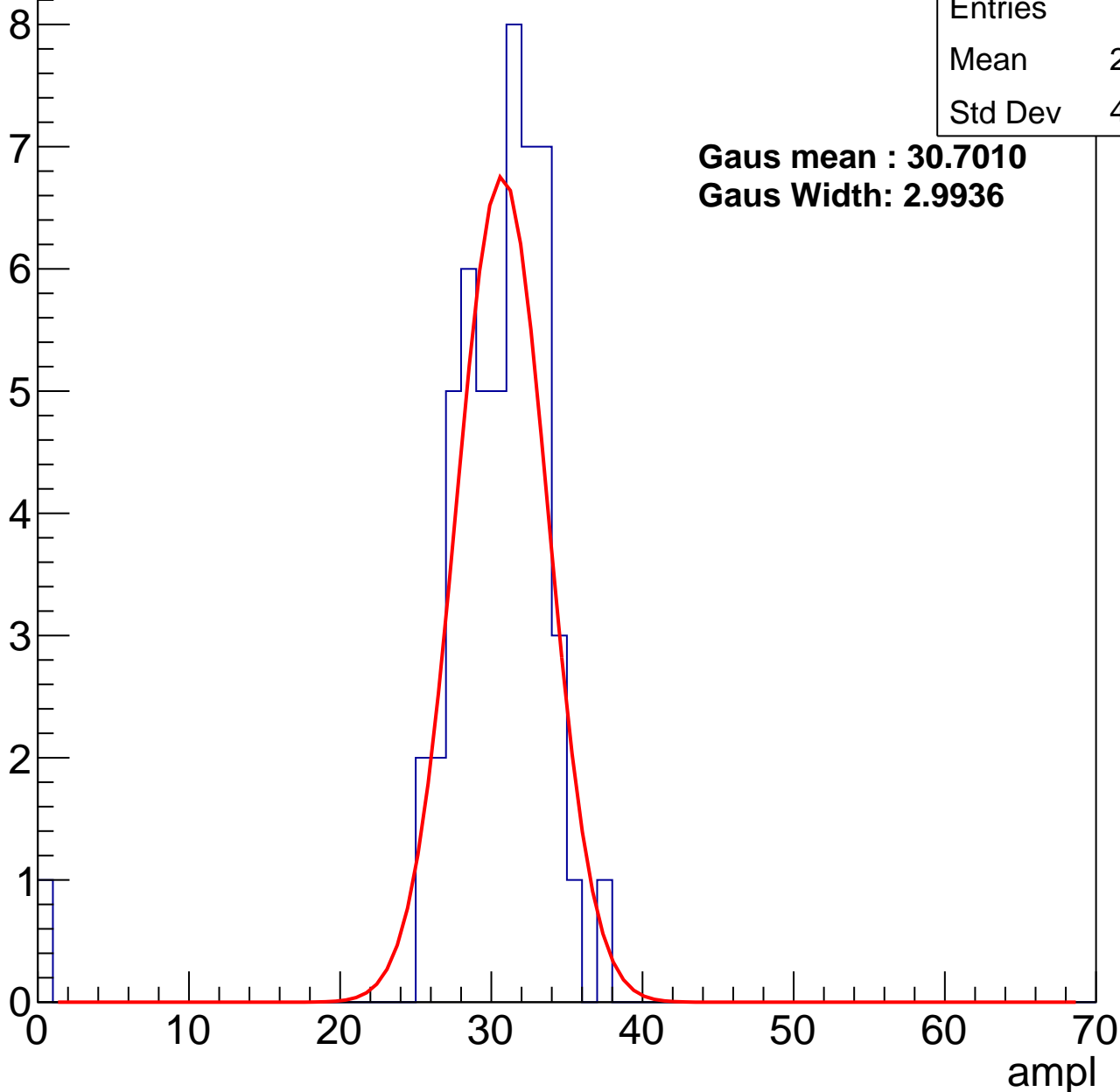
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.75 |
| Std Dev | 4.902 |

Gaus mean : 30.7010

Gaus Width: 2.9936



B1L100S, U7-ch18, adc1

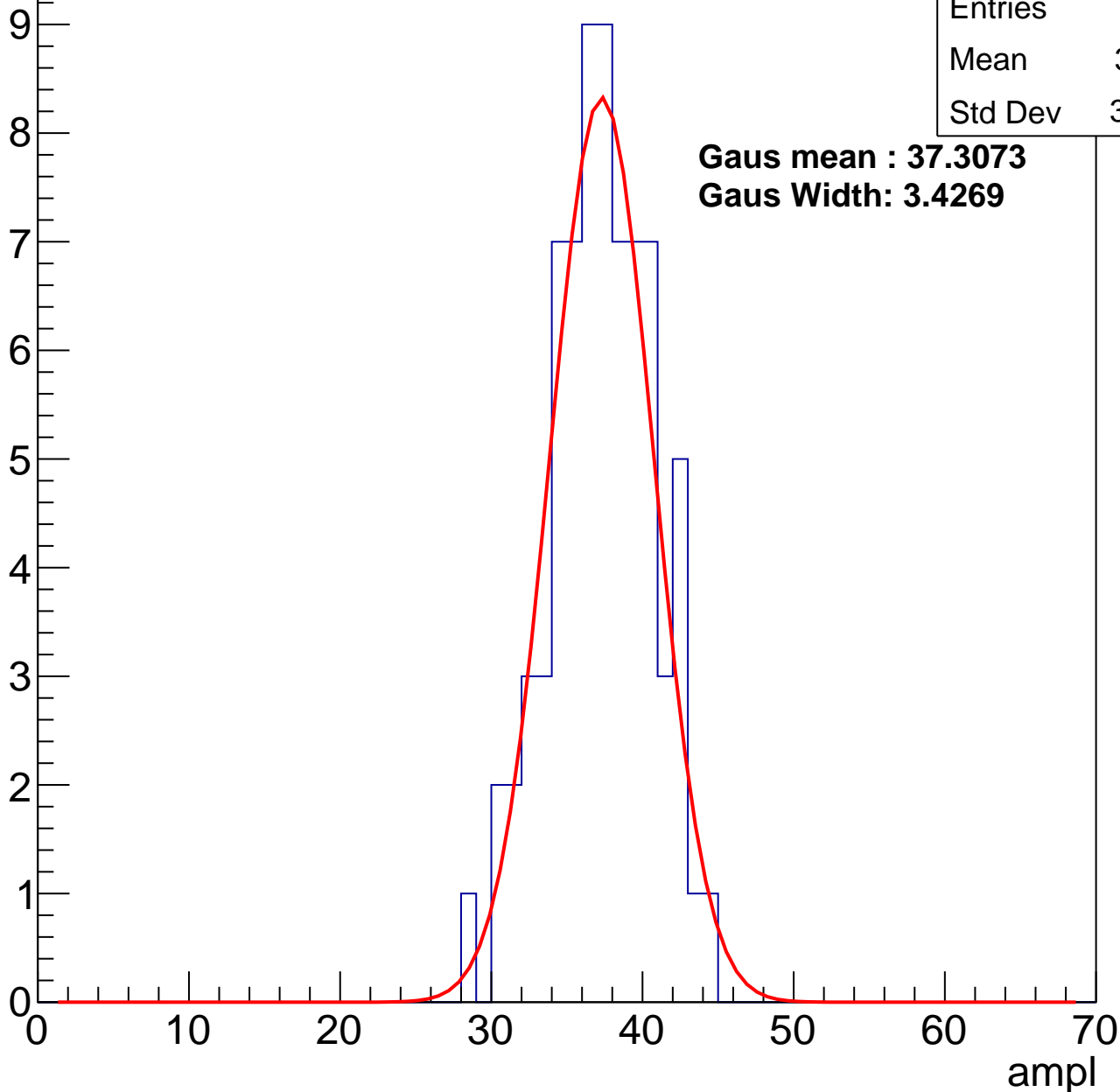
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.81 |
| Std Dev | 3.344 |

Gaus mean : 37.3073

Gaus Width: 3.4269



B1L100S, U7-ch18, adc2

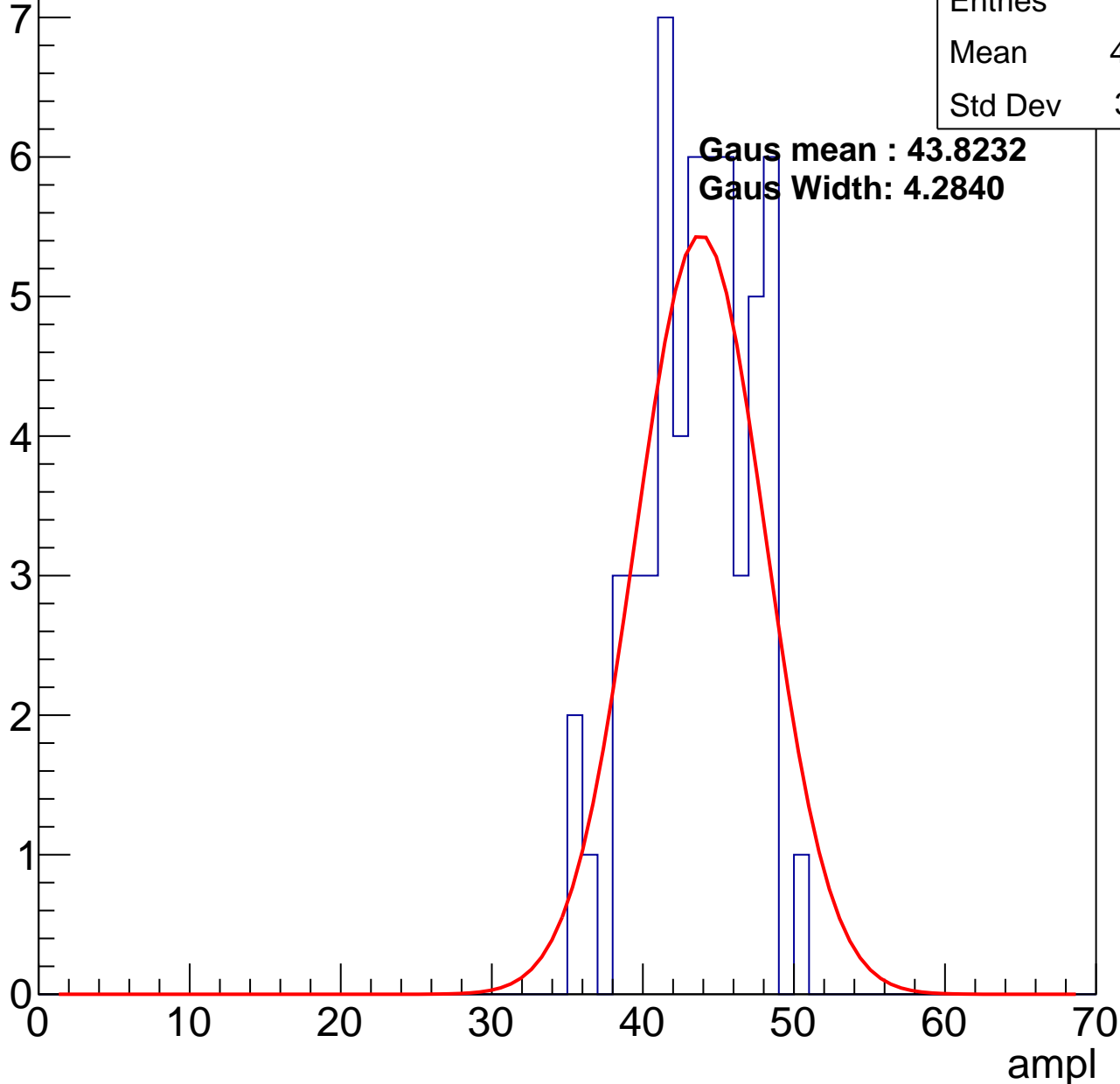
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43.12 |
| Std Dev | 3.521 |

Gaus mean : 43.8232

Gaus Width: 4.2840

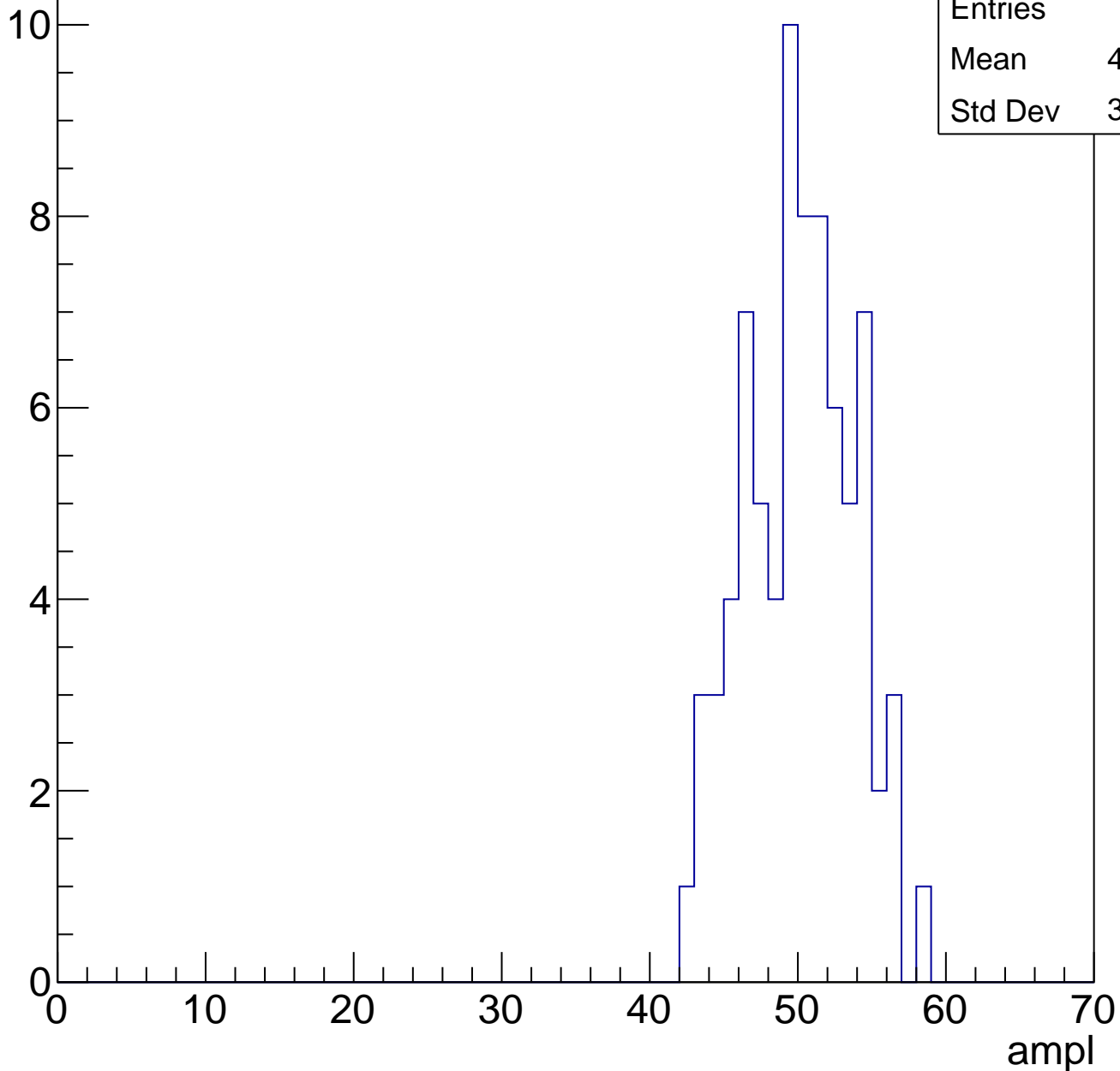


B1L100S, U7-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 49.62 |
| Std Dev | 3.607 |

Entry

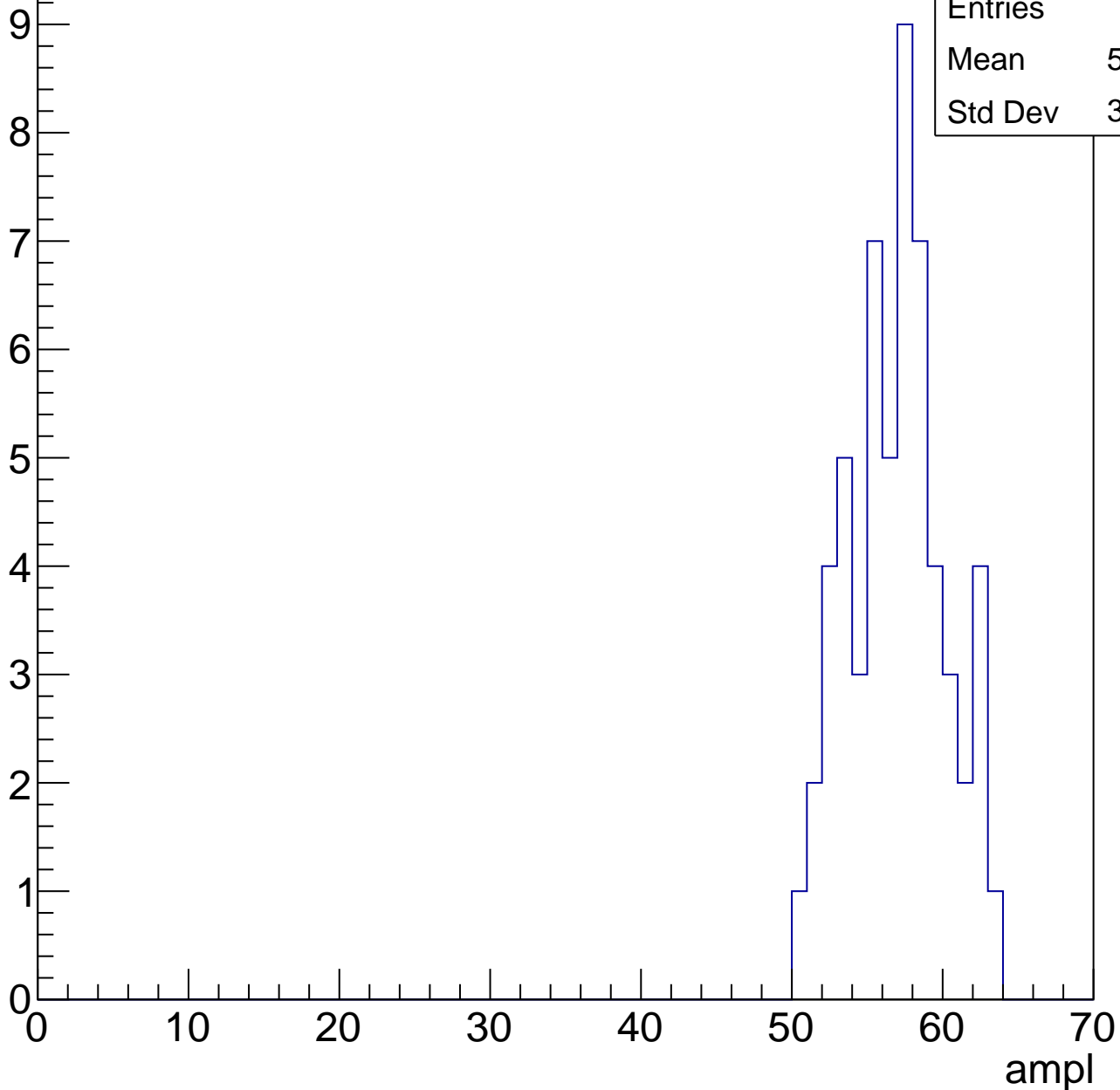


B1L100S, U7-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

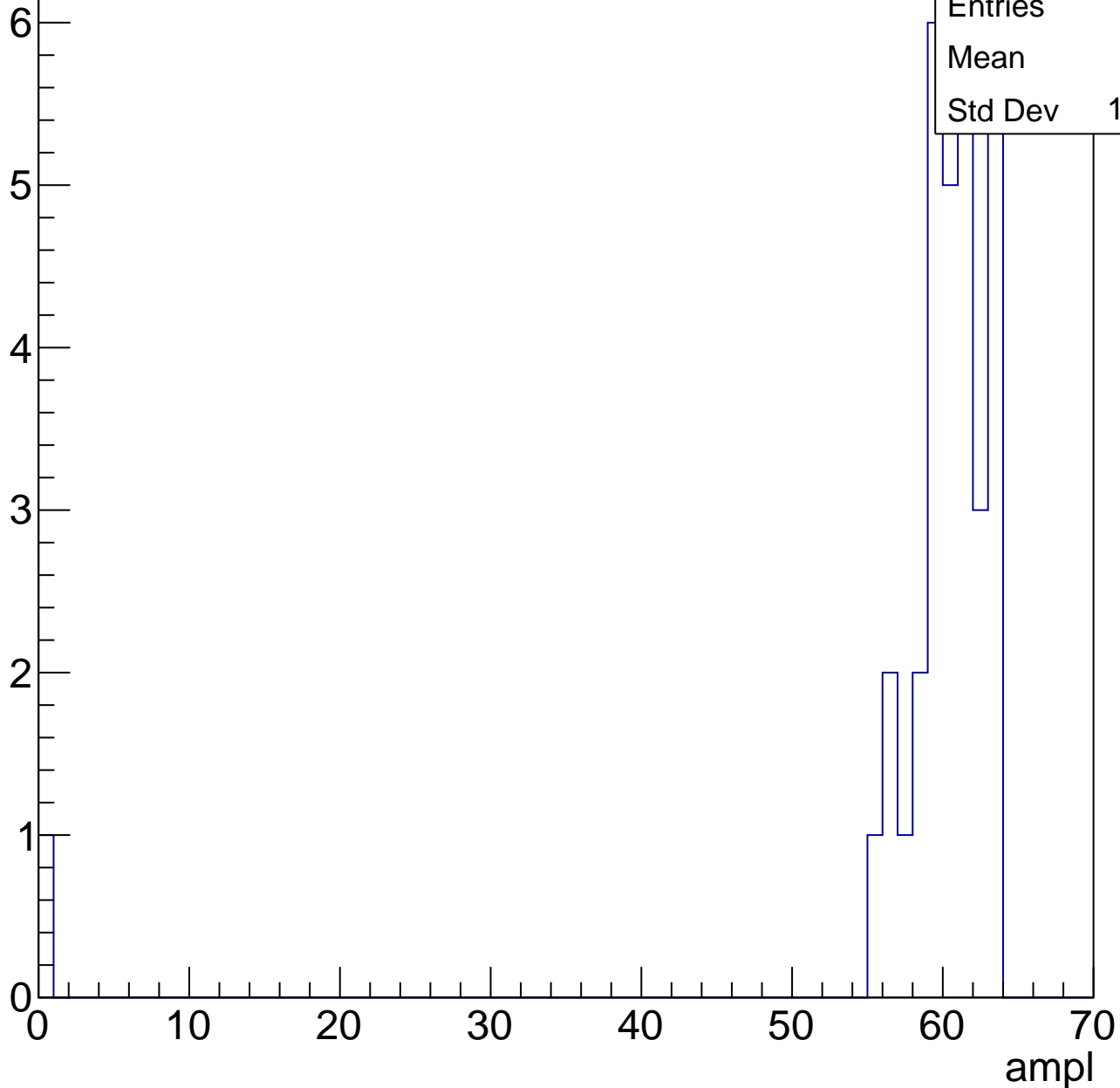
| | |
|---------|-------|
| Entries | 57 |
| Mean | 56.49 |
| Std Dev | 3.146 |



B1L100S, U7-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

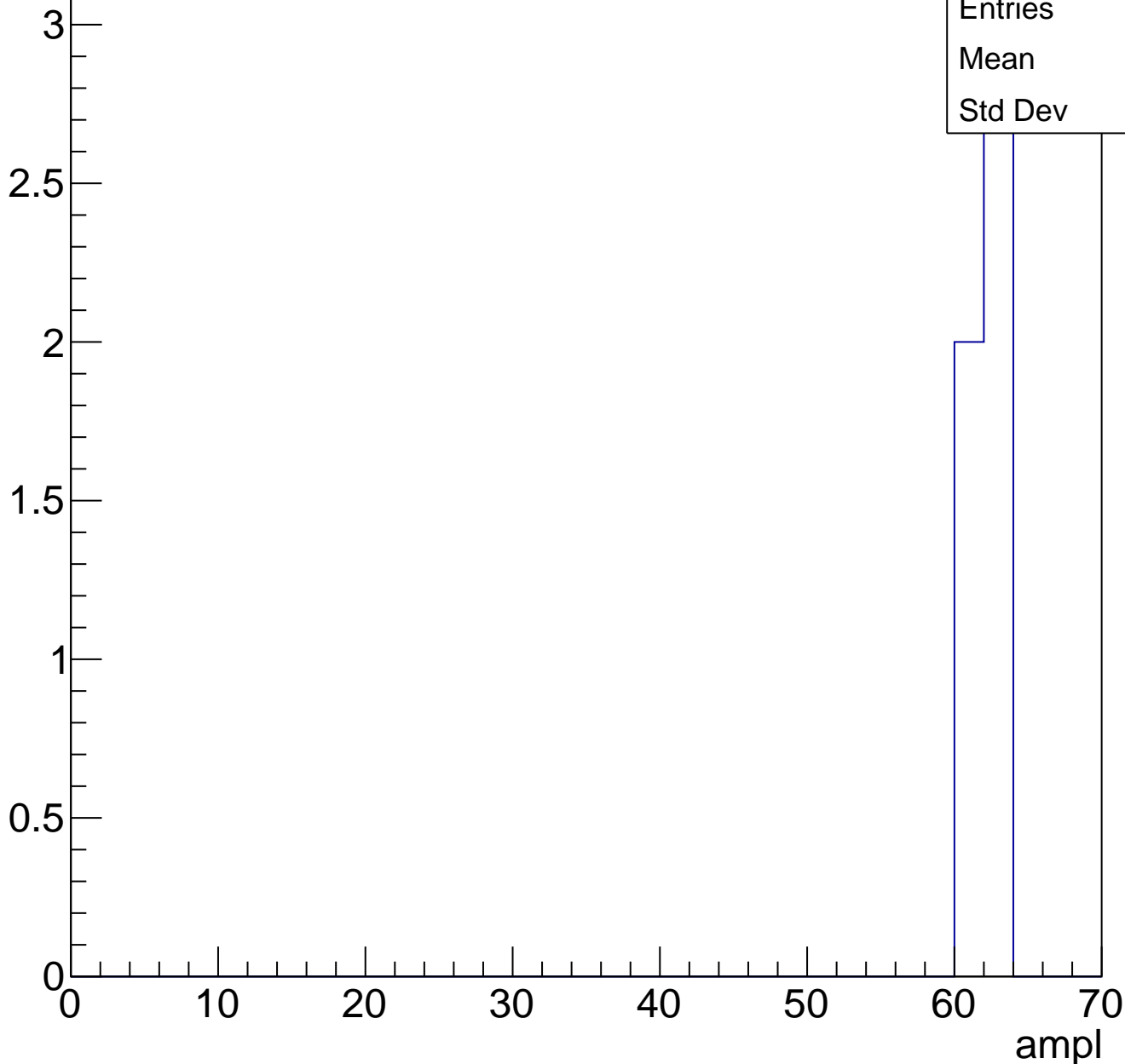
Entry



B1L100S, U7-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch19, adc0

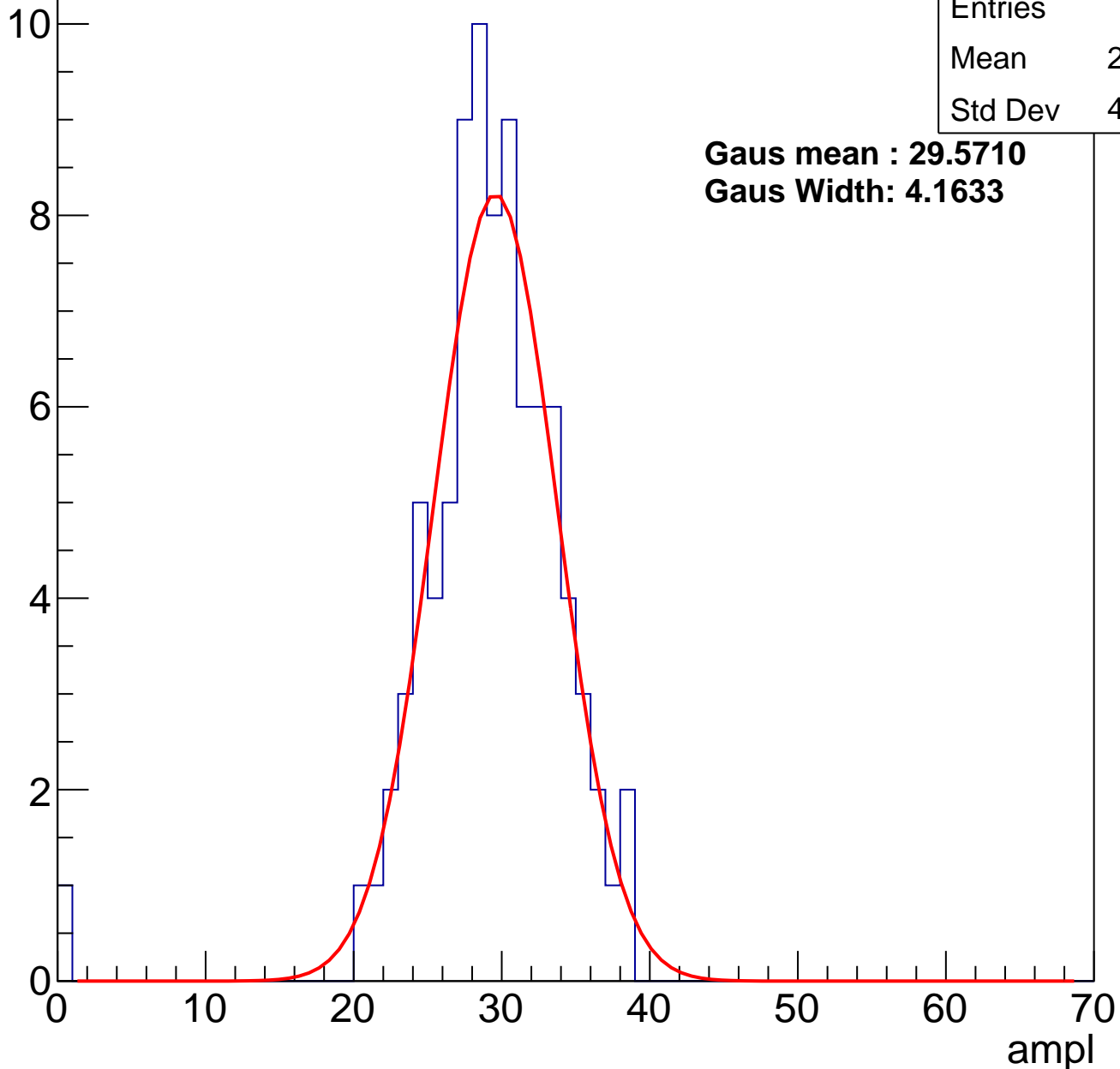
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 88 |
| Mean | 28.76 |
| Std Dev | 4.979 |

Gaus mean : 29.5710

Gaus Width: 4.1633

Entry



B1L100S, U7-ch19, adc1

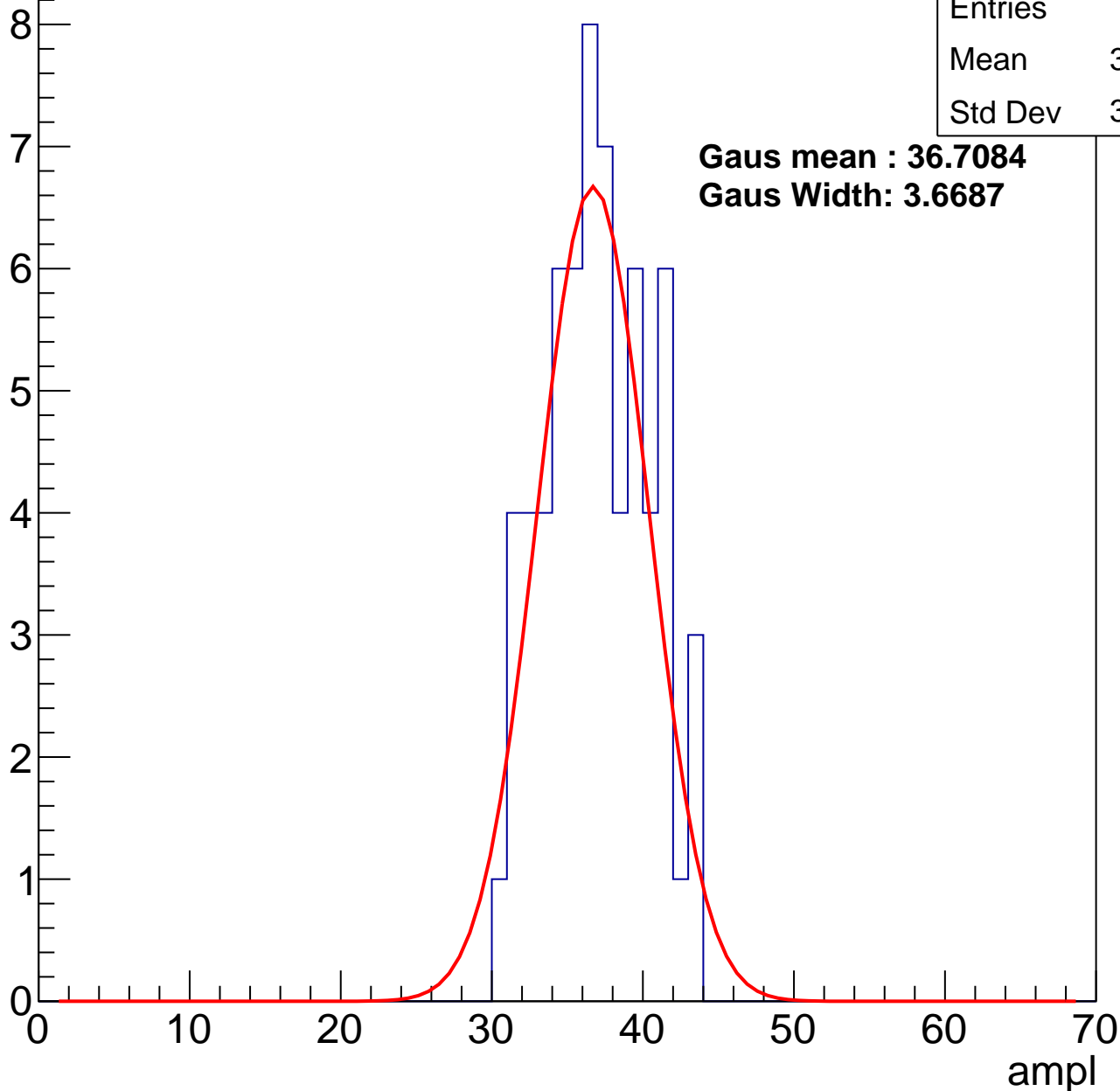
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.53 |
| Std Dev | 3.359 |

Gaus mean : 36.7084

Gaus Width: 3.6687



B1L100S, U7-ch19, adc2

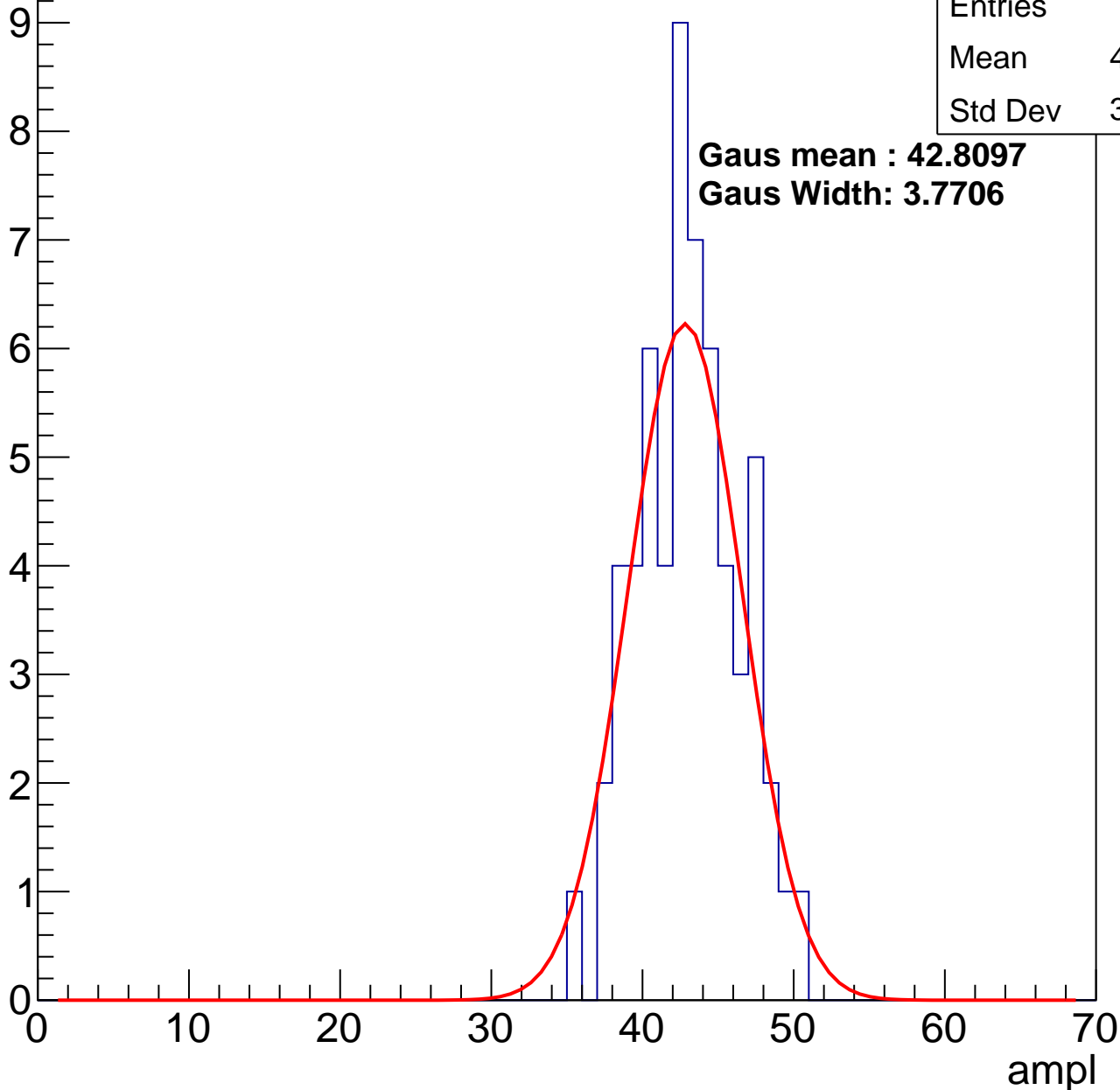
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.58 |
| Std Dev | 3.274 |

Gaus mean : 42.8097

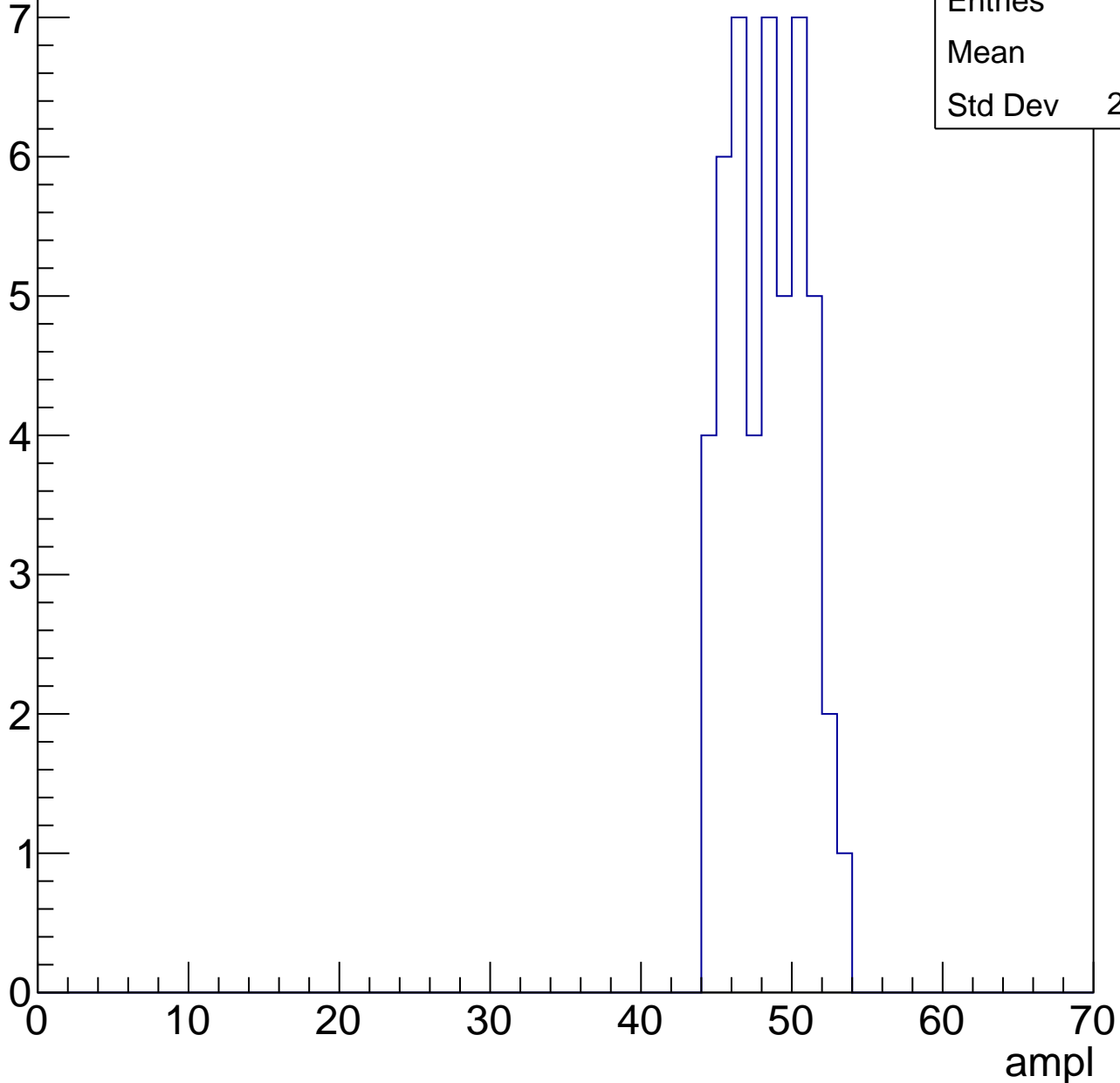
Gaus Width: 3.7706



B1L100S, U7-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

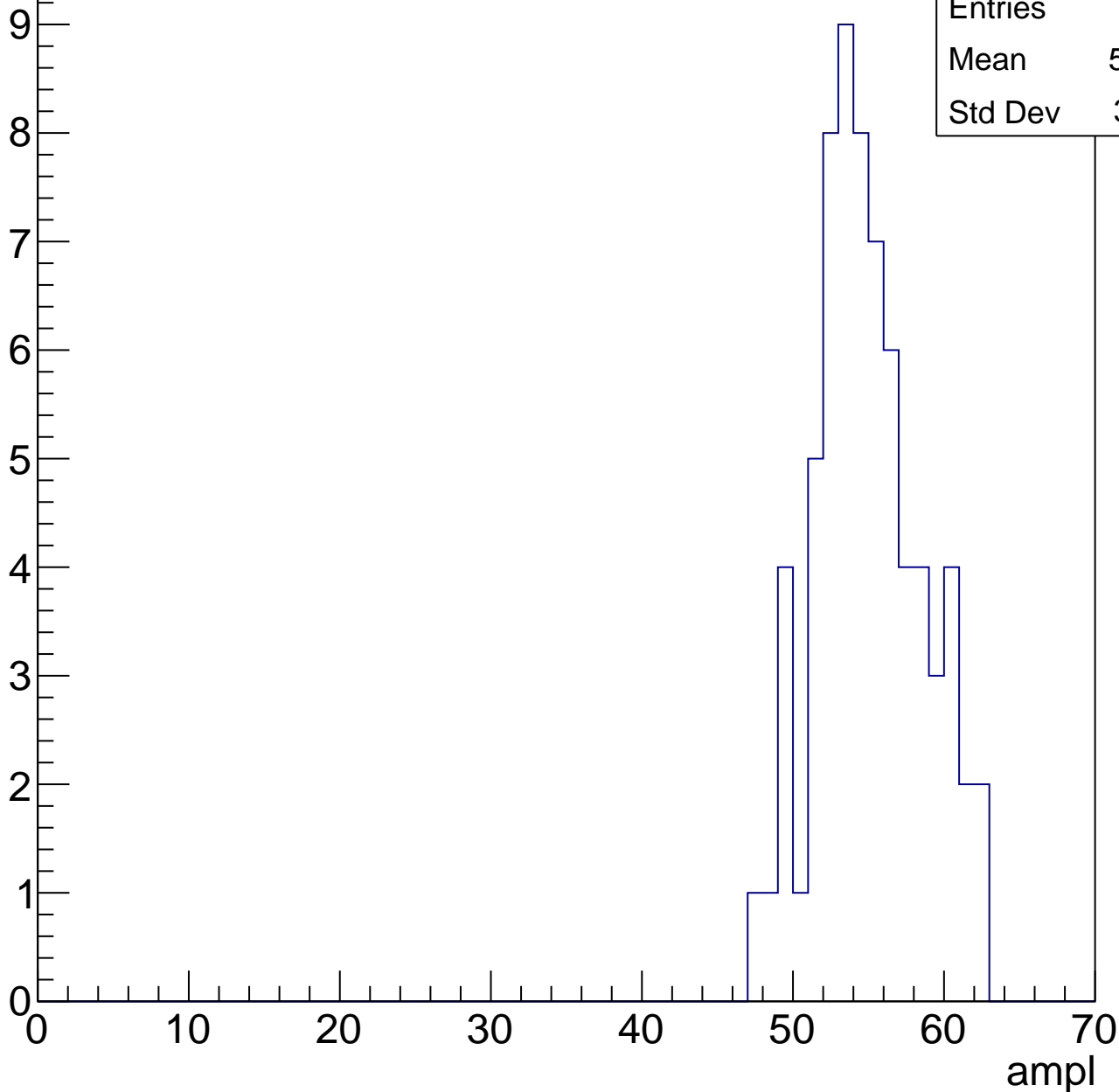


B1L100S, U7-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 54.57 |
| Std Dev | 3.491 |

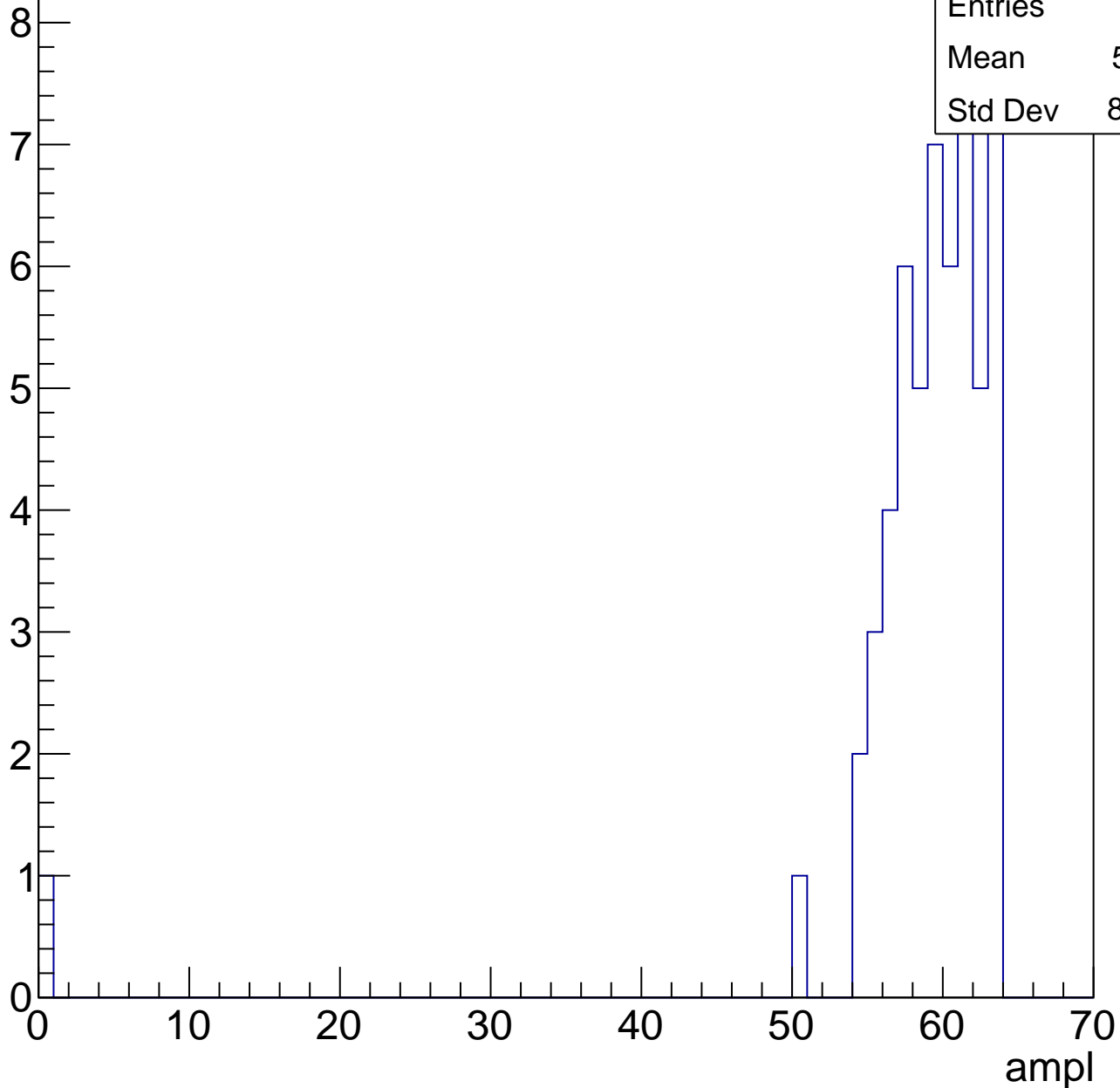


B1L100S, U7-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

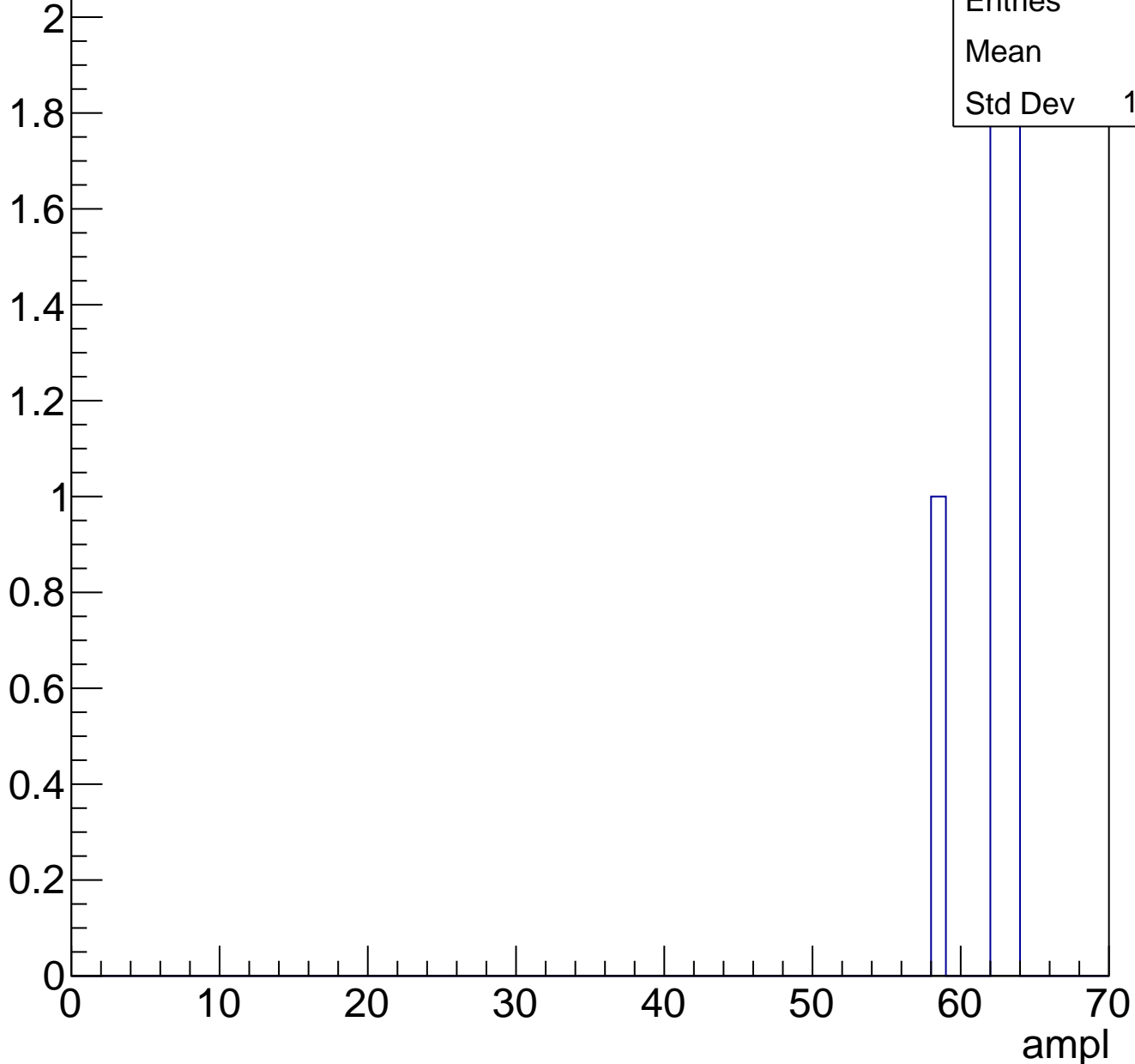
| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.11 |
| Std Dev | 8.336 |



B1L100S, U7-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

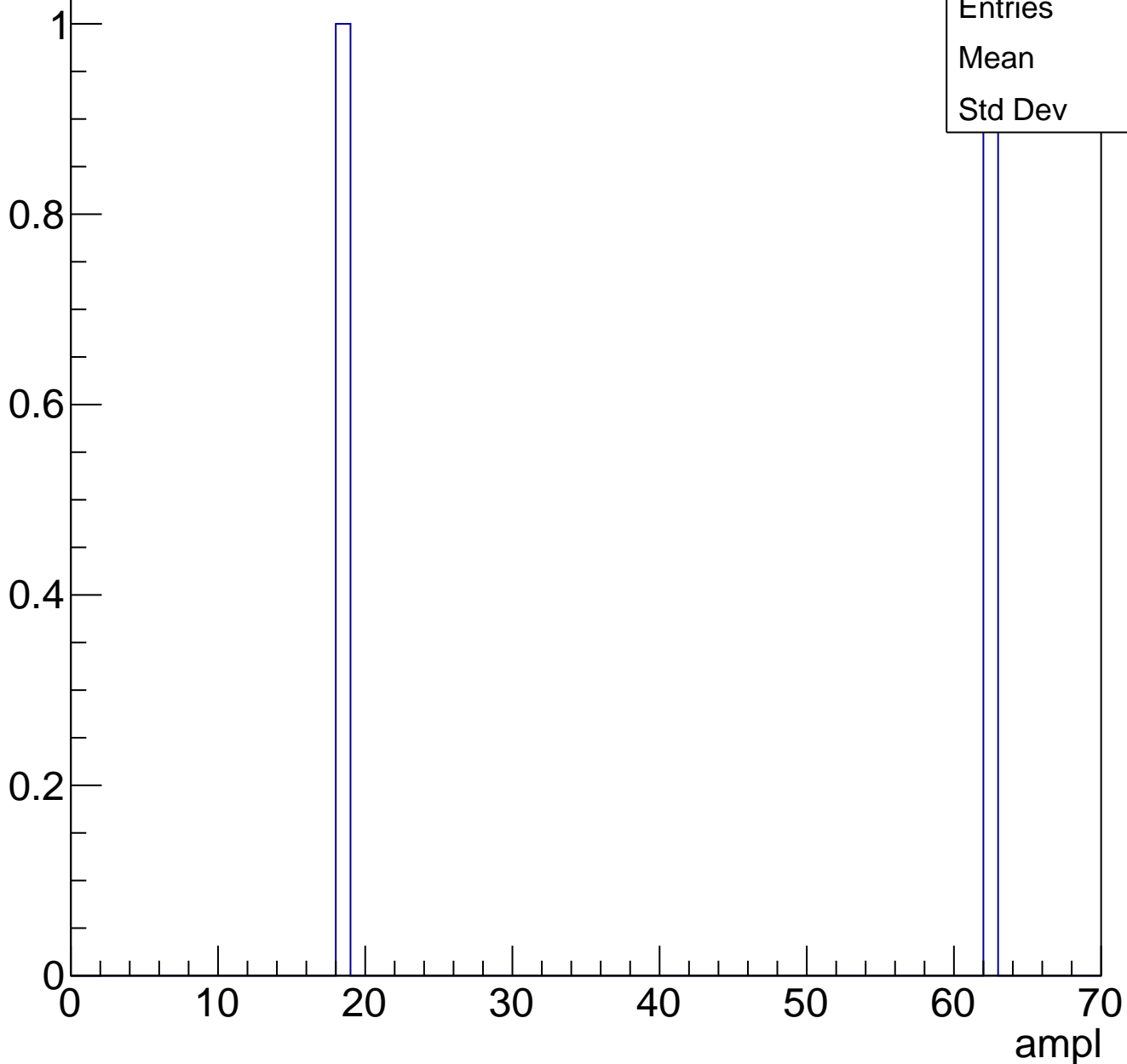
Entry



B1L100S, U7-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 40 |
| Std Dev | 22 |

B1L100S, U7-ch20, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 26.71 |
| Std Dev | 5.577 |

Gaus mean : 27.6585

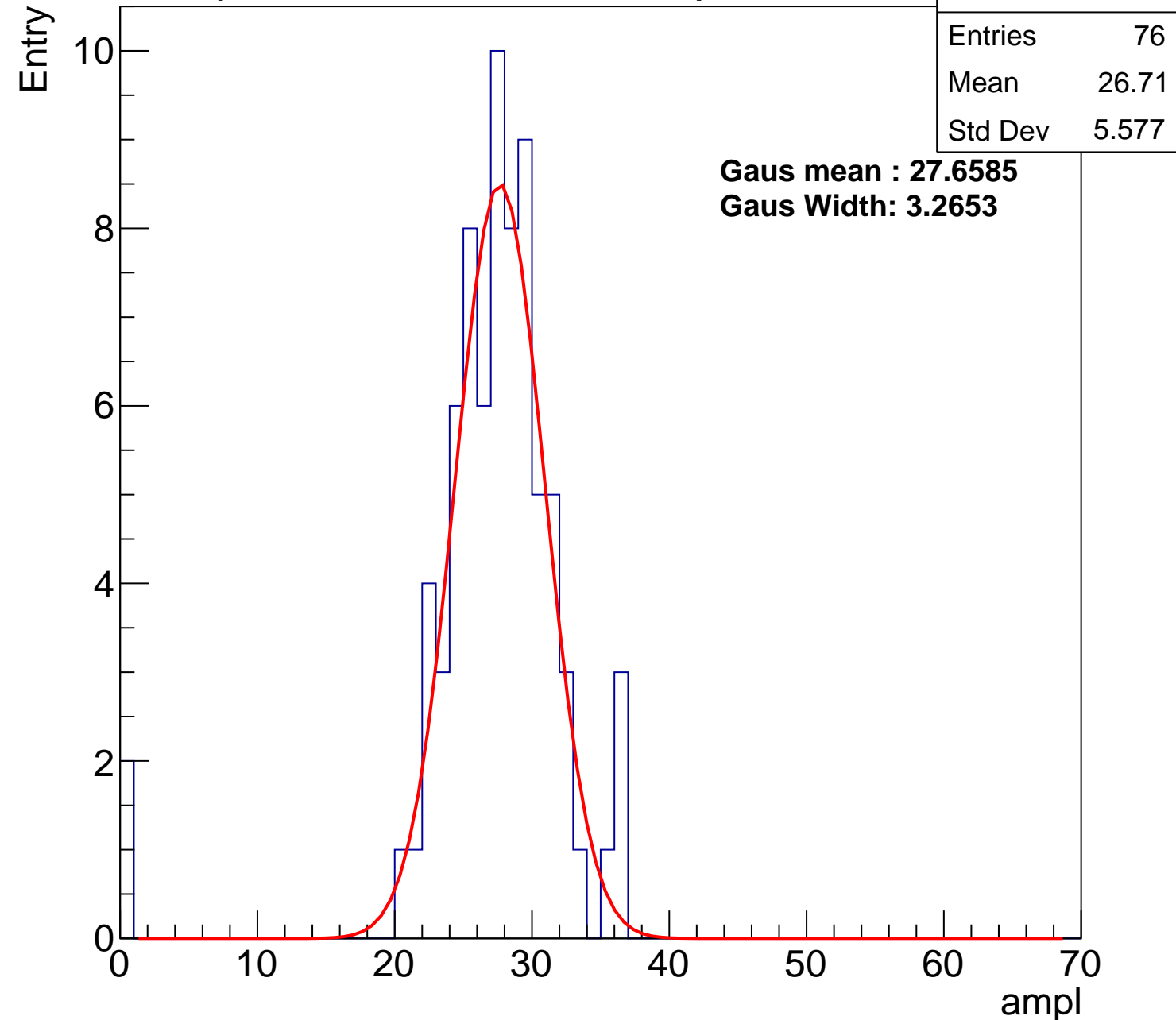
Gaus Width: 3.2653

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch20, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 35.16 |
| Std Dev | 3.42 |

Gaus mean : 35.7647

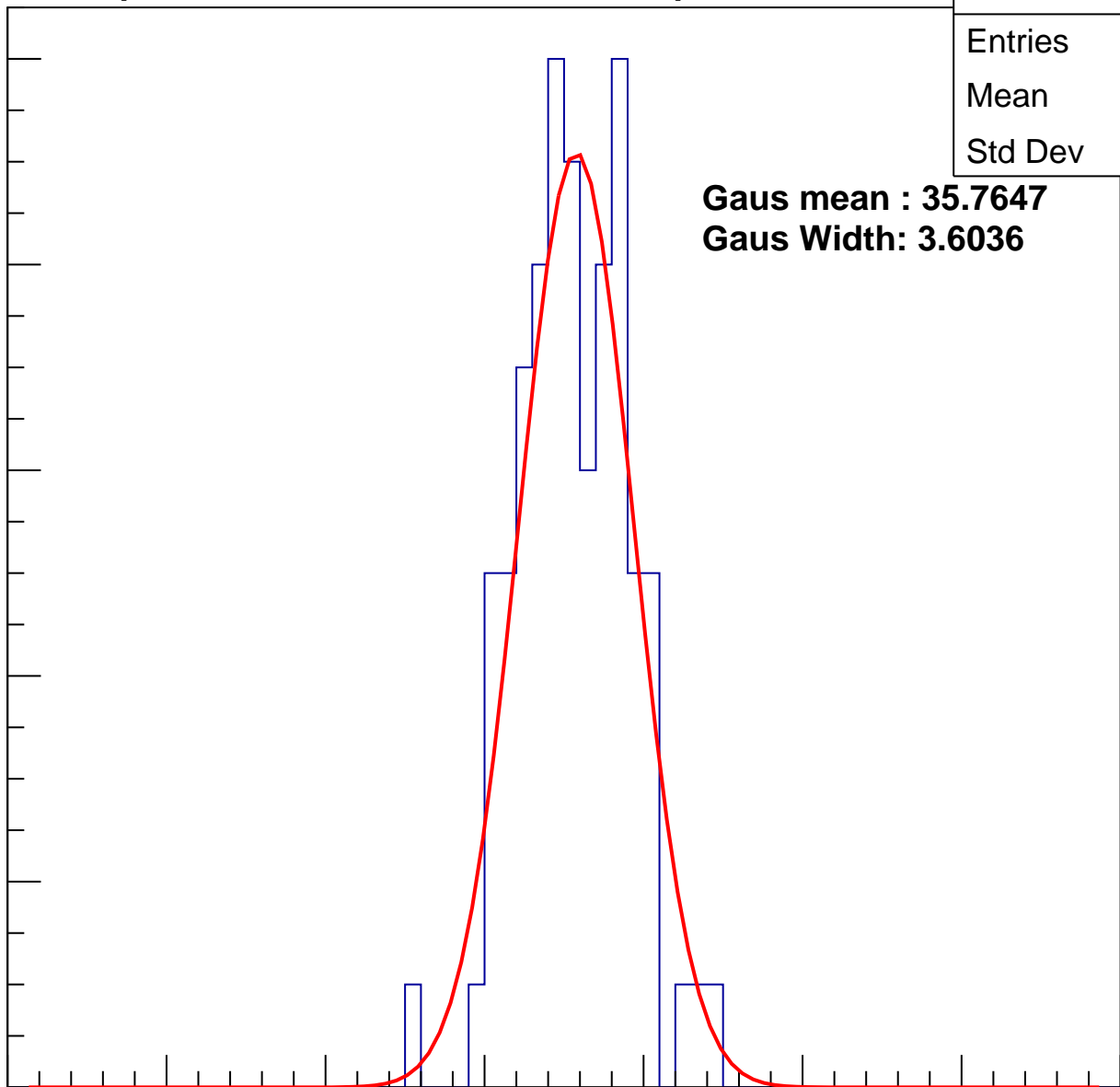
Gaus Width: 3.6036

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch20, adc2

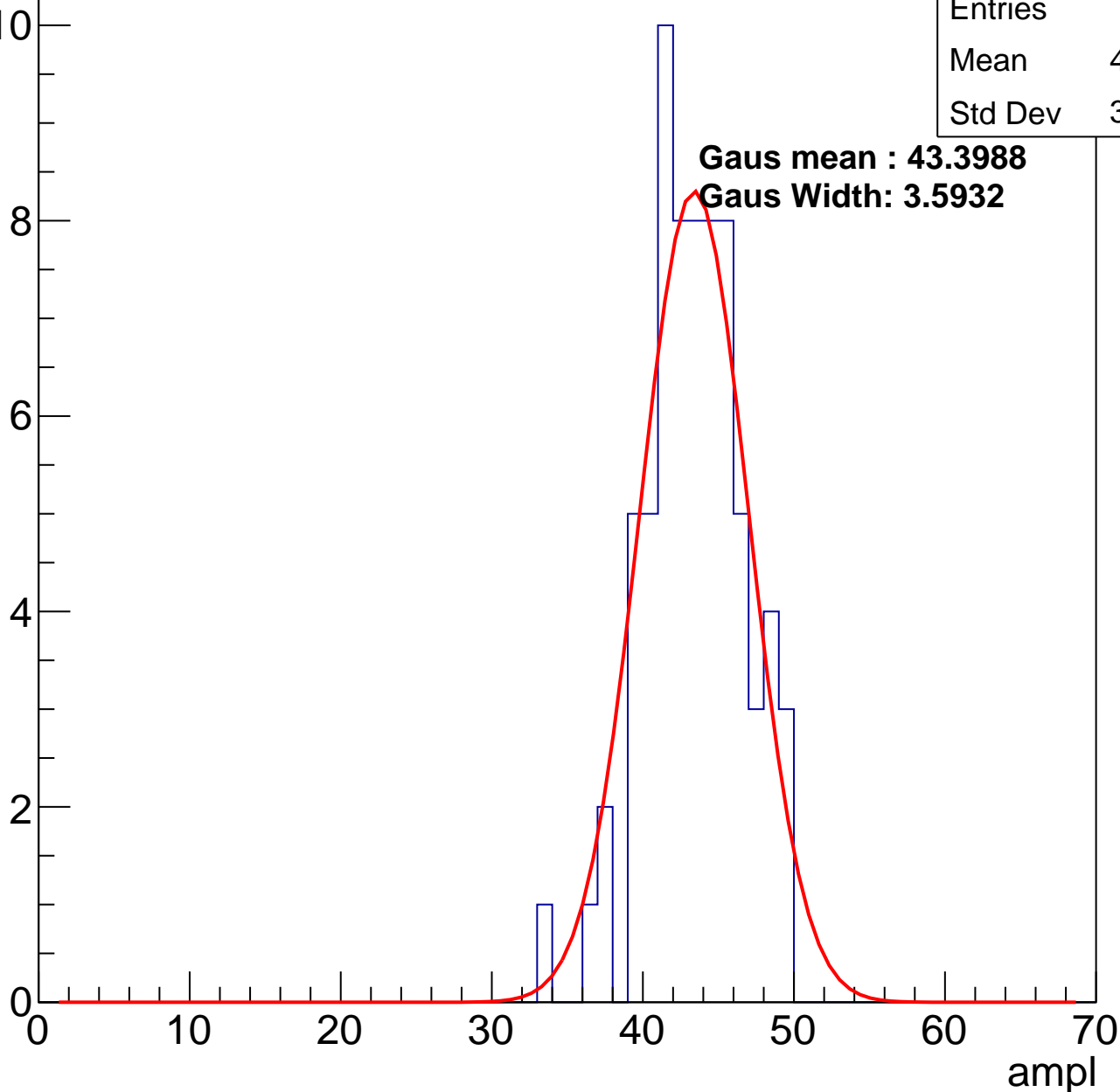
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.96 |
| Std Dev | 3.217 |

Gaus mean : 43.3988

Gaus Width: 3.5932

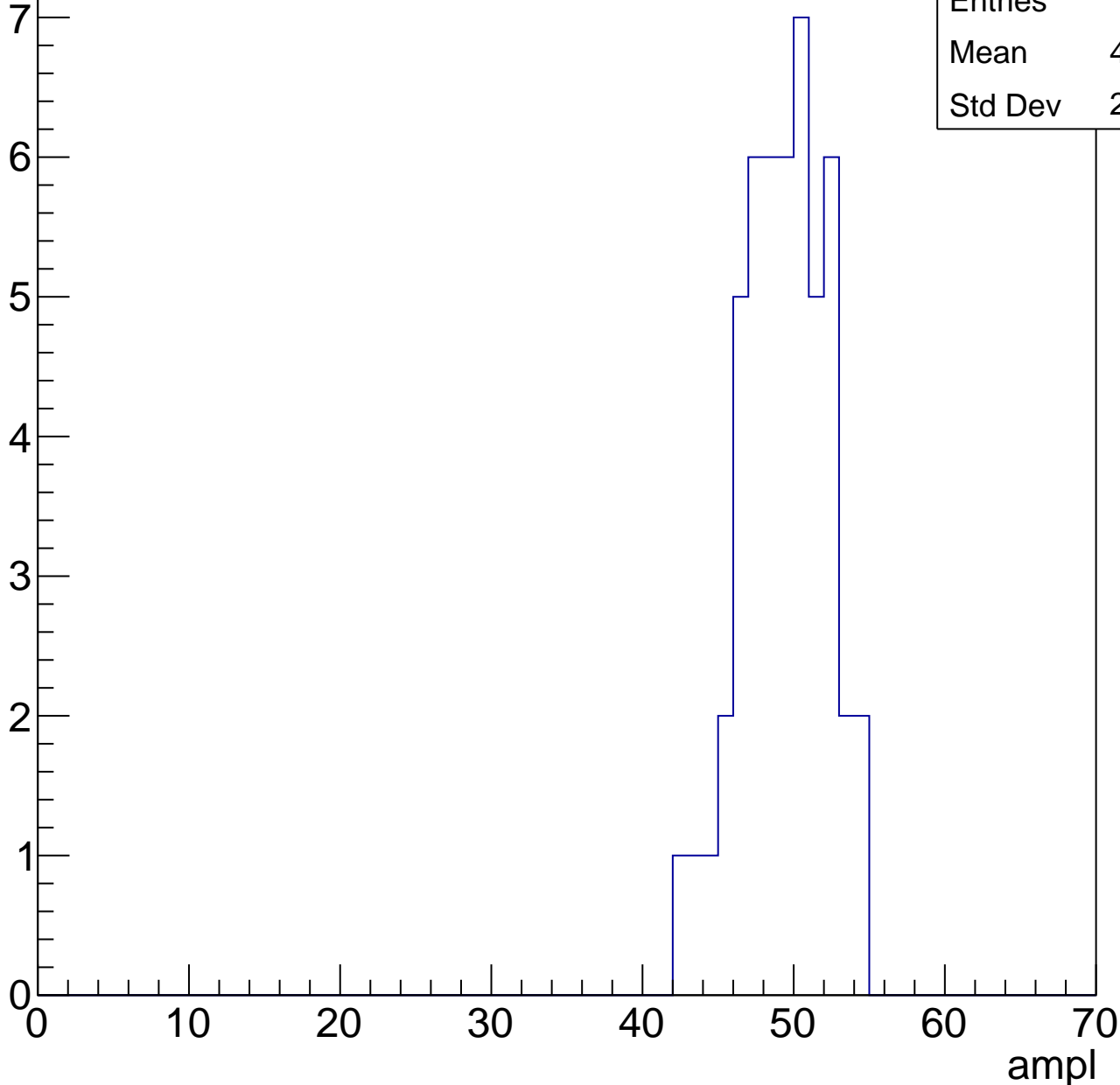


B1L100S, U7-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

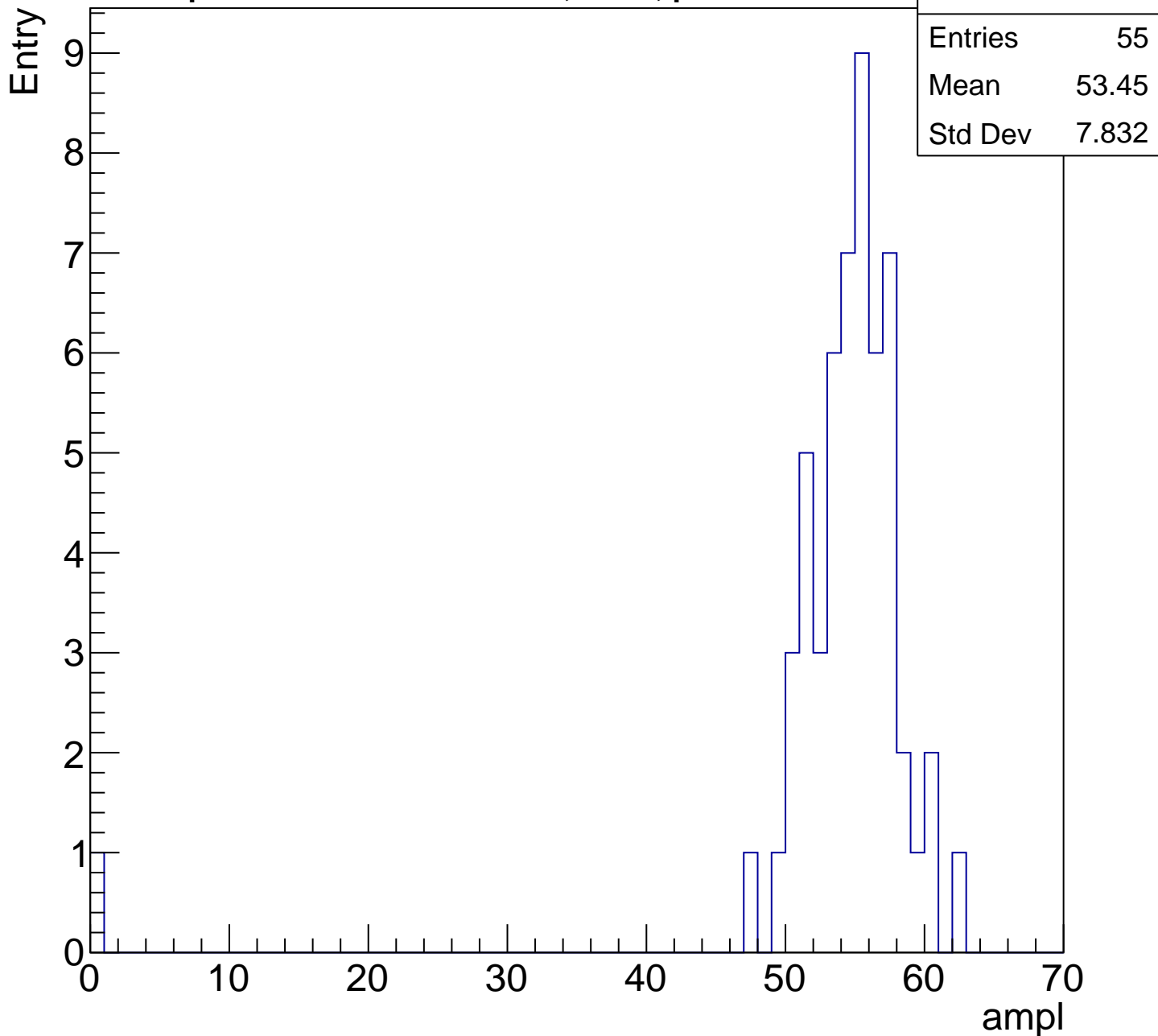
Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 48.88 |
| Std Dev | 2.754 |



B1L100S, U7-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

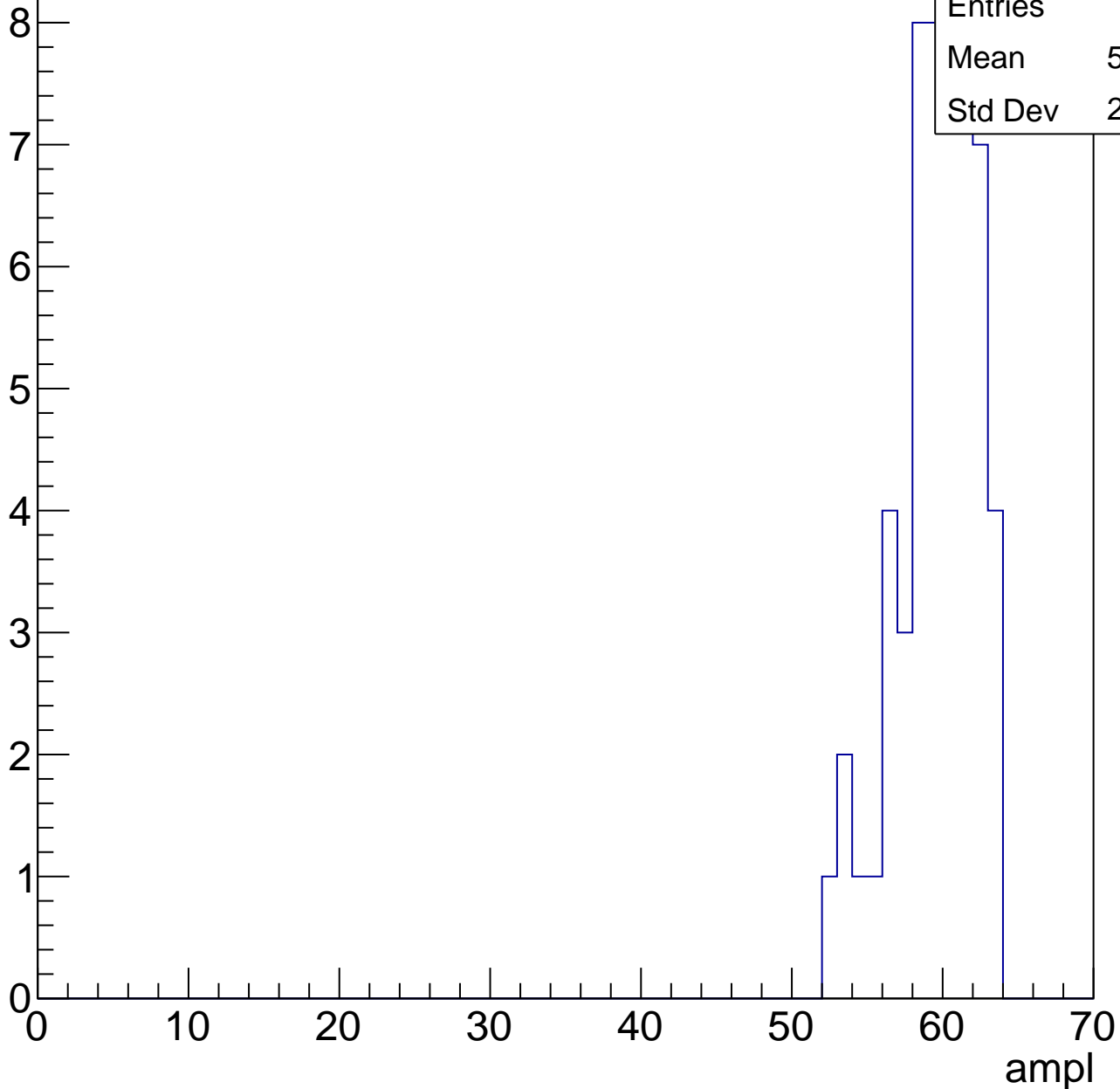


B1L100S, U7-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 59.13 |
| Std Dev | 2.643 |

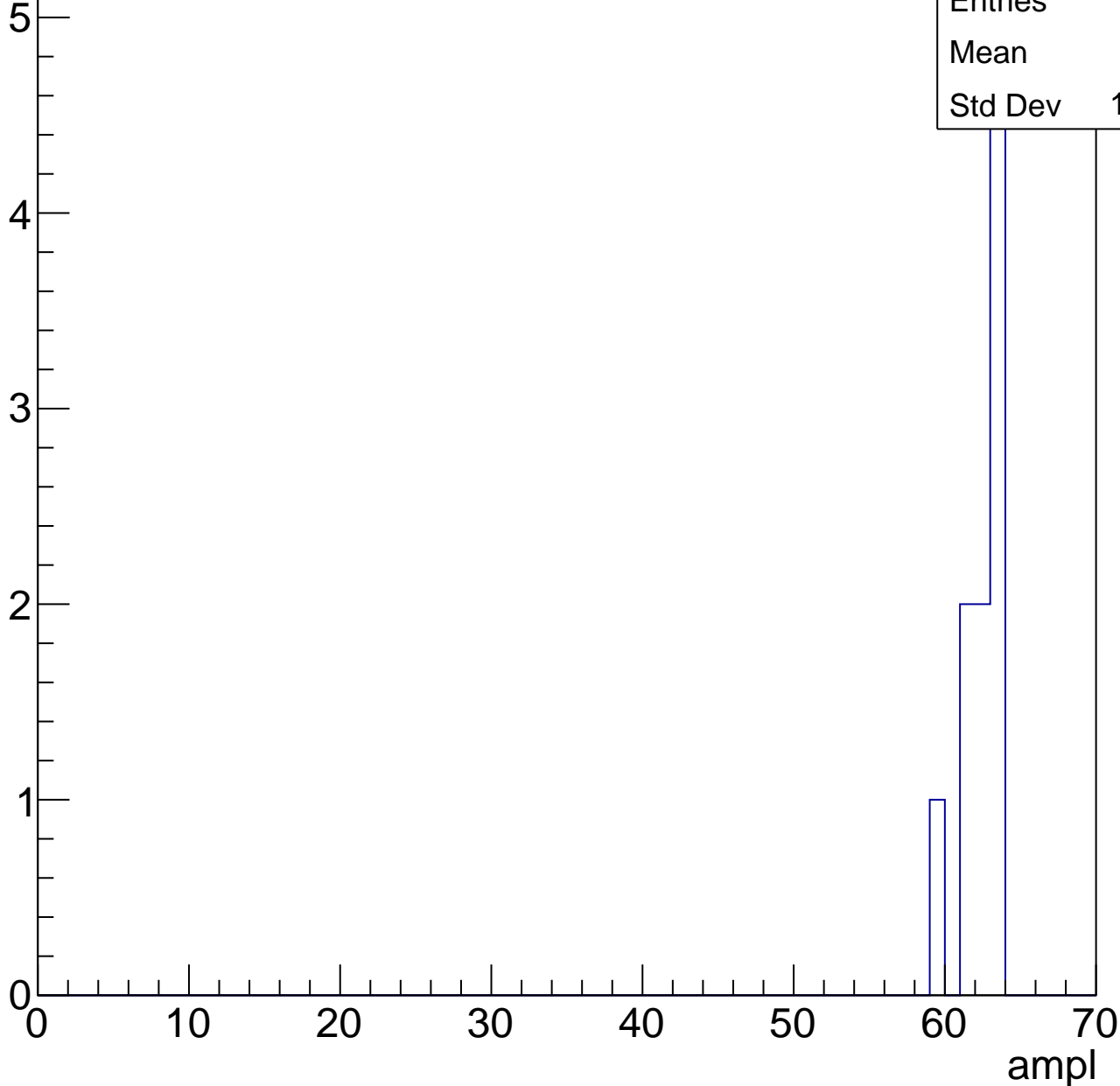


B1L100S, U7-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

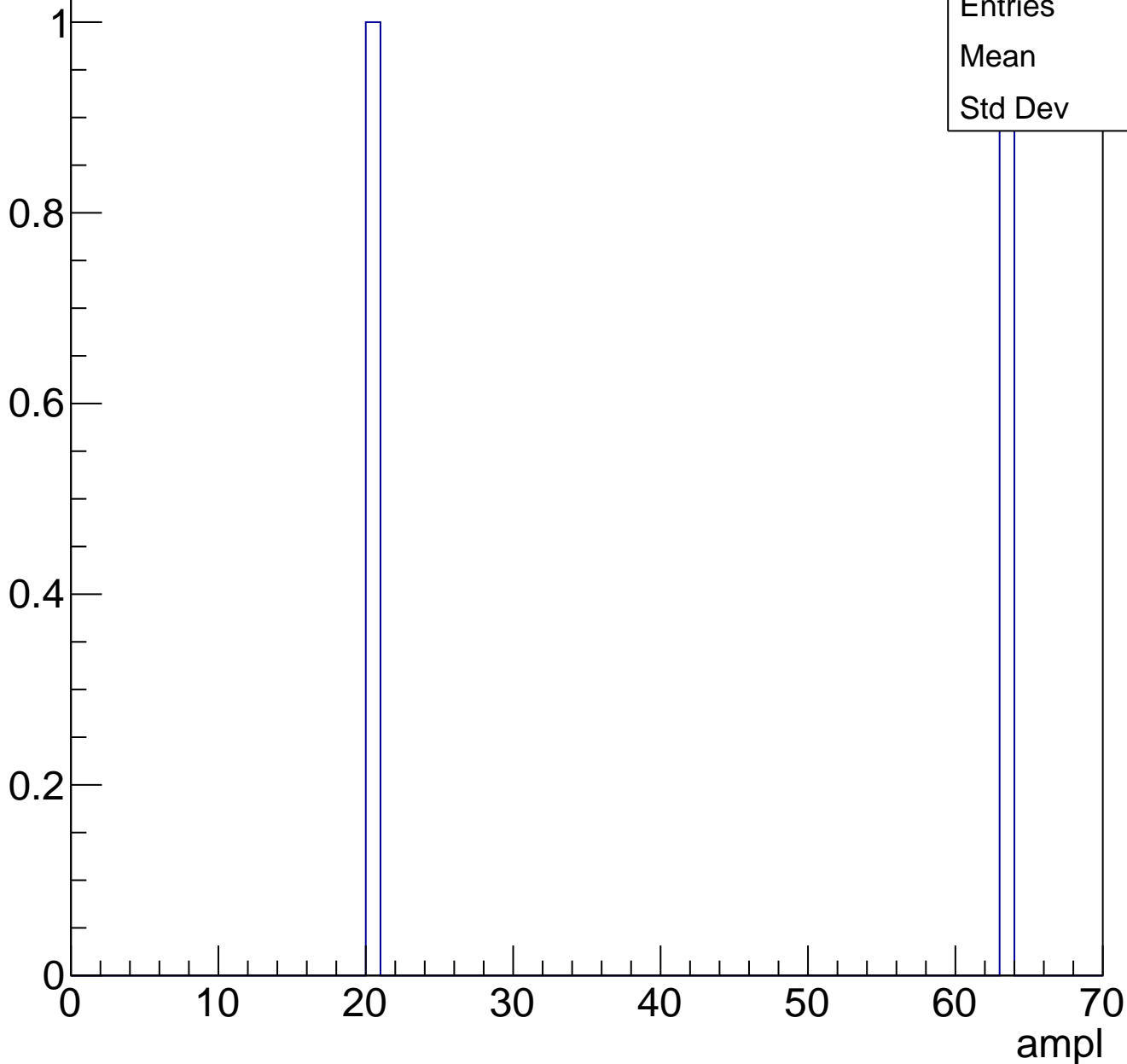
| | |
|---------|-------|
| Entries | 10 |
| Mean | 62 |
| Std Dev | 1.265 |



B1L100S, U7-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch21, adc0

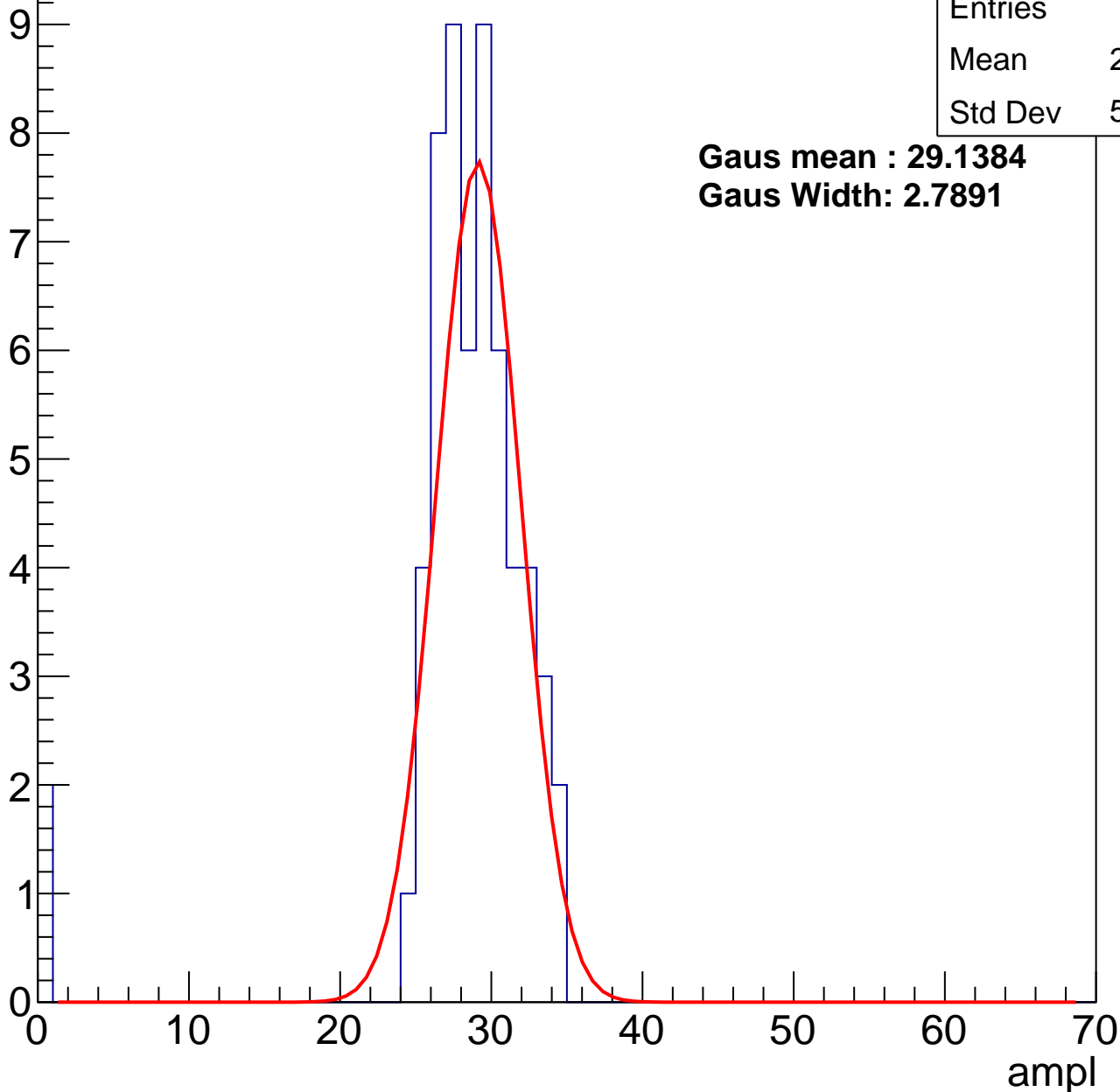
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 27.64 |
| Std Dev | 5.774 |

Gaus mean : 29.1384

Gaus Width: 2.7891



B1L100S, U7-ch21, adc1

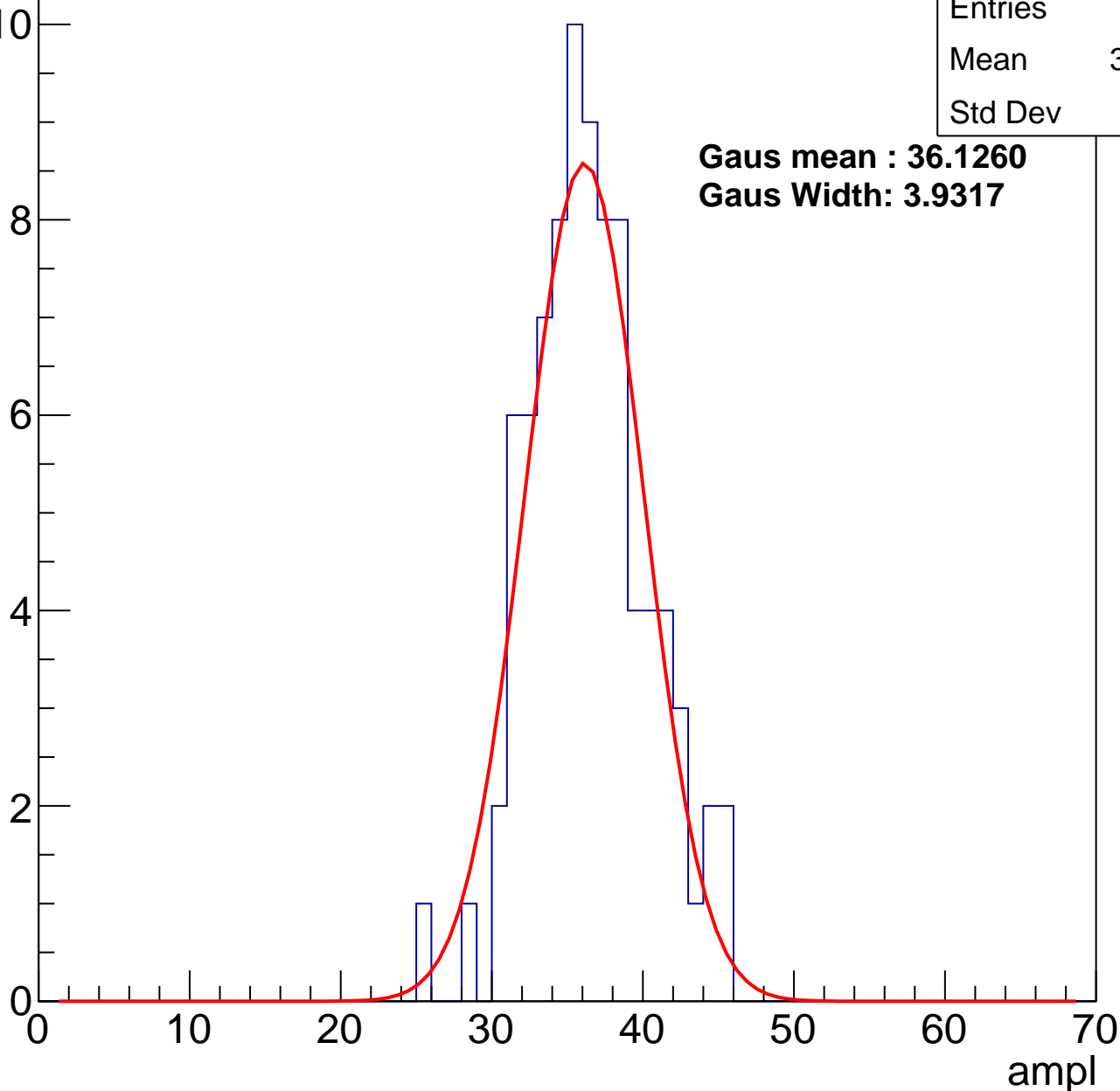
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 35.99 |
| Std Dev | 3.88 |

Gaus mean : 36.1260

Gaus Width: 3.9317



B1L100S, U7-ch21, adc2

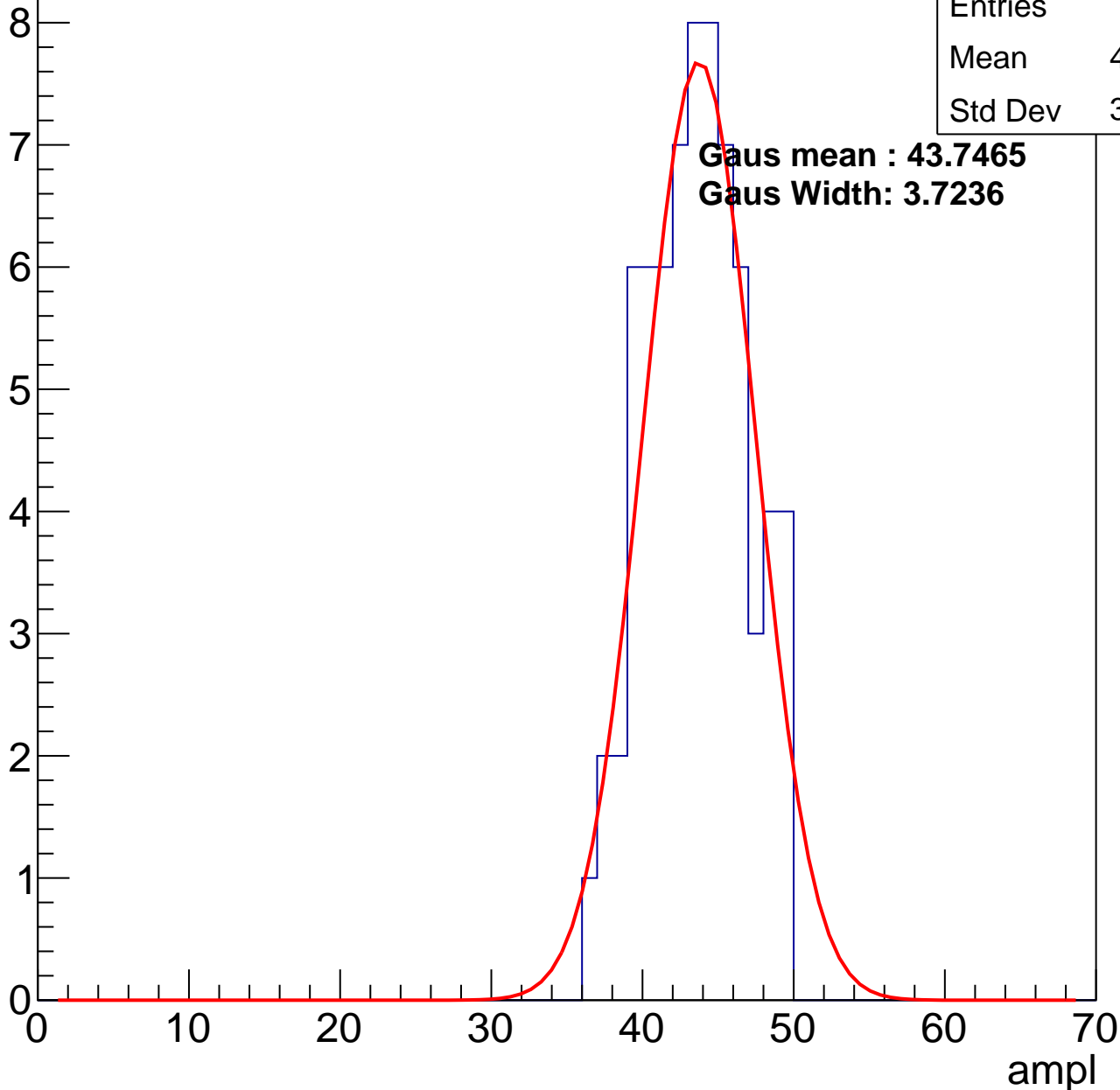
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 43.09 |
| Std Dev | 3.237 |

Gaus mean : 43.7465

Gaus Width: 3.7236

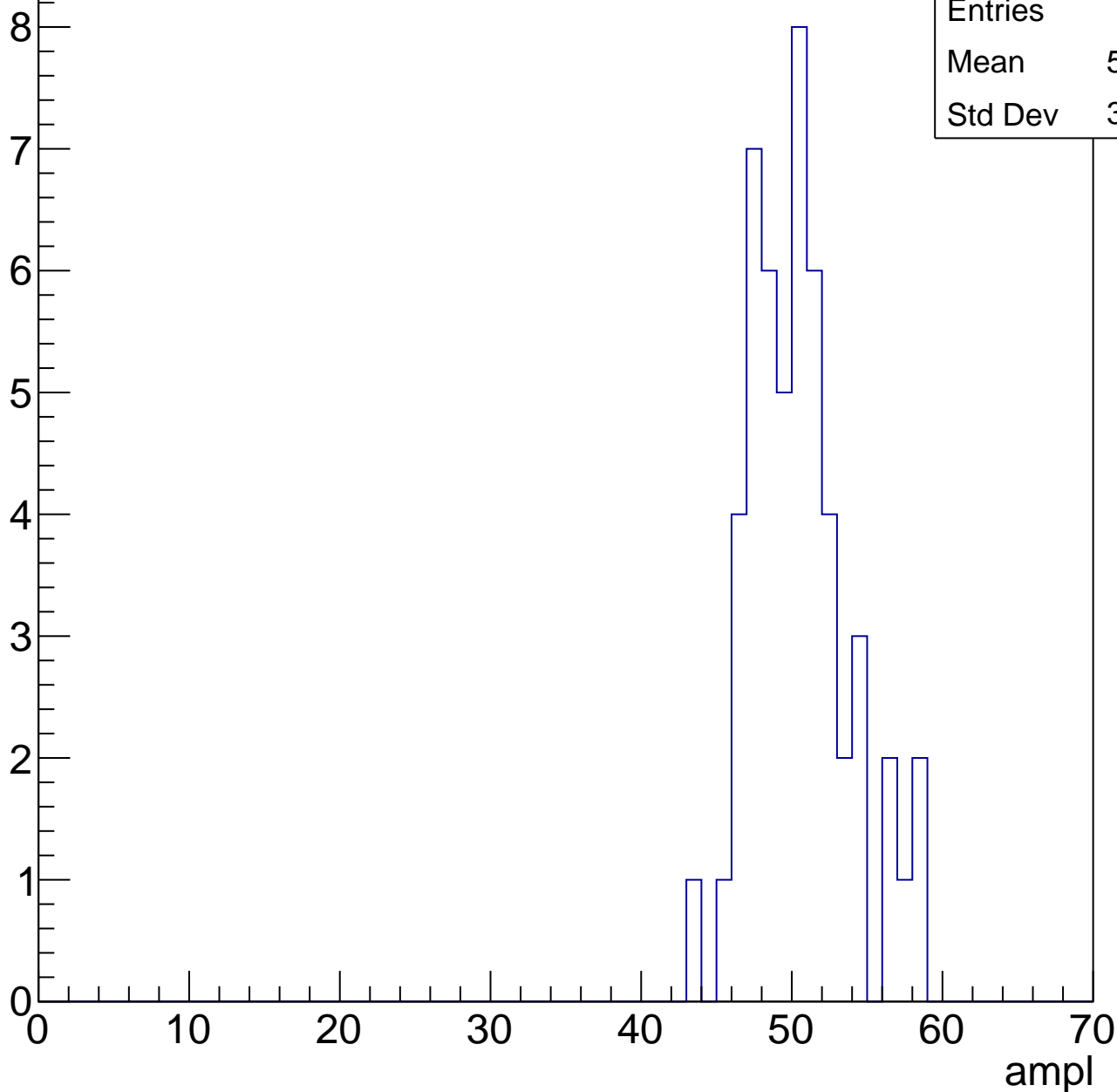


B1L100S, U7-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 50.02 |
| Std Dev | 3.302 |

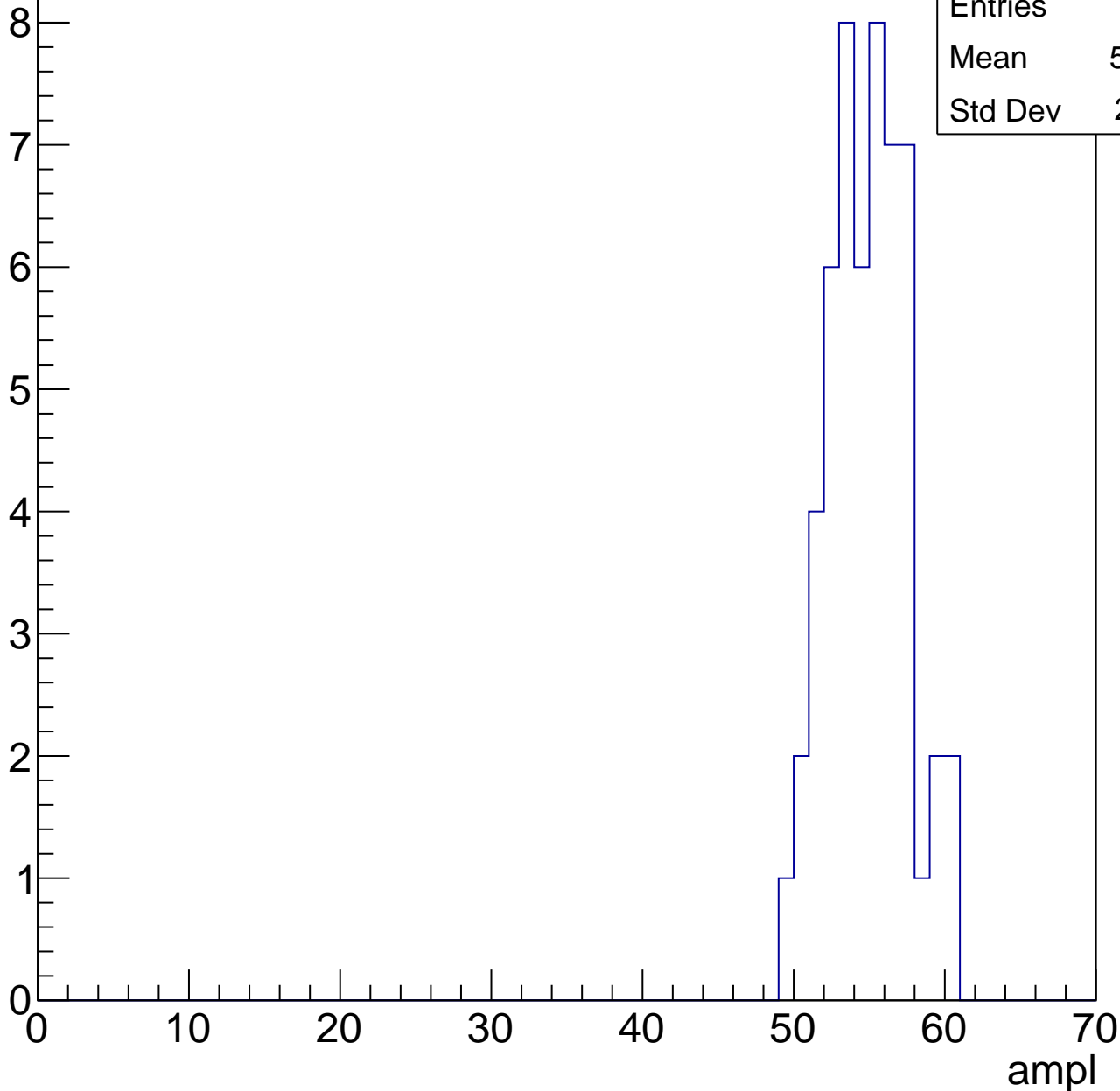


B1L100S, U7-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

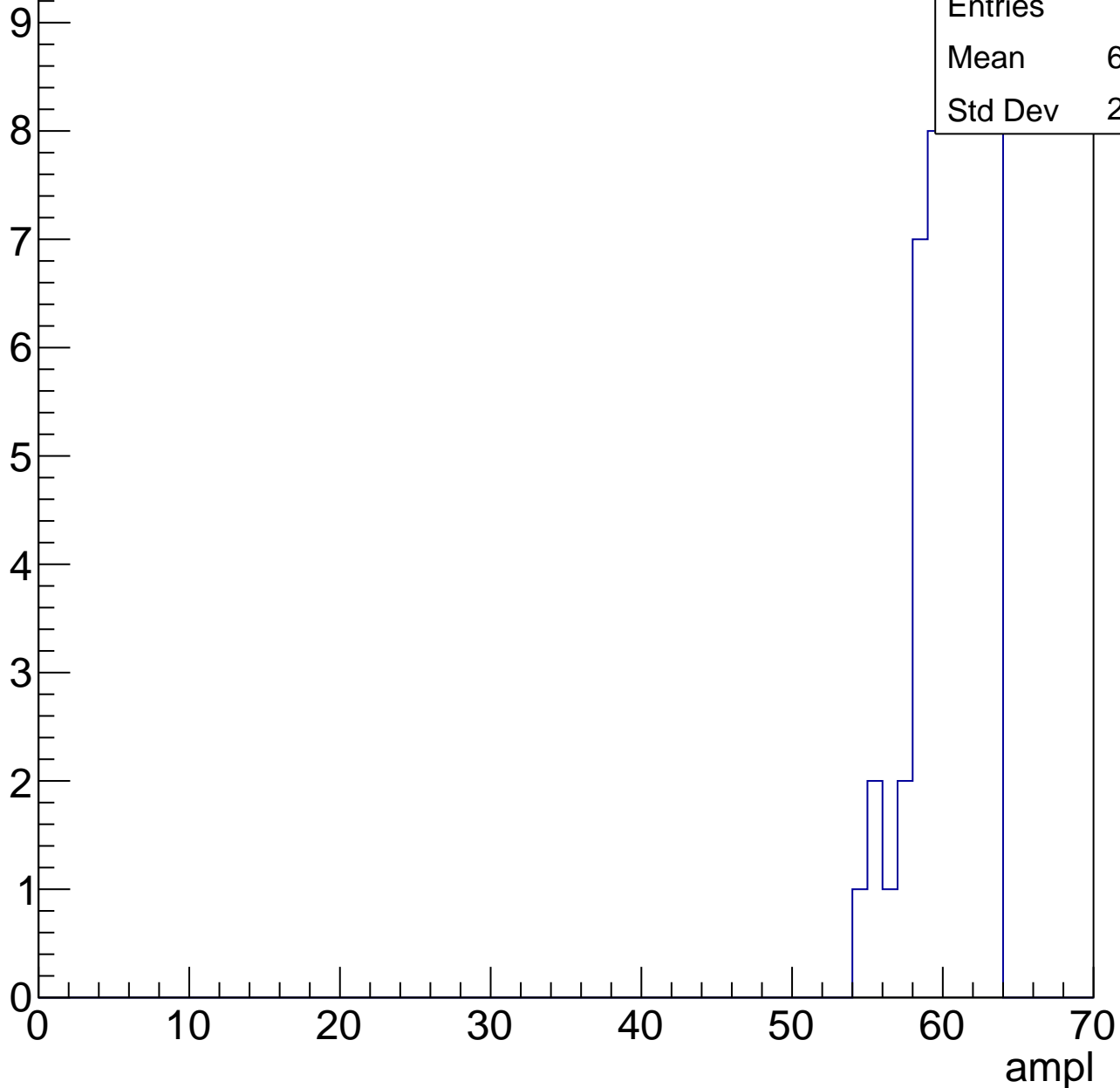
| | |
|---------|-------|
| Entries | 54 |
| Mean | 54.44 |
| Std Dev | 2.551 |



B1L100S, U7-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 54 |
| Mean | 60.02 |
| Std Dev | 2.232 |

B1L100S, U7-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.83 |
| Std Dev | 23.19 |

B1L100S, U7-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch22, adc0

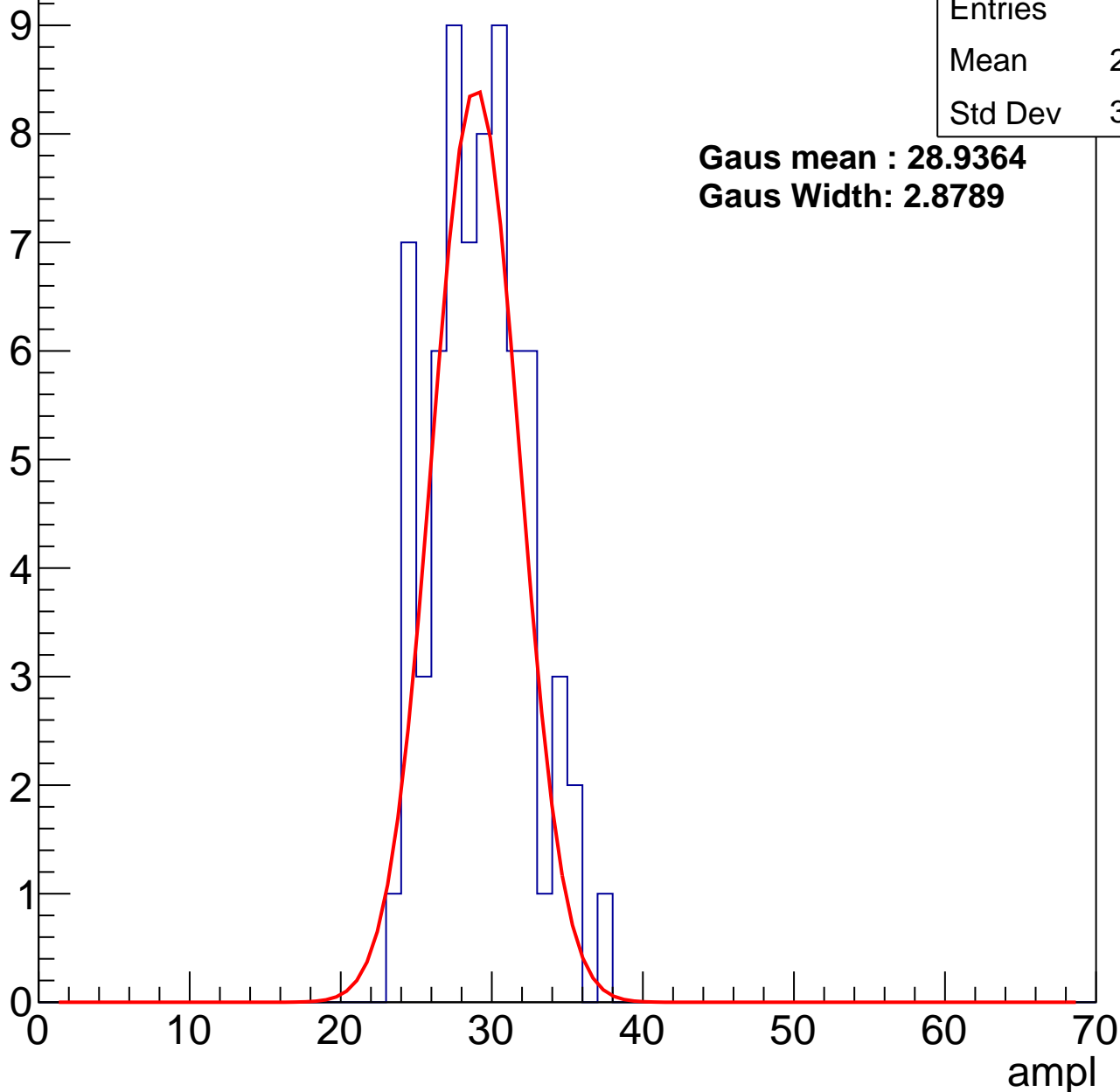
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.74 |
| Std Dev | 3.105 |

Gaus mean : 28.9364

Gaus Width: 2.8789



B1L100S, U7-ch22, adc1

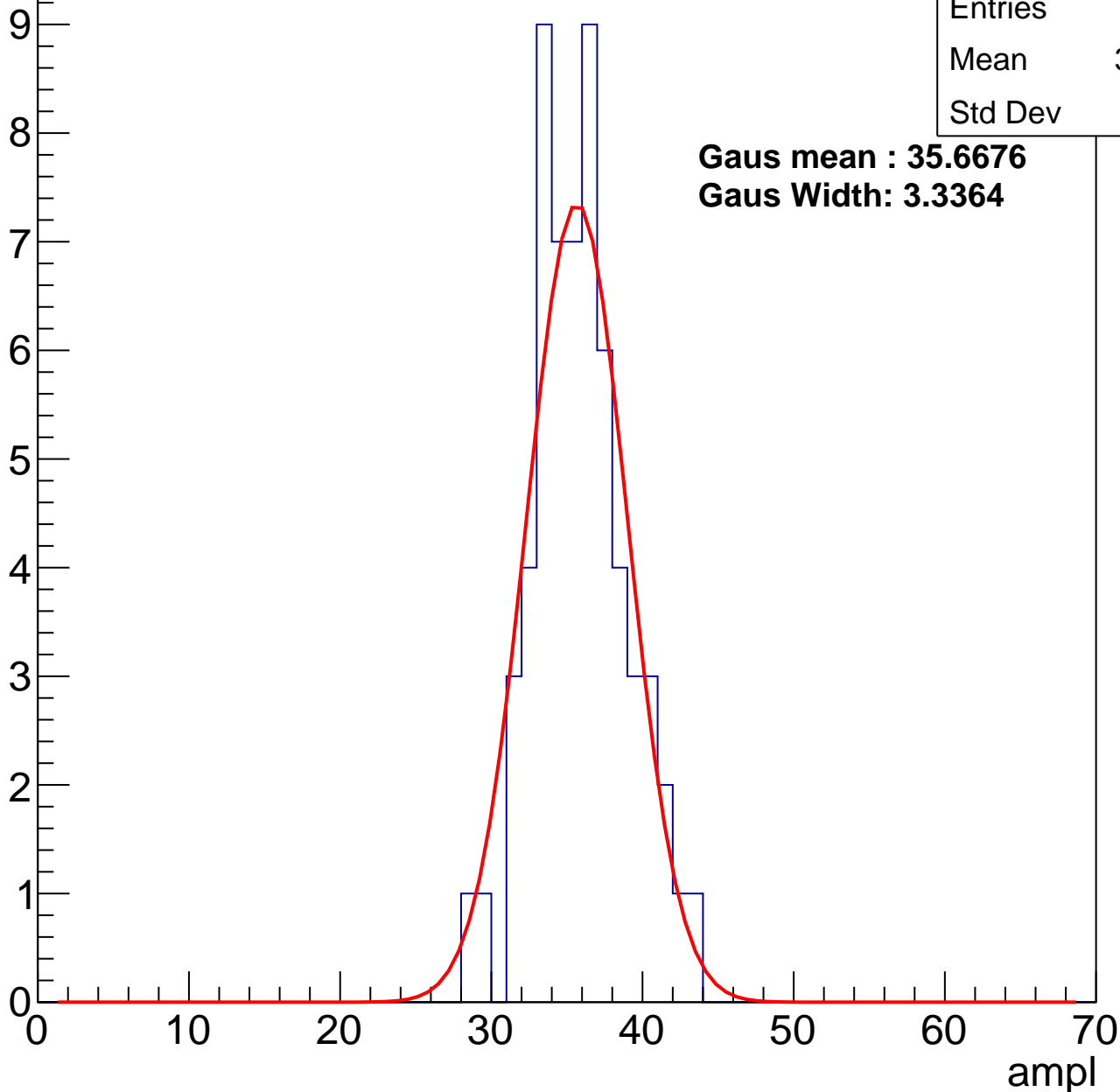
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.41 |
| Std Dev | 3.08 |

Gaus mean : 35.6676

Gaus Width: 3.3364



B1L100S, U7-ch22, adc2

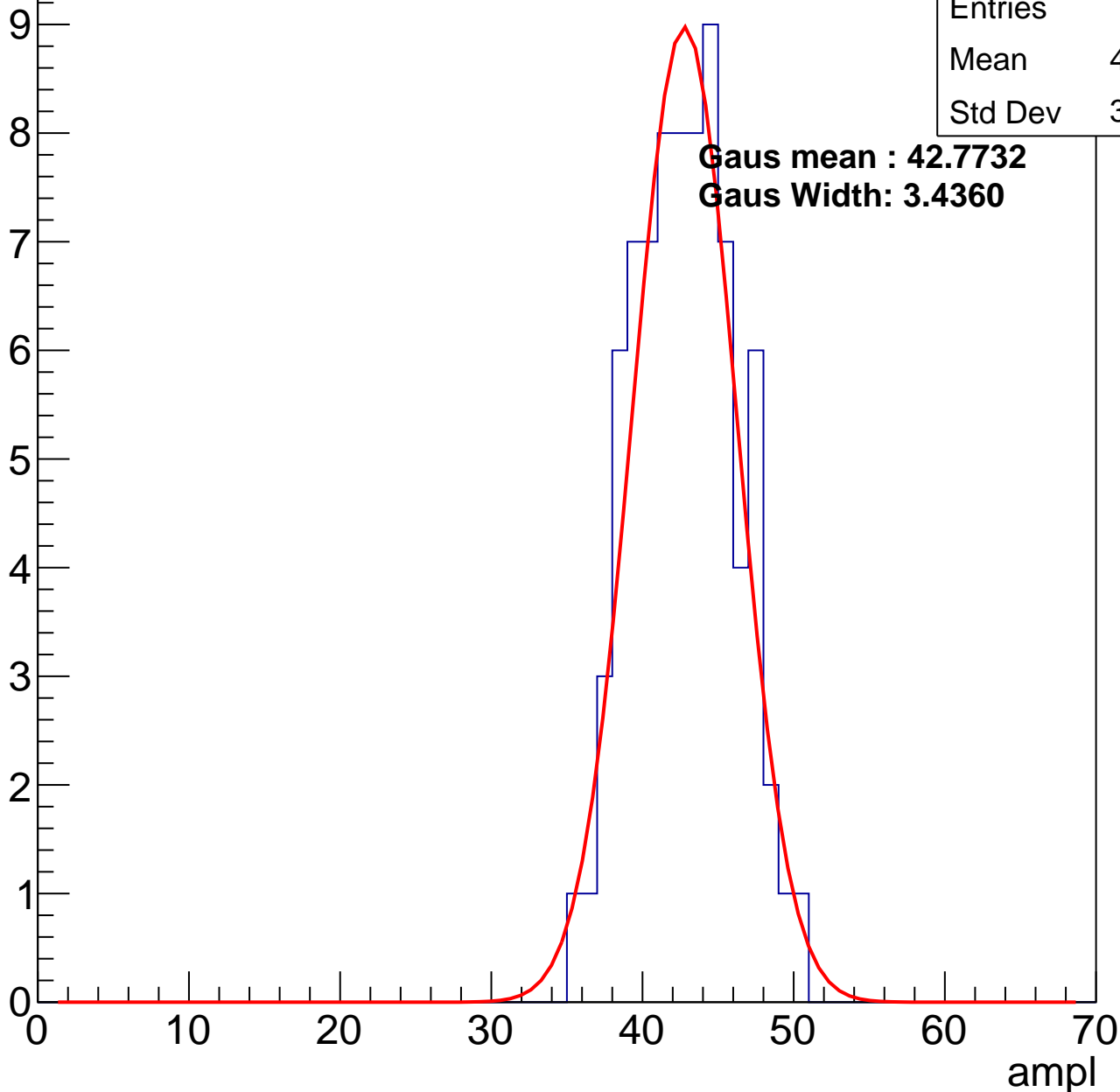
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 42.32 |
| Std Dev | 3.286 |

Gaus mean : 42.7732

Gaus Width: 3.4360

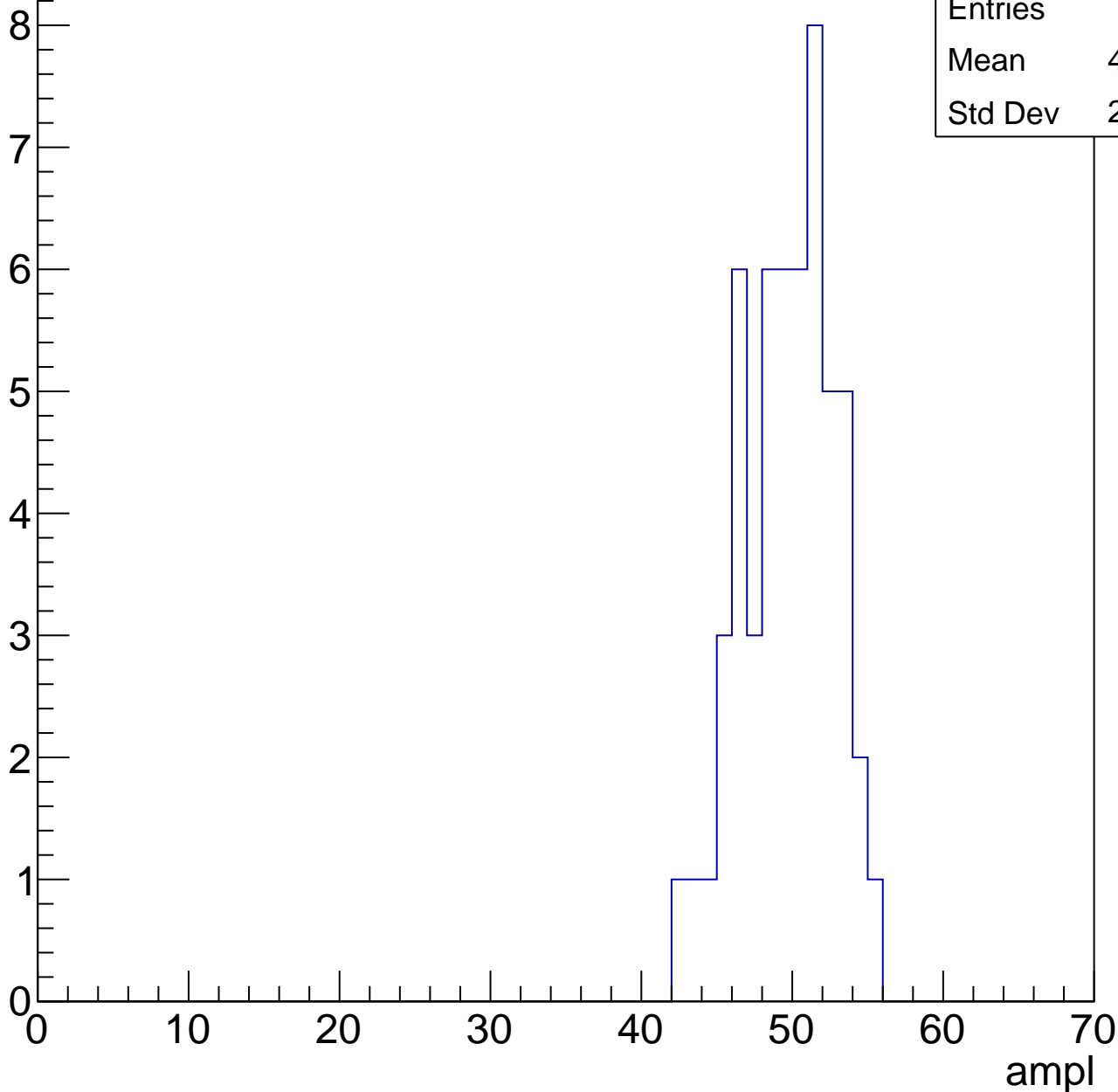


B1L100S, U7-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

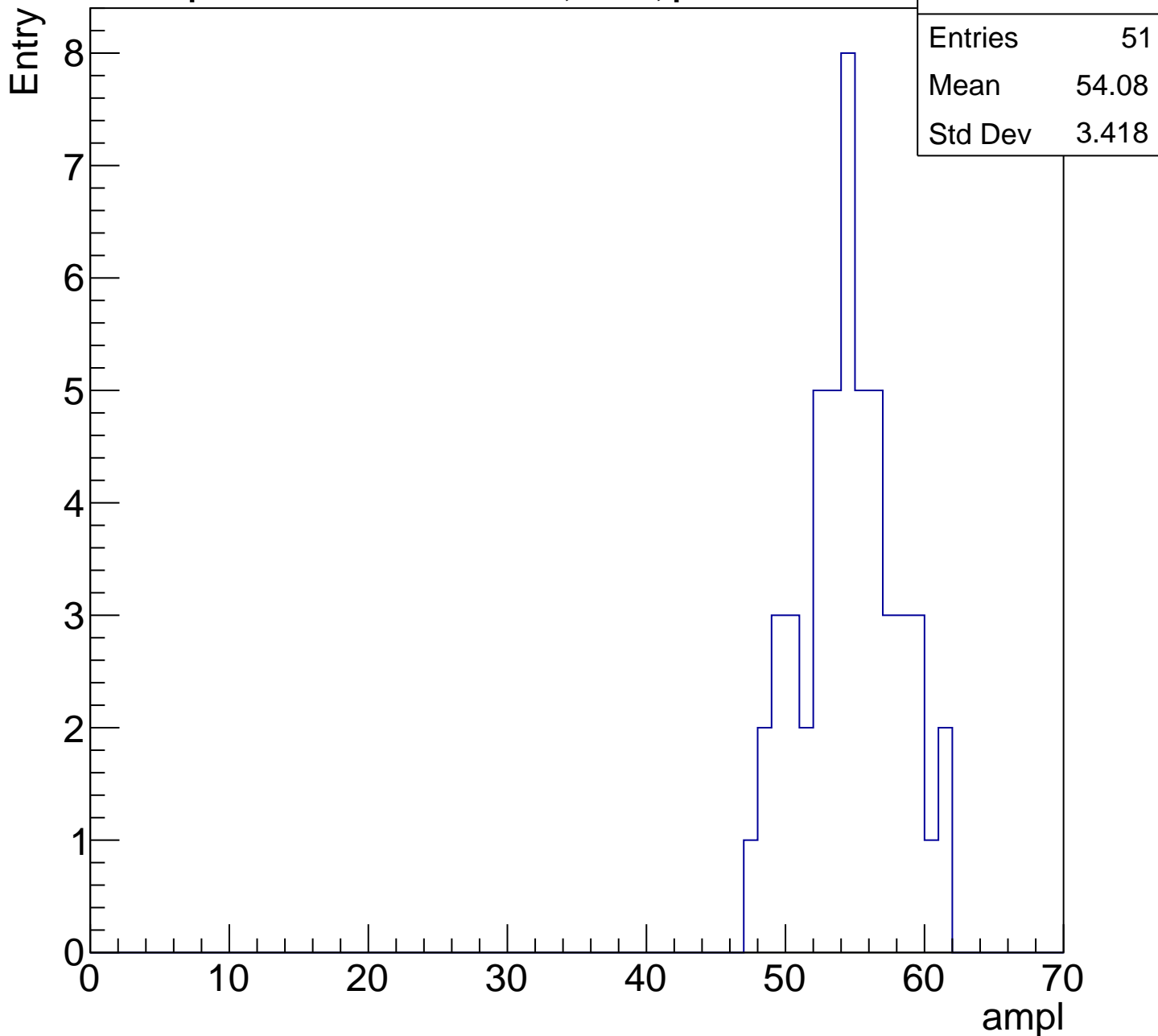
Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 49.24 |
| Std Dev | 2.969 |



B1L100S, U7-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

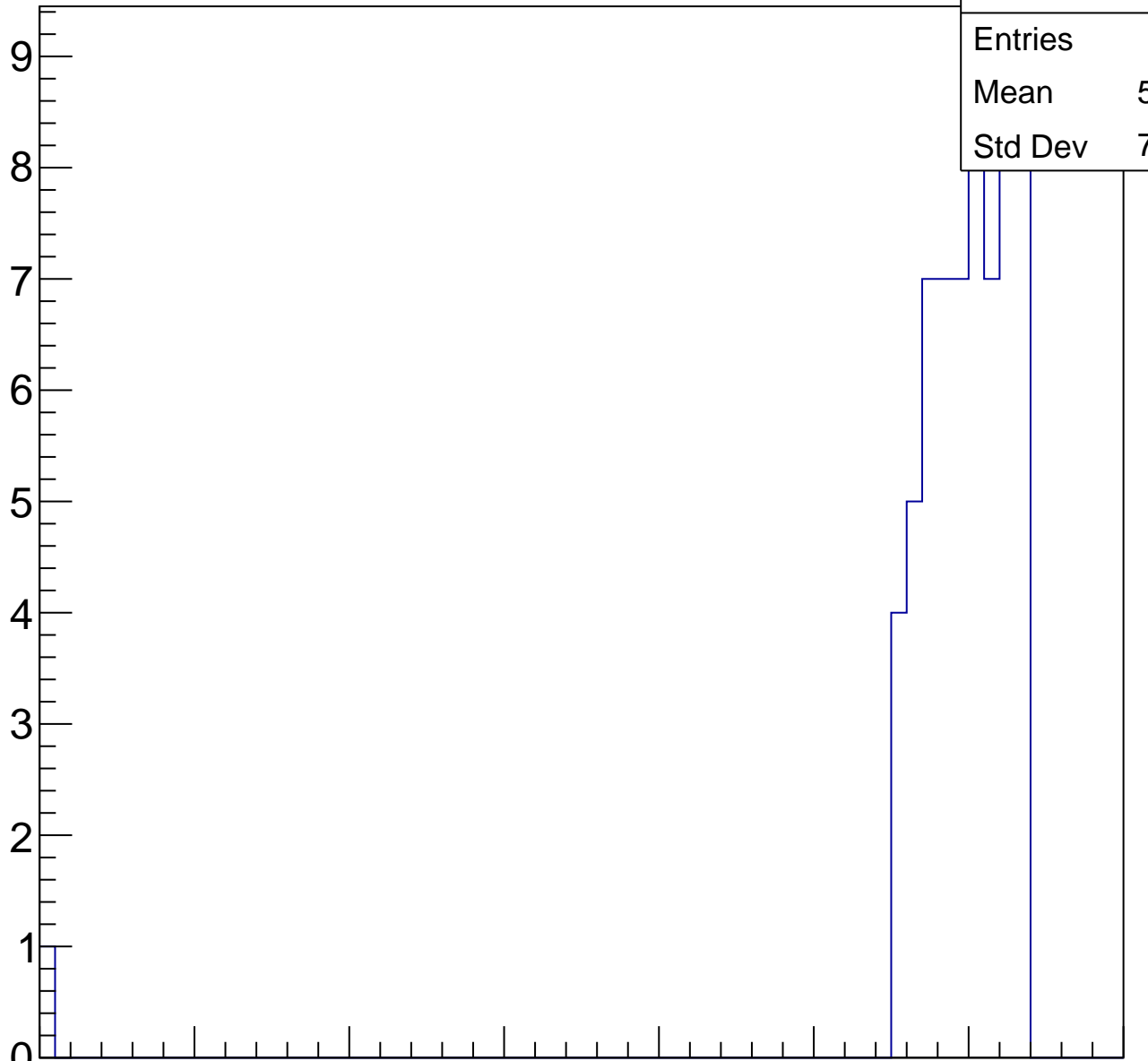
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 64 |
| Mean | 58.55 |
| Std Dev | 7.764 |

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch23, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.36 |
| Std Dev | 4.93 |

Gaus mean : 30.6833

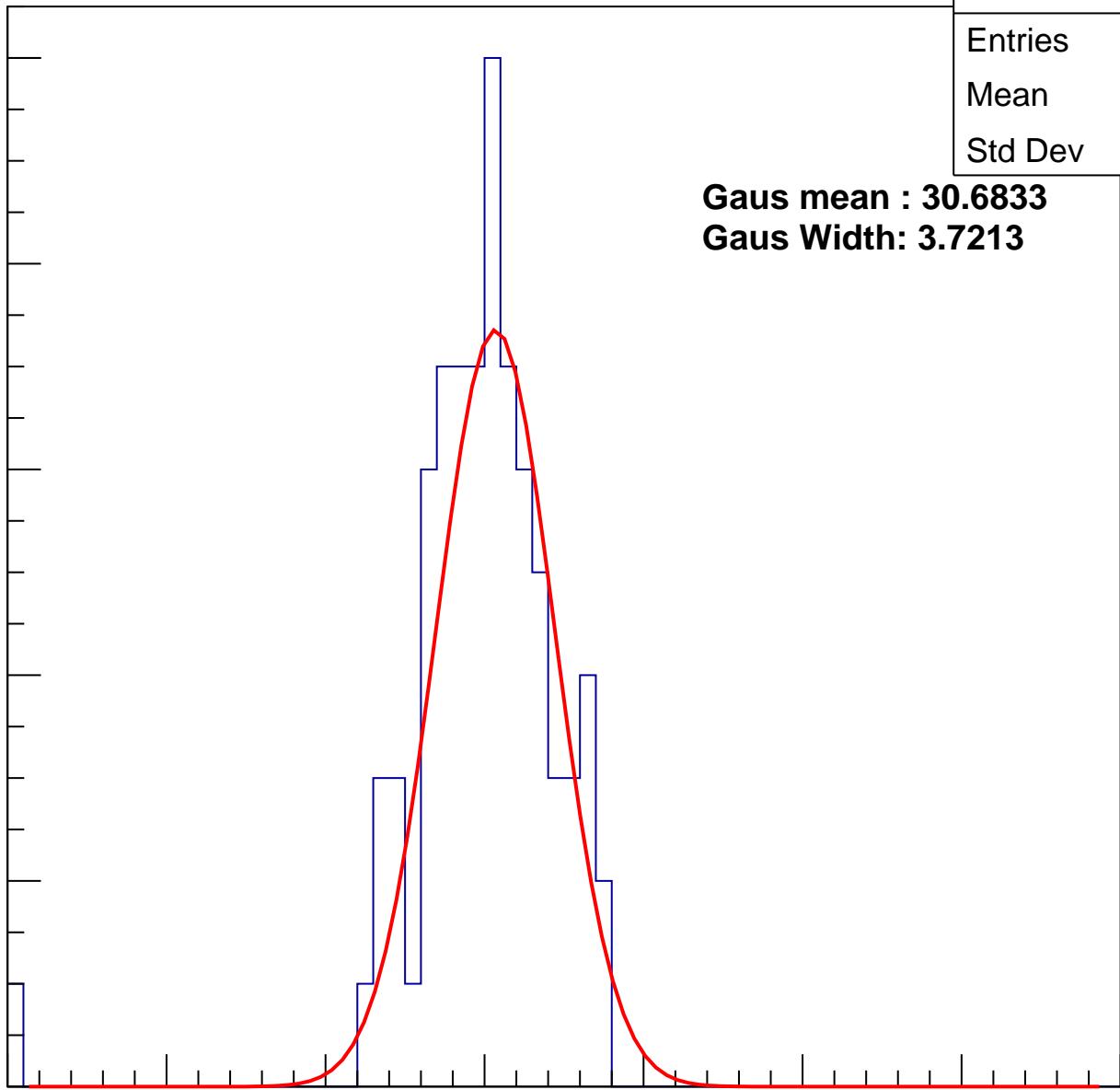
Gaus Width: 3.7213

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch23, adc1

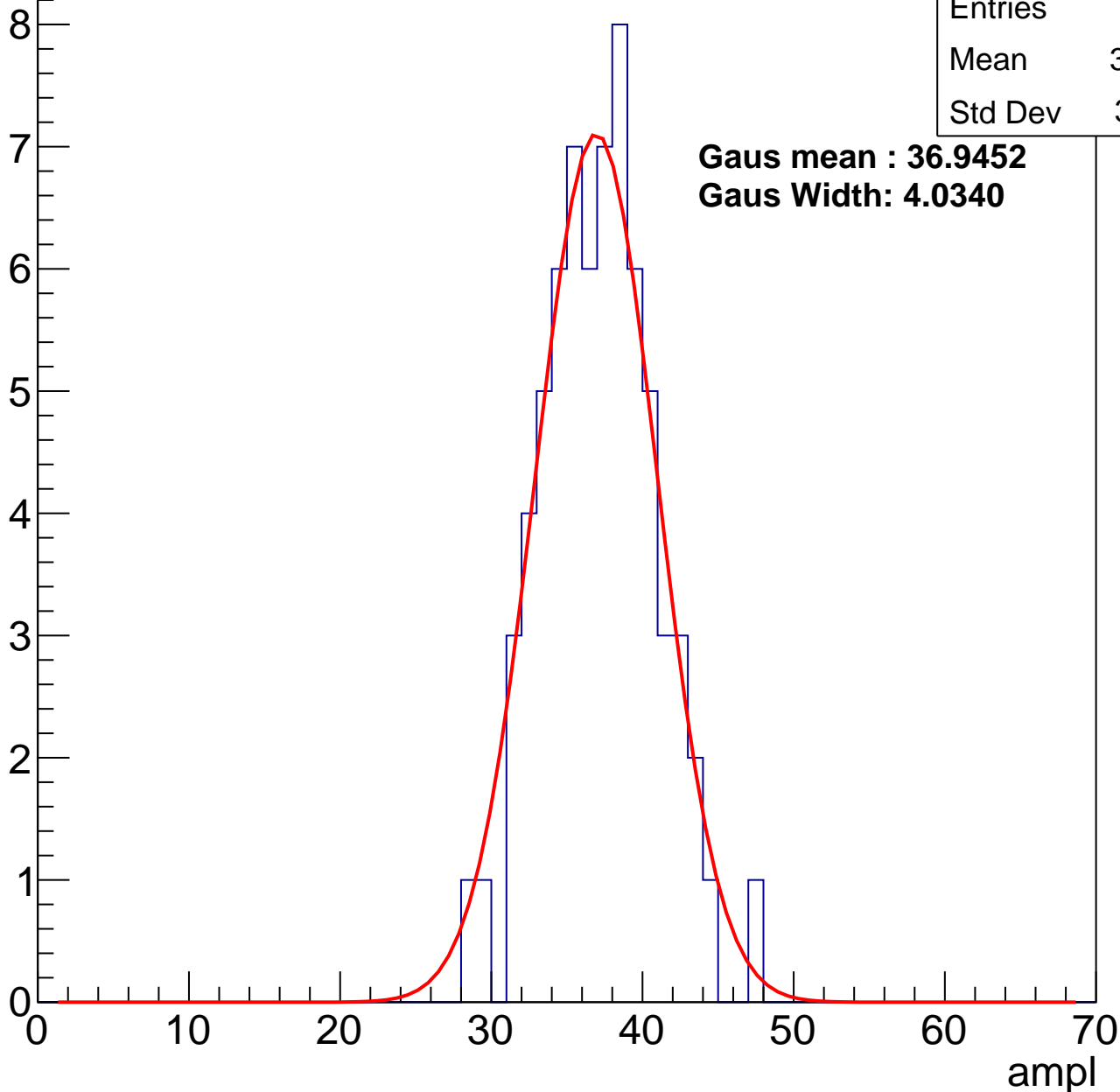
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.68 |
| Std Dev | 3.681 |

Gaus mean : 36.9452

Gaus Width: 4.0340



B1L100S, U7-ch23, adc2

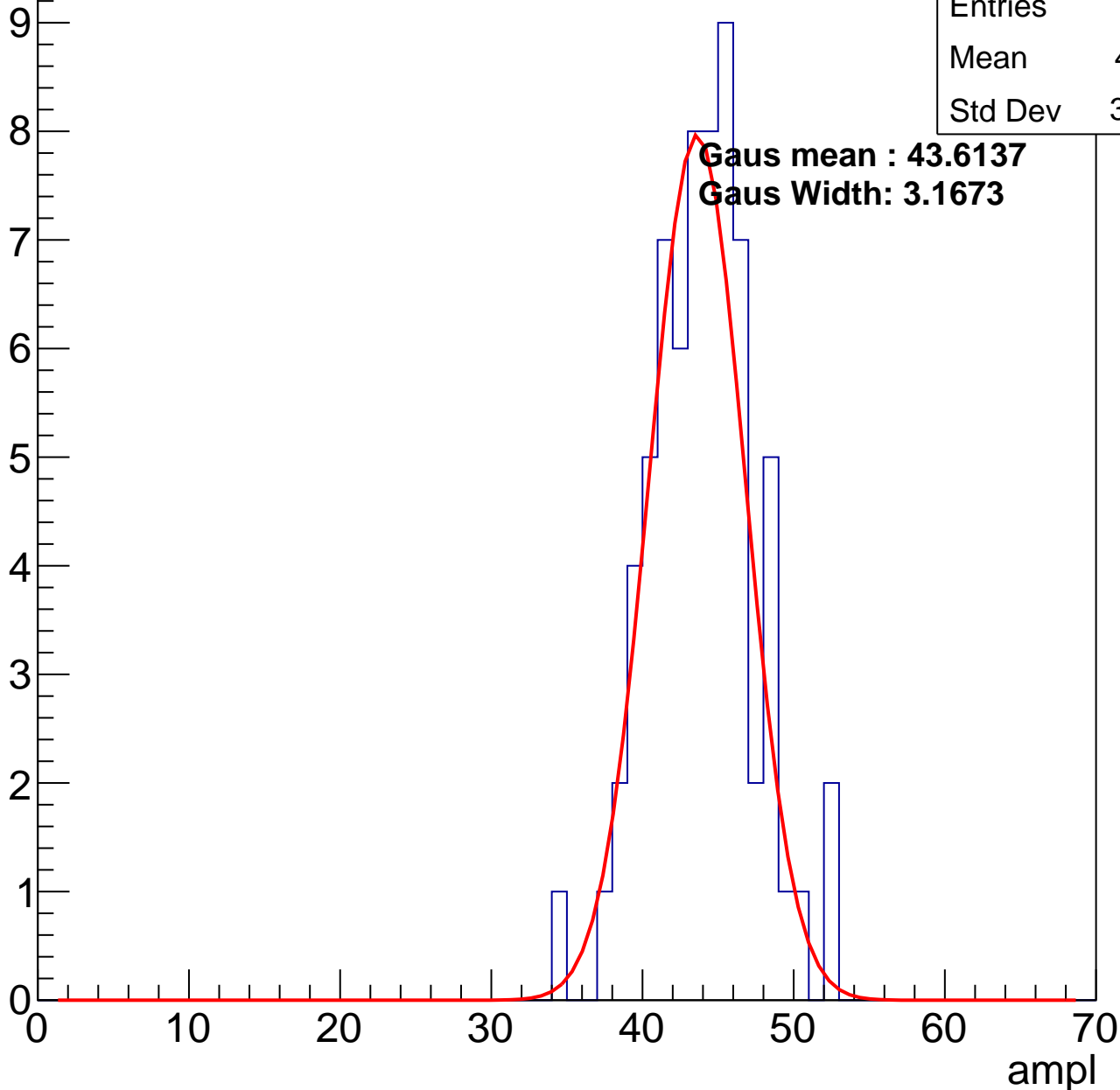
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.51 |
| Std Dev | 3.412 |

Gaus mean : 43.6137

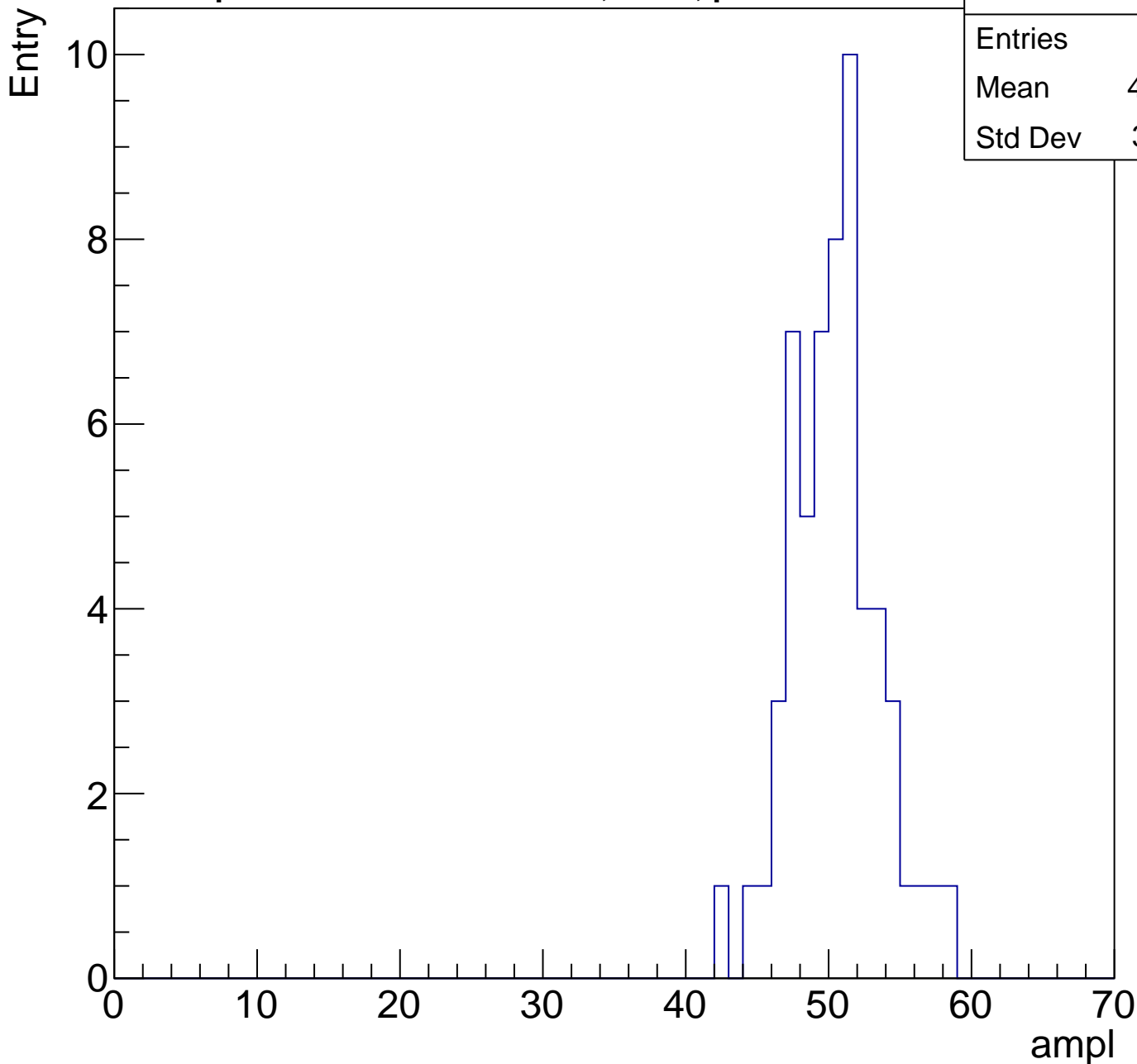
Gaus Width: 3.1673



B1L100S, U7-ch23, adc3

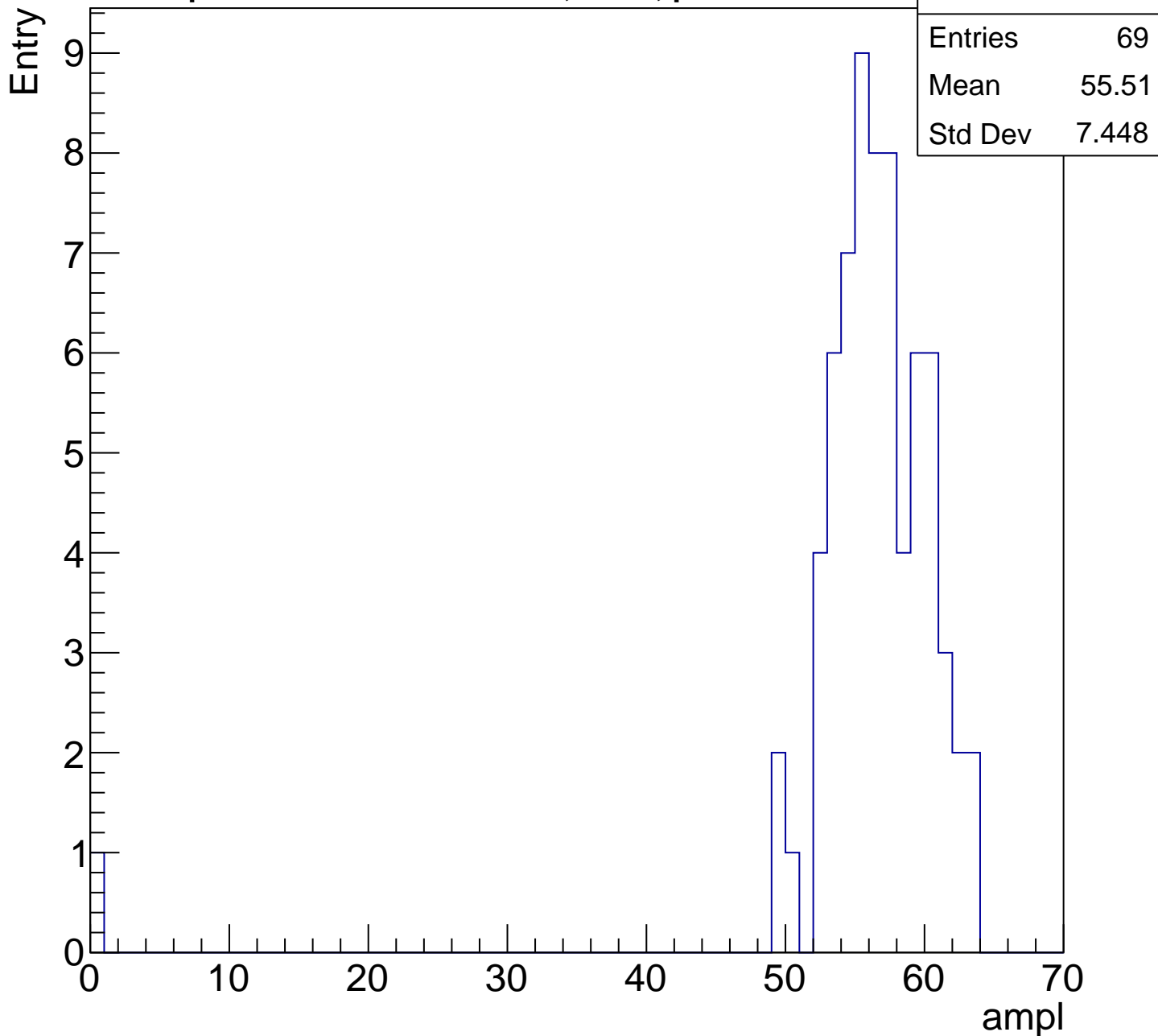
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 58 |
| Mean | 49.98 |
| Std Dev | 3.071 |



B1L100S, U7-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

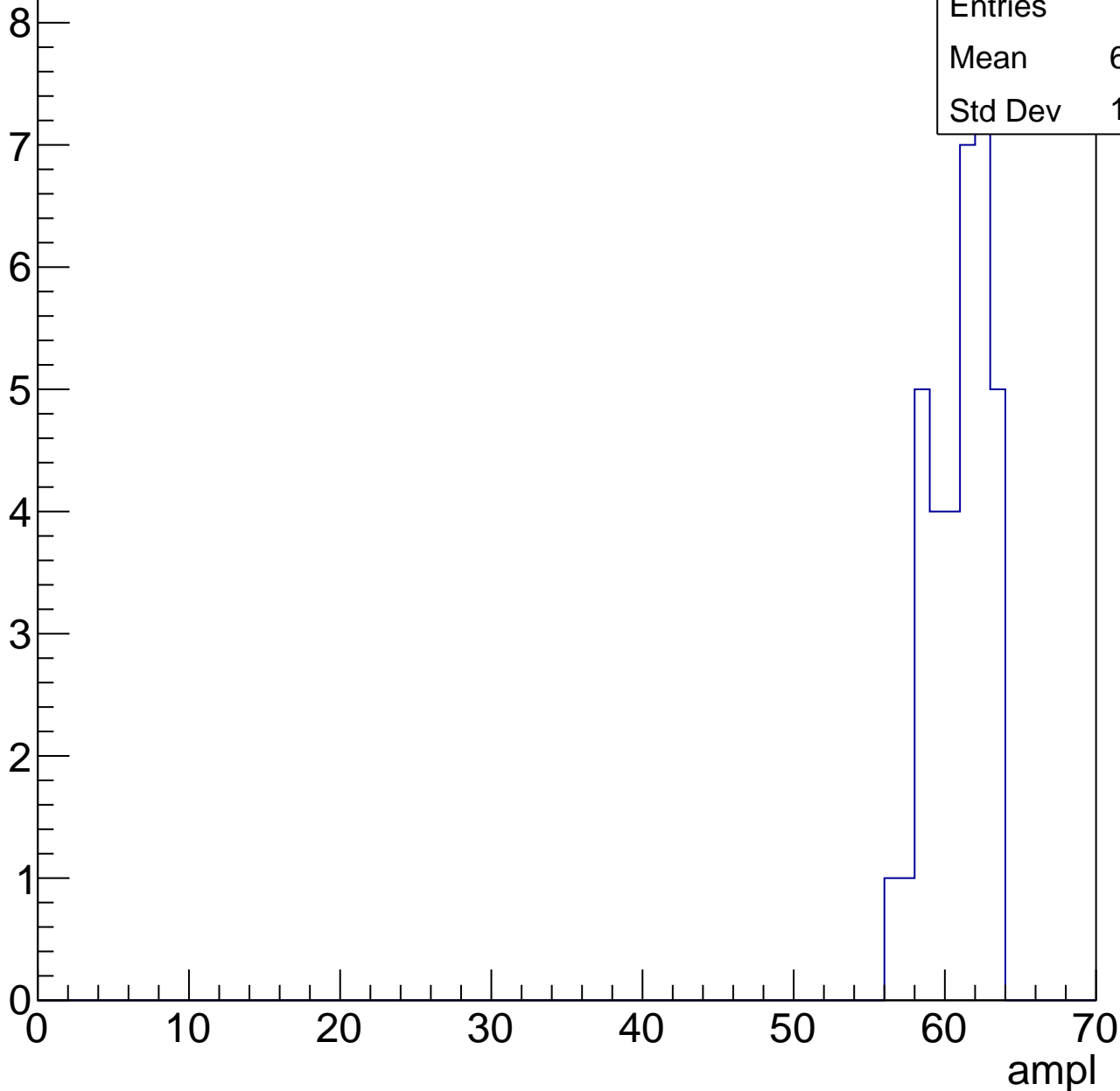


B1L100S, U7-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

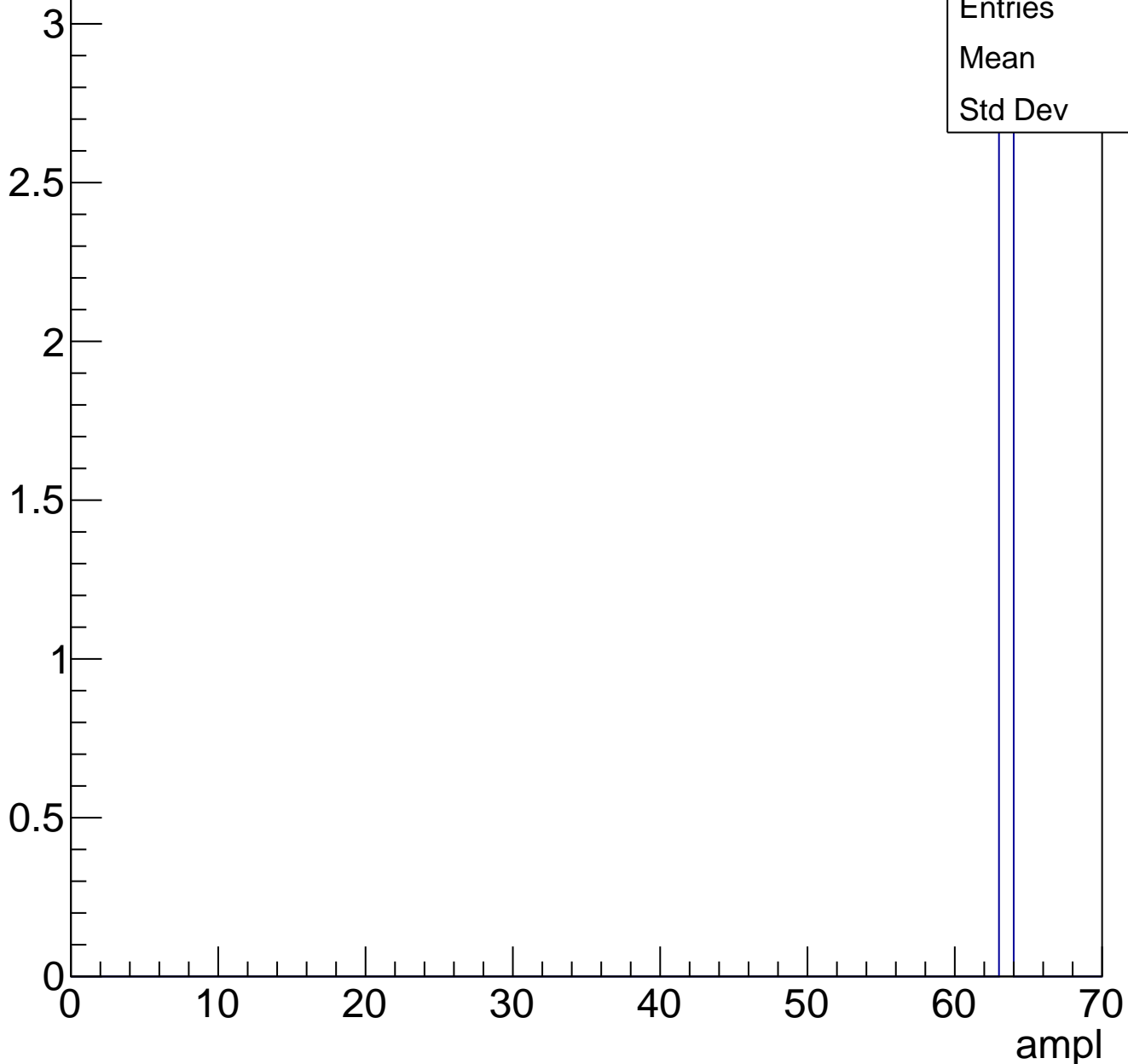
| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.49 |
| Std Dev | 1.888 |



B1L100S, U7-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch24, adc0

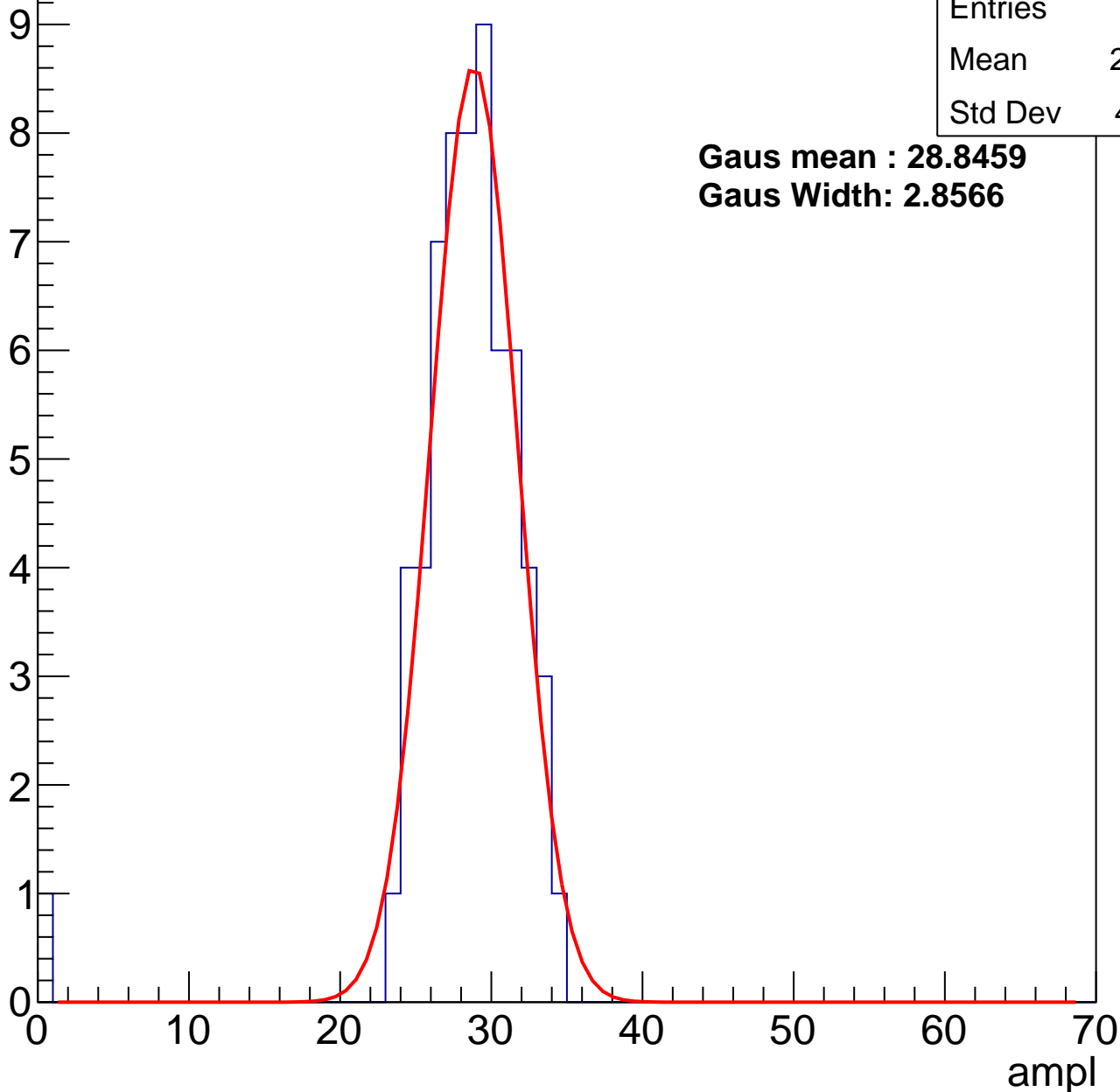
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 27.89 |
| Std Dev | 4.411 |

Gaus mean : 28.8459

Gaus Width: 2.8566



B1L100S, U7-ch24, adc1

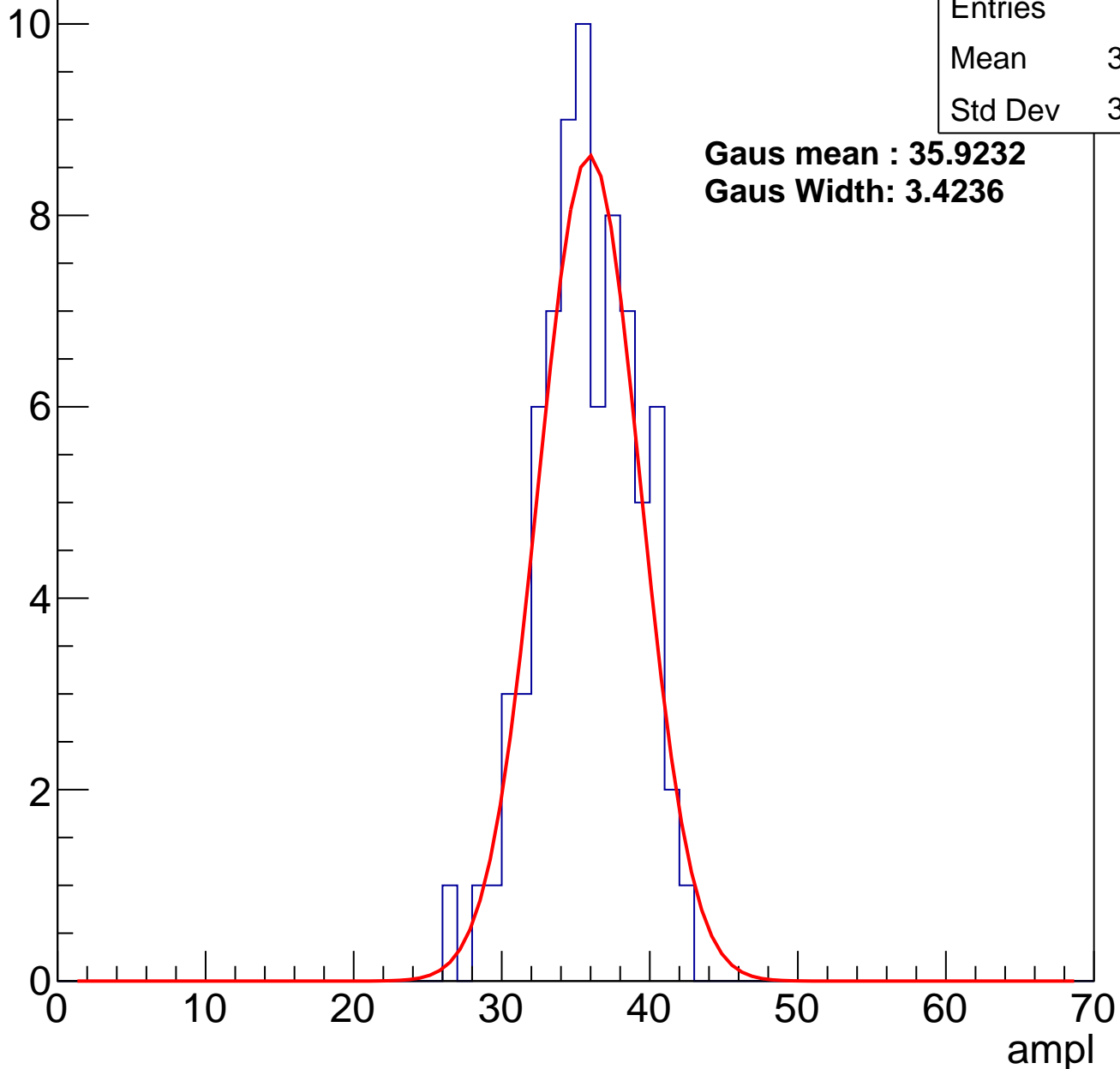
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.29 |
| Std Dev | 3.296 |

Gaus mean : 35.9232

Gaus Width: 3.4236

Entry



B1L100S, U7-ch24, adc2

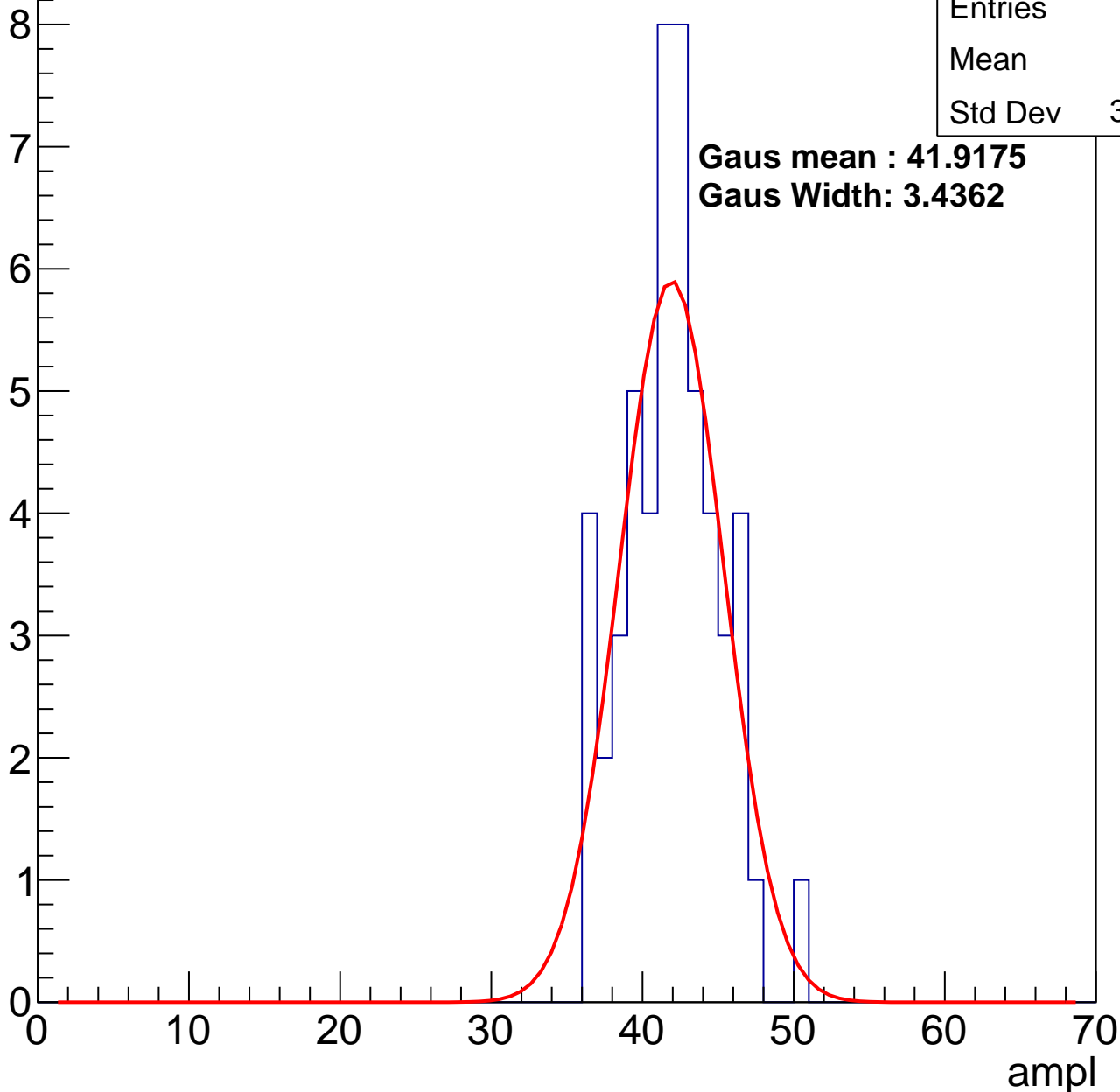
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 41.5 |
| Std Dev | 3.098 |

Gaus mean : 41.9175

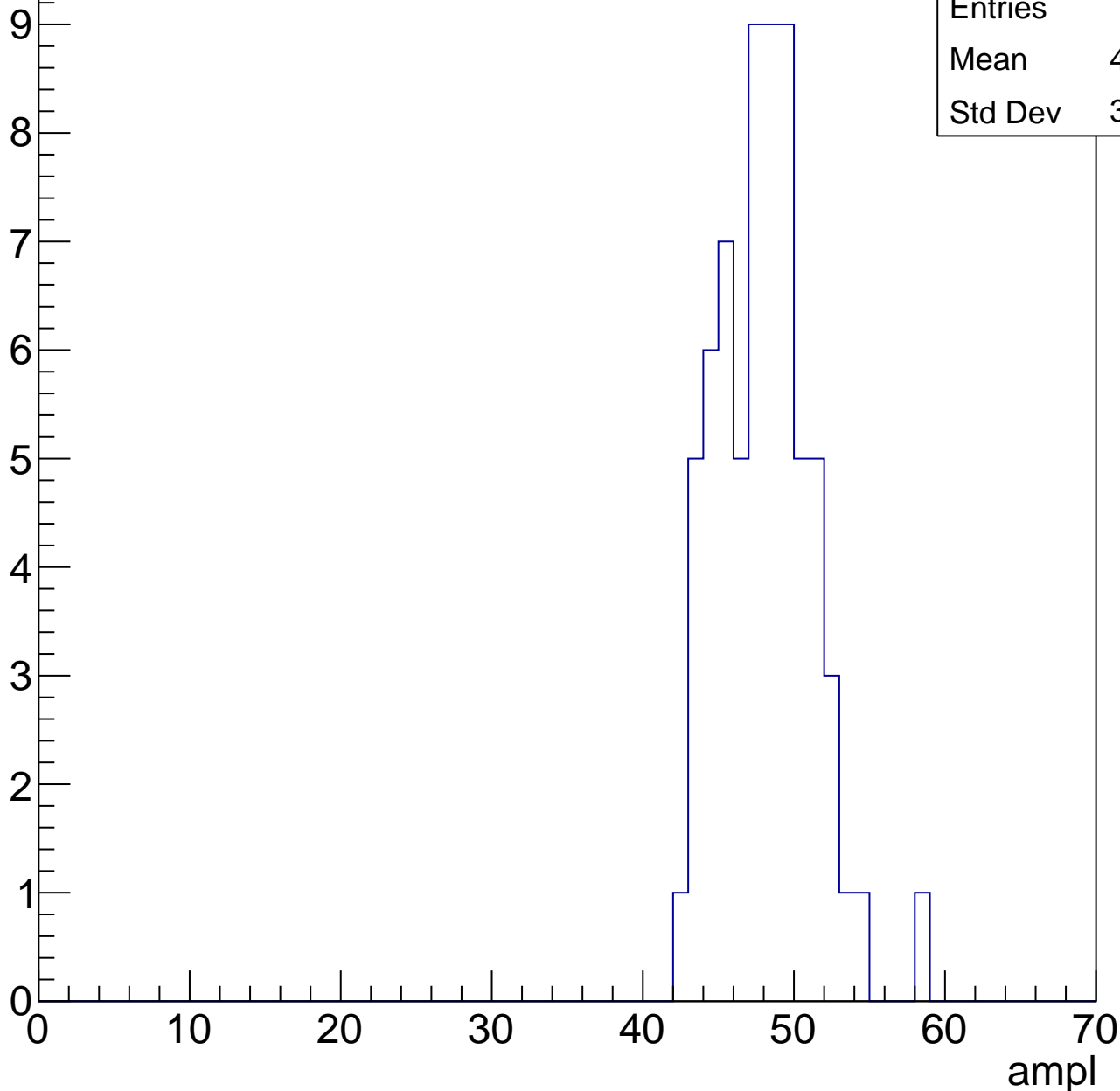
Gaus Width: 3.4362



B1L100S, U7-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

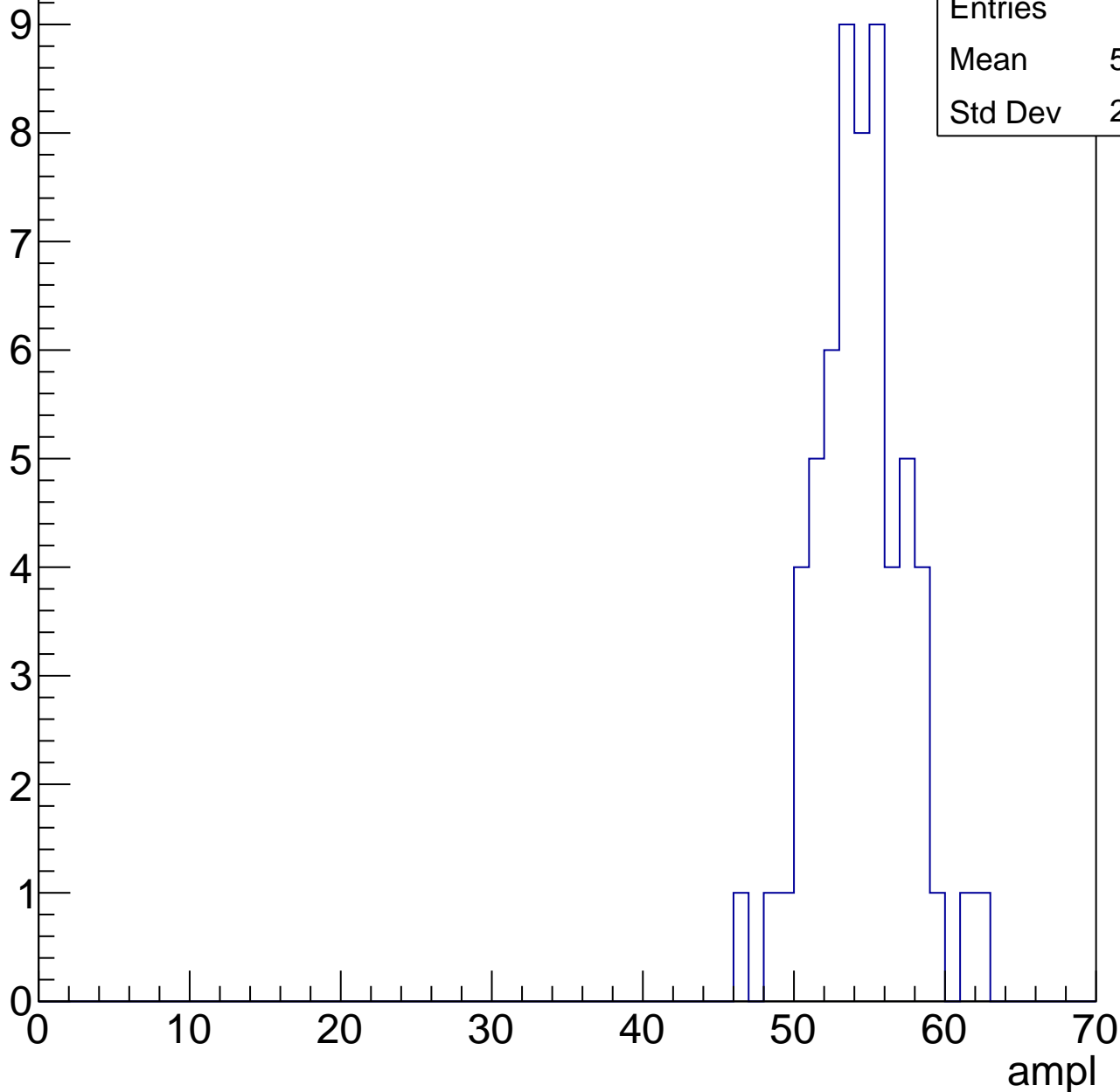


| | |
|---------|-------|
| Entries | 67 |
| Mean | 47.58 |
| Std Dev | 3.043 |

B1L100S, U7-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

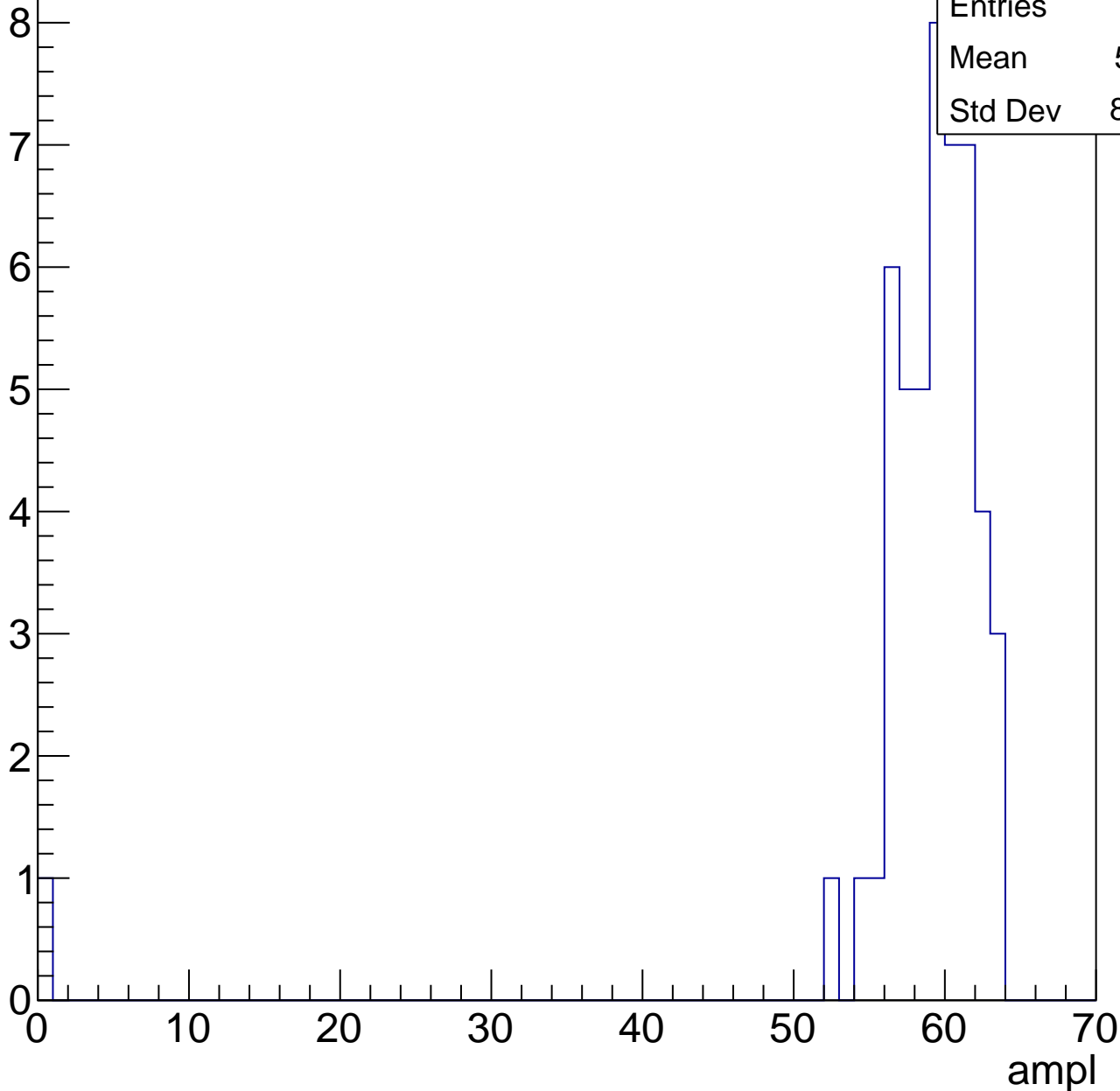


B1L100S, U7-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.71 |
| Std Dev | 8.676 |



B1L100S, U7-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 18

Mean 61.78

Std Dev 1.272

ampl

0

10

20

30

40

50

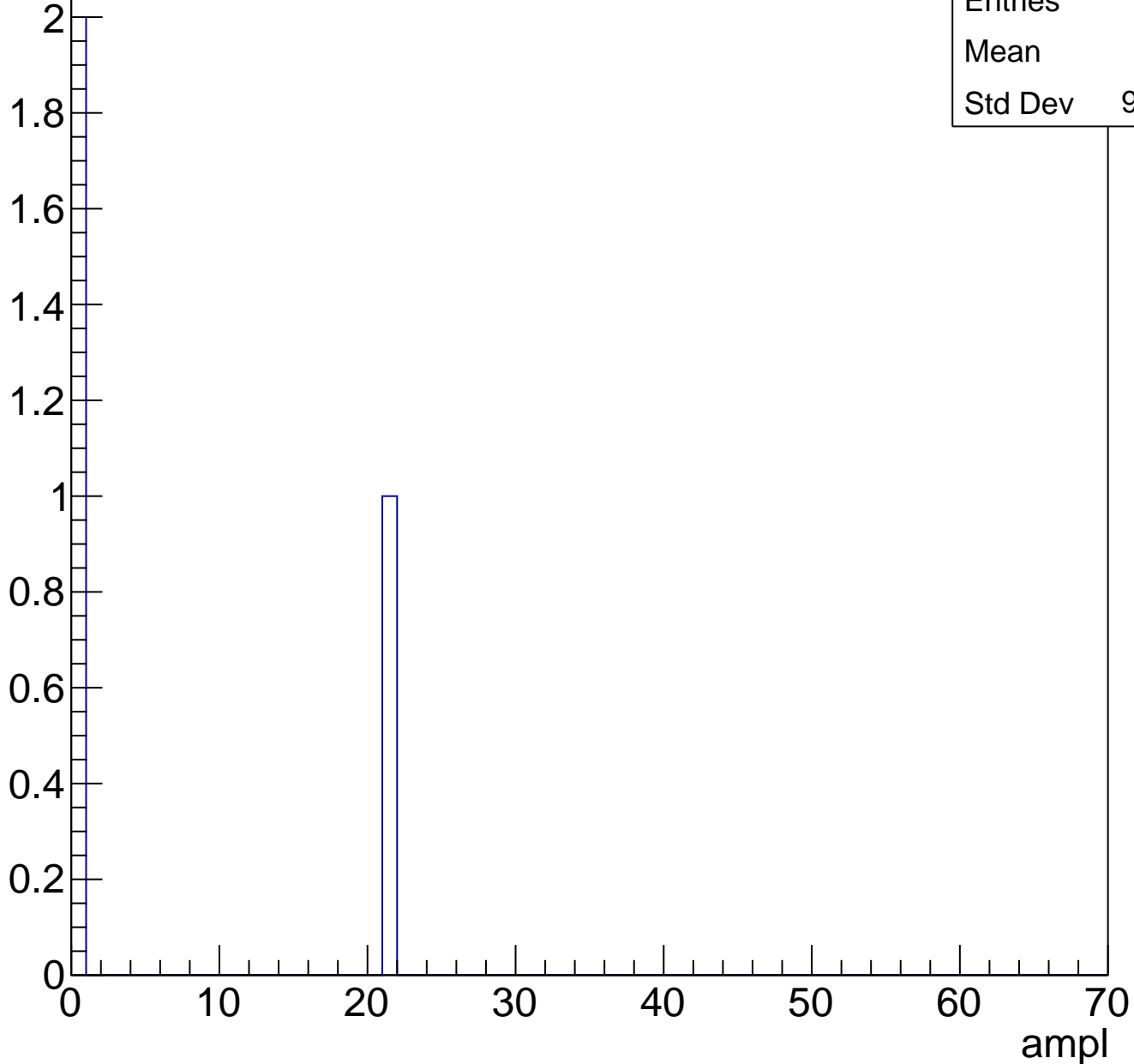
60

70

B1L100S, U7-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7 |
| Std Dev | 9.899 |

B1L100S, U7-ch25, adc0

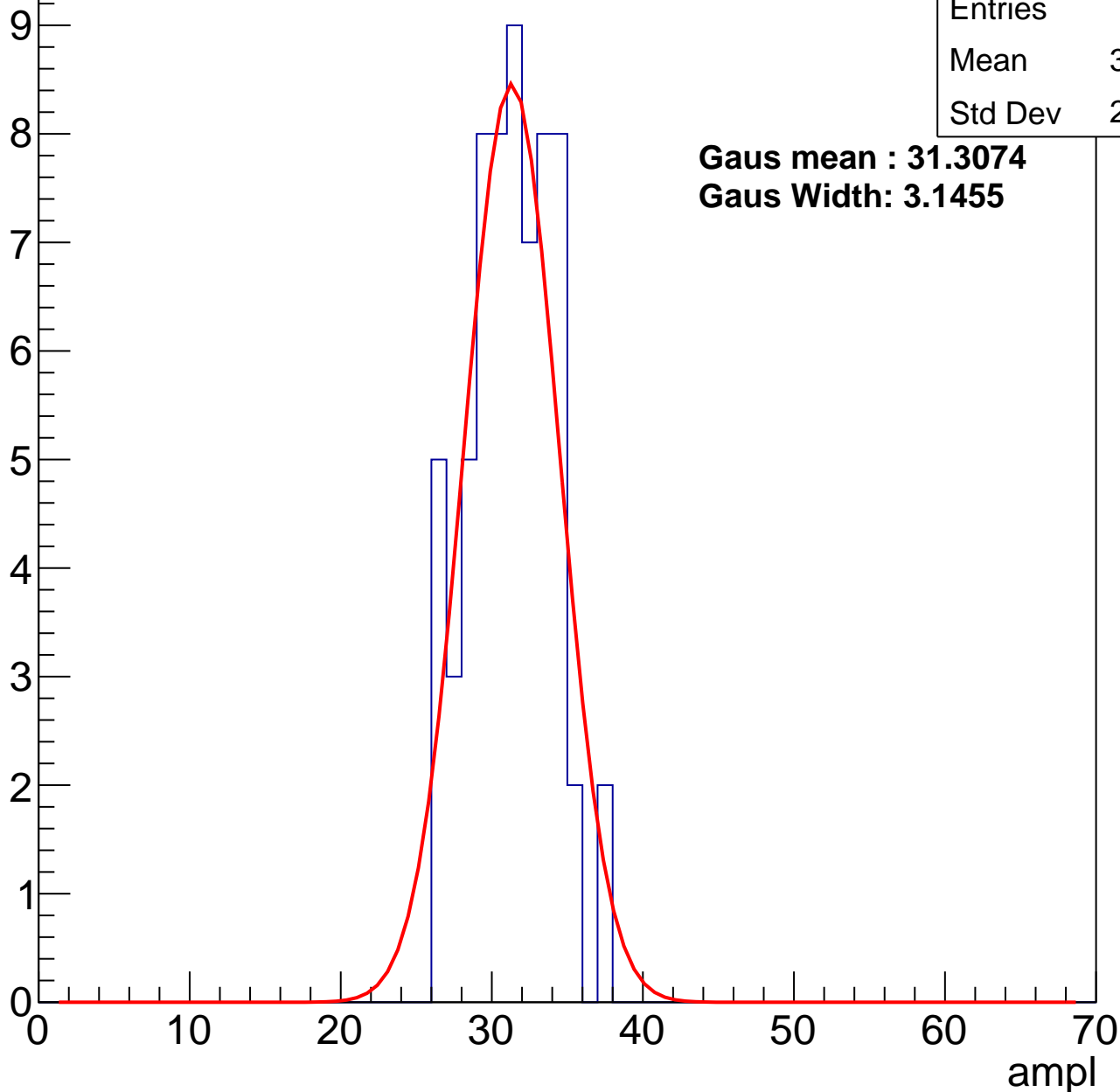
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 30.86 |
| Std Dev | 2.694 |

Gaus mean : 31.3074

Gaus Width: 3.1455



B1L100S, U7-ch25, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 63 |
| Mean | 37.13 |
| Std Dev | 2.728 |

Gaus mean : 38.1000

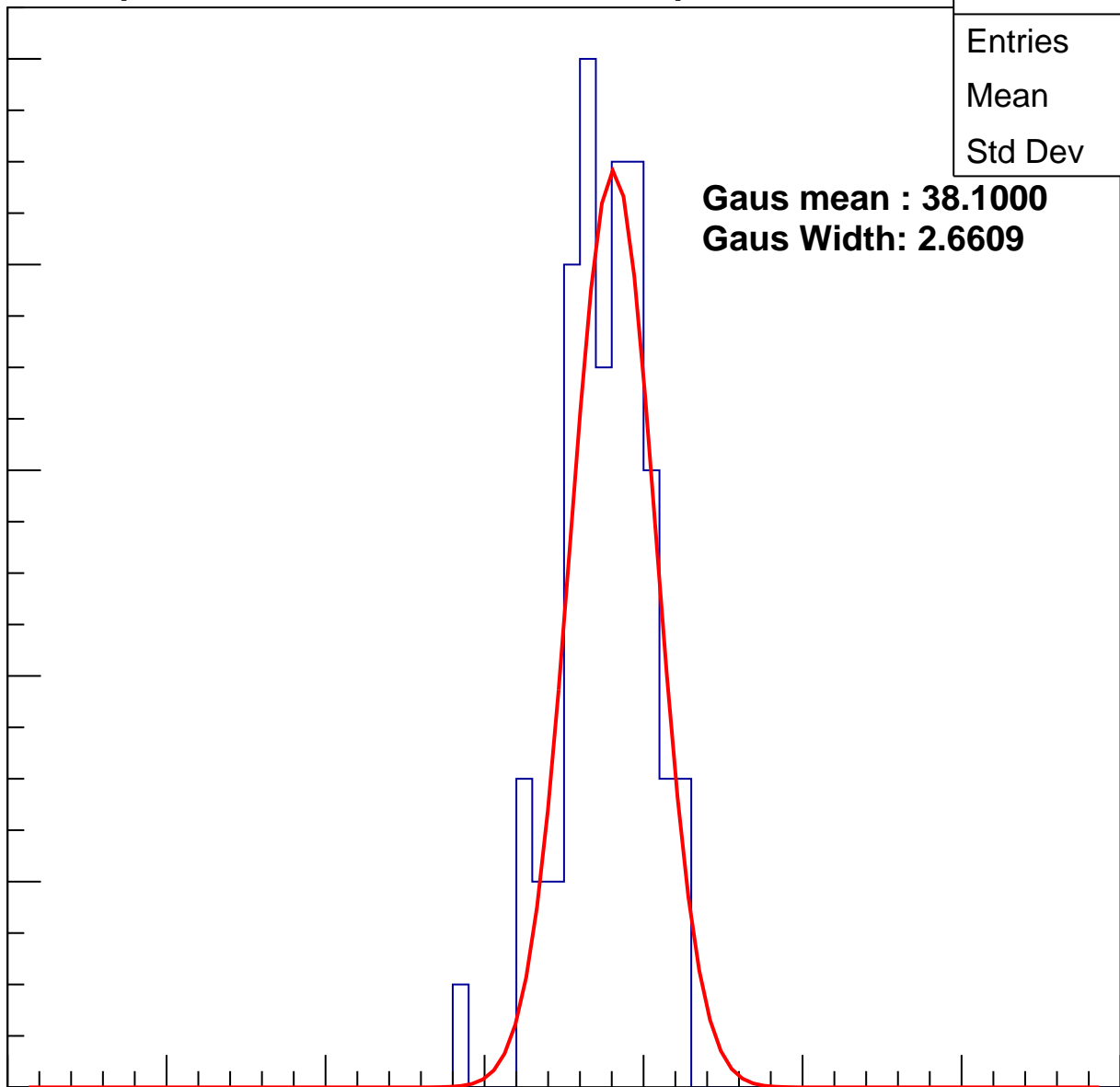
Gaus Width: 2.6609

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch25, adc2

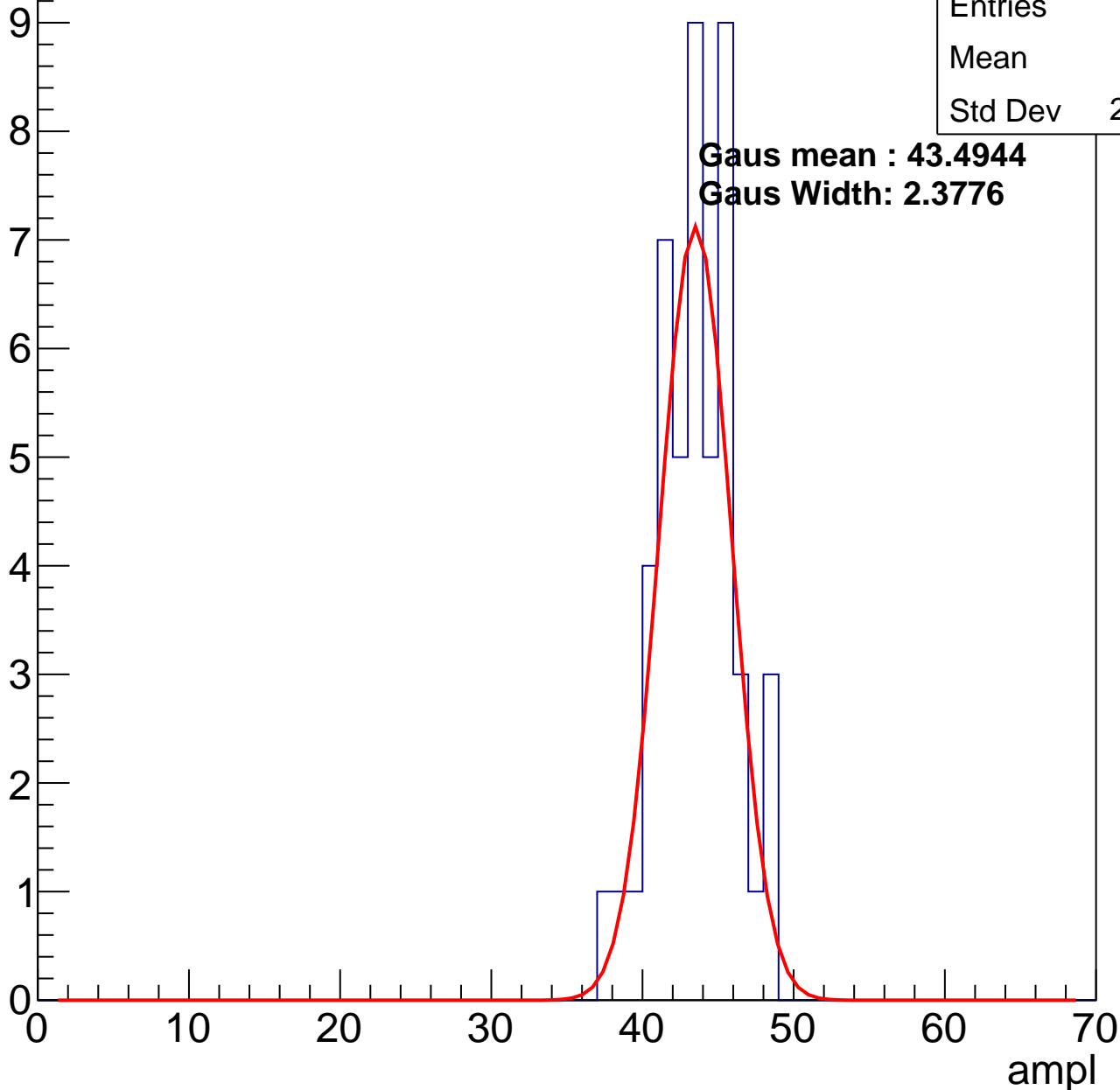
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.1 |
| Std Dev | 2.493 |

Gaus mean : 43.4944

Gaus Width: 2.3776

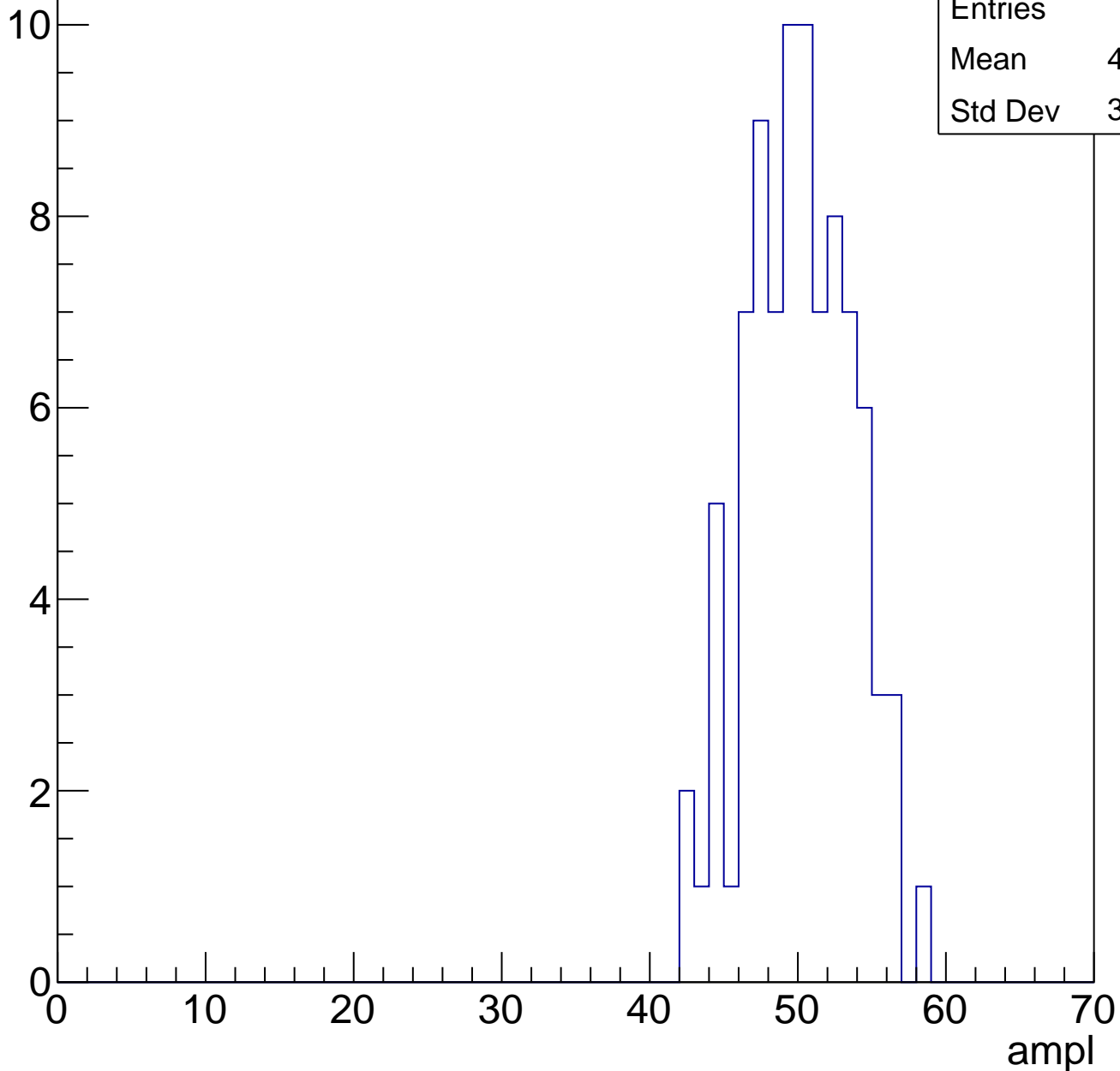


B1L100S, U7-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 87 |
| Mean | 49.68 |
| Std Dev | 3.472 |

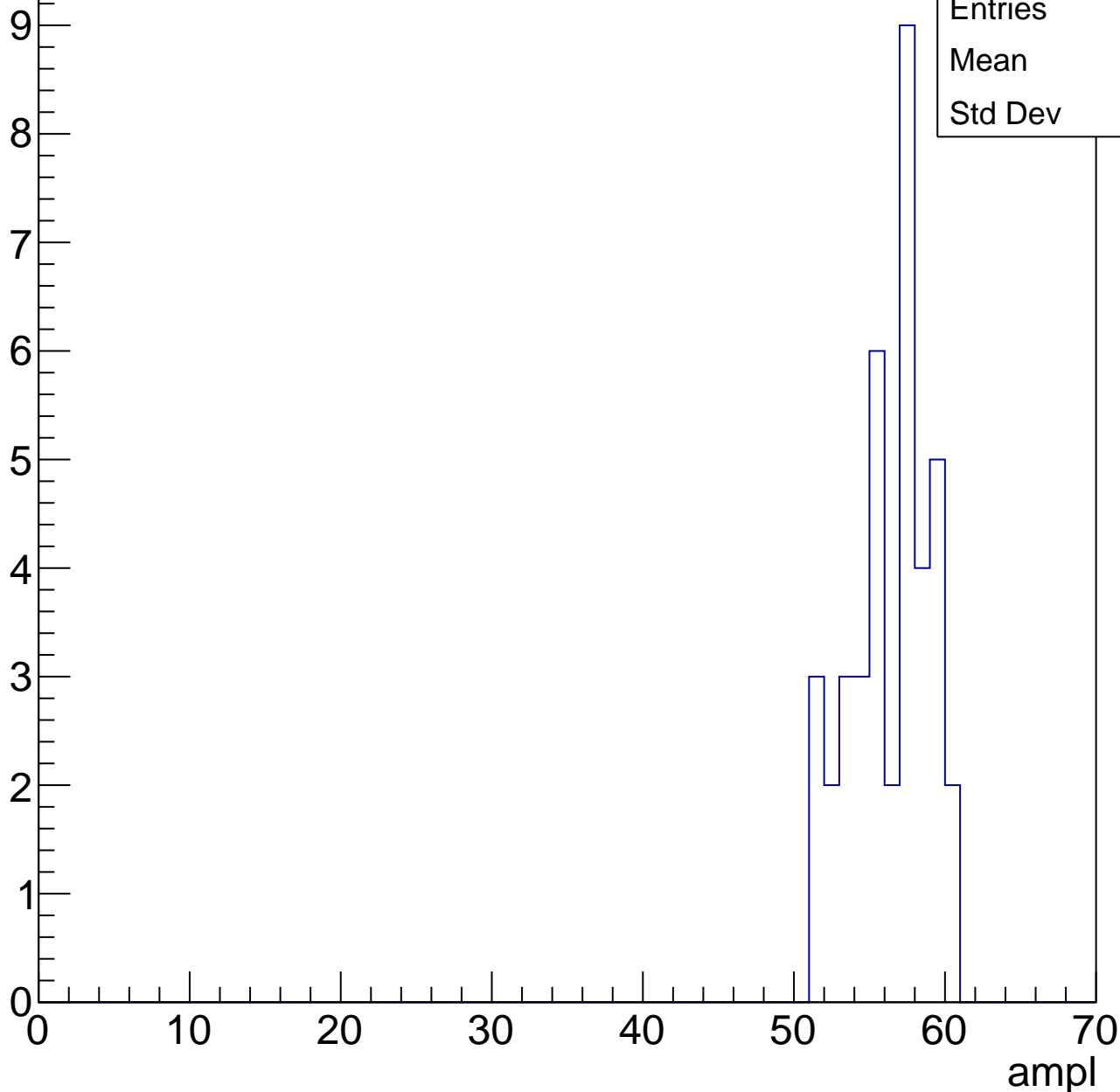
Entry



B1L100S, U7-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

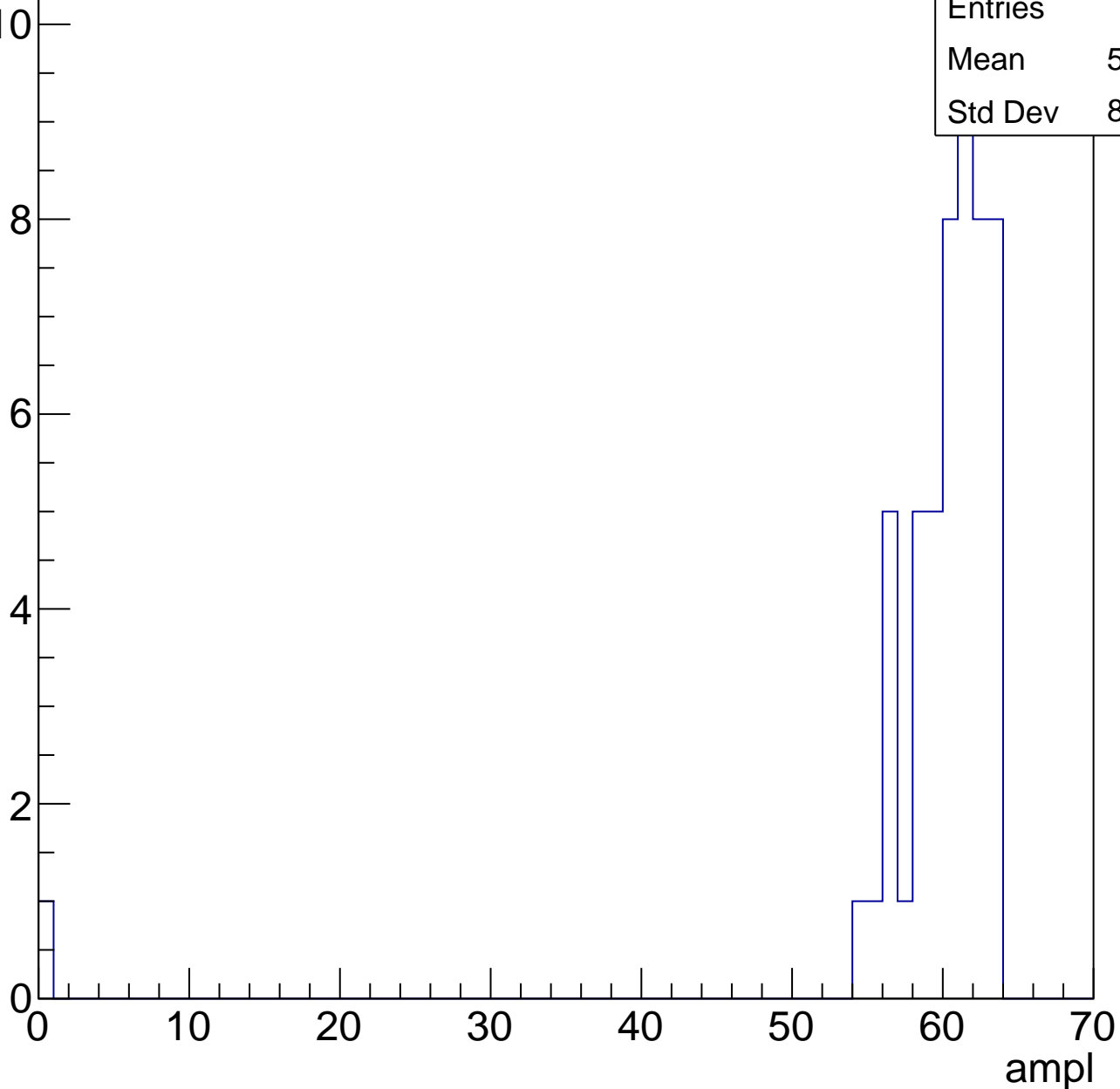


B1L100S, U7-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

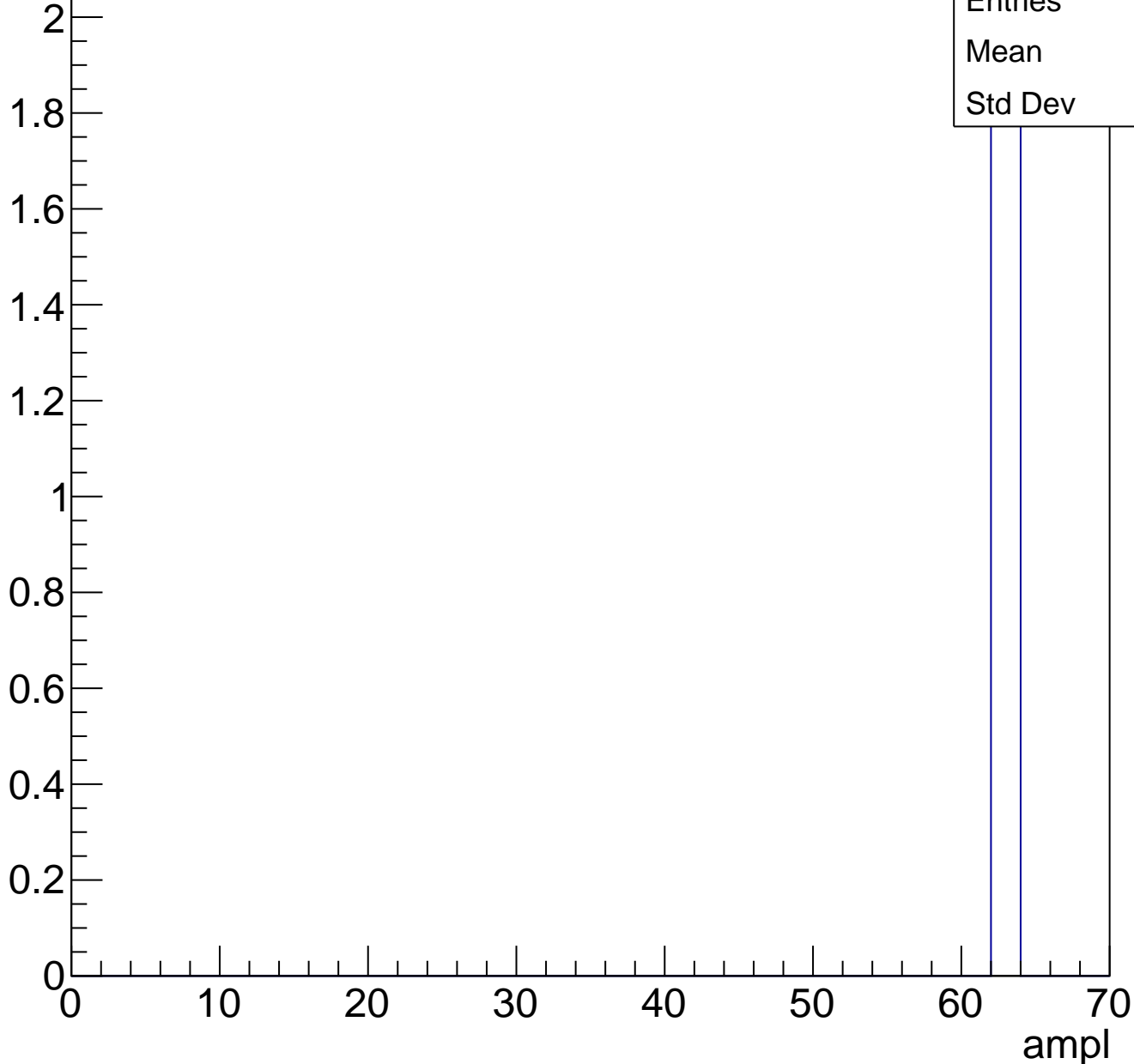
| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.89 |
| Std Dev | 8.493 |



B1L100S, U7-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 12.5 |
| Std Dev | 12.5 |

B1L100S, U7-ch26, adc0

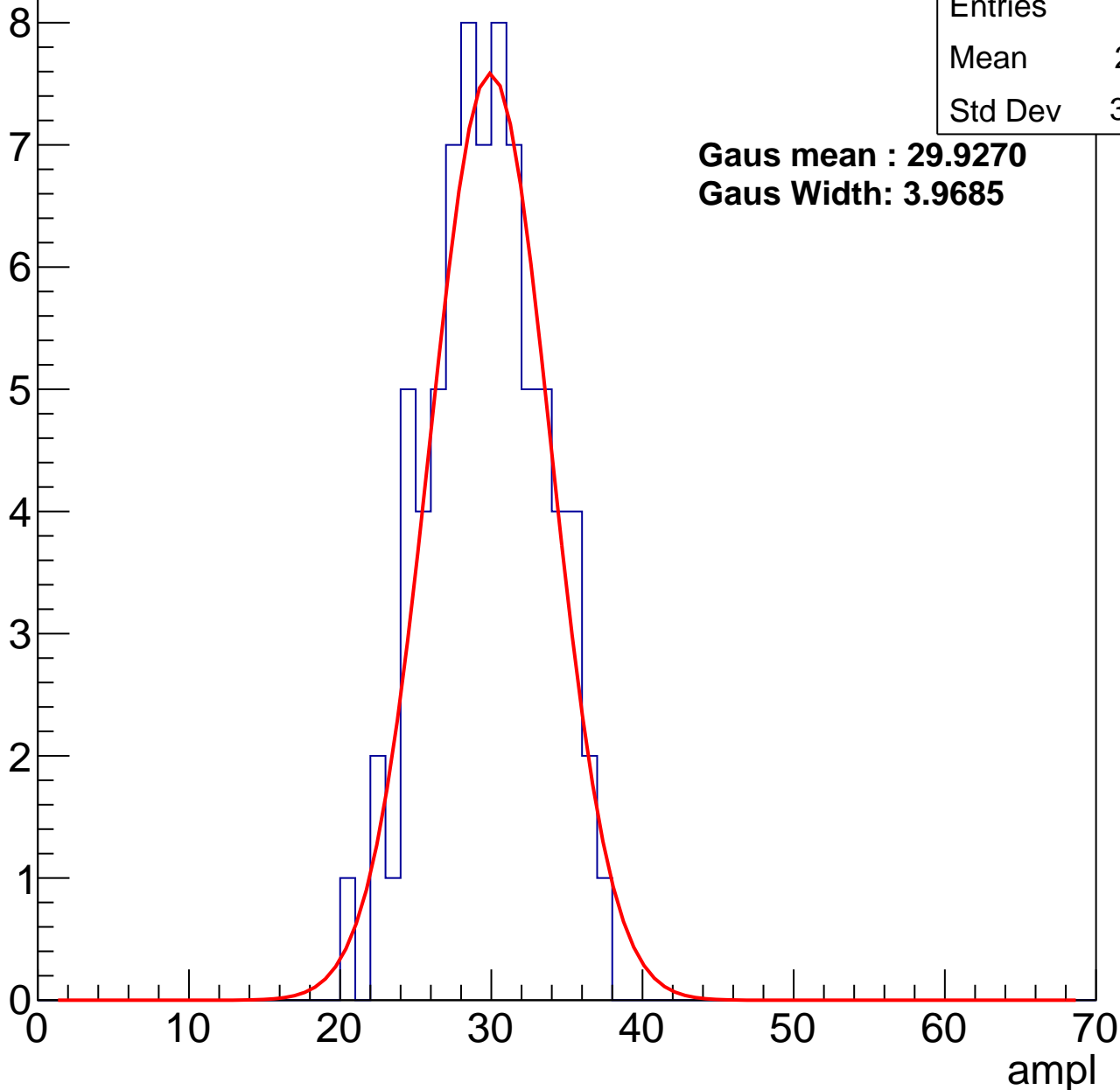
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.21 |
| Std Dev | 3.715 |

Gaus mean : 29.9270

Gaus Width: 3.9685



B1L100S, U7-ch26, adc1

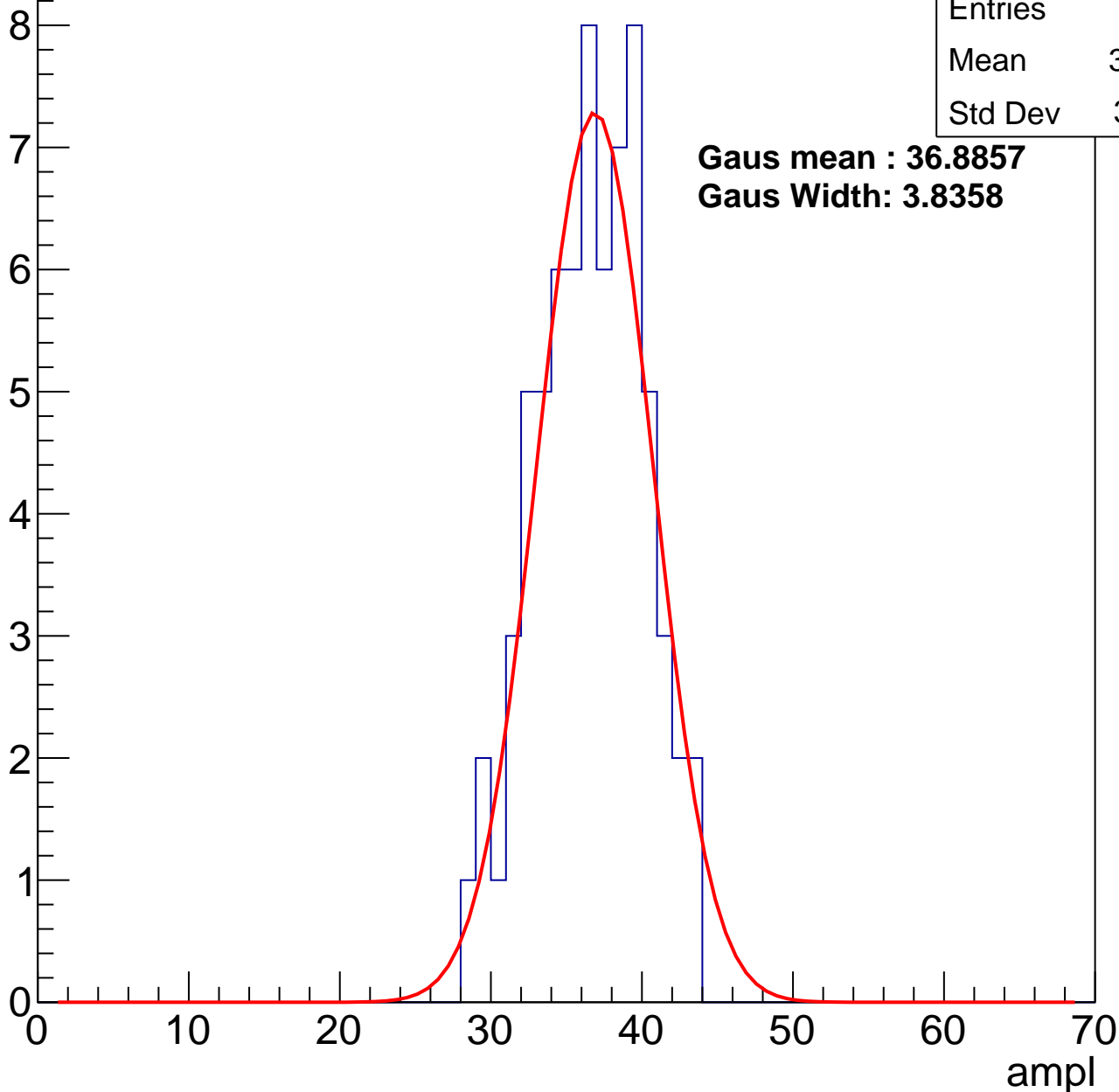
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 36.13 |
| Std Dev | 3.501 |

Gaus mean : 36.8857

Gaus Width: 3.8358



B1L100S, U7-ch26, adc2

calib_packv5_042523_0143.root, FC#4, port A2

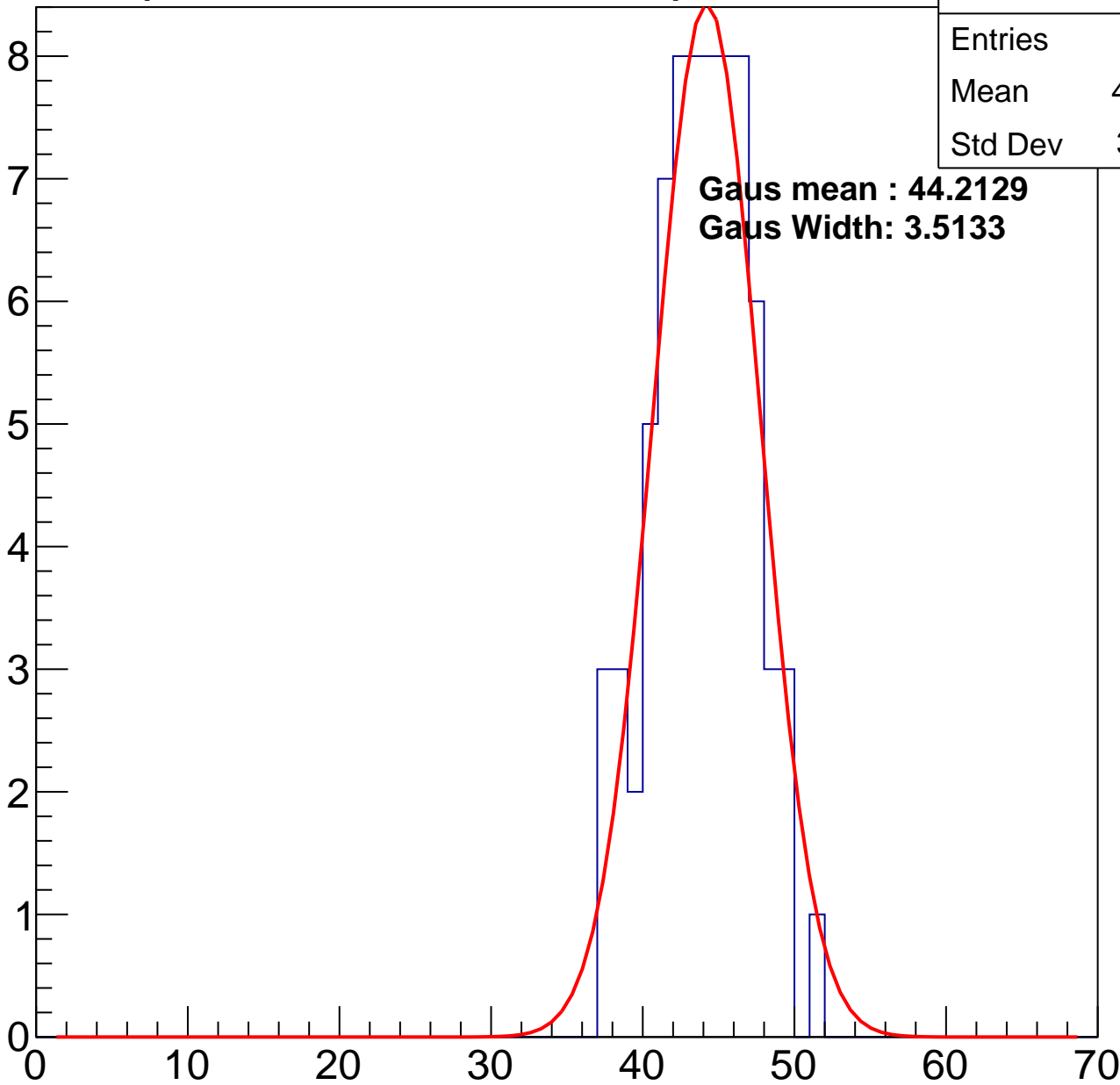
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.48 |
| Std Dev | 3.171 |

Gaus mean : 44.2129
Gaus Width: 3.5133

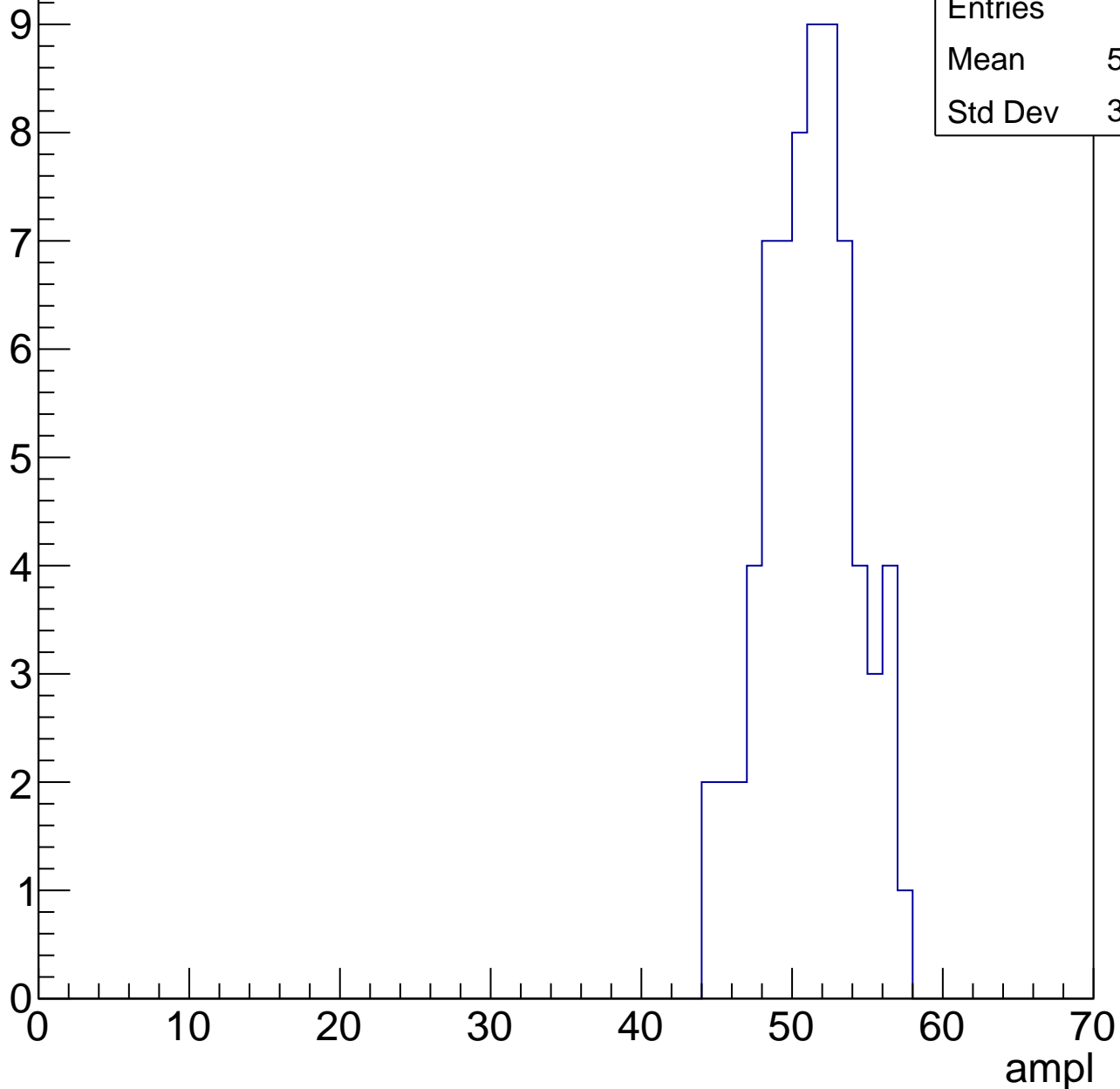
ampl



B1L100S, U7-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



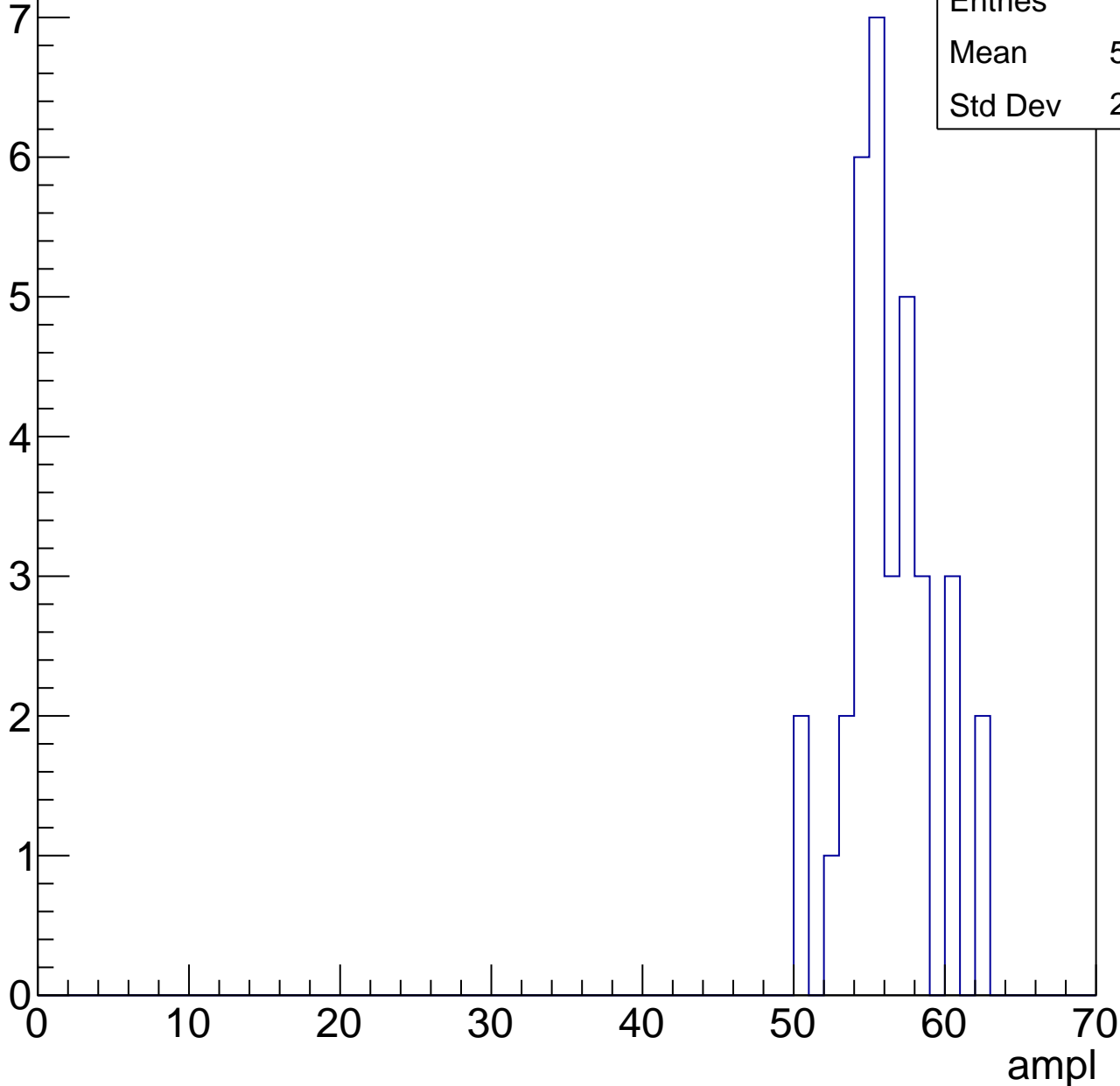
| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.68 |
| Std Dev | 3.029 |

B1L100S, U7-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

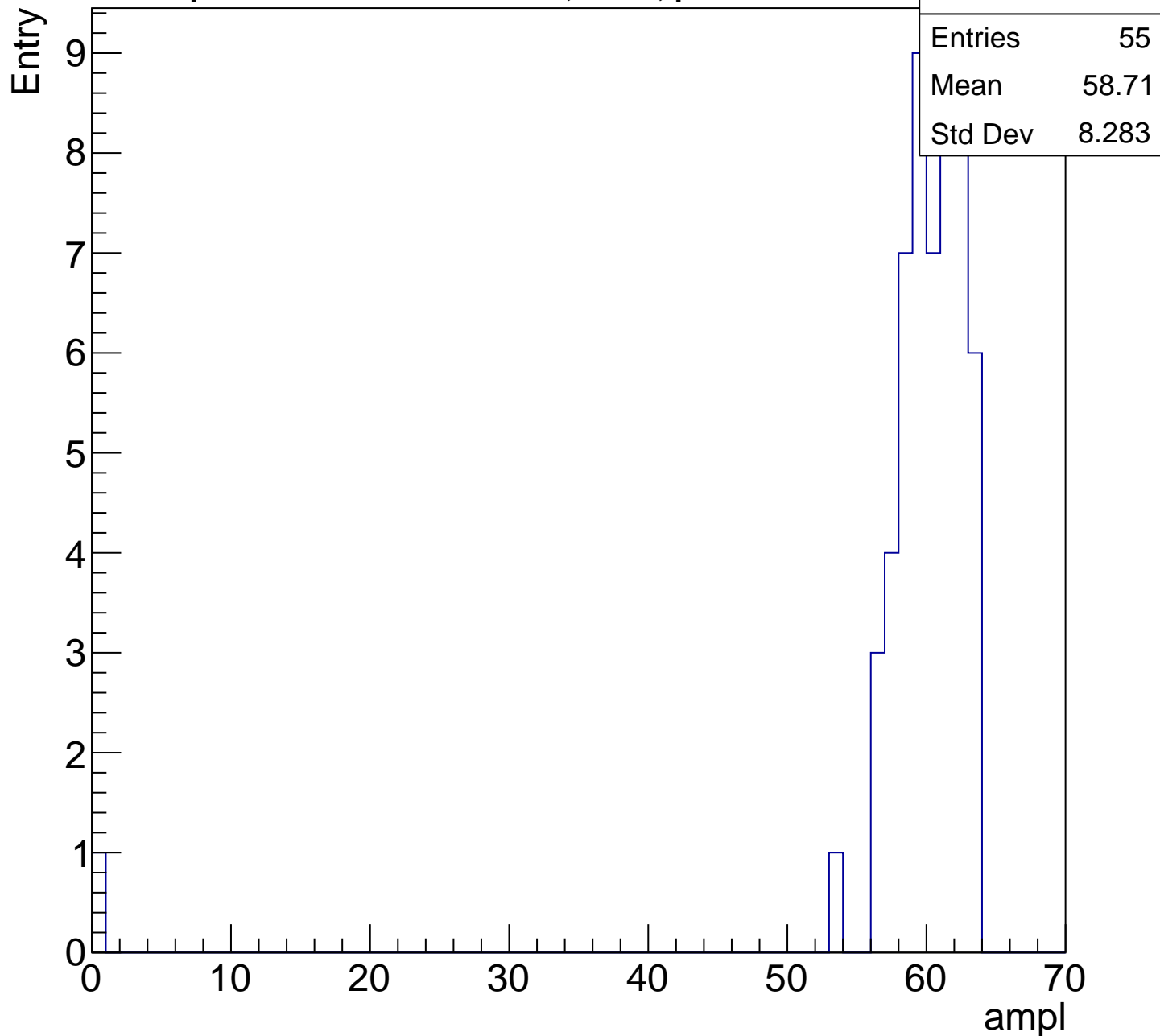
Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 55.82 |
| Std Dev | 2.833 |



B1L100S, U7-ch26, adc5

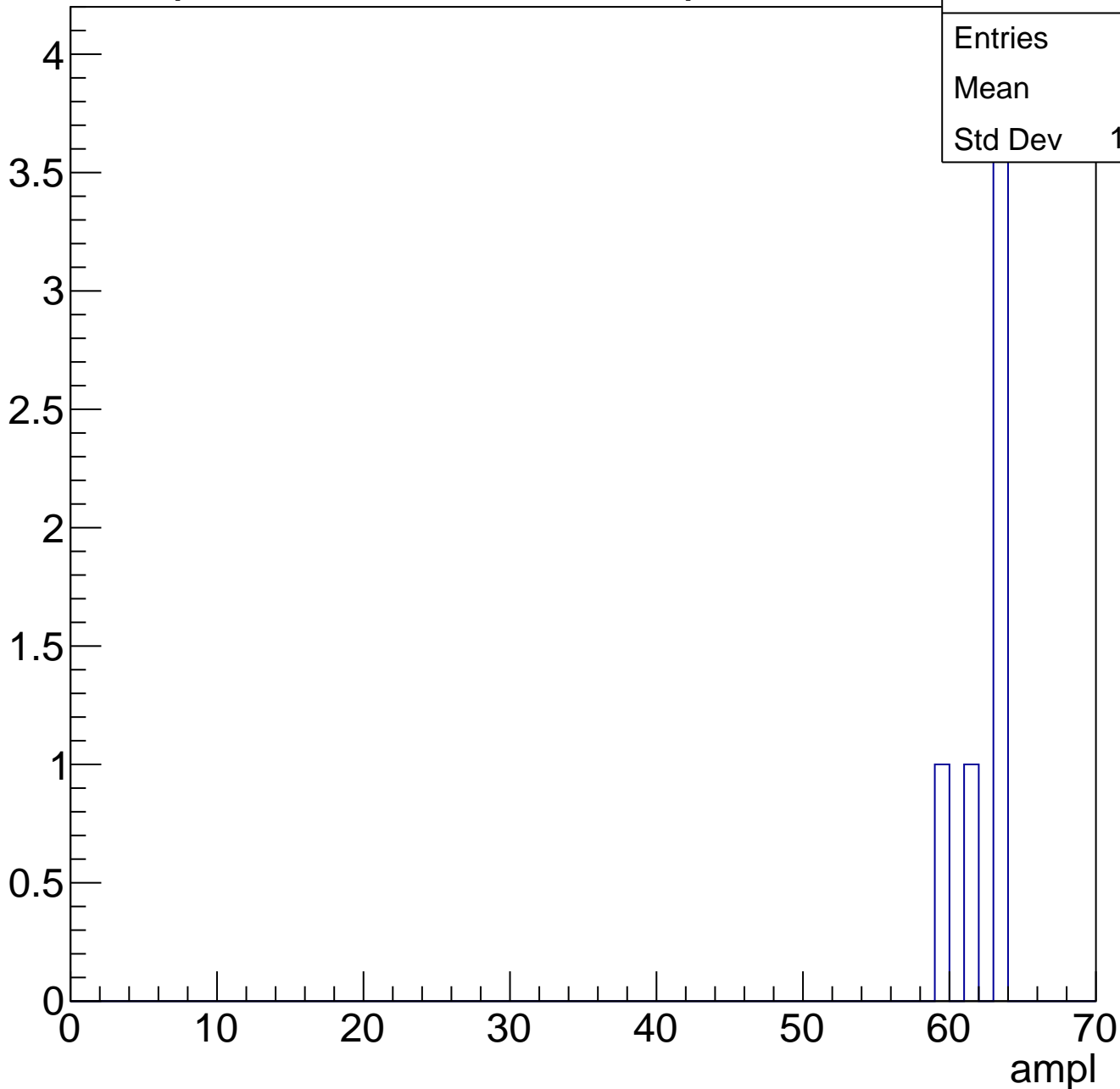
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch27, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 30.23 |
| Std Dev | 3.295 |

Gaus mean : 31.1209

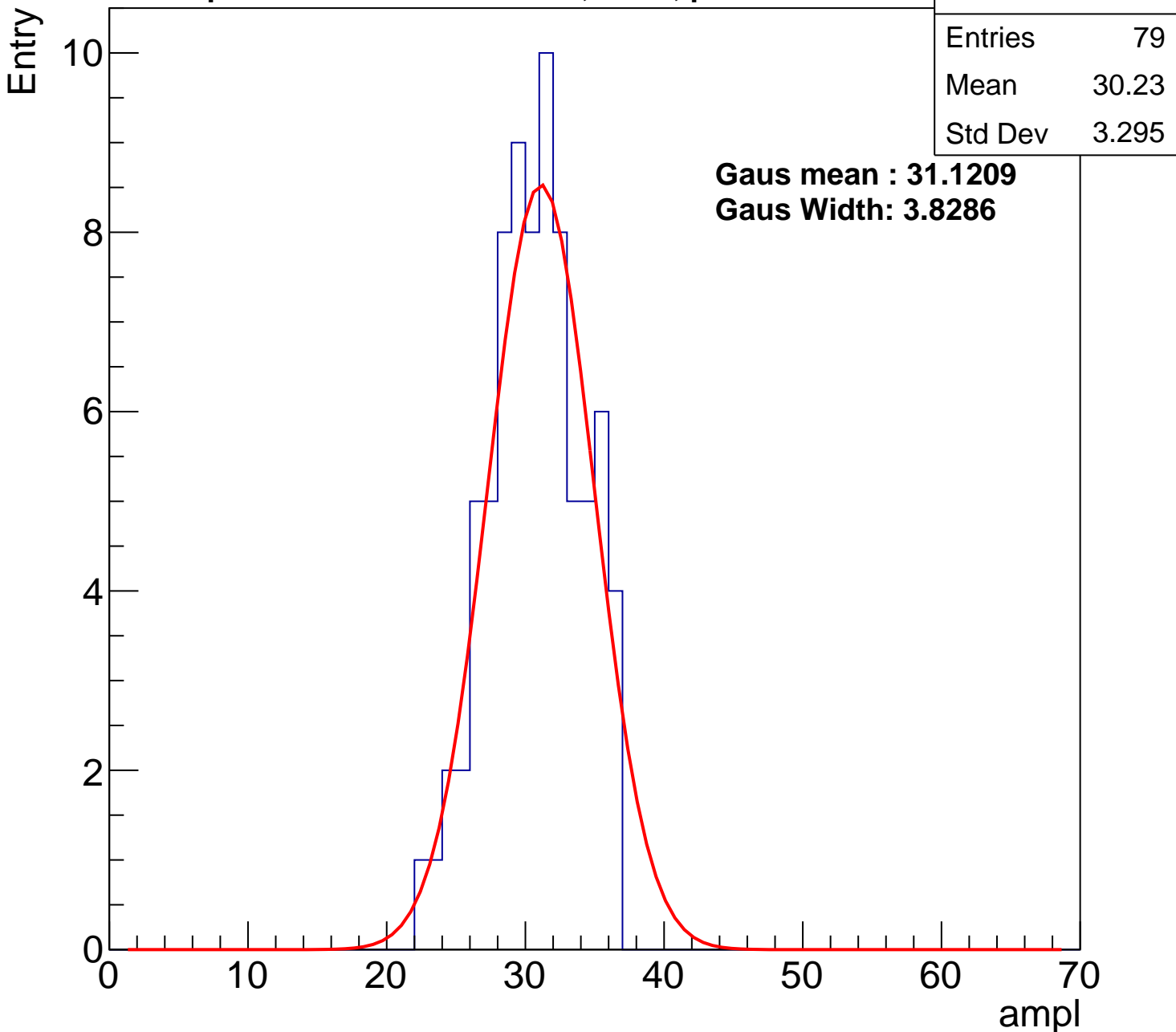
Gaus Width: 3.8286

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch27, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 58 |
| Mean | 37.09 |
| Std Dev | 3.339 |

Gaus mean : 37.4454

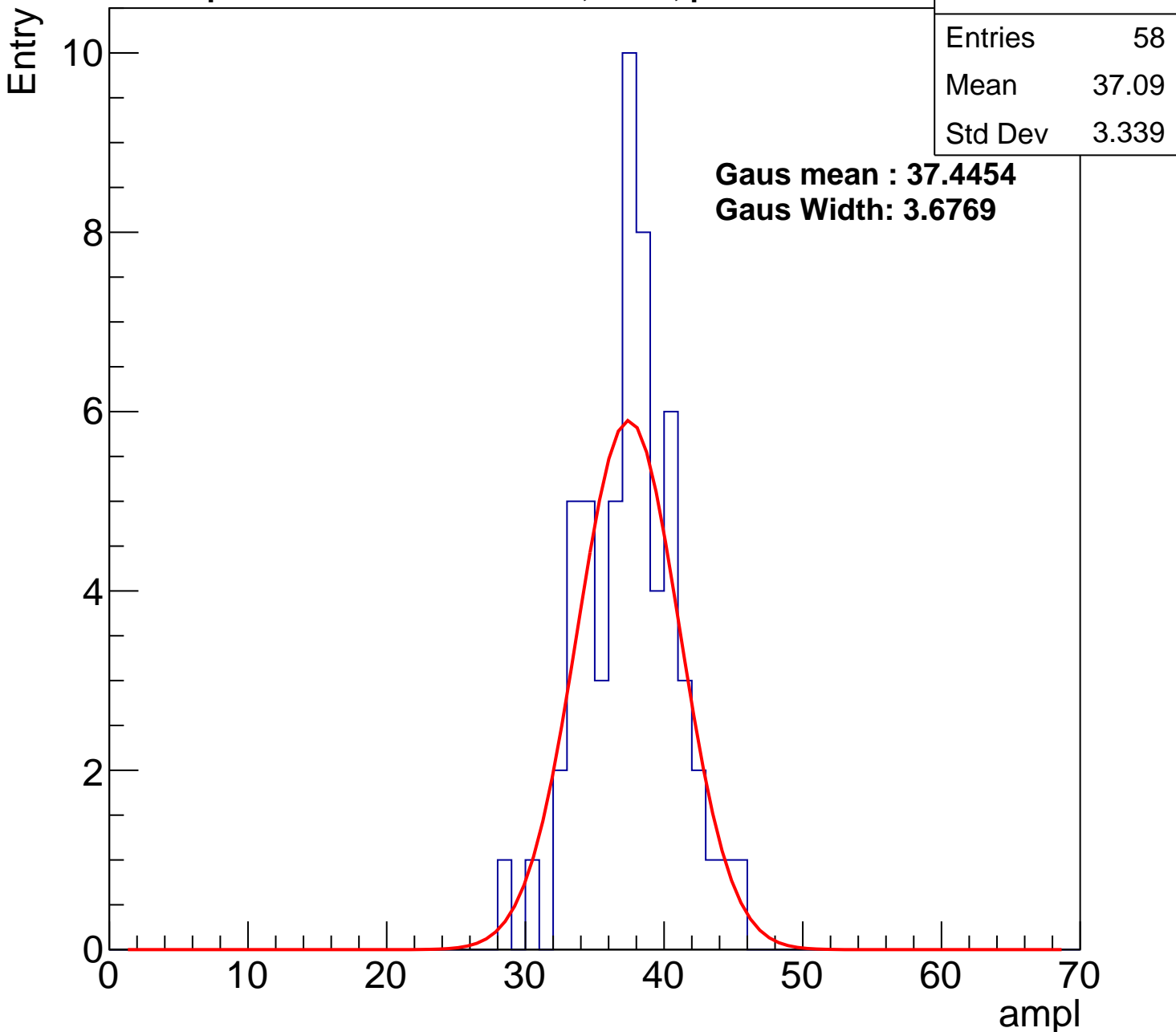
Gaus Width: 3.6769

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch27, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 93 |
| Mean | 44.49 |
| Std Dev | 3.764 |

Gaus mean : 44.9544

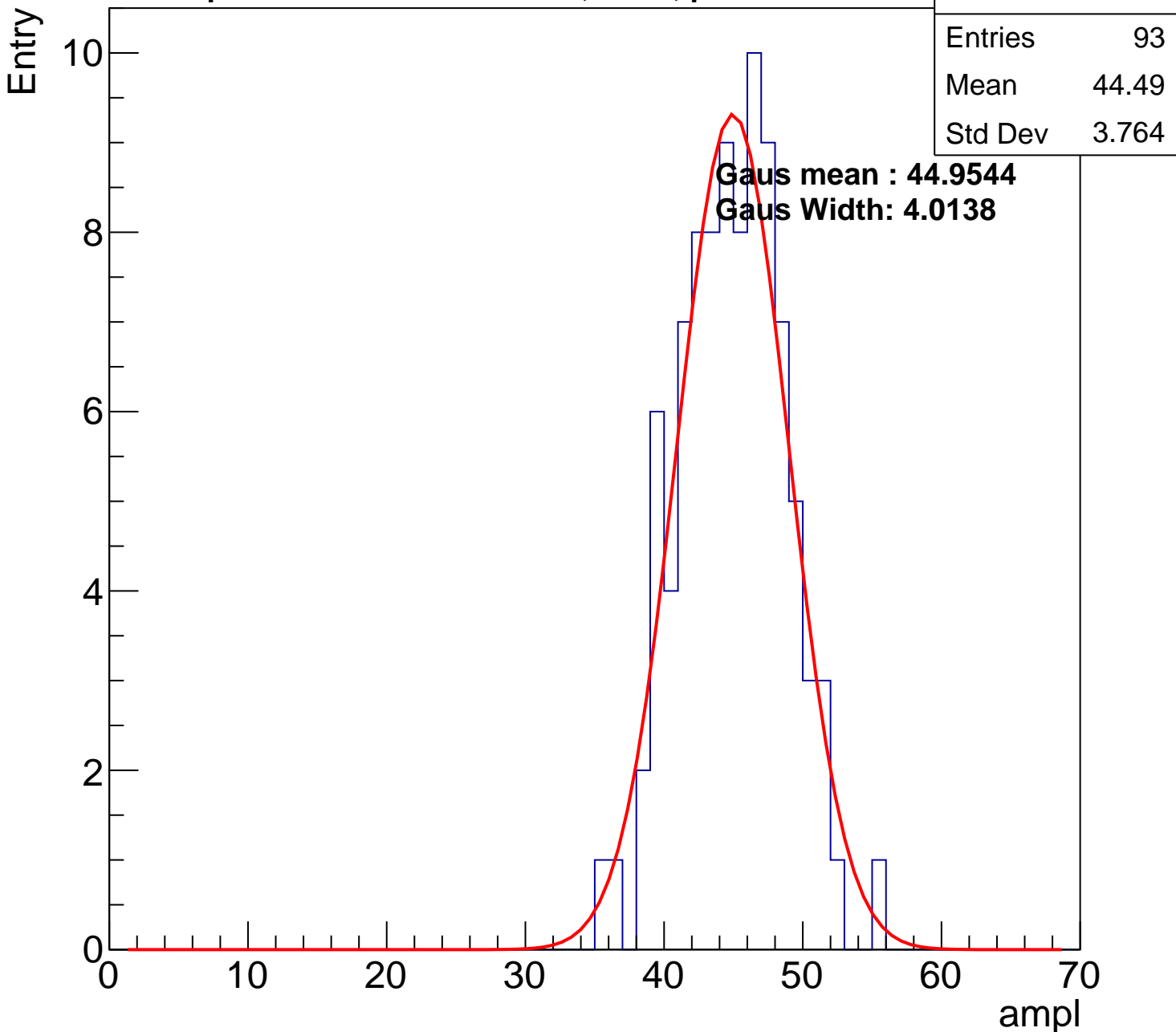
Gaus Width: 4.0138

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

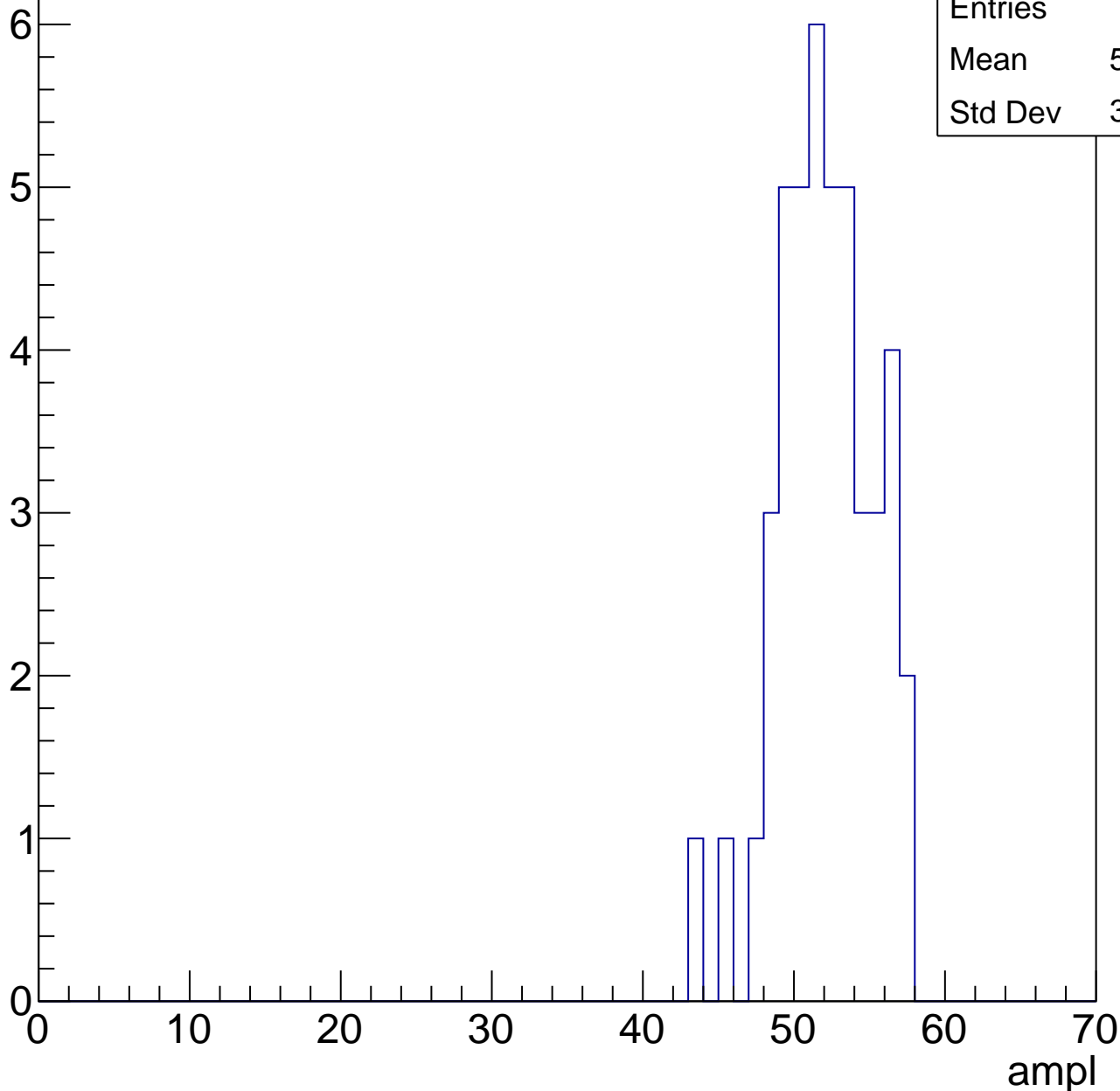


B1L100S, U7-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

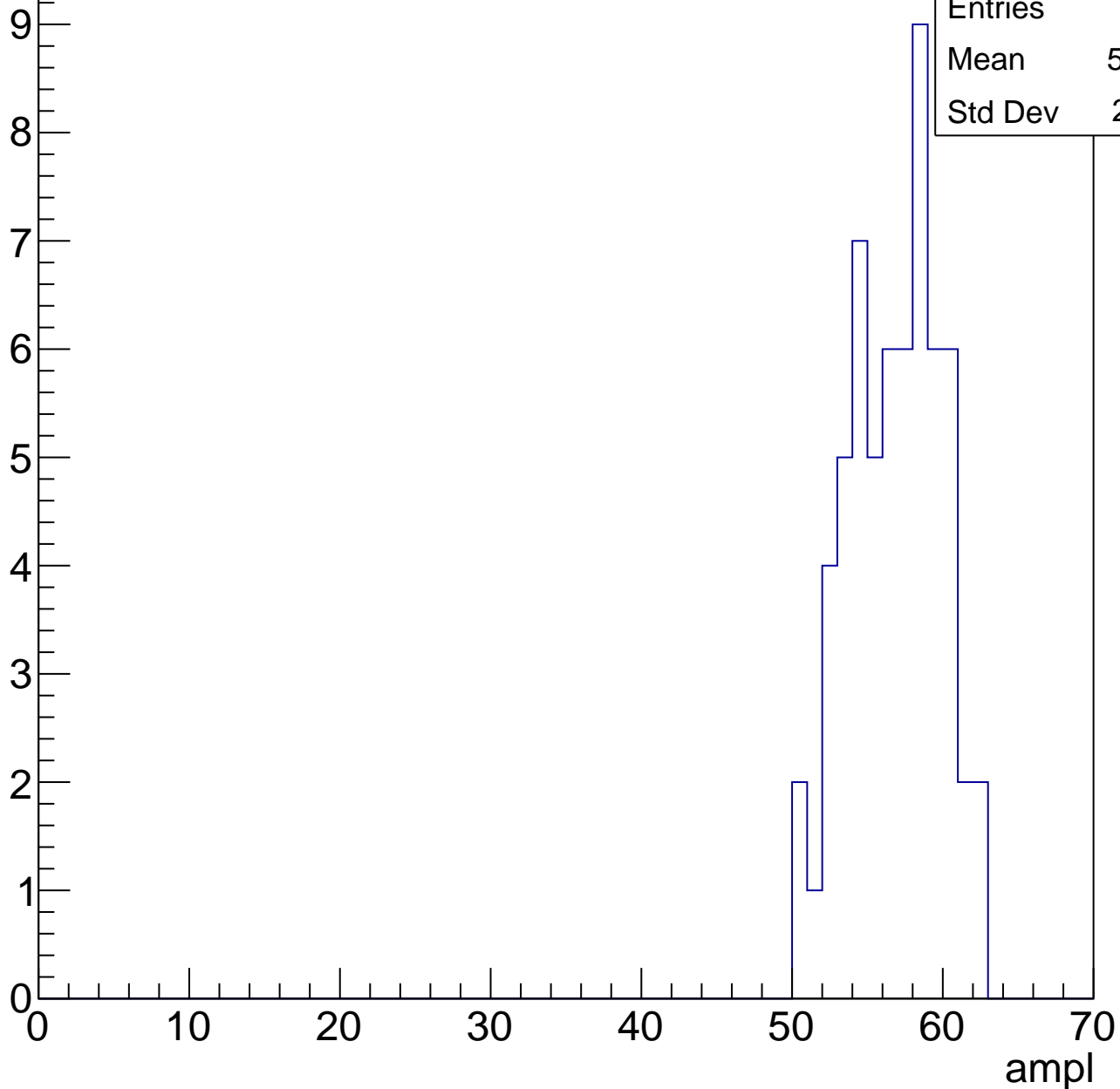
| | |
|---------|-------|
| Entries | 44 |
| Mean | 51.59 |
| Std Dev | 3.107 |



B1L100S, U7-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

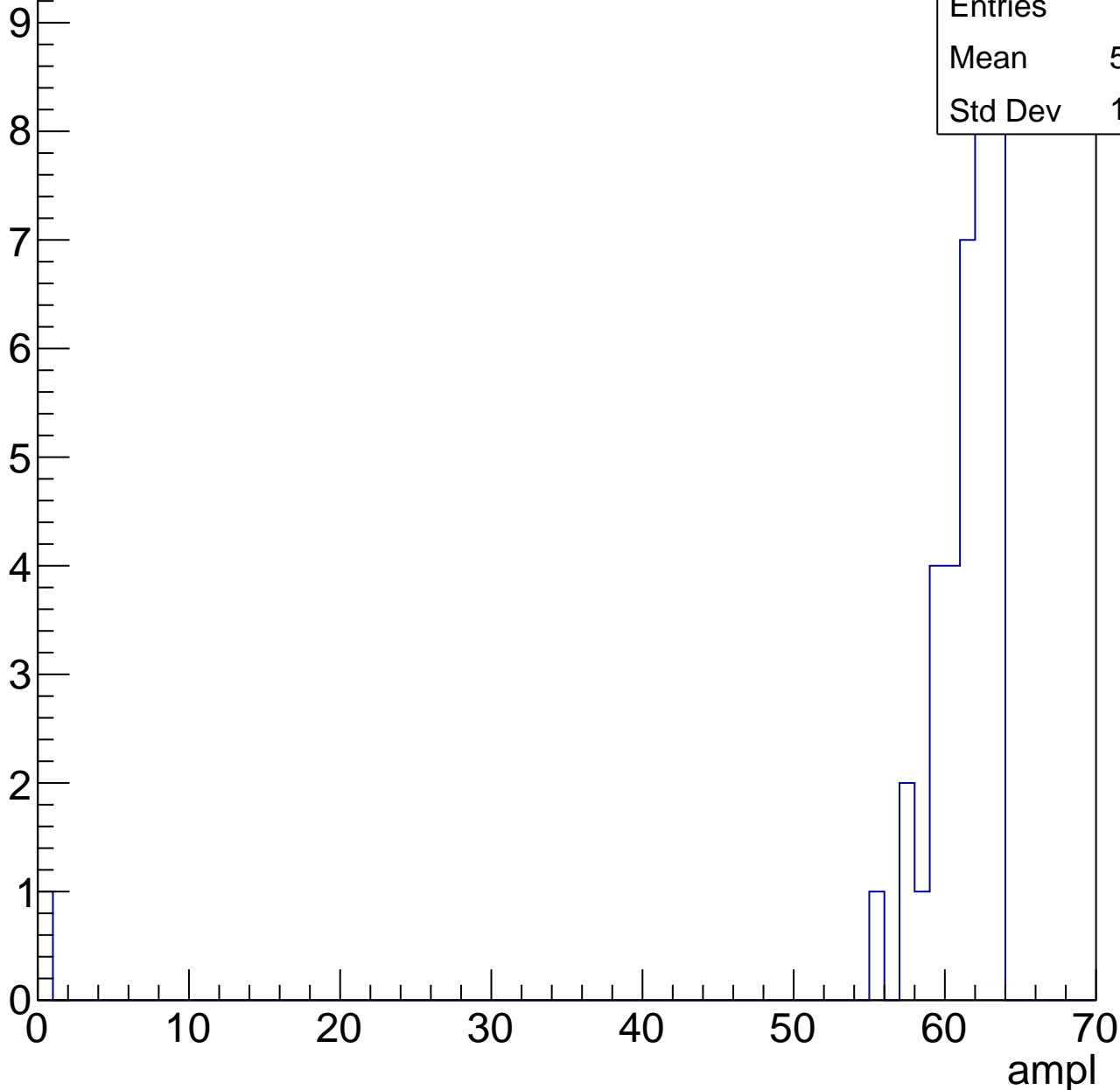


B1L100S, U7-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

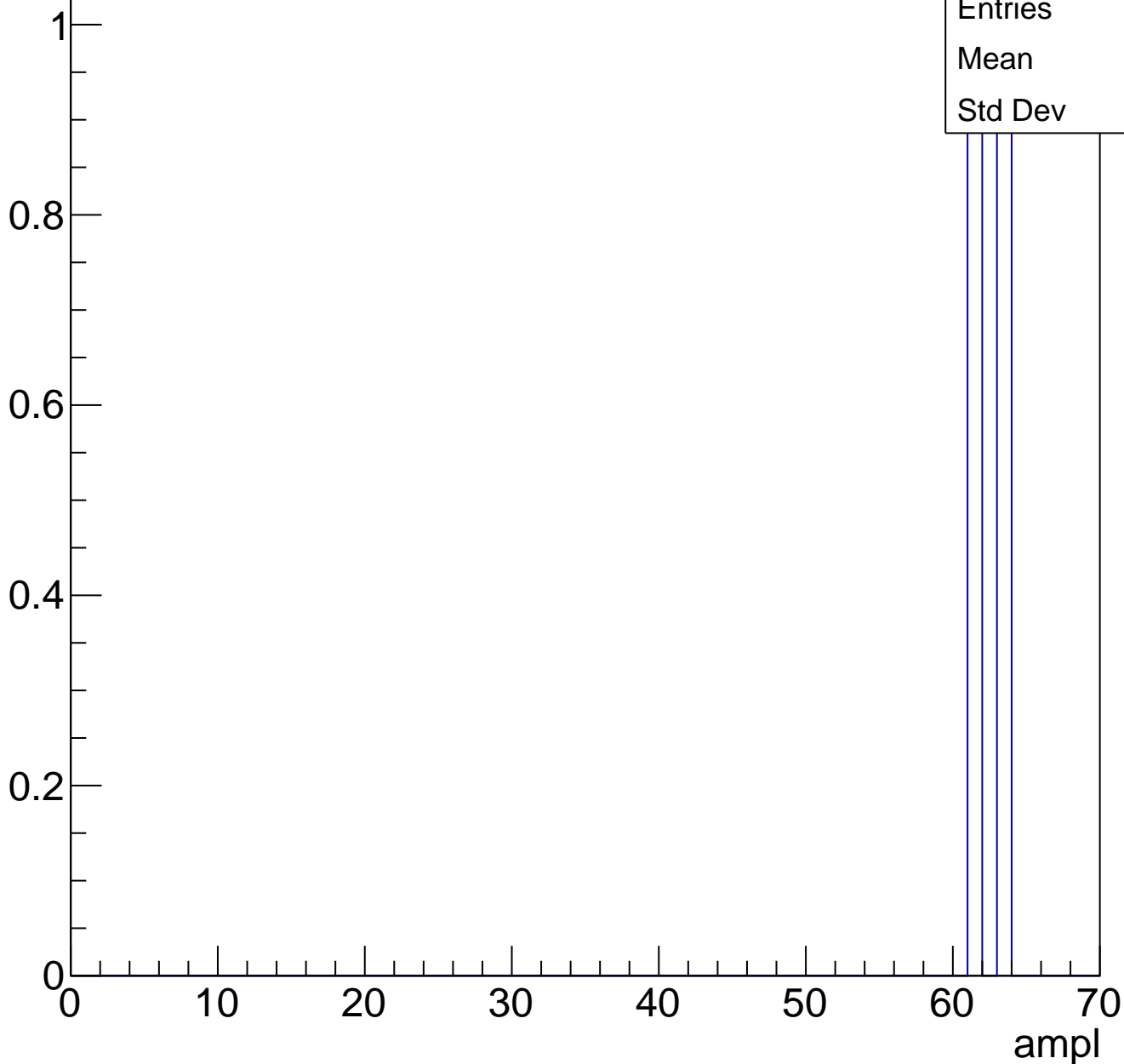
| | |
|---------|-------|
| Entries | 37 |
| Mean | 59.27 |
| Std Dev | 10.07 |



B1L100S, U7-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch28, adc0

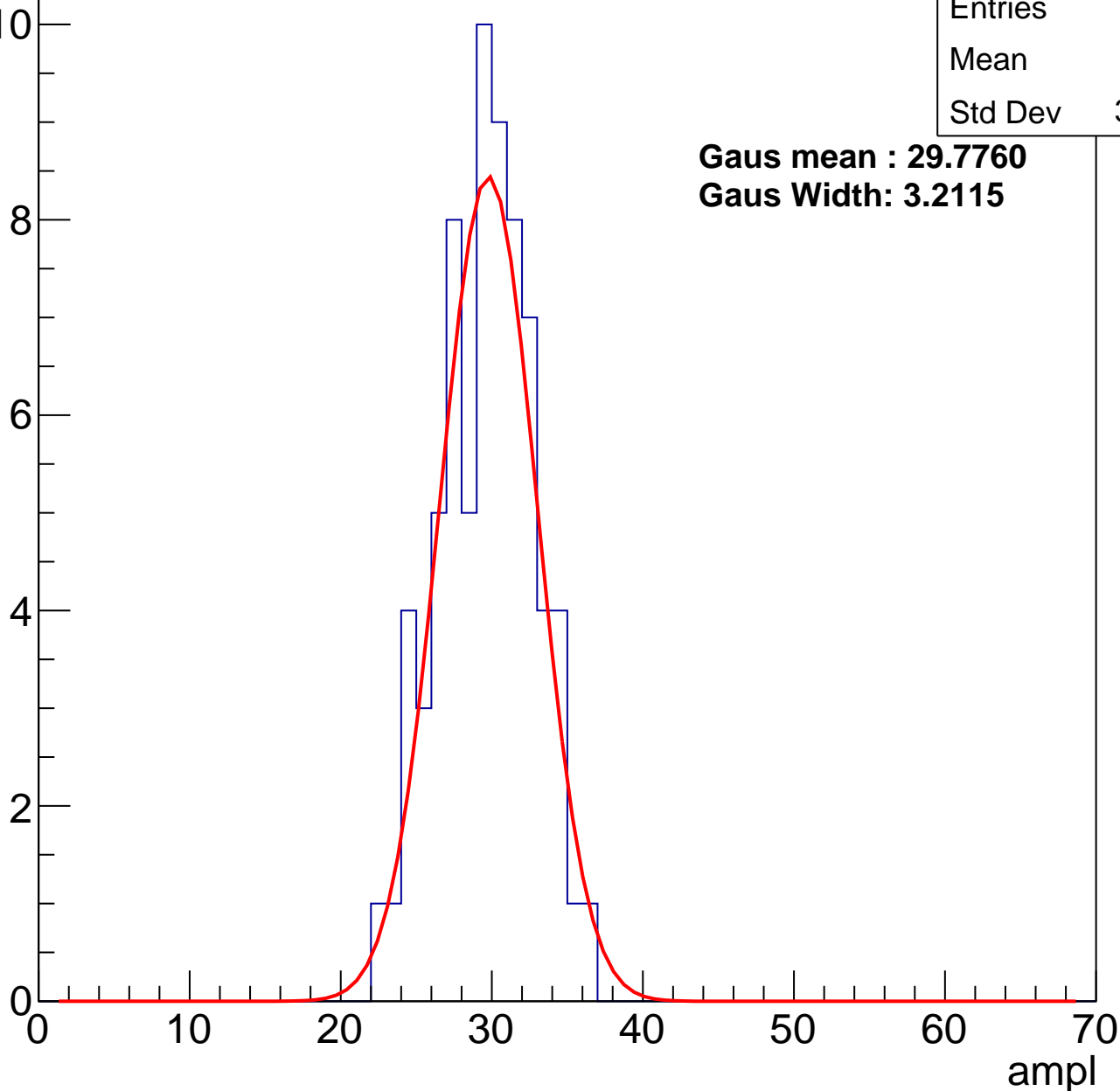
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.2 |
| Std Dev | 3.061 |

Gaus mean : 29.7760

Gaus Width: 3.2115



B1L100S, U7-ch28, adc1

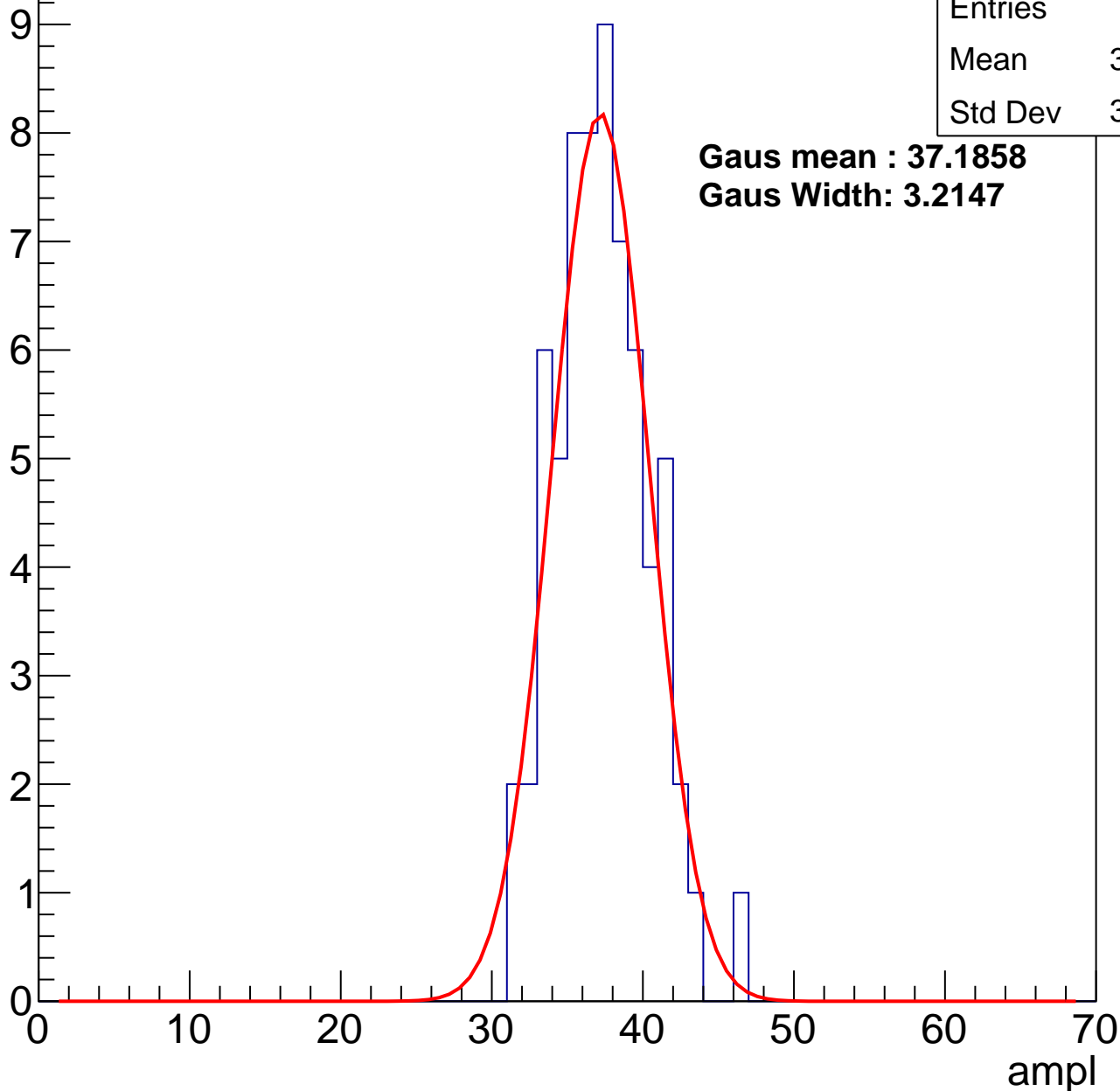
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.86 |
| Std Dev | 3.054 |

Gaus mean : 37.1858

Gaus Width: 3.2147



B1L100S, U7-ch28, adc2

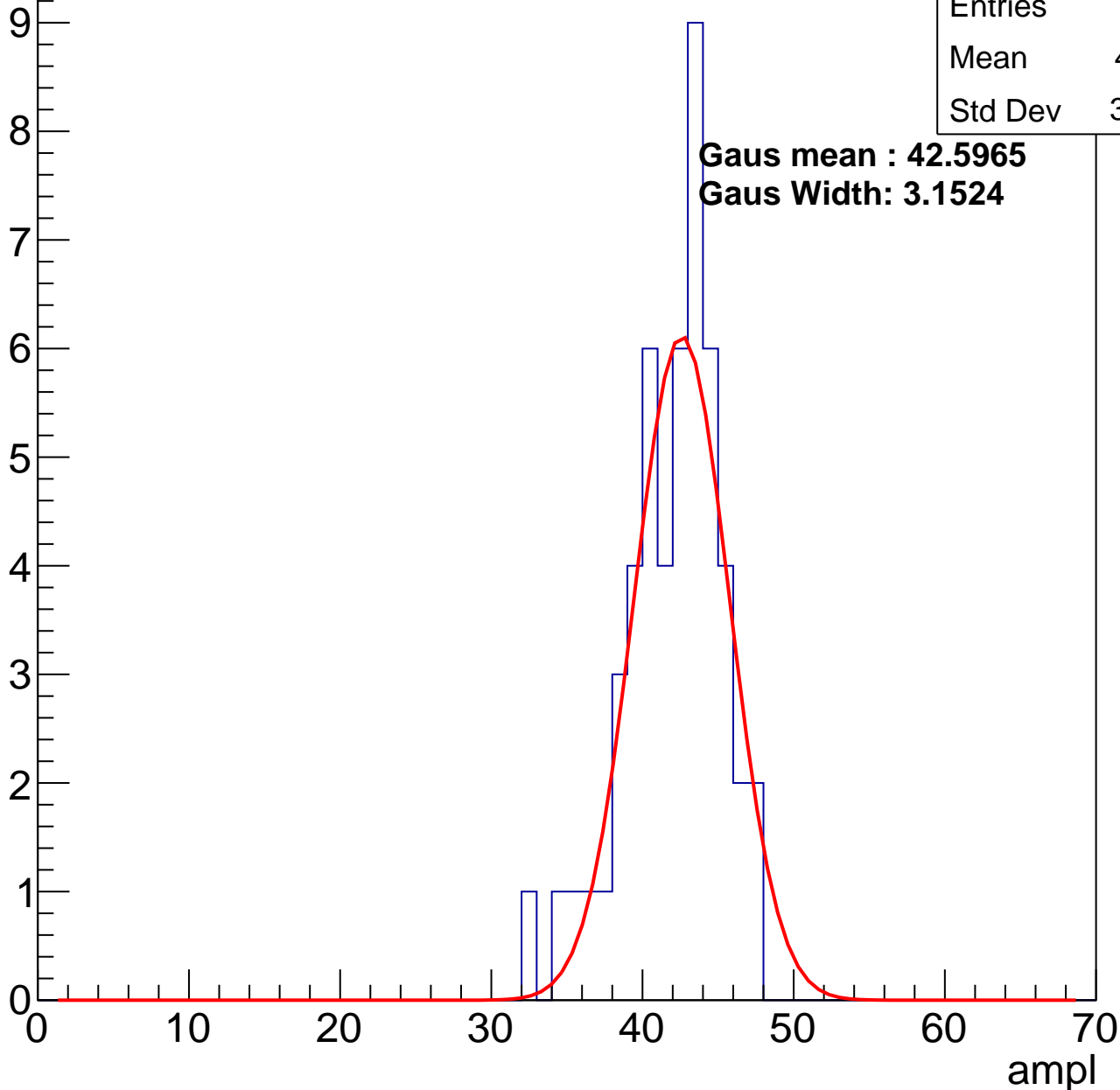
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 41.51 |
| Std Dev | 3.208 |

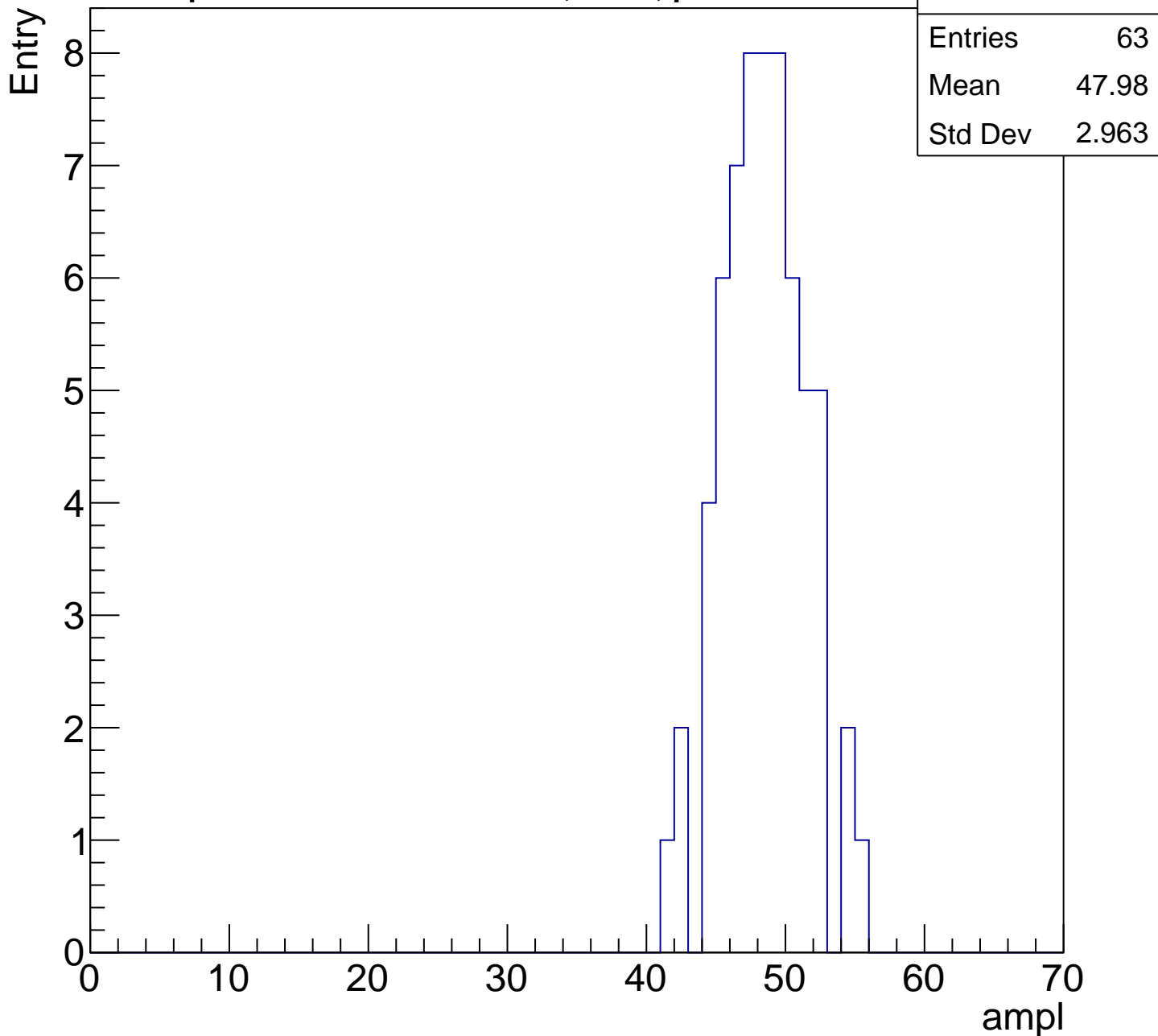
Gaus mean : 42.5965

Gaus Width: 3.1524



B1L100S, U7-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

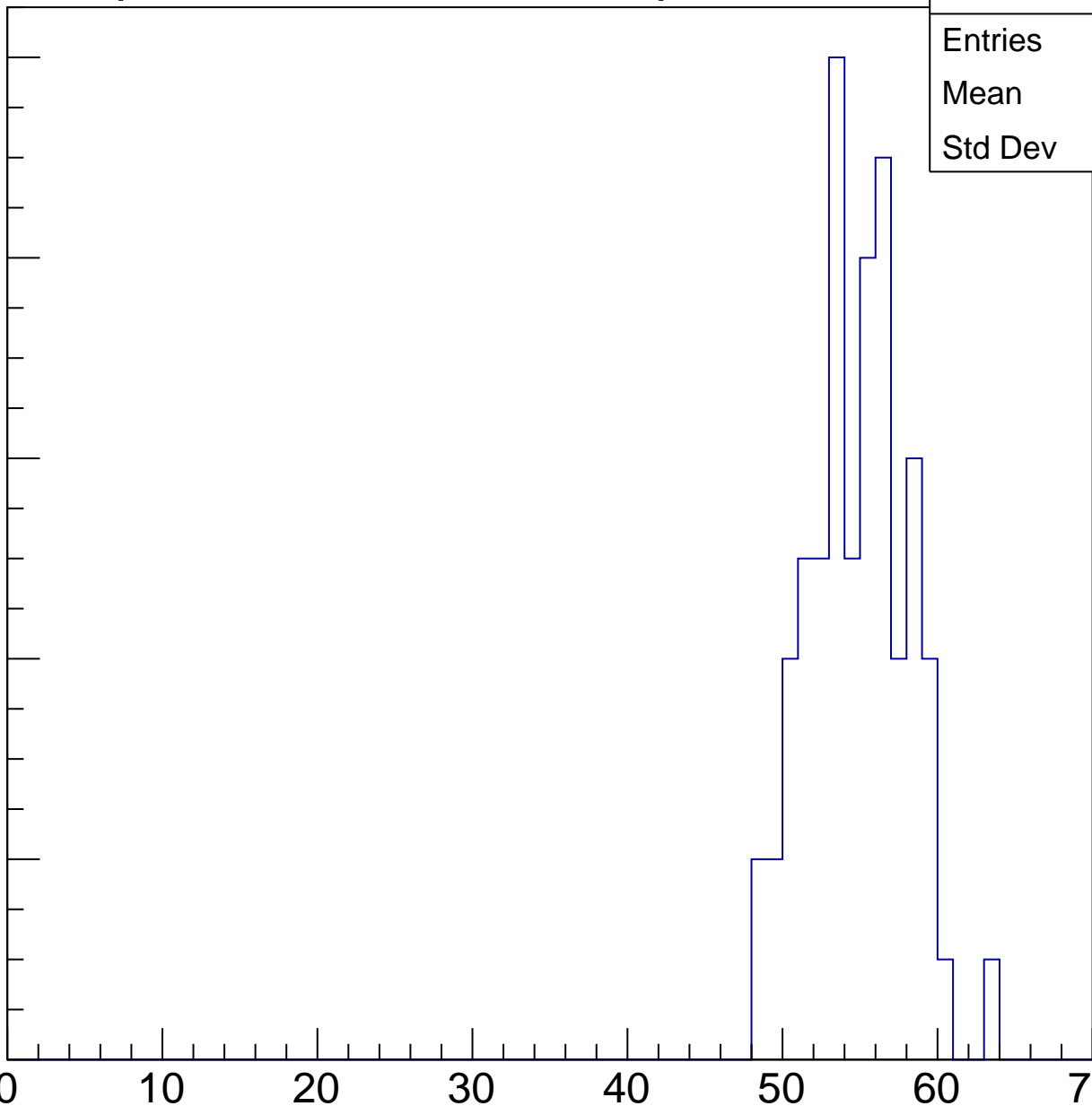
| | |
|---------|-------|
| Entries | 66 |
| Mean | 54.36 |
| Std Dev | 3.127 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

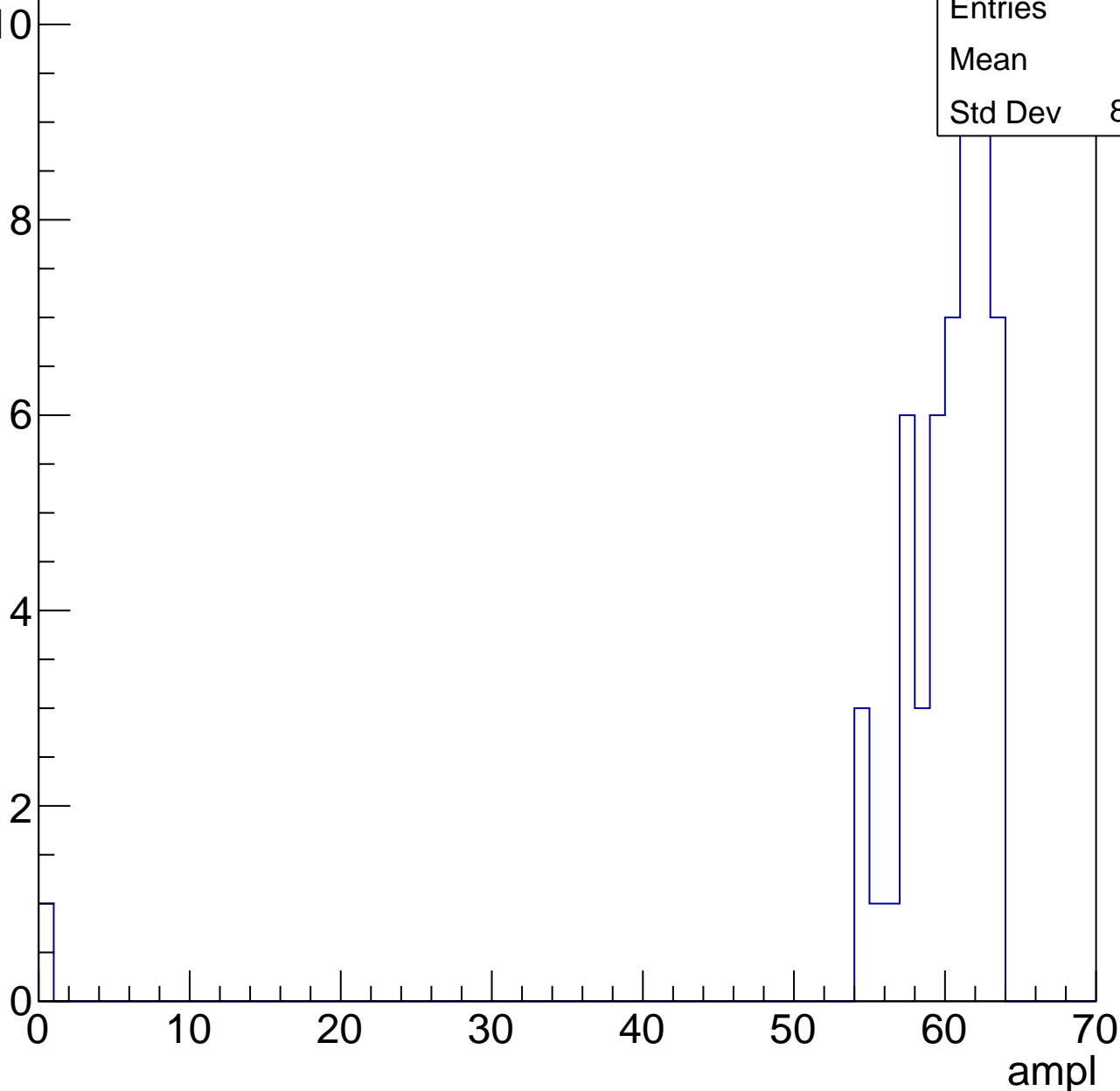


B1L100S, U7-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.8 |
| Std Dev | 8.372 |



B1L100S, U7-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

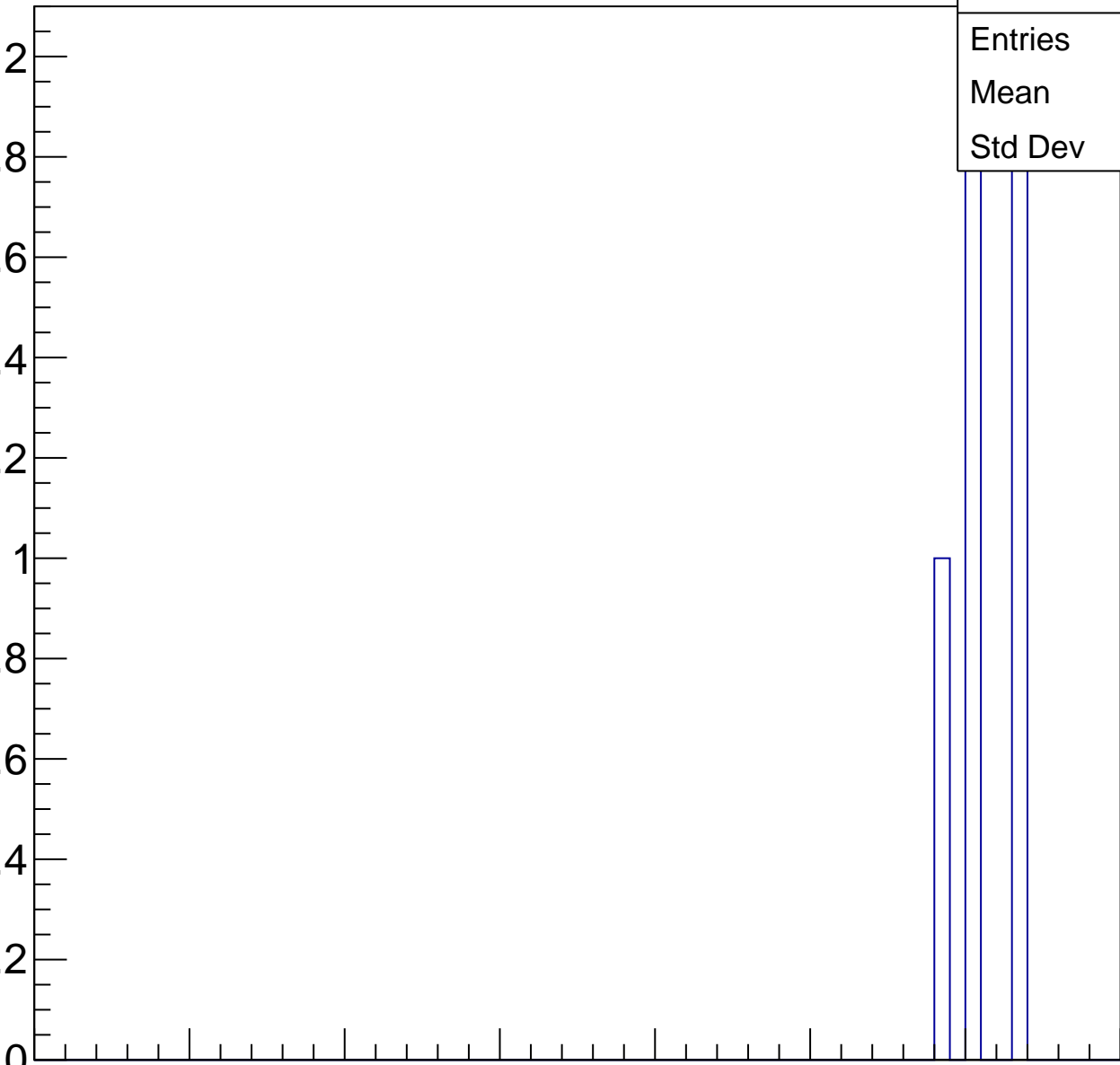
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 5 |
| Mean | 60.8 |
| Std Dev | 1.939 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch29, adc0

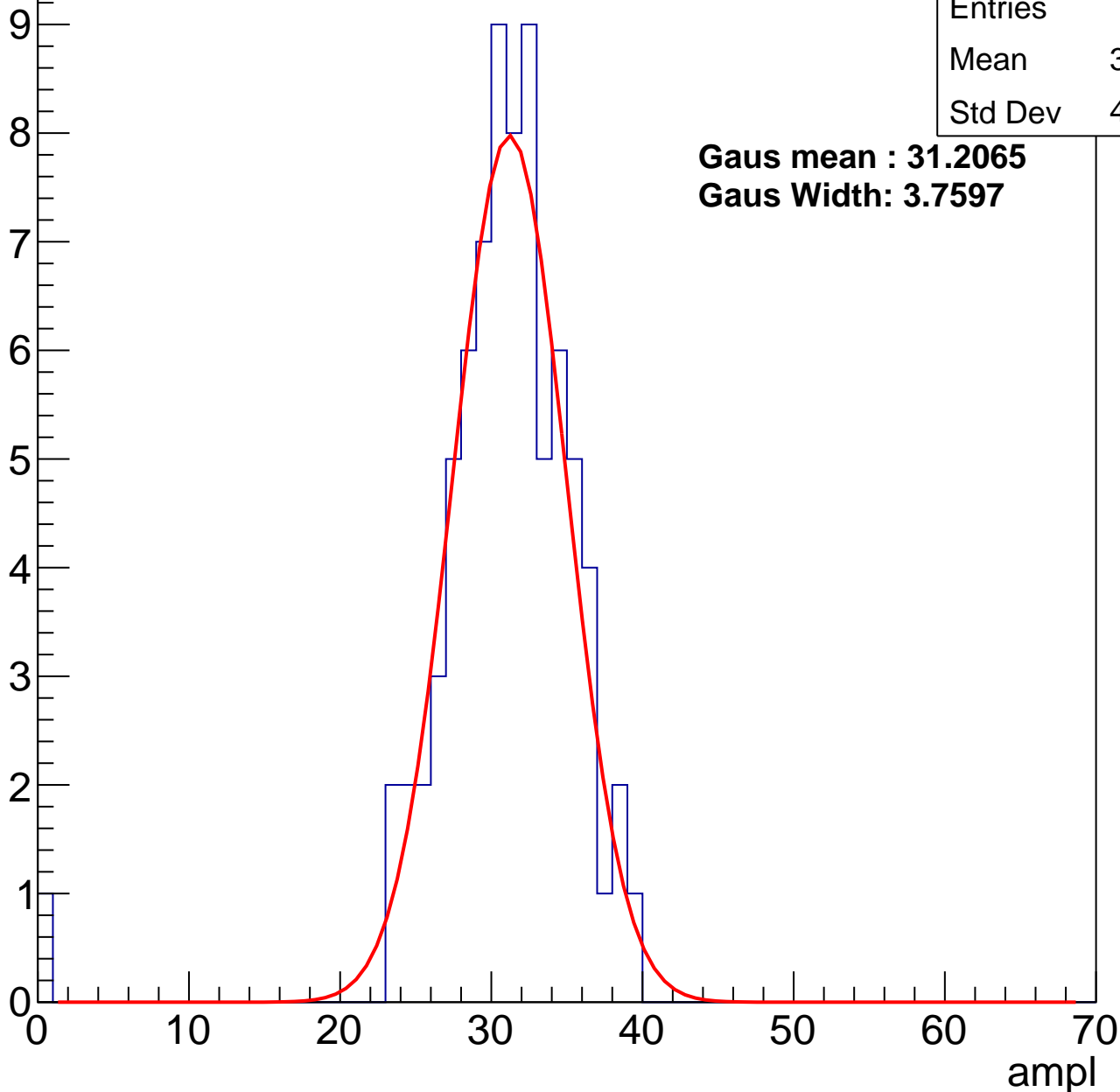
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 30.44 |
| Std Dev | 4.989 |

Gaus mean : 31.2065

Gaus Width: 3.7597



B1L100S, U7-ch29, adc1

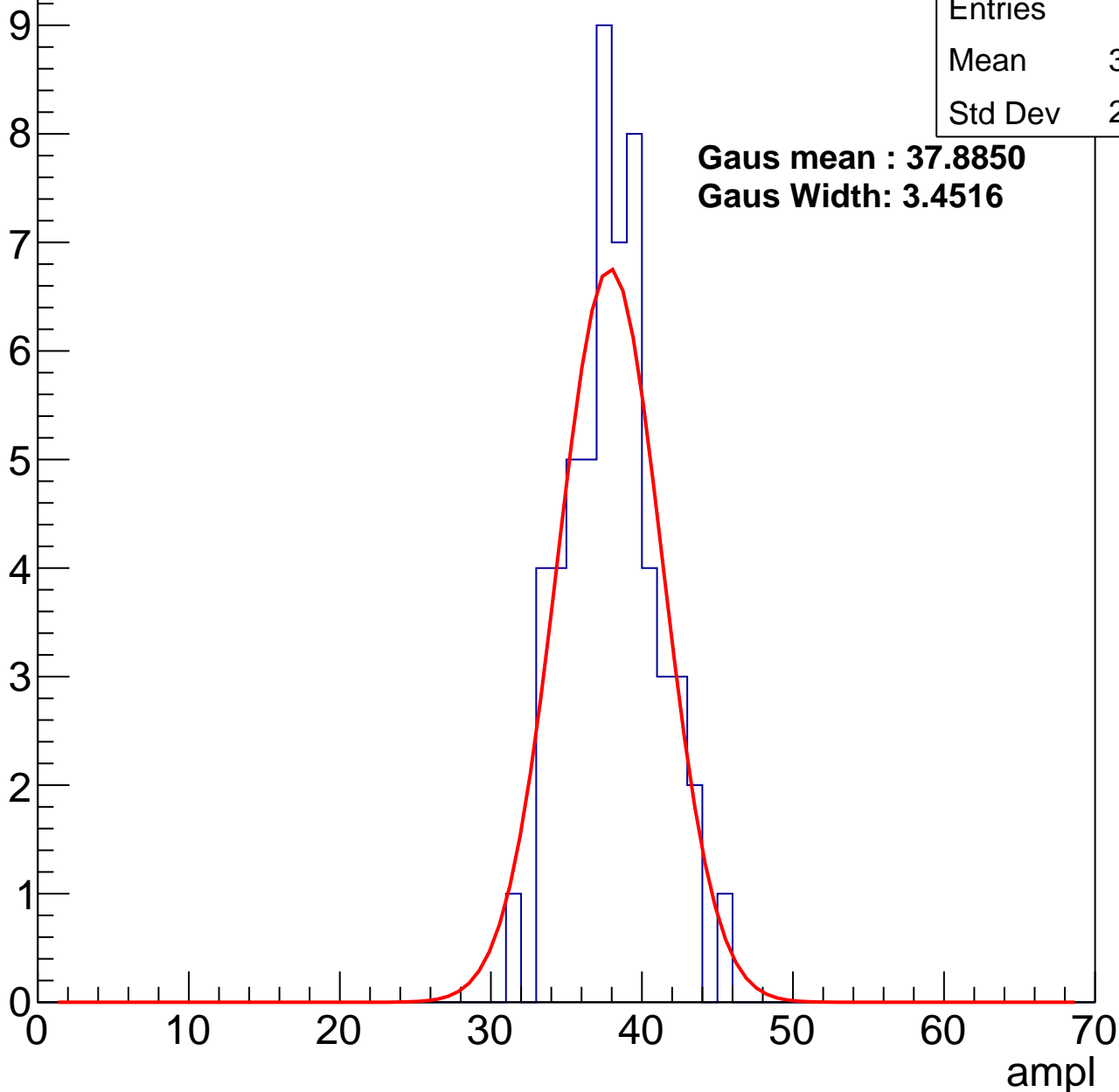
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 37.59 |
| Std Dev | 2.914 |

Gaus mean : 37.8850

Gaus Width: 3.4516



B1L100S, U7-ch29, adc2

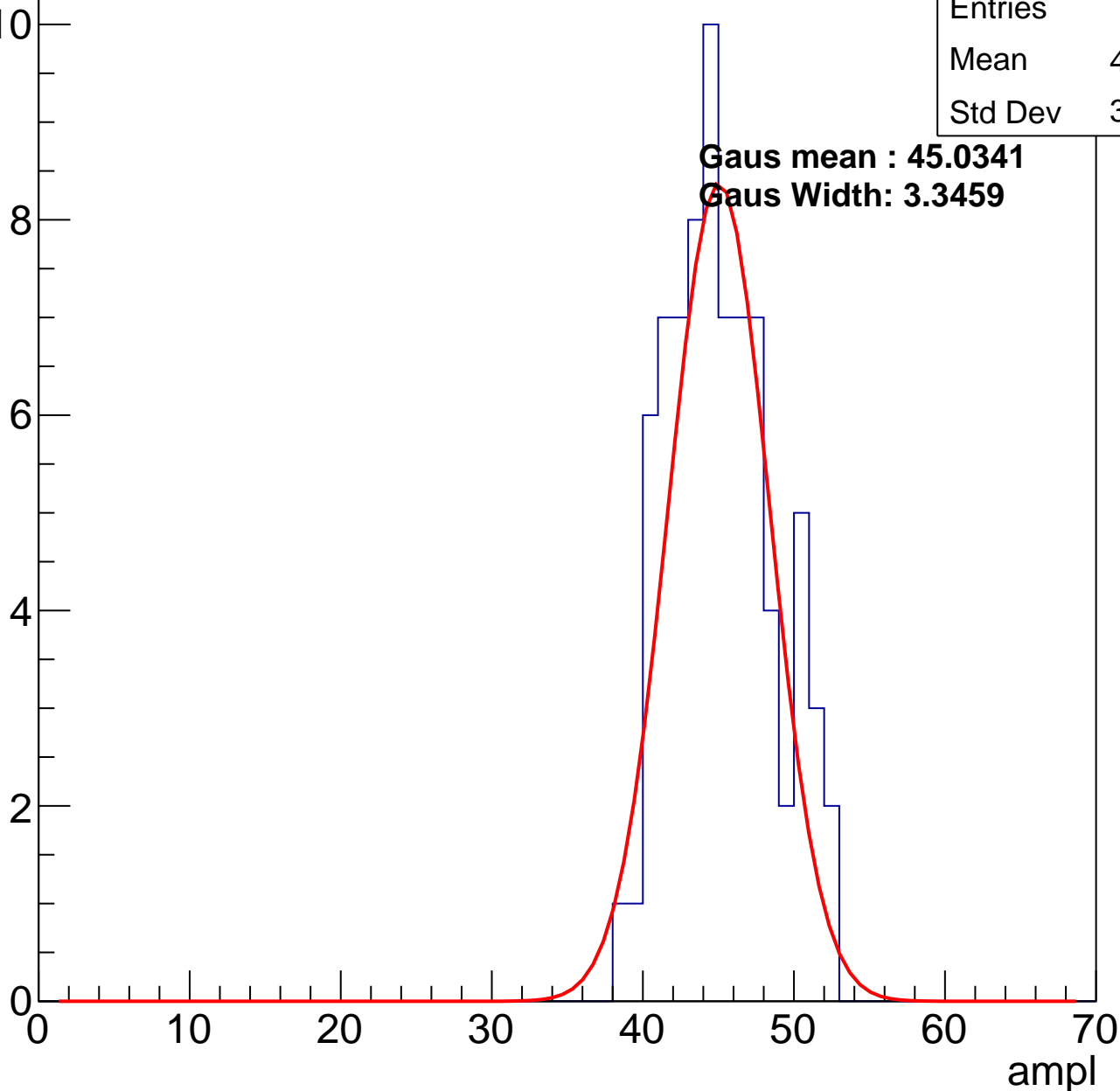
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 44.74 |
| Std Dev | 3.382 |

Gaus mean : 45.0341

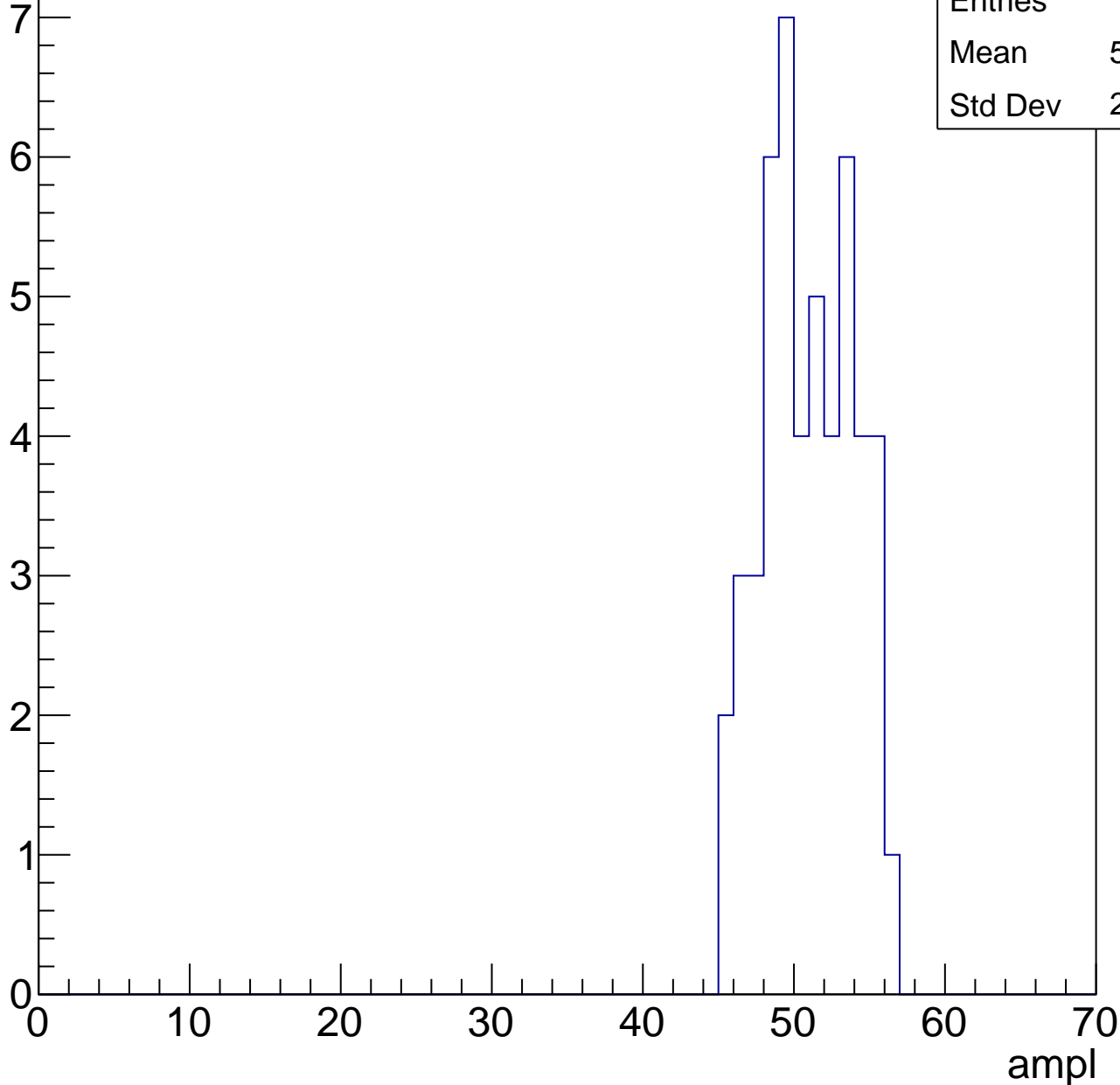
Gaus Width: 3.3459



B1L100S, U7-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



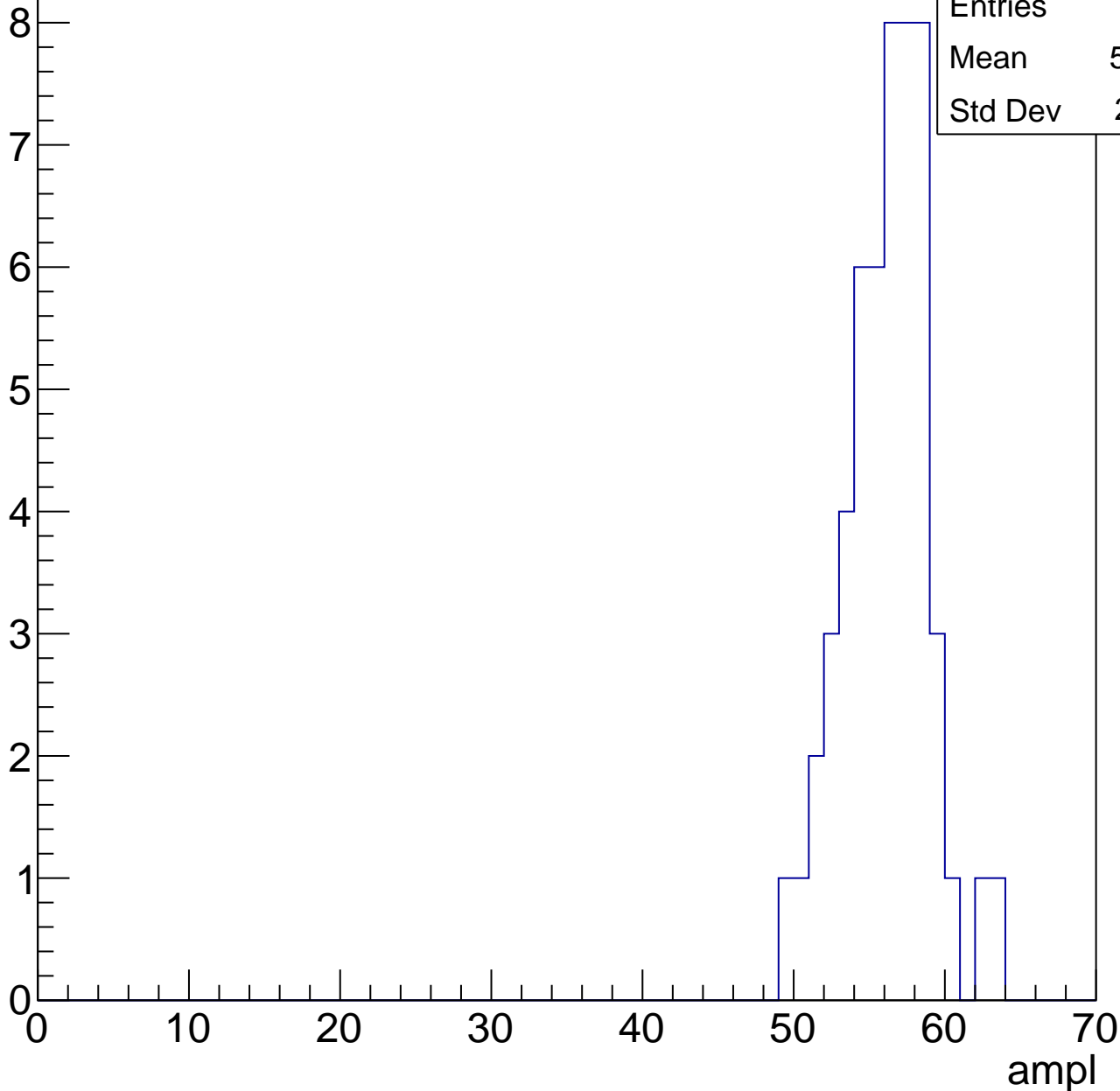
| | |
|---------|-------|
| Entries | 49 |
| Mean | 50.47 |
| Std Dev | 2.928 |

B1L100S, U7-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.72 |
| Std Dev | 2.791 |



B1L100S, U7-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

Entries

47

Mean

59.06

Std Dev

8.959

0

0

10

20

30

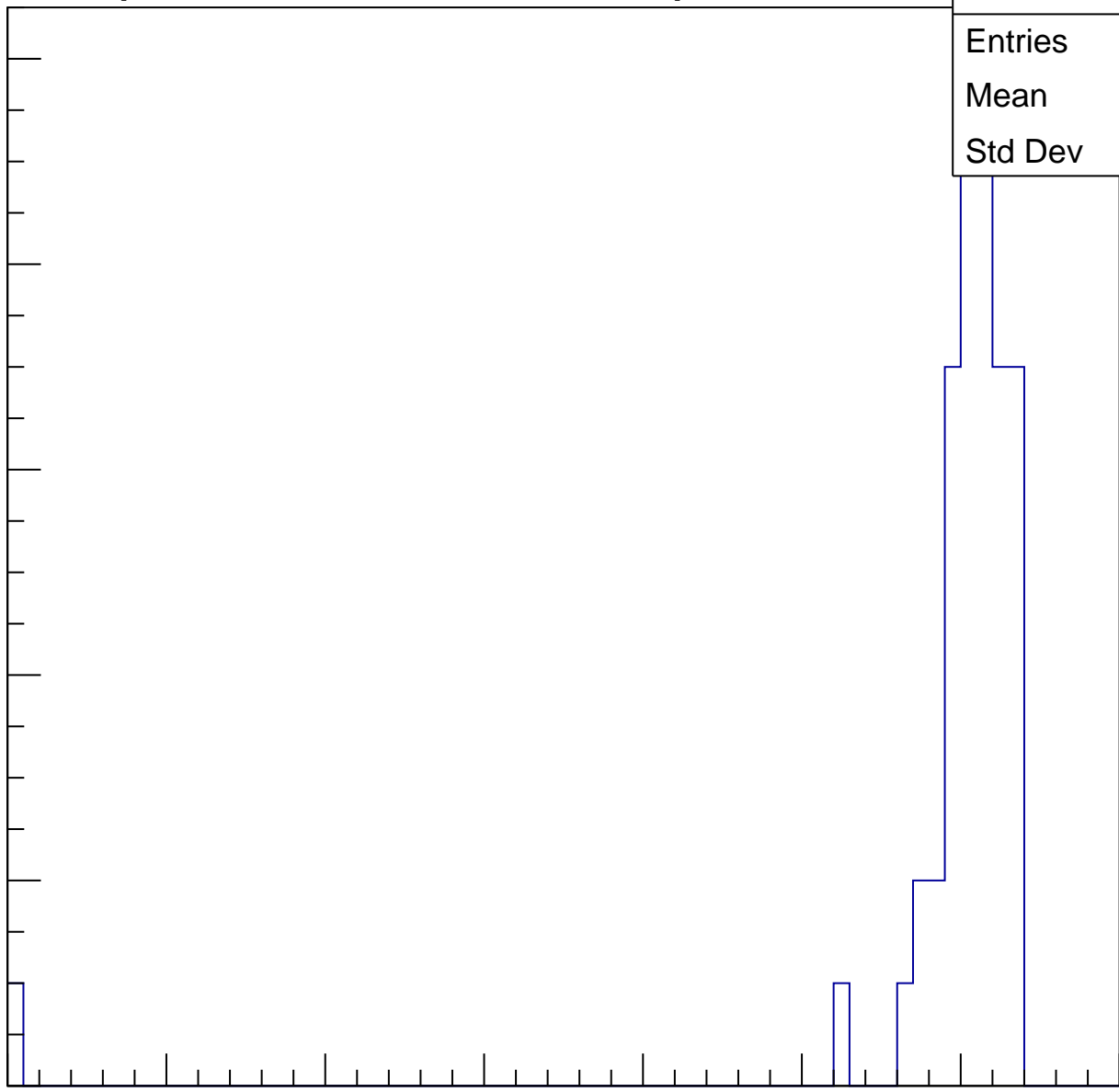
40

50

60

70

ampl



B1L100S, U7-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch30, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.95 |
| Std Dev | 4.811 |

Gaus mean : 31.0750

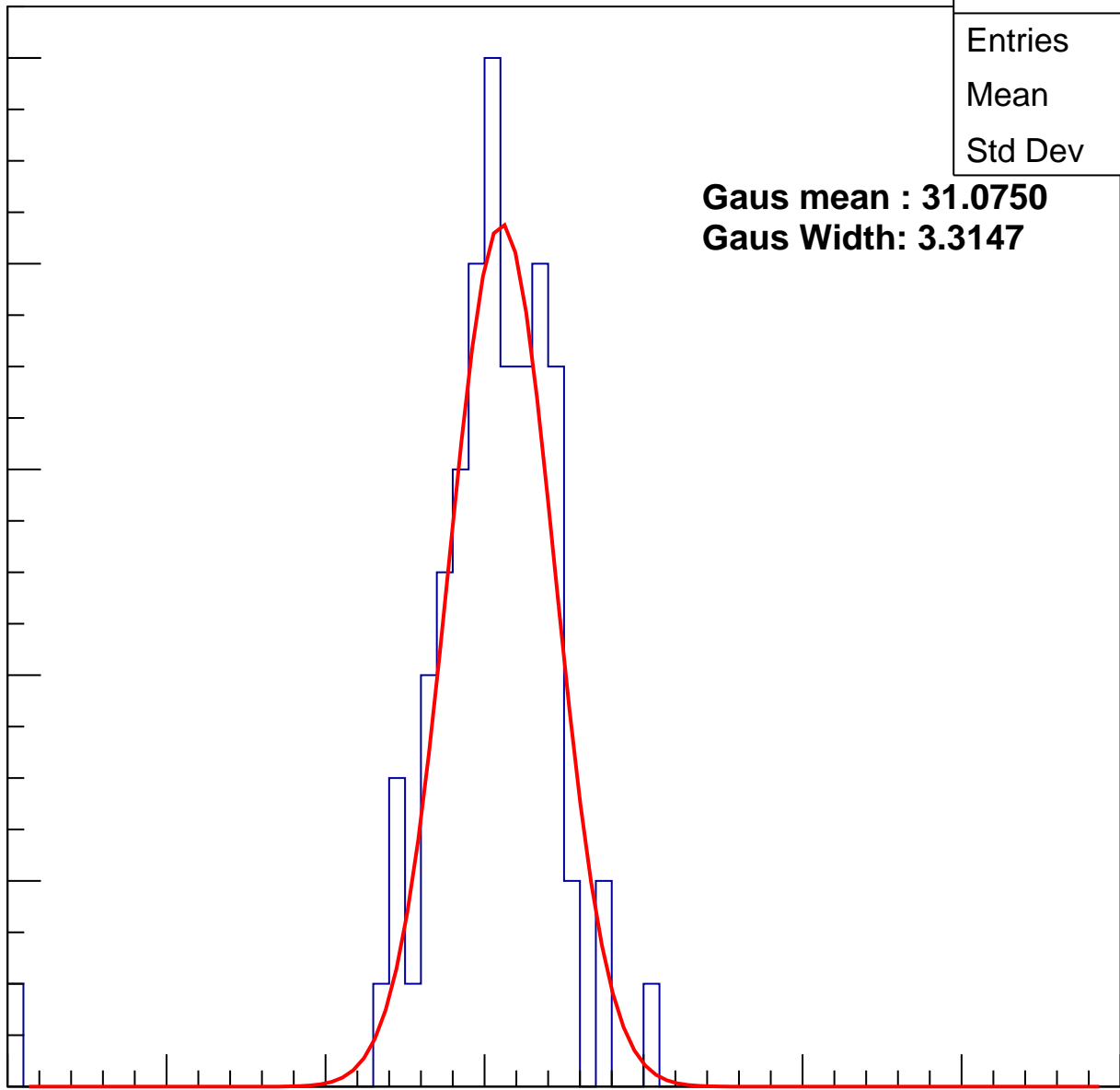
Gaus Width: 3.3147

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch30, adc1

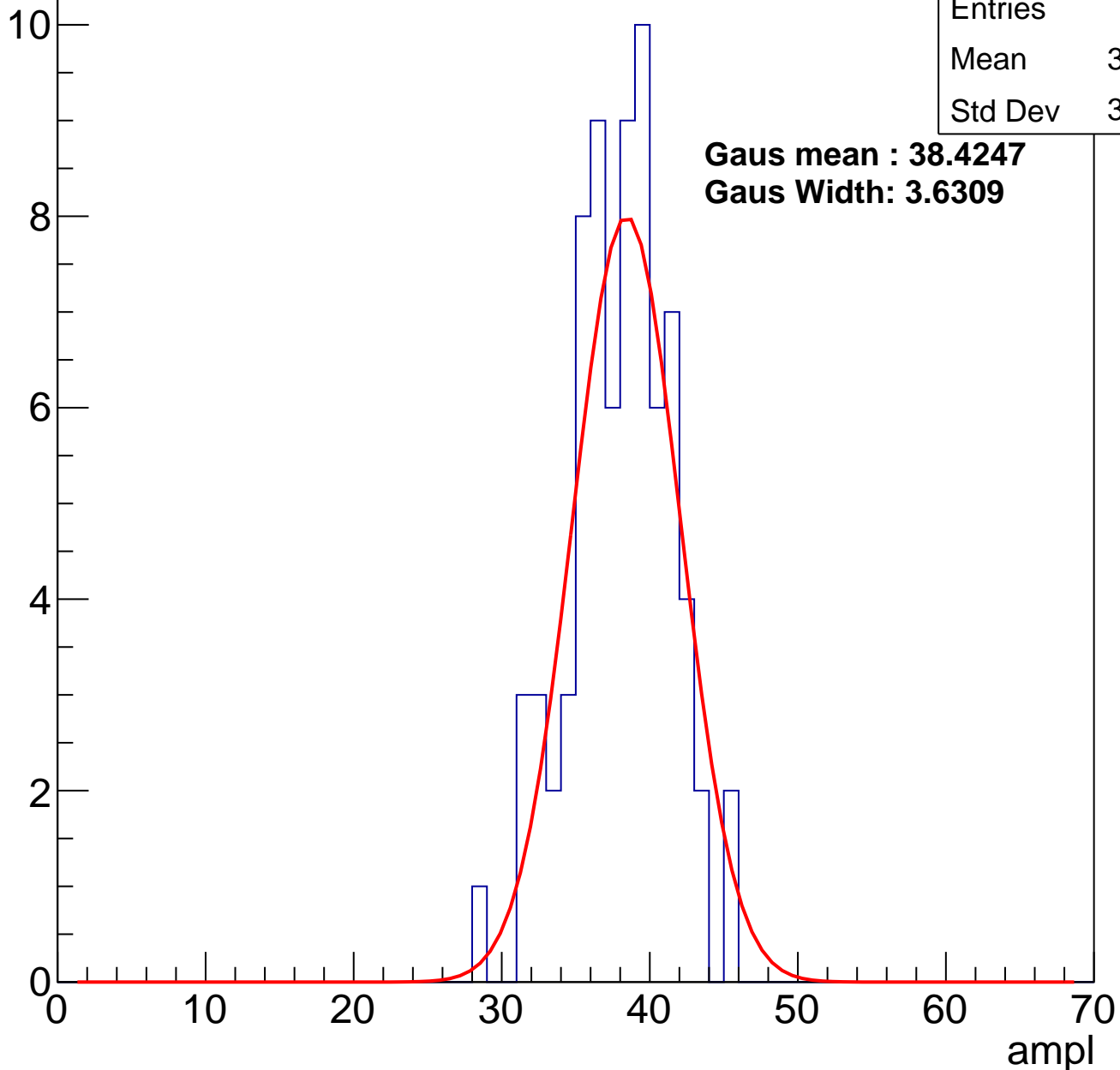
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 37.52 |
| Std Dev | 3.384 |

Gaus mean : 38.4247

Gaus Width: 3.6309

Entry



B1L100S, U7-ch30, adc2

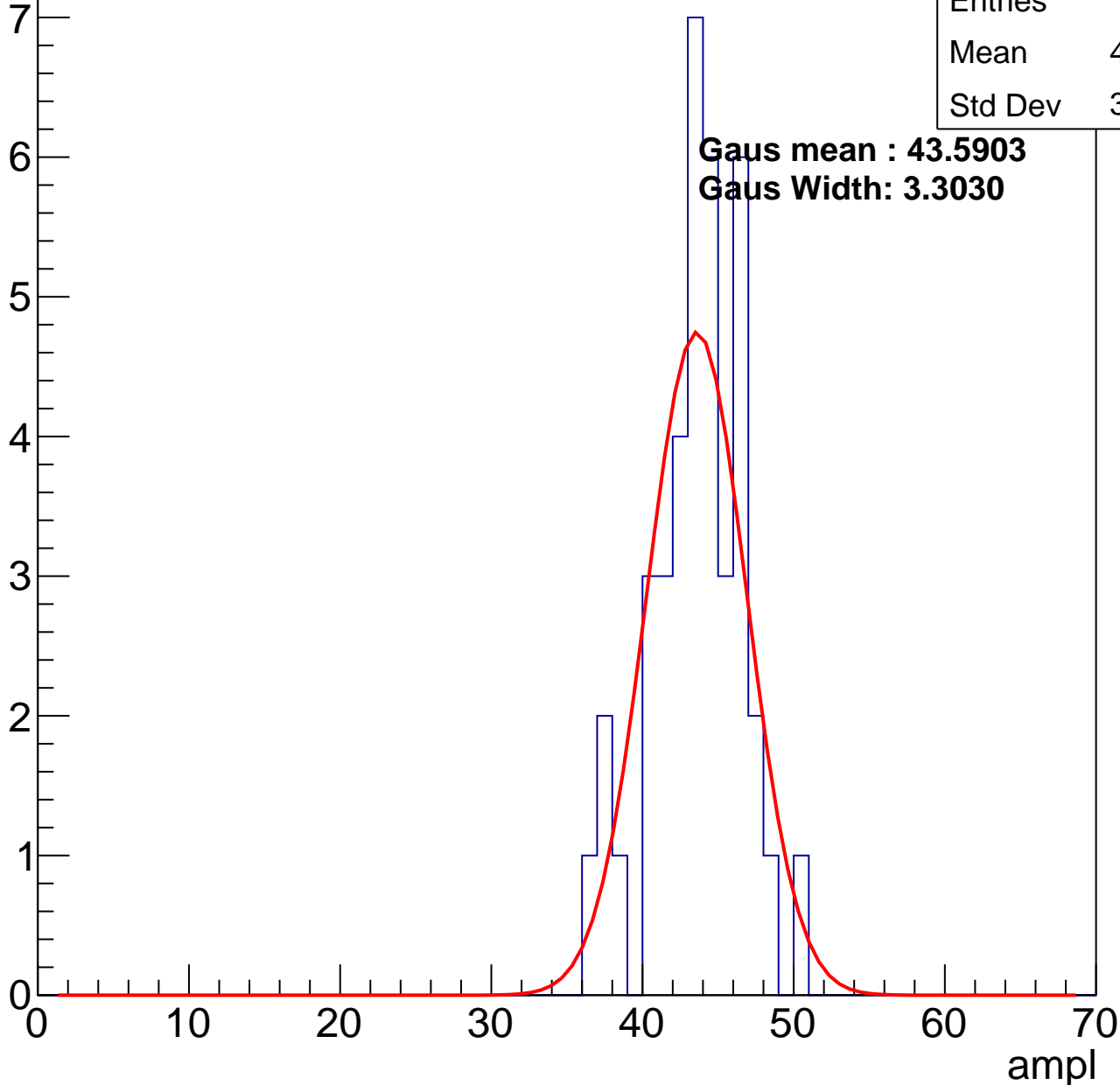
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 43.17 |
| Std Dev | 3.024 |

Gaus mean : 43.5903

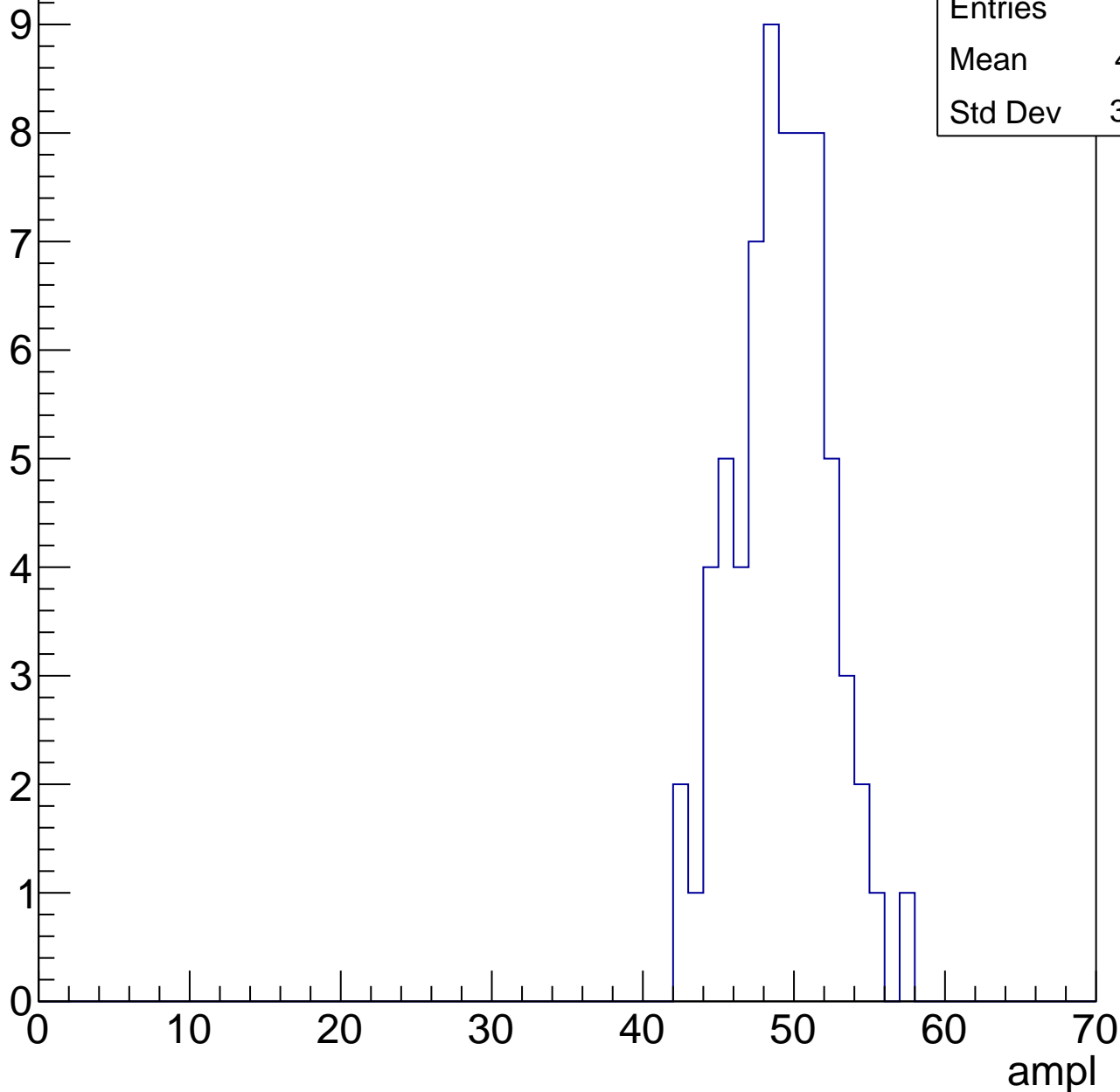
Gaus Width: 3.3030



B1L100S, U7-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



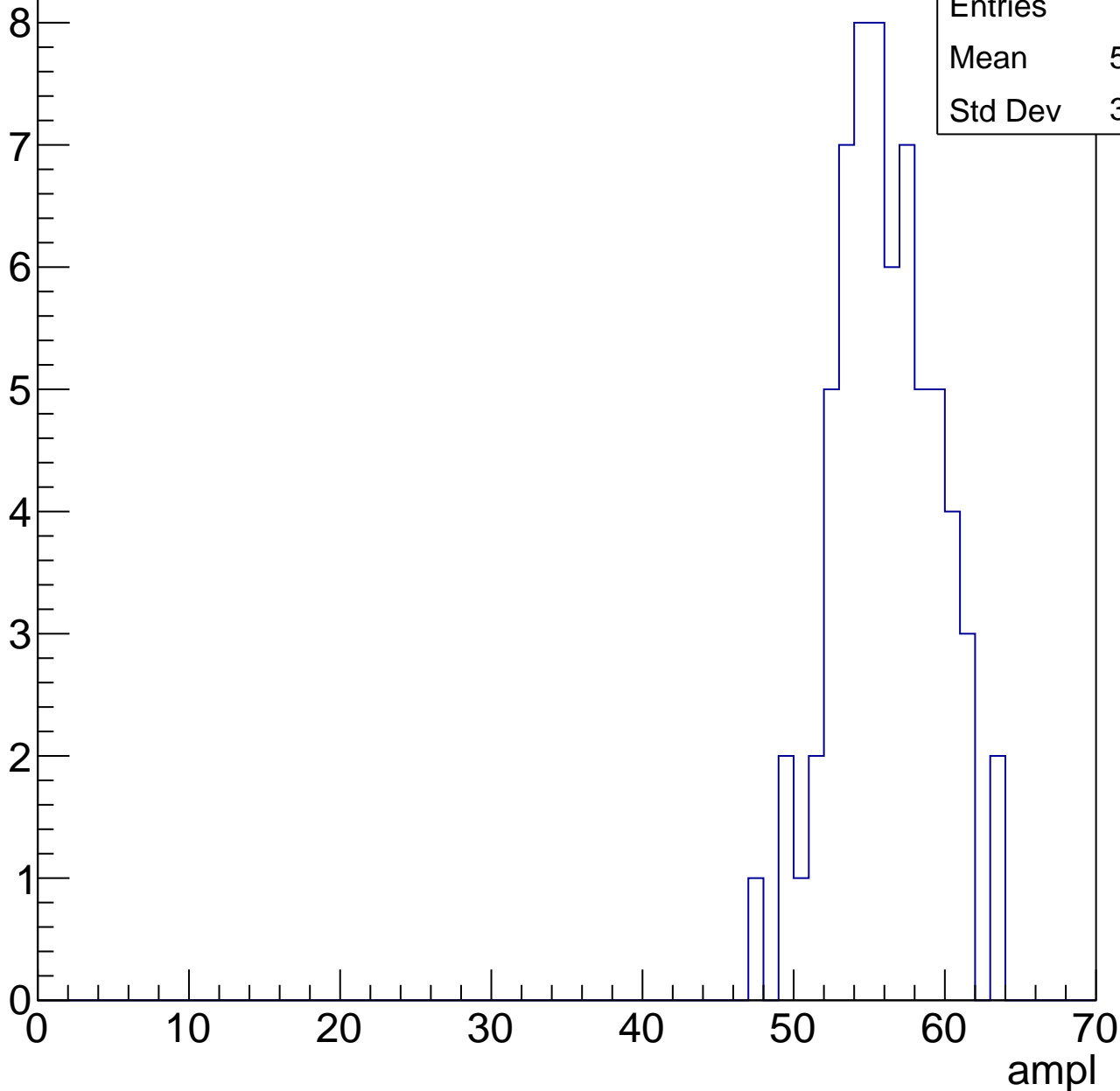
| | |
|---------|-------|
| Entries | 68 |
| Mean | 48.71 |
| Std Dev | 3.125 |

B1L100S, U7-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.59 |
| Std Dev | 3.362 |

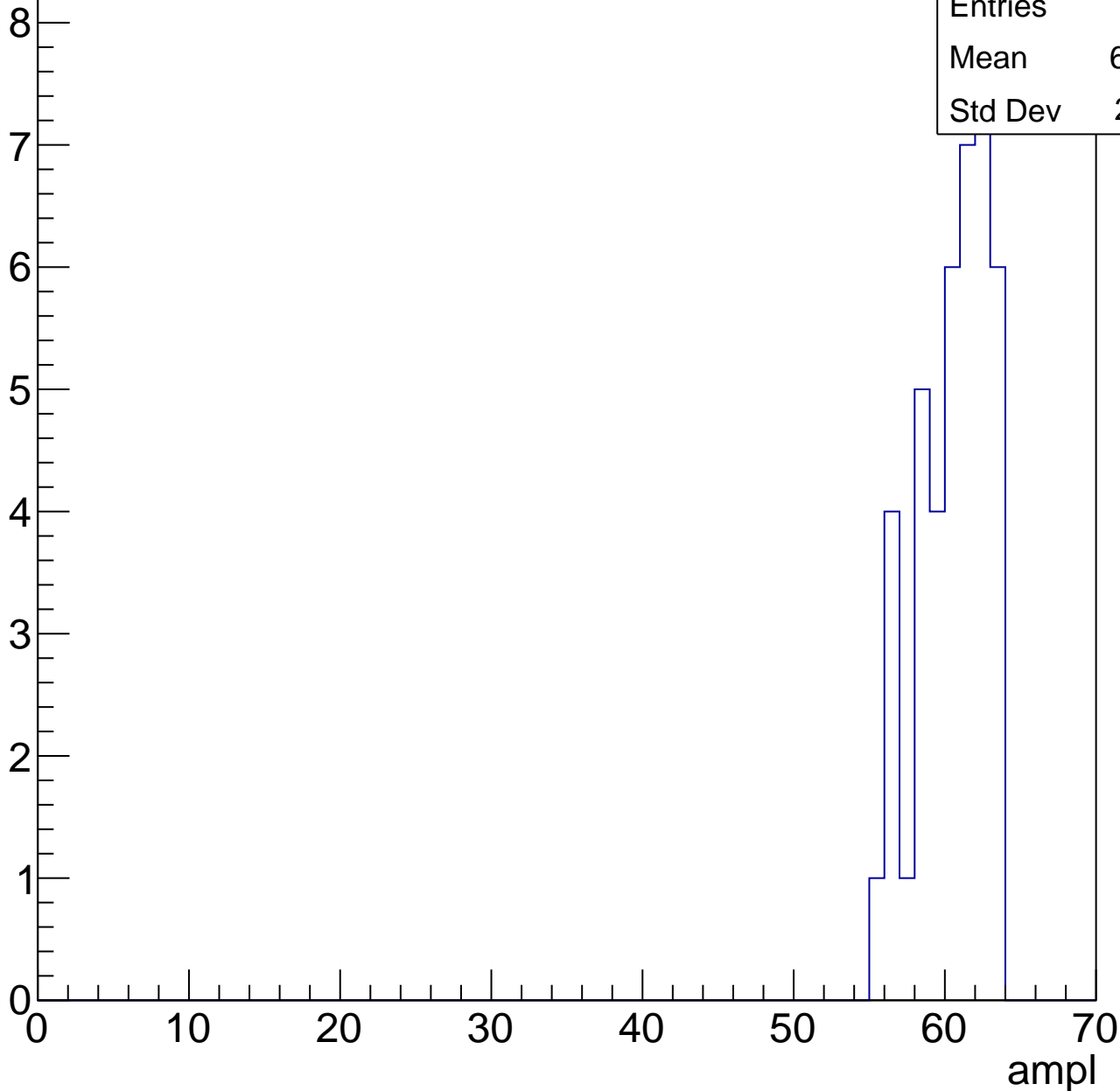


B1L100S, U7-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 60.07 |
| Std Dev | 2.261 |



B1L100S, U7-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

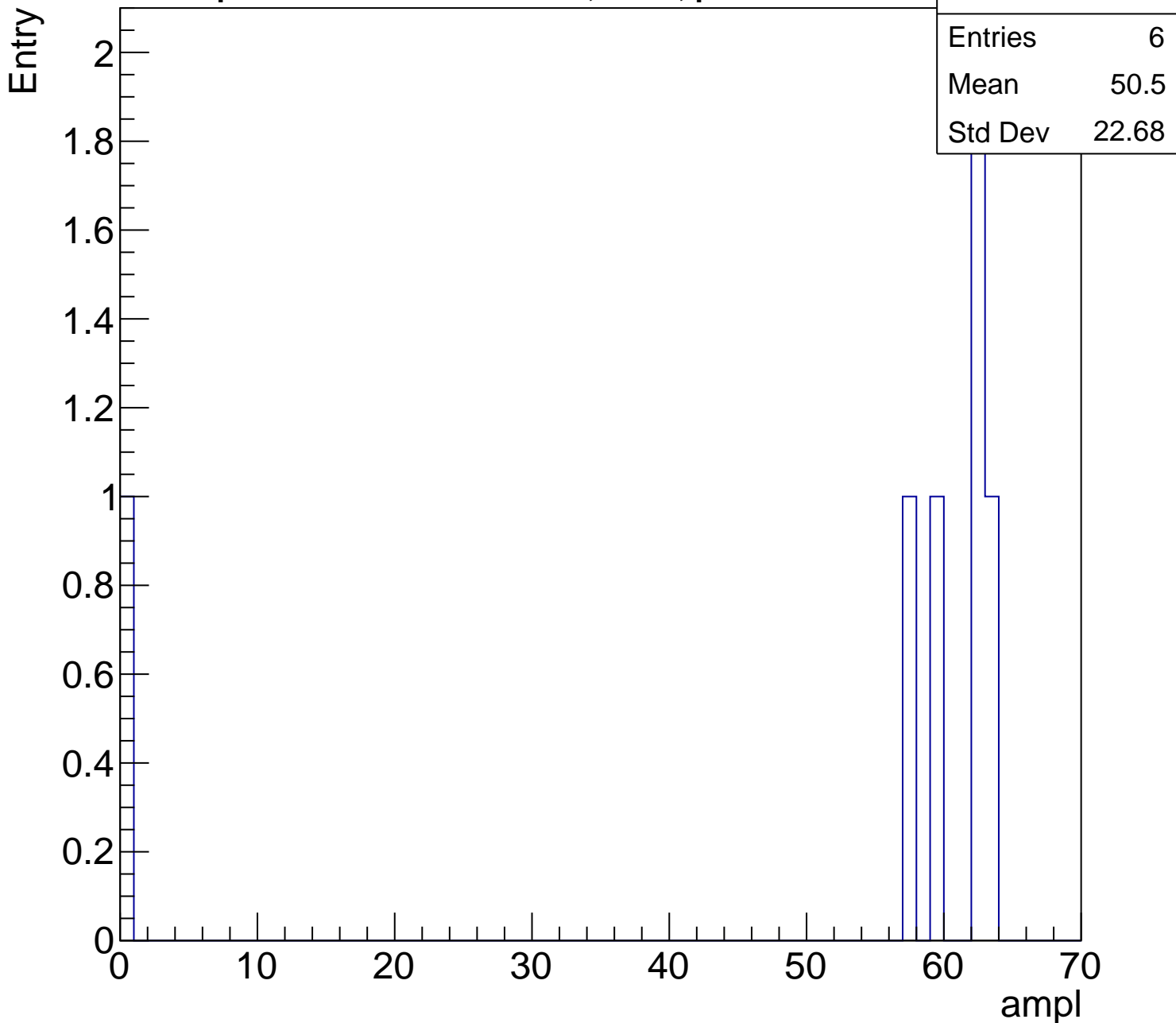
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 50.5 |
| Std Dev | 22.68 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch31, adc0

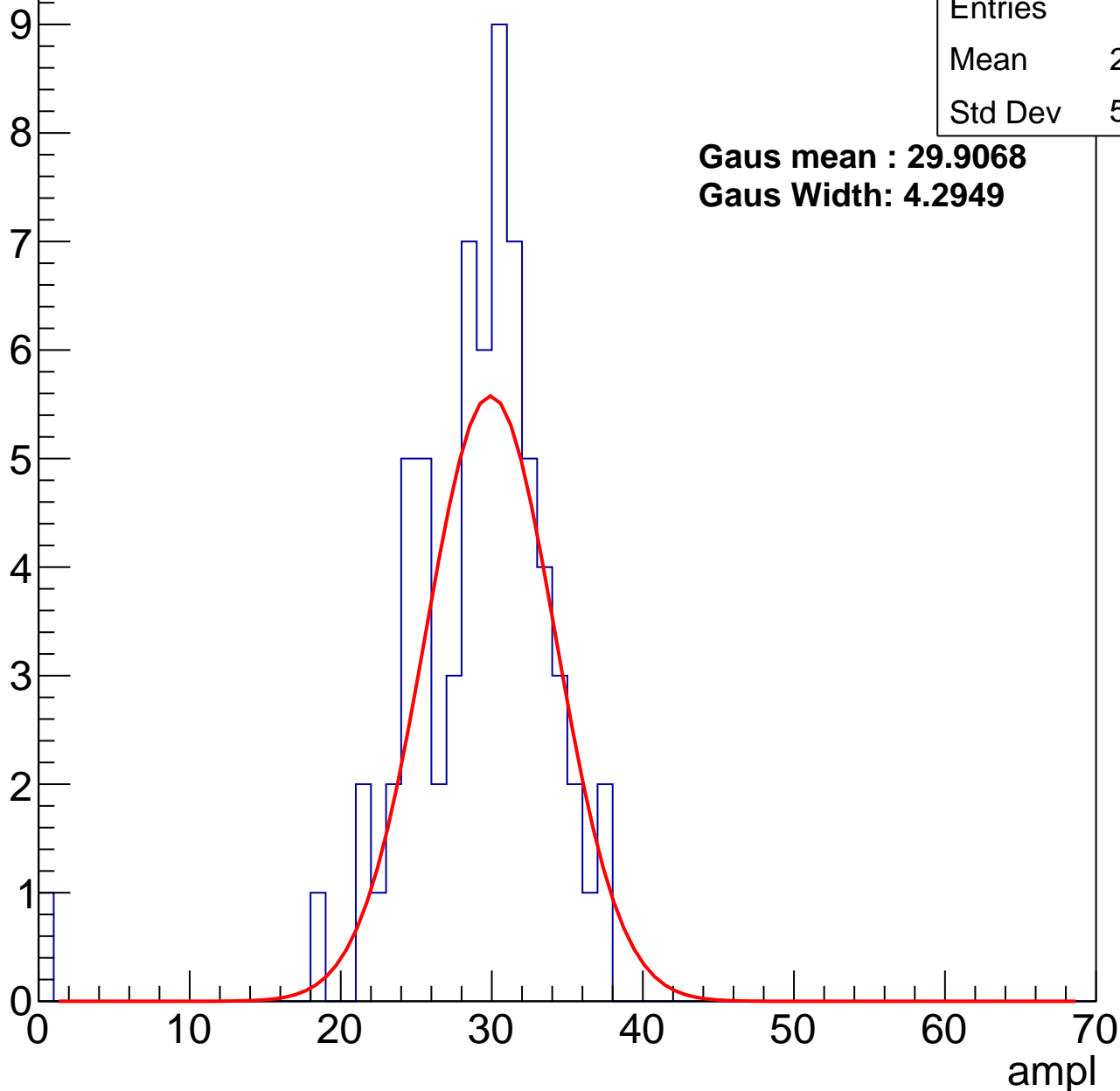
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.49 |
| Std Dev | 5.293 |

Gaus mean : 29.9068

Gaus Width: 4.2949



B1L100S, U7-ch31, adc1

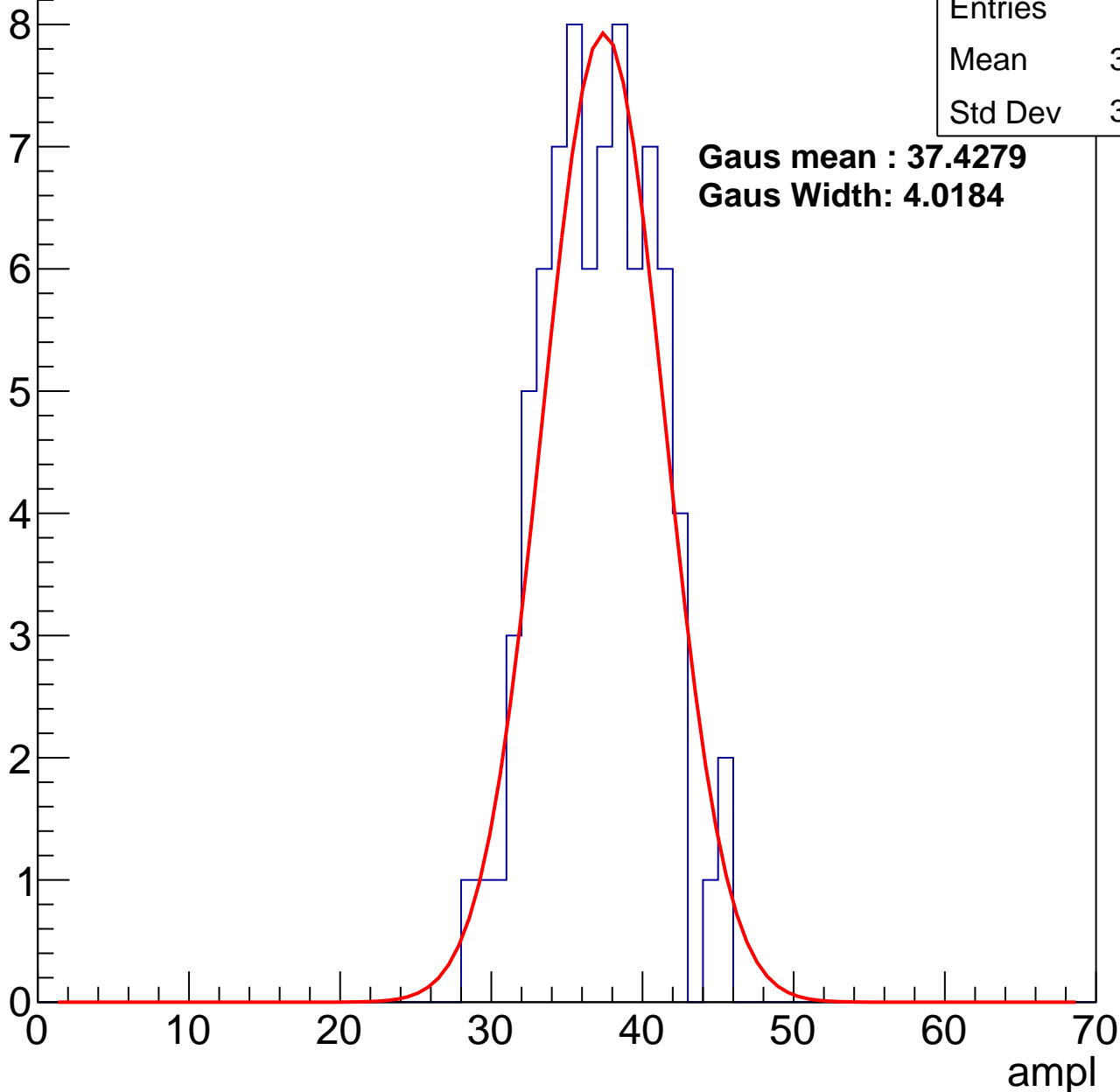
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 36.67 |
| Std Dev | 3.703 |

Gaus mean : 37.4279

Gaus Width: 4.0184



B1L100S, U7-ch31, adc2

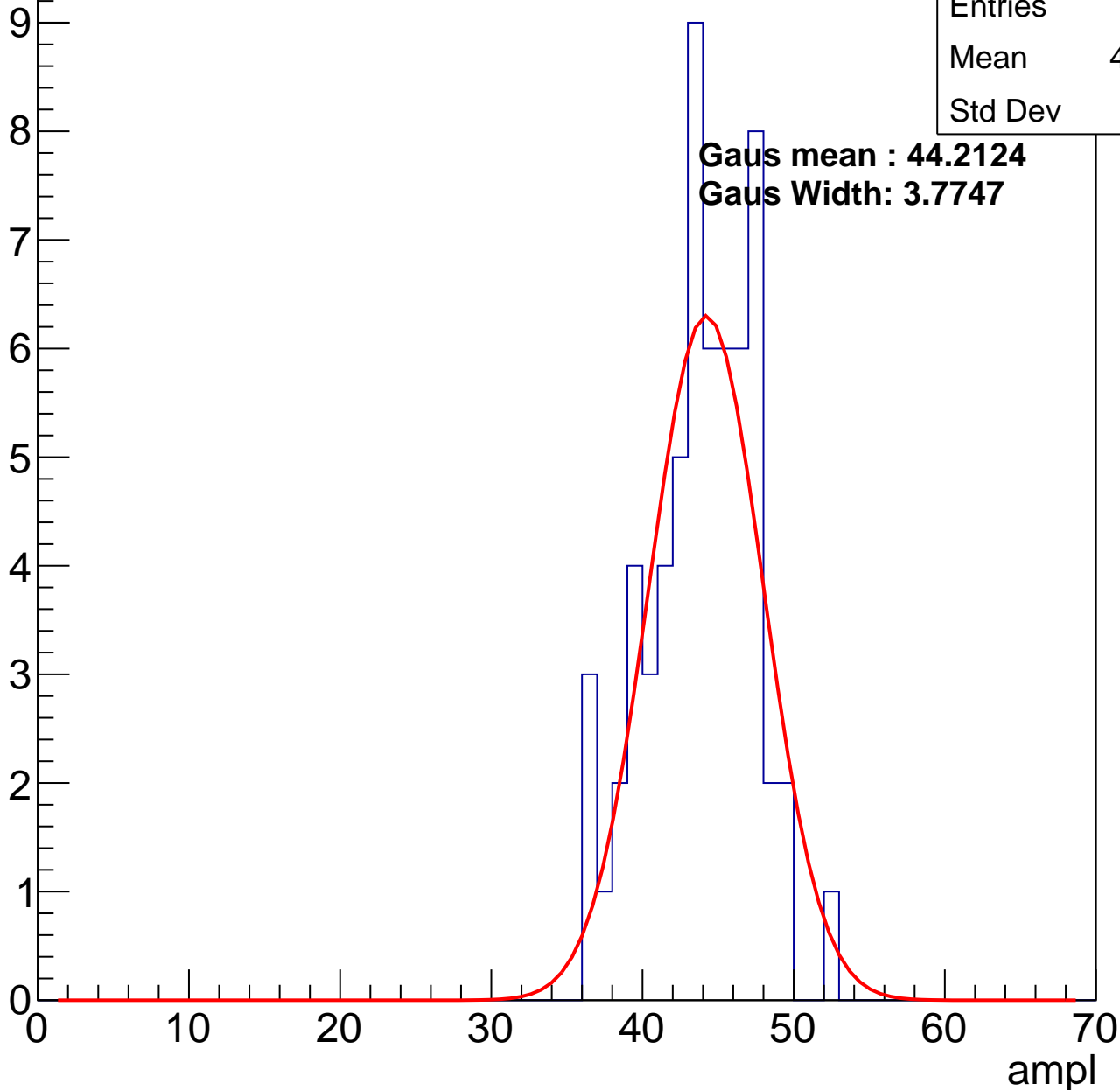
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.39 |
| Std Dev | 3.48 |

Gaus mean : 44.2124

Gaus Width: 3.7747

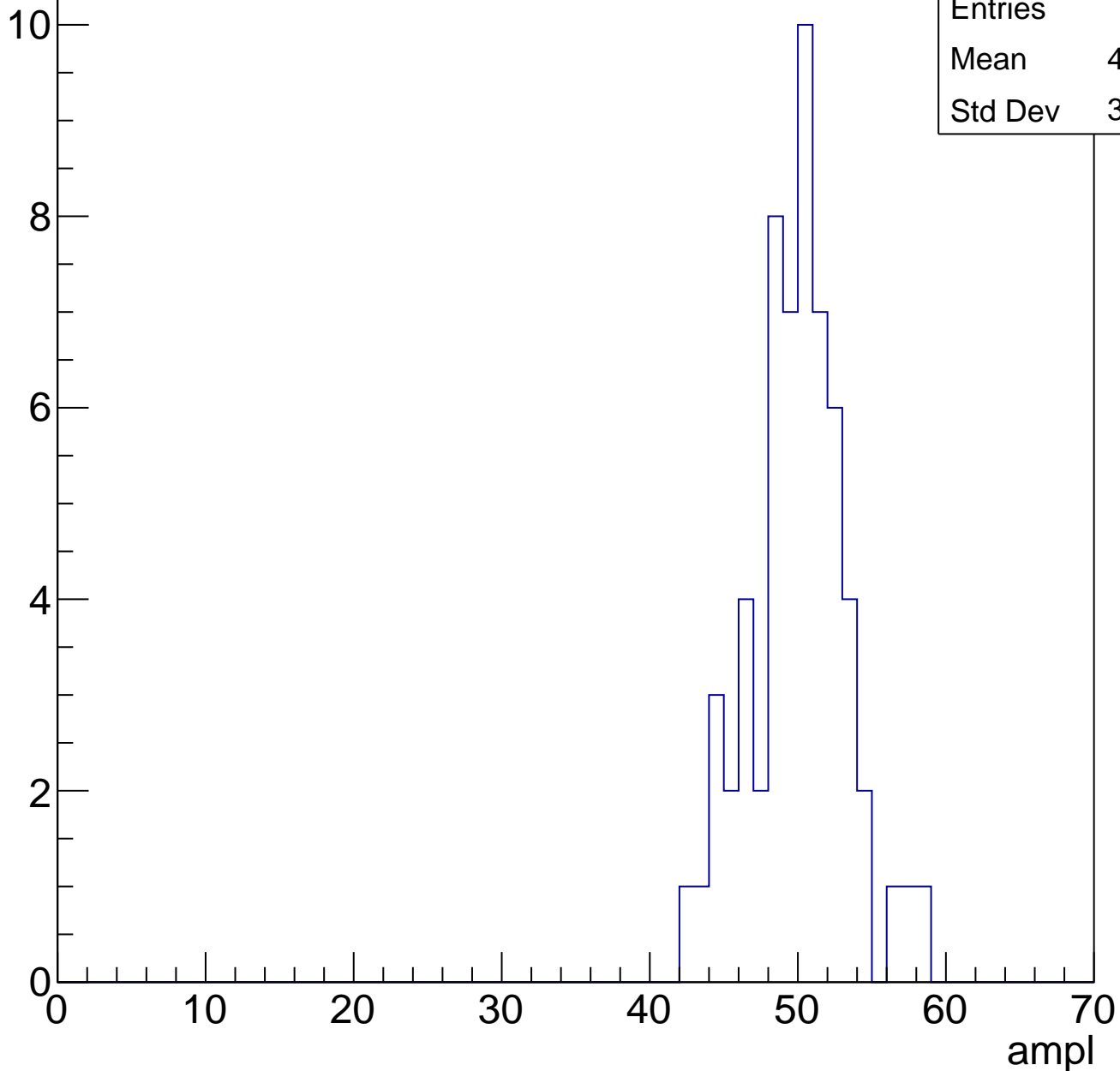


B1L100S, U7-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

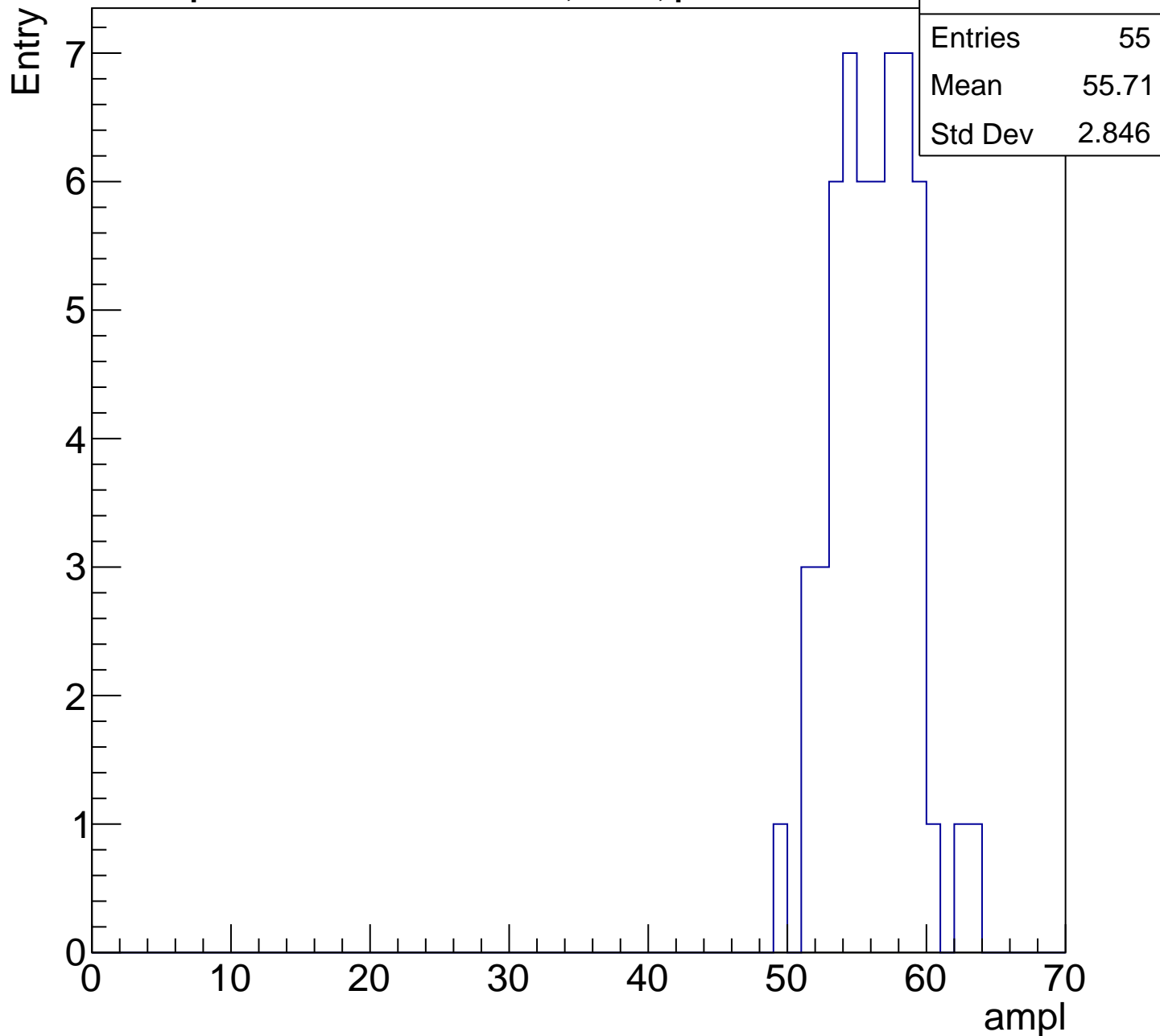
| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.53 |
| Std Dev | 3.232 |

Entry



B1L100S, U7-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.72 |
| Std Dev | 8.759 |

0

2

4

6

8

10

B1L100S, U7-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch32, adc0

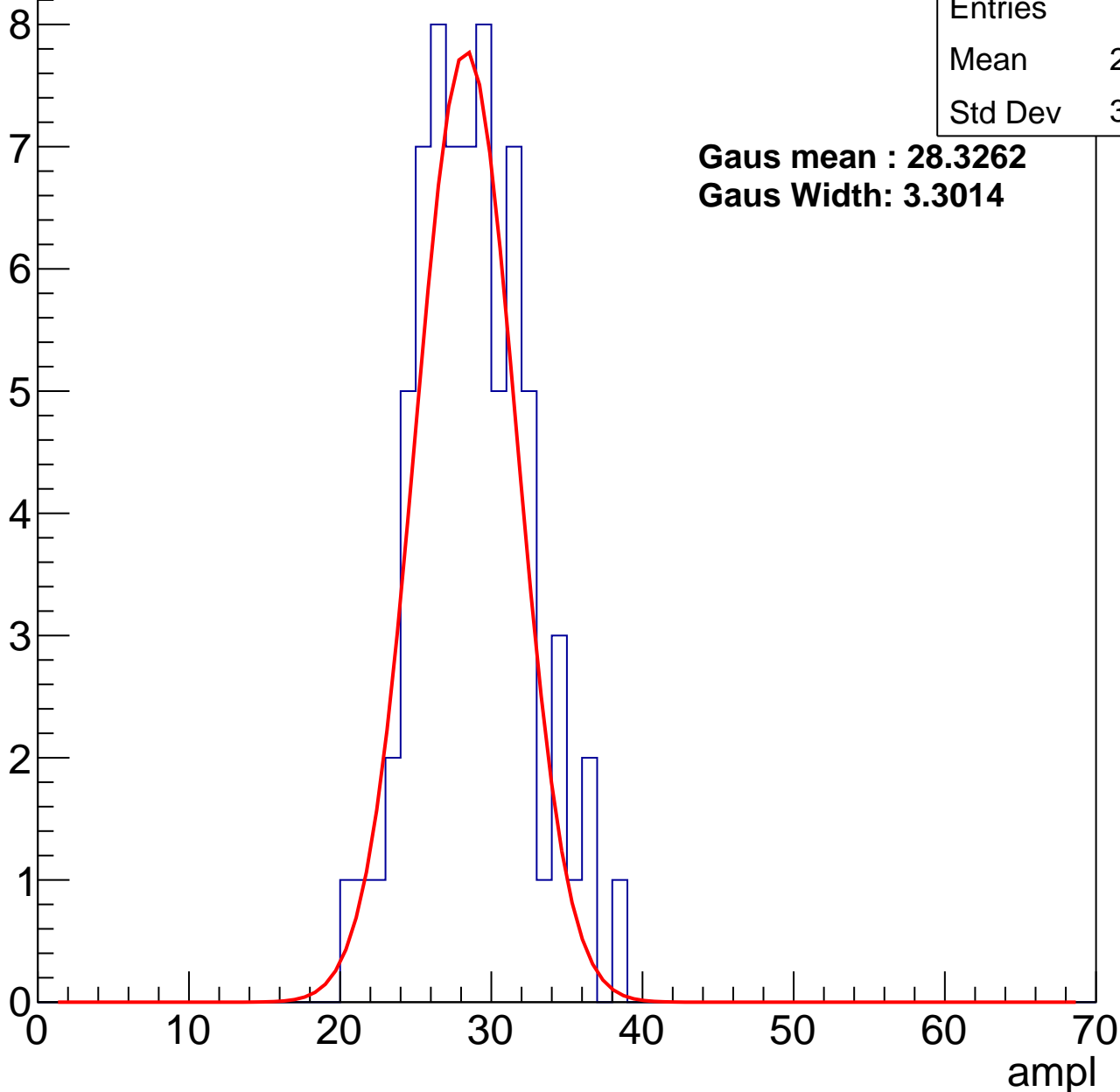
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 28.28 |
| Std Dev | 3.645 |

Gaus mean : 28.3262

Gaus Width: 3.3014



B1L100S, U7-ch32, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

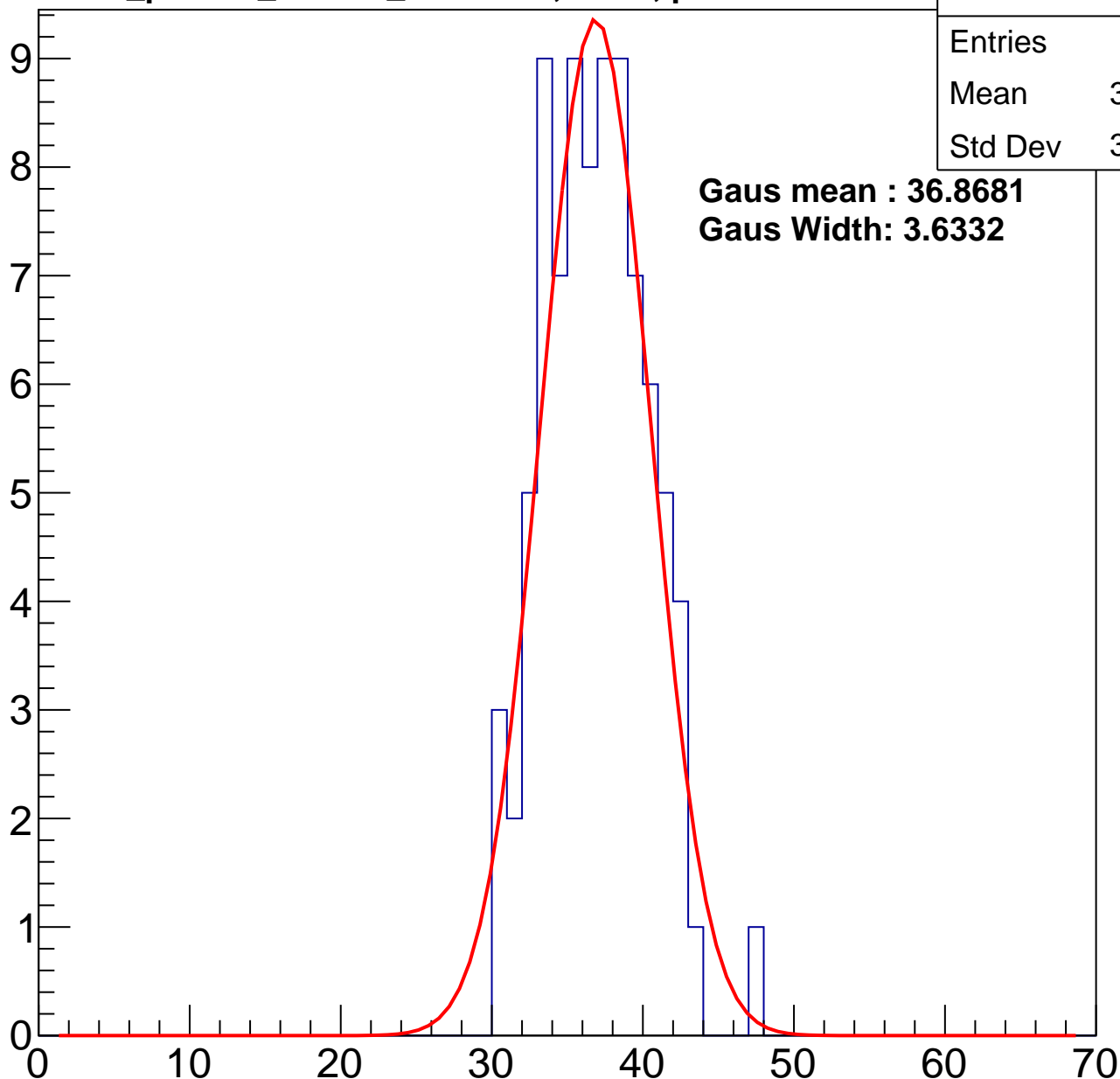
9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 85 |
| Mean | 36.48 |
| Std Dev | 3.394 |

Gaus mean : 36.8681

Gaus Width: 3.6332

ampl



B1L100S, U7-ch32, adc2

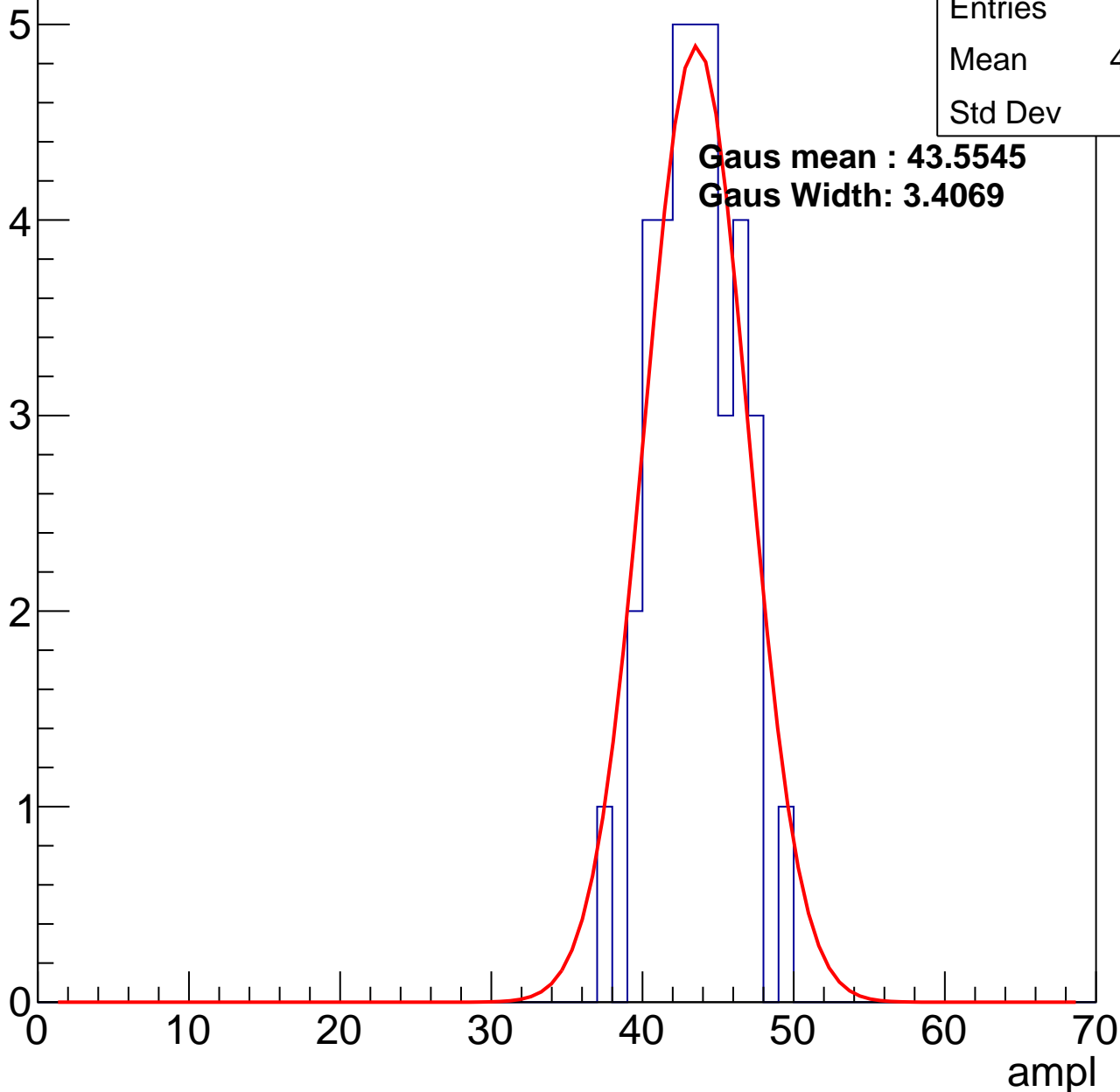
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 43.05 |
| Std Dev | 2.66 |

Gaus mean : 43.5545

Gaus Width: 3.4069

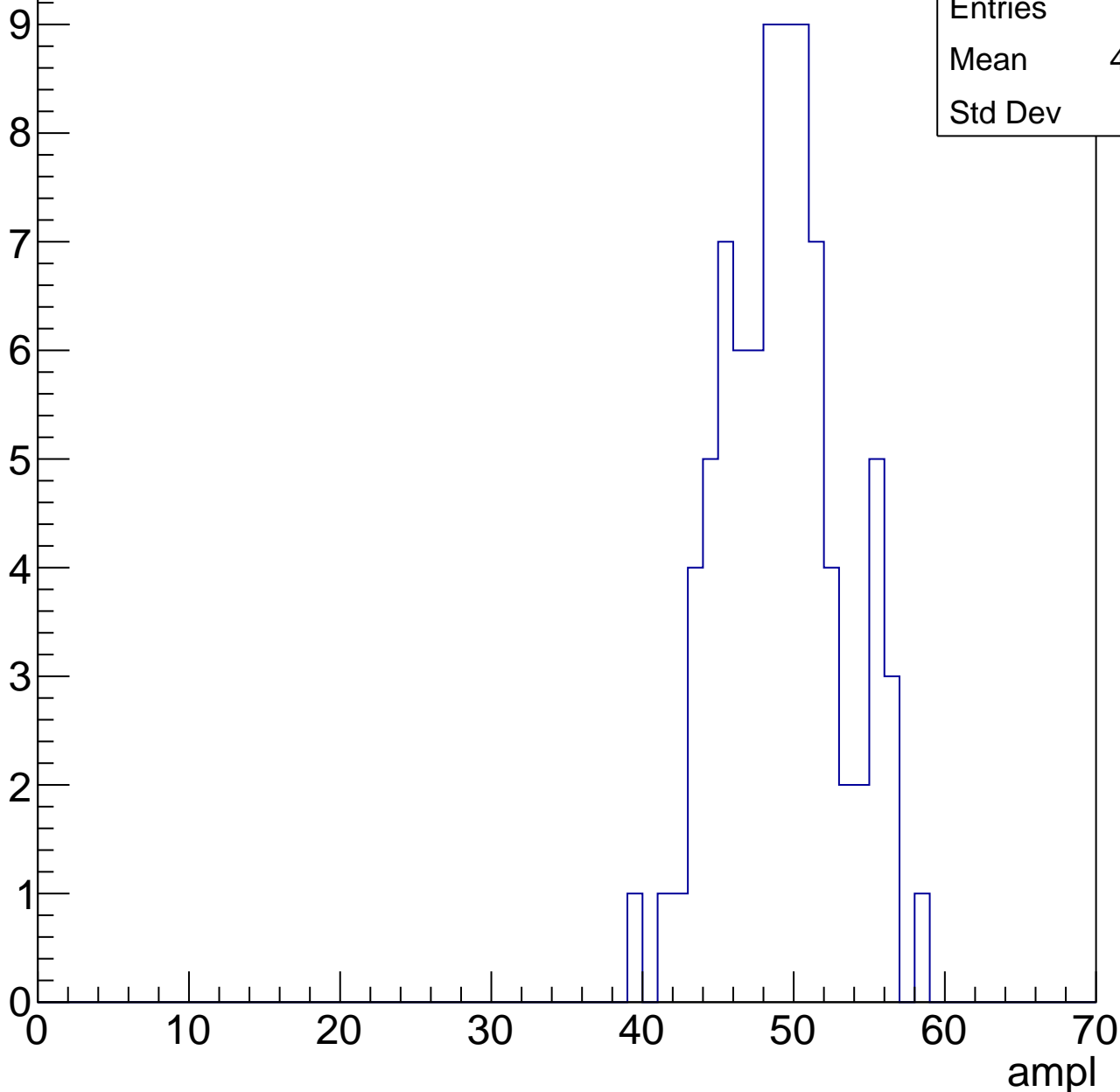


B1L100S, U7-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 48.66 |
| Std Dev | 3.88 |

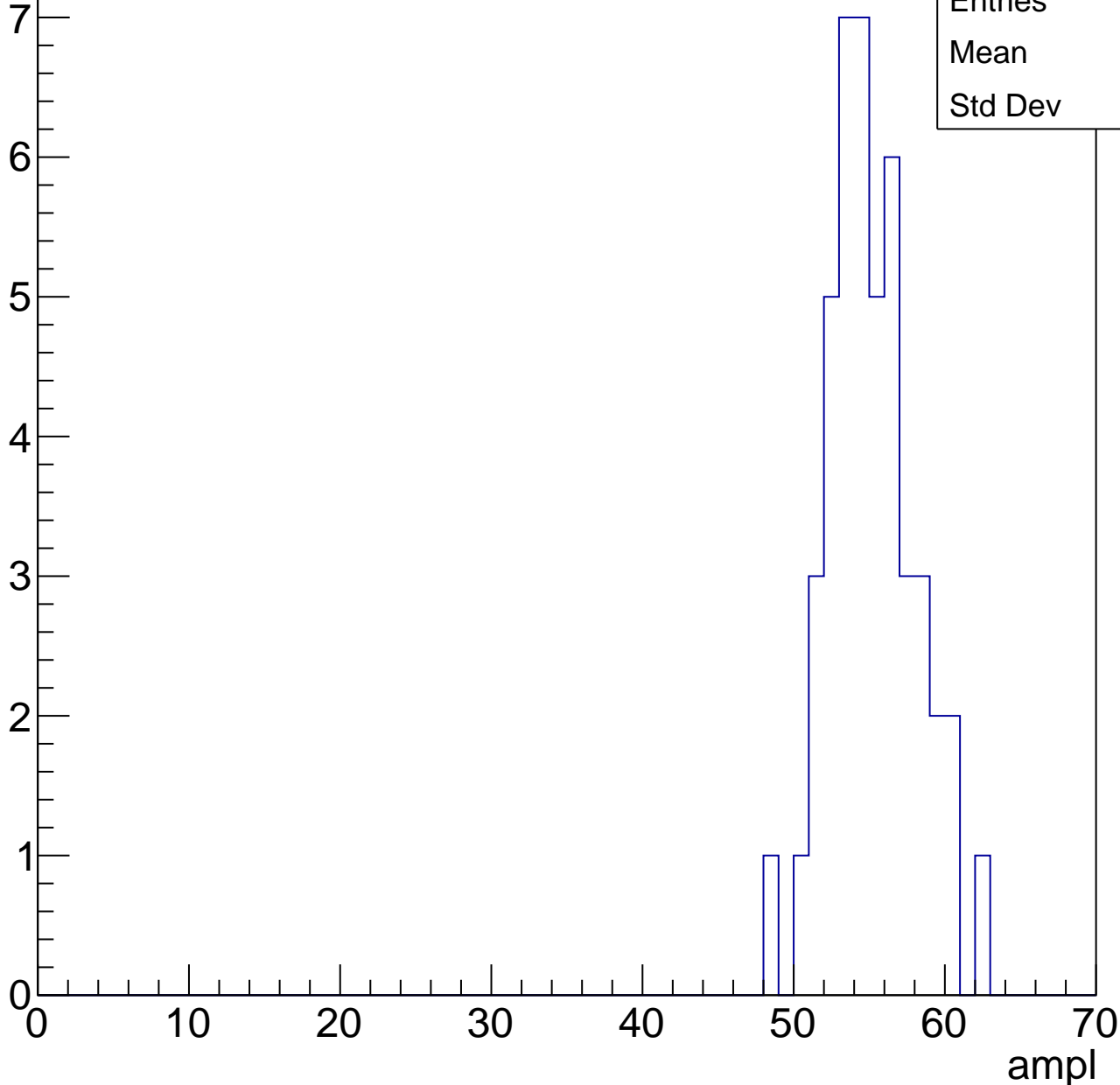


B1L100S, U7-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 46 |
| Mean | 54.7 |
| Std Dev | 2.85 |



B1L100S, U7-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 57

Mean 59.74

Std Dev 2.586

8

6

4

2

0

0

10

20

30

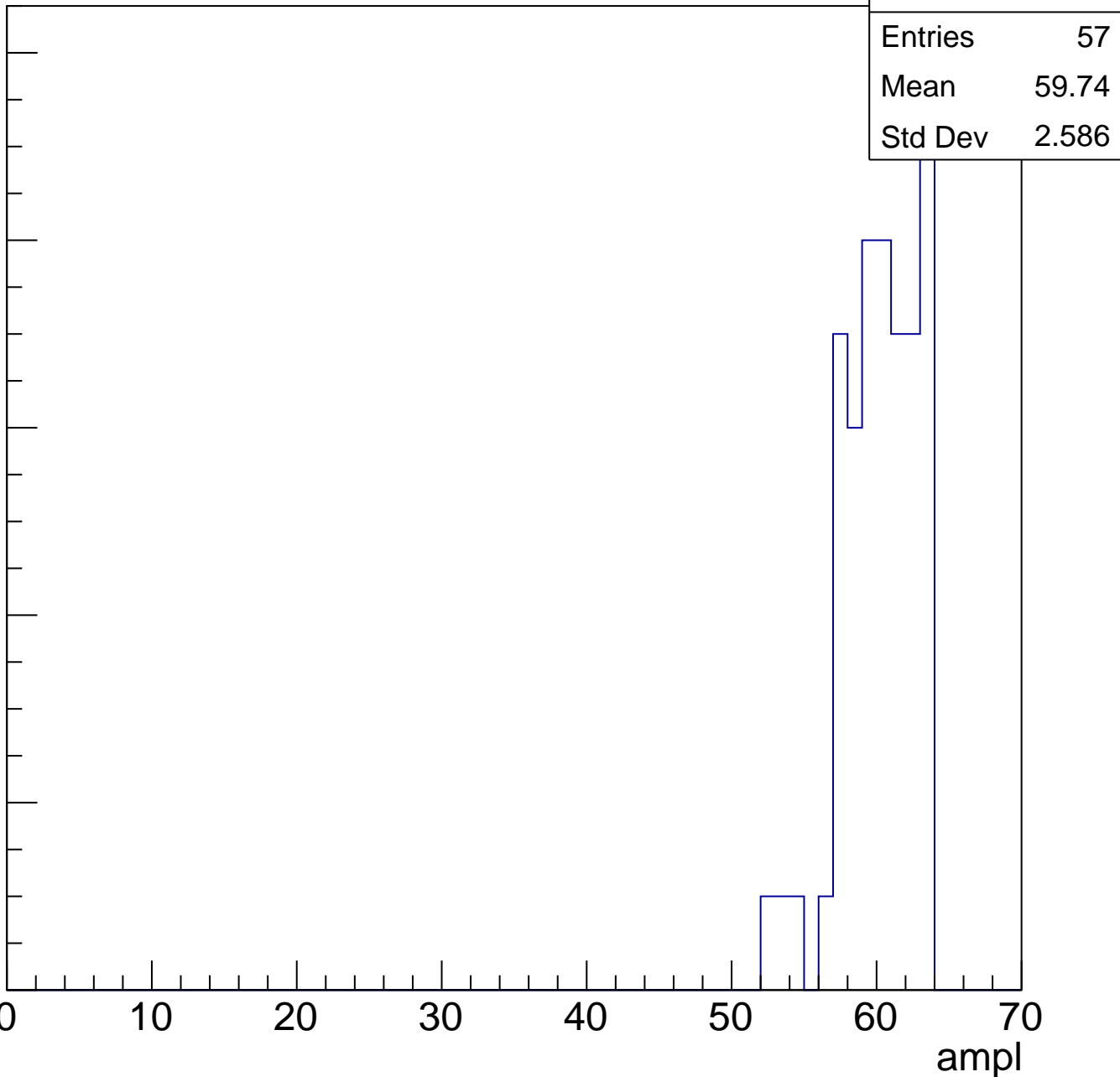
40

50

60

70

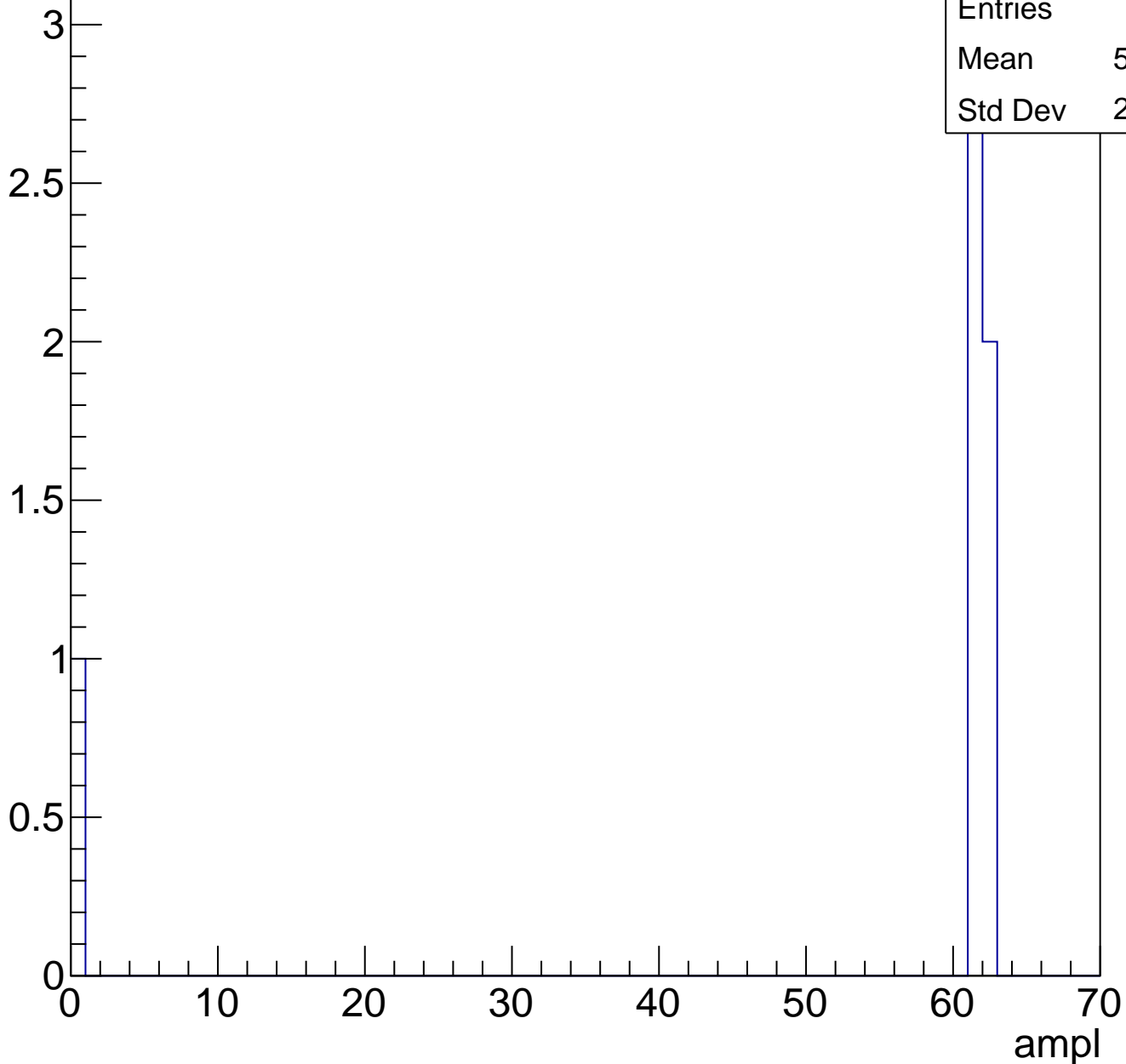
ampl



B1L100S, U7-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch33, adc0

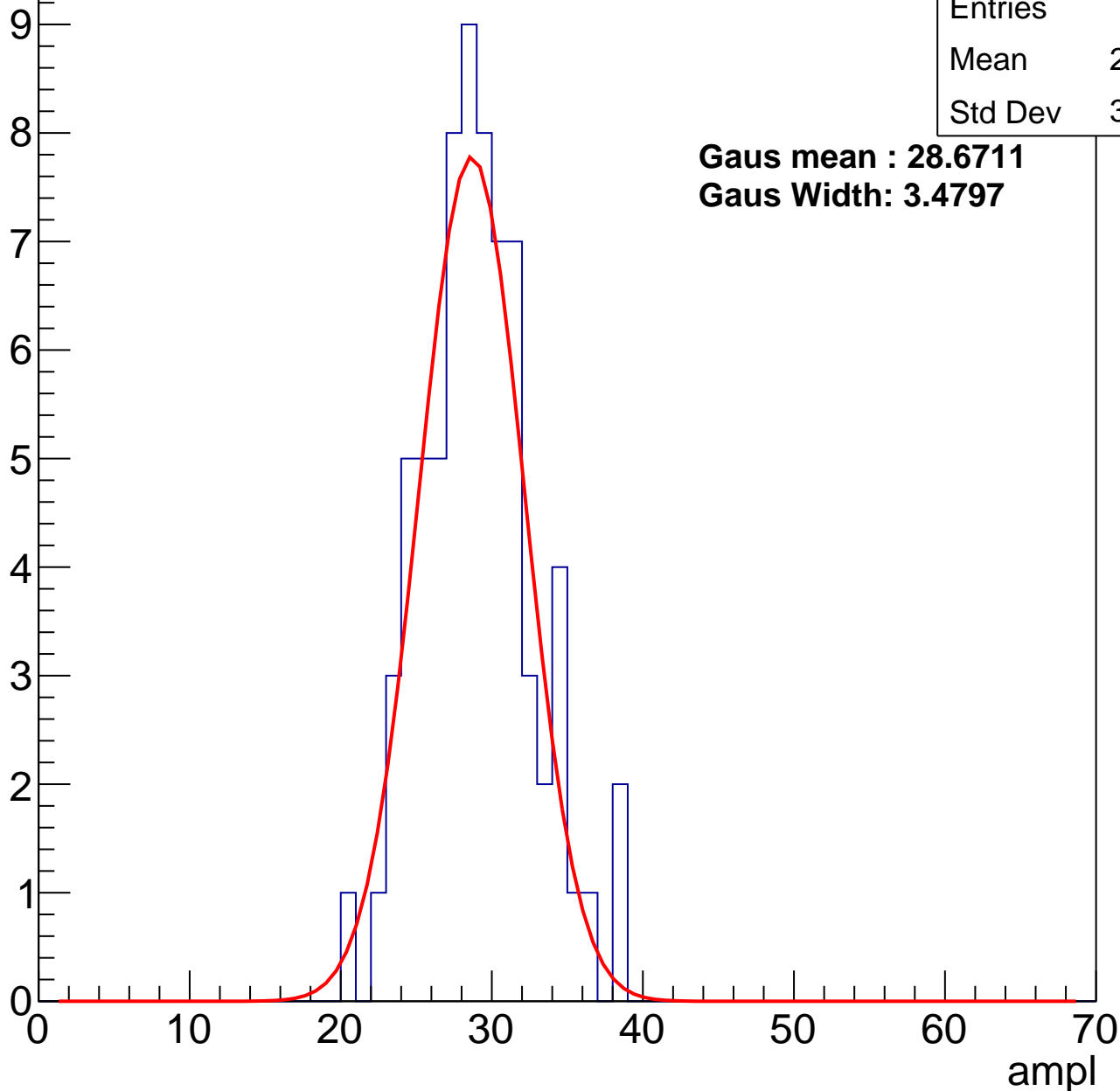
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 28.58 |
| Std Dev | 3.647 |

Gaus mean : 28.6711

Gaus Width: 3.4797



B1L100S, U7-ch33, adc1

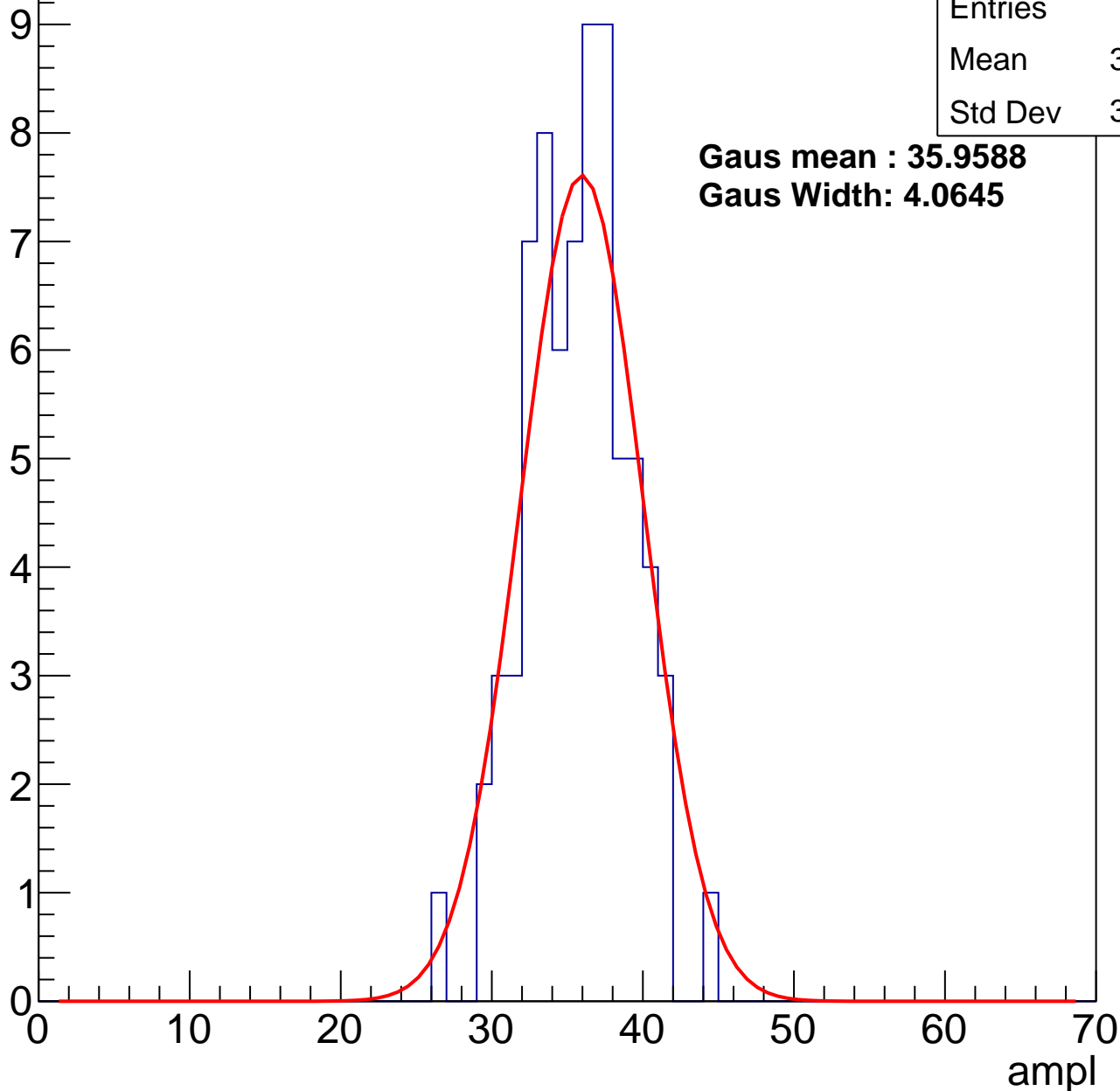
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 35.25 |
| Std Dev | 3.375 |

Gaus mean : 35.9588

Gaus Width: 4.0645



B1L100S, U7-ch33, adc2

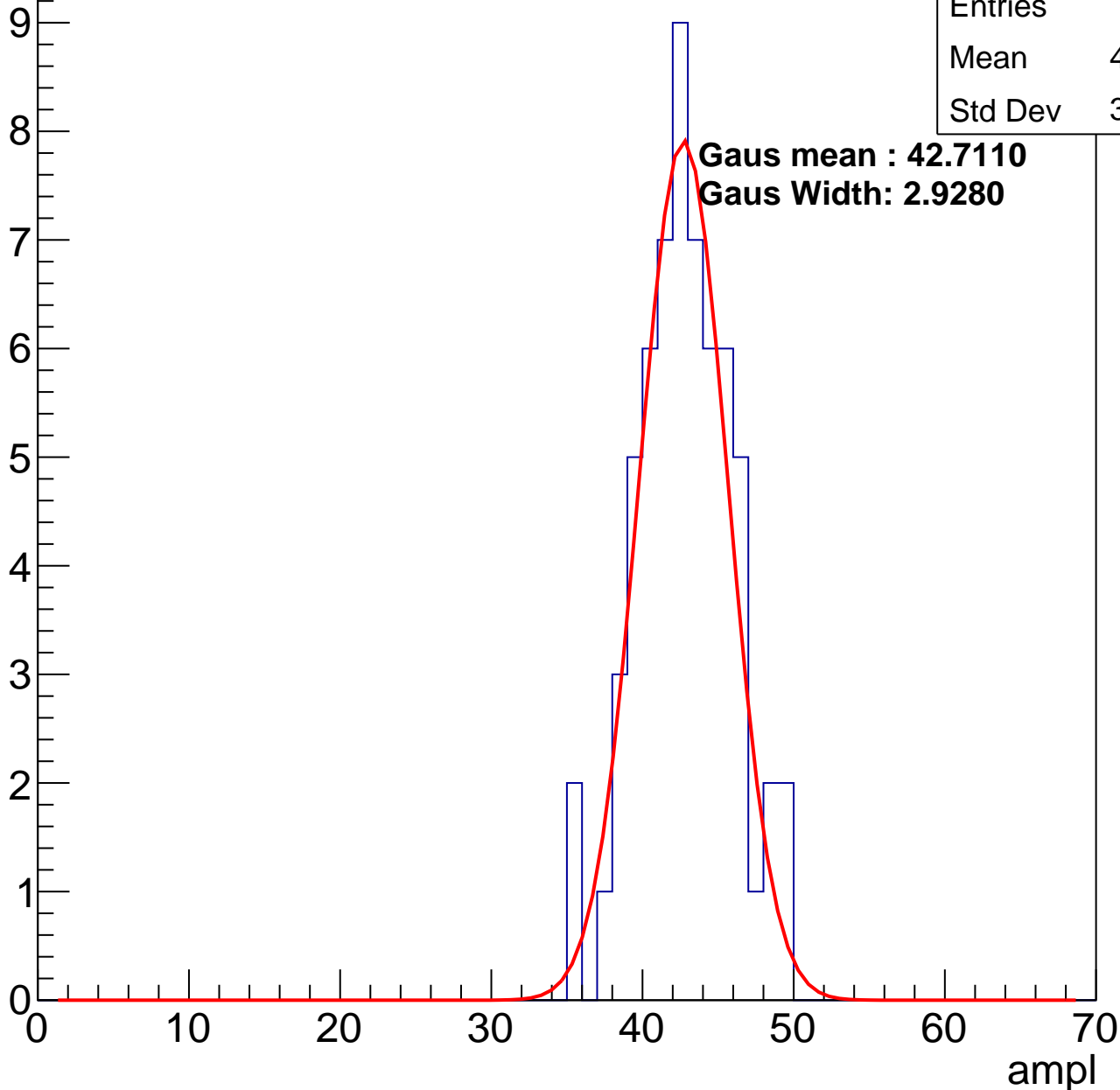
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.37 |
| Std Dev | 3.107 |

Gaus mean : 42.7110

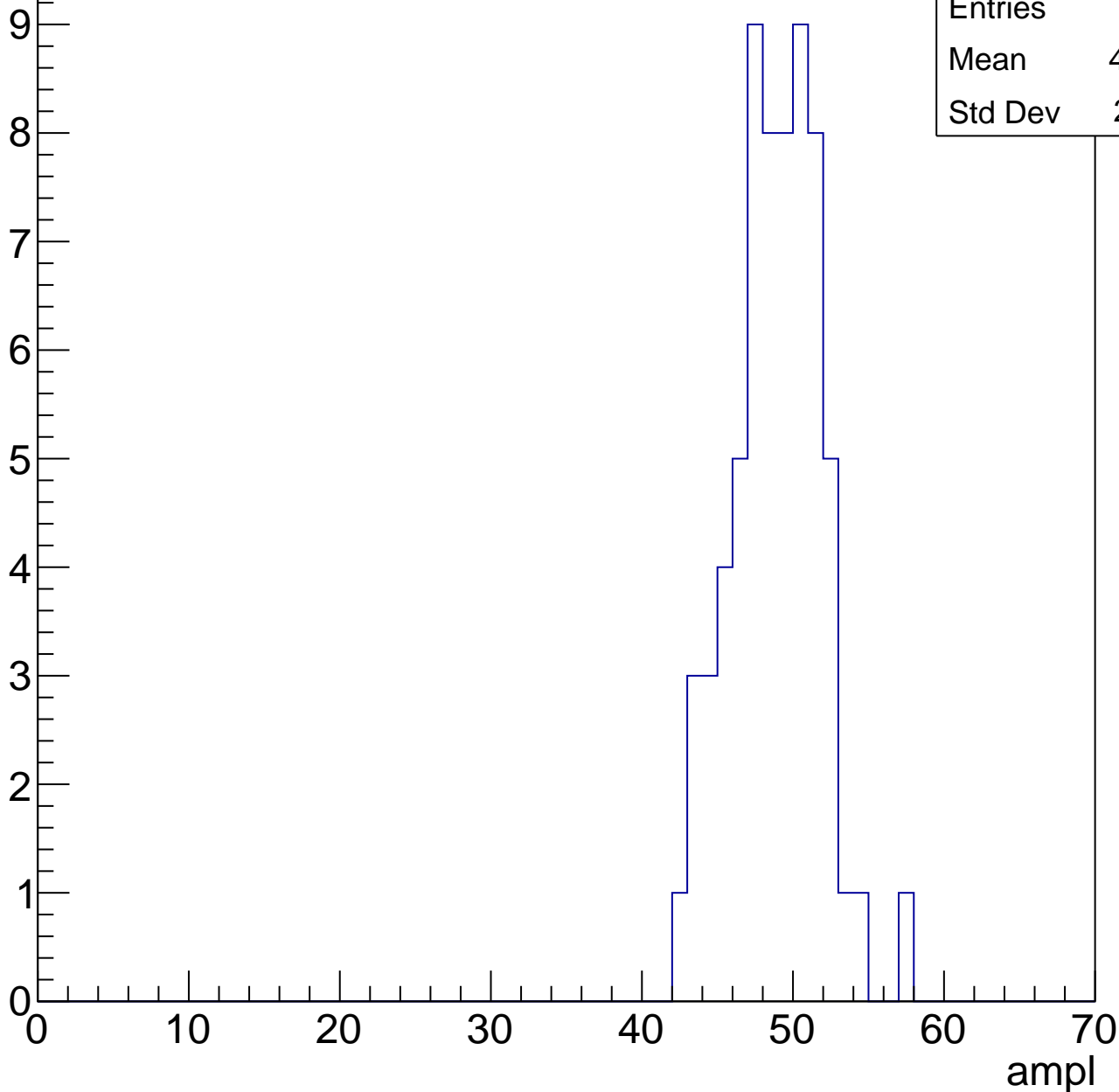
Gaus Width: 2.9280



B1L100S, U7-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

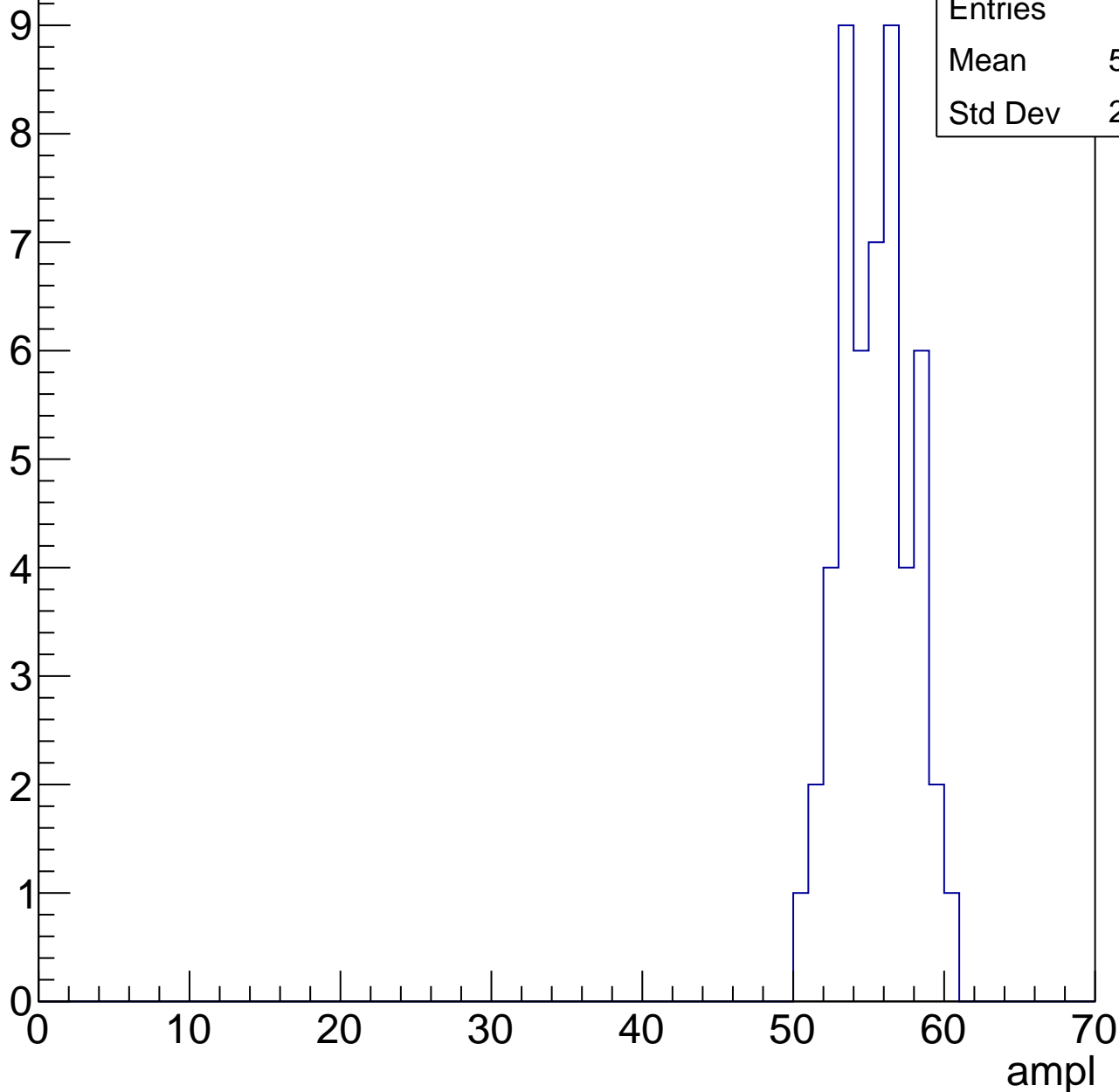


| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.39 |
| Std Dev | 2.891 |

B1L100S, U7-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.98 |
| Std Dev | 2.305 |

B1L100S, U7-ch33, adc5

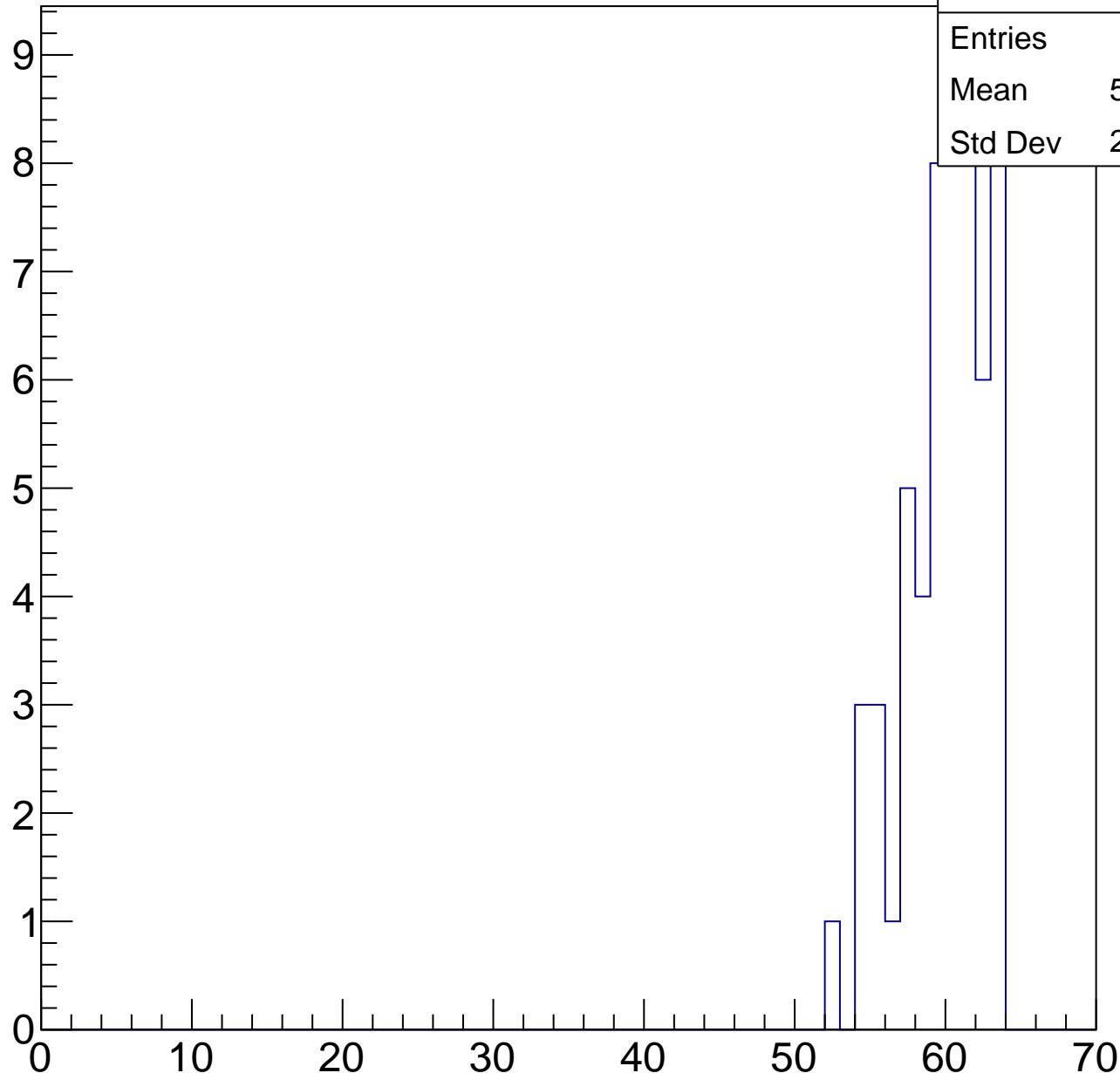
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 57 |
| Mean | 59.46 |
| Std Dev | 2.715 |

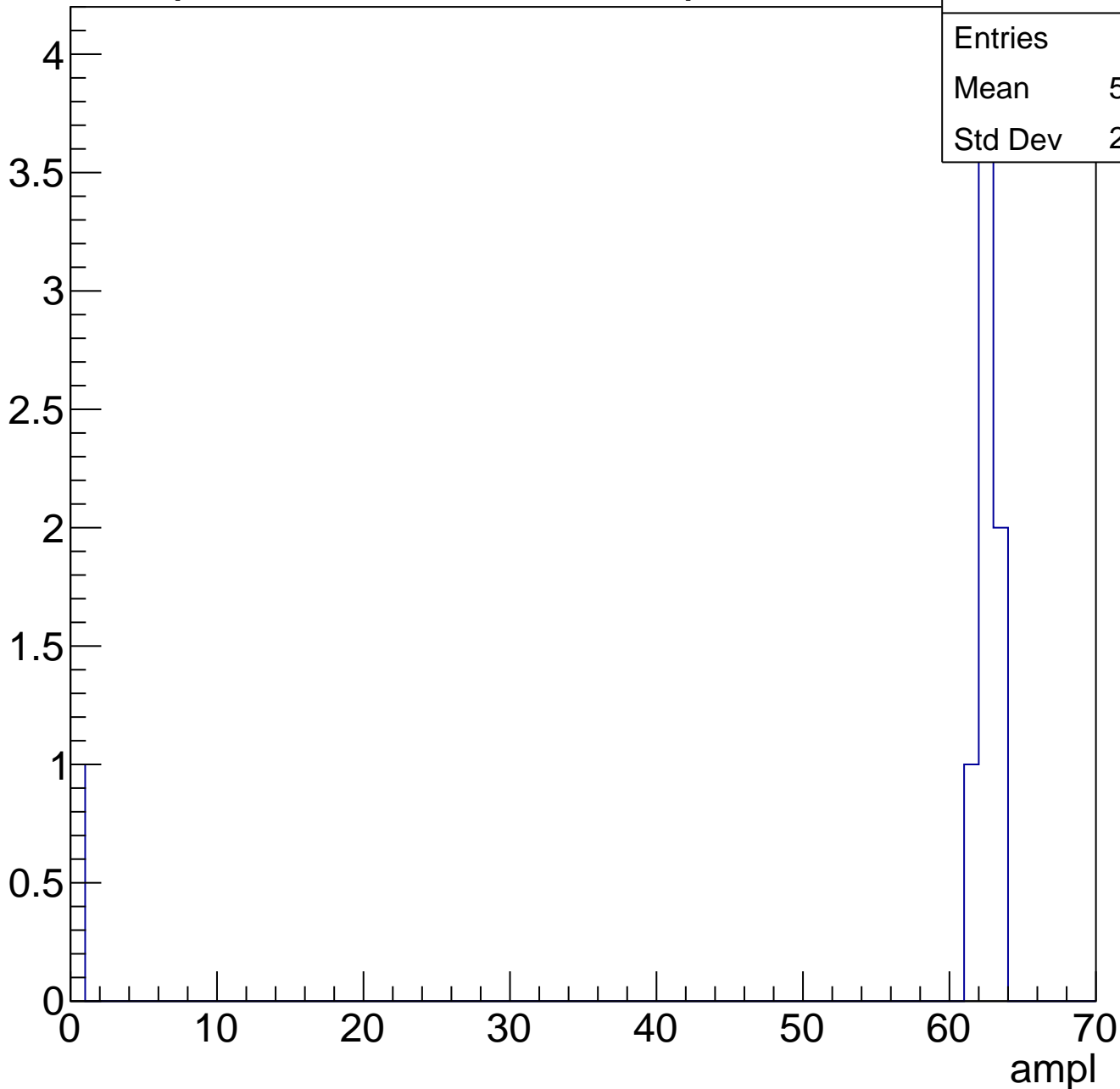
ampl



B1L100S, U7-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch34, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 28.39 |
| Std Dev | 3.439 |

Gaus mean : 28.6334

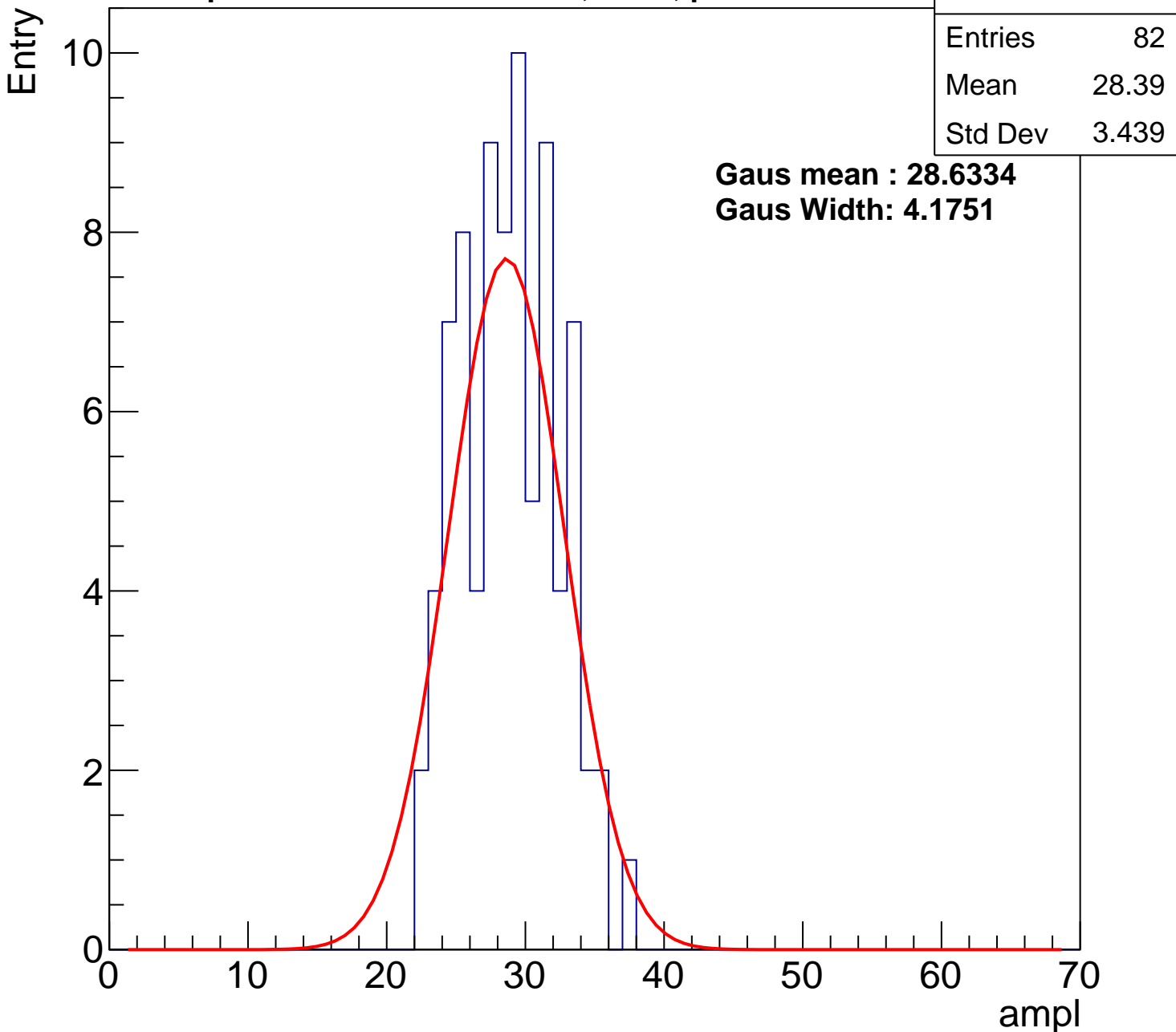
Gaus Width: 4.1751

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch34, adc1

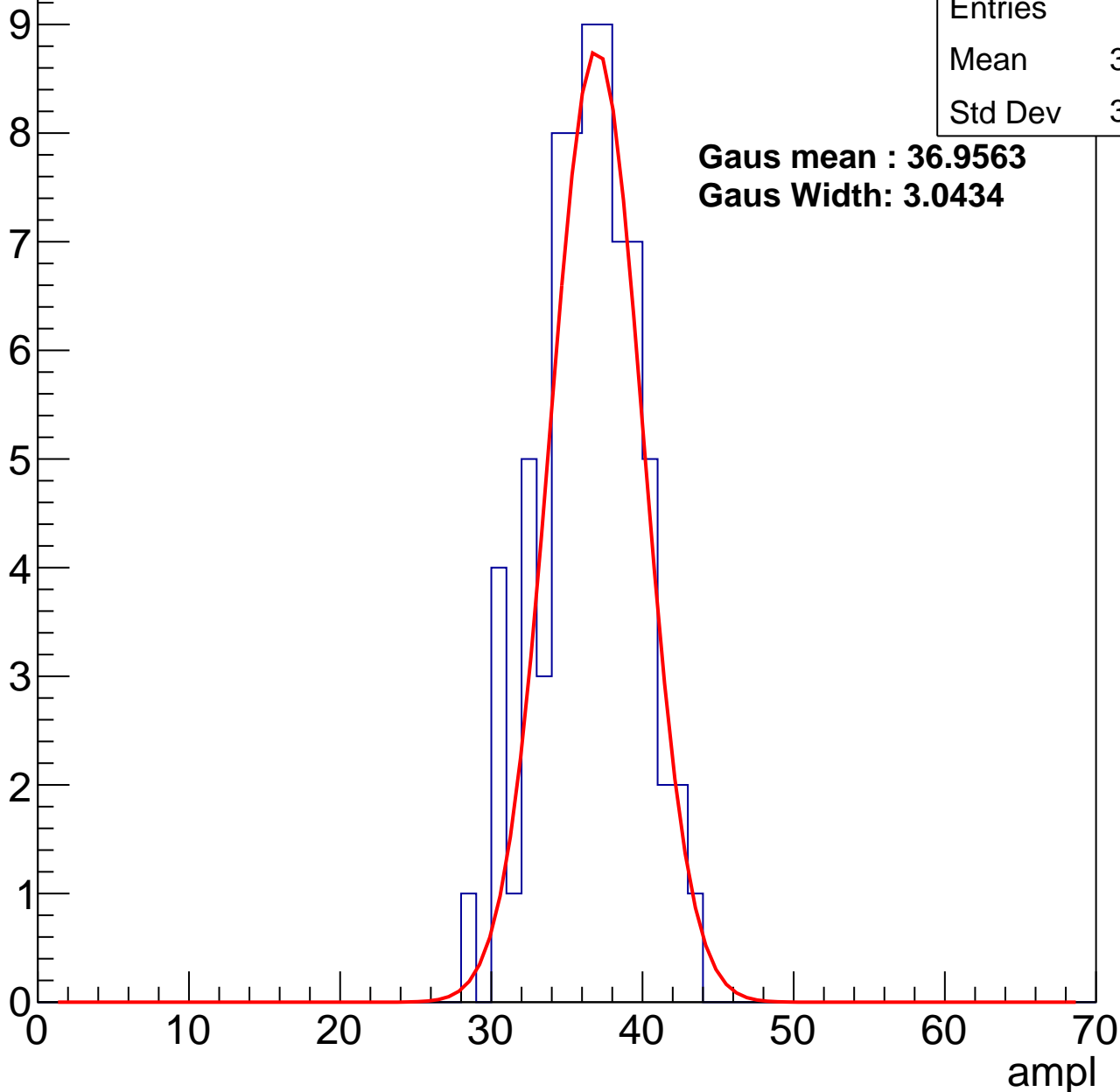
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.04 |
| Std Dev | 3.186 |

Gaus mean : 36.9563

Gaus Width: 3.0434

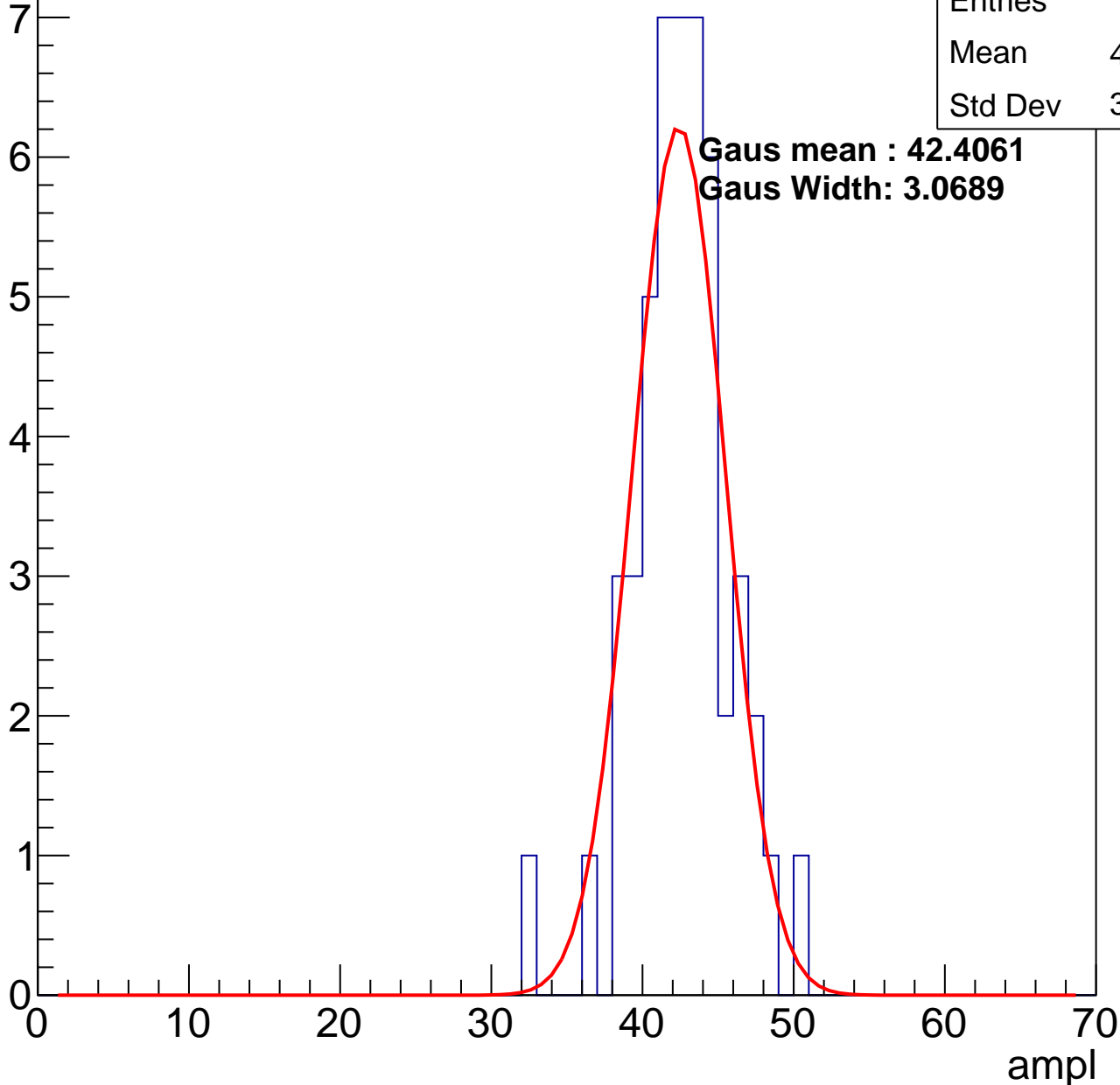


B1L100S, U7-ch34, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 42.14 |
| Std Dev | 3.143 |

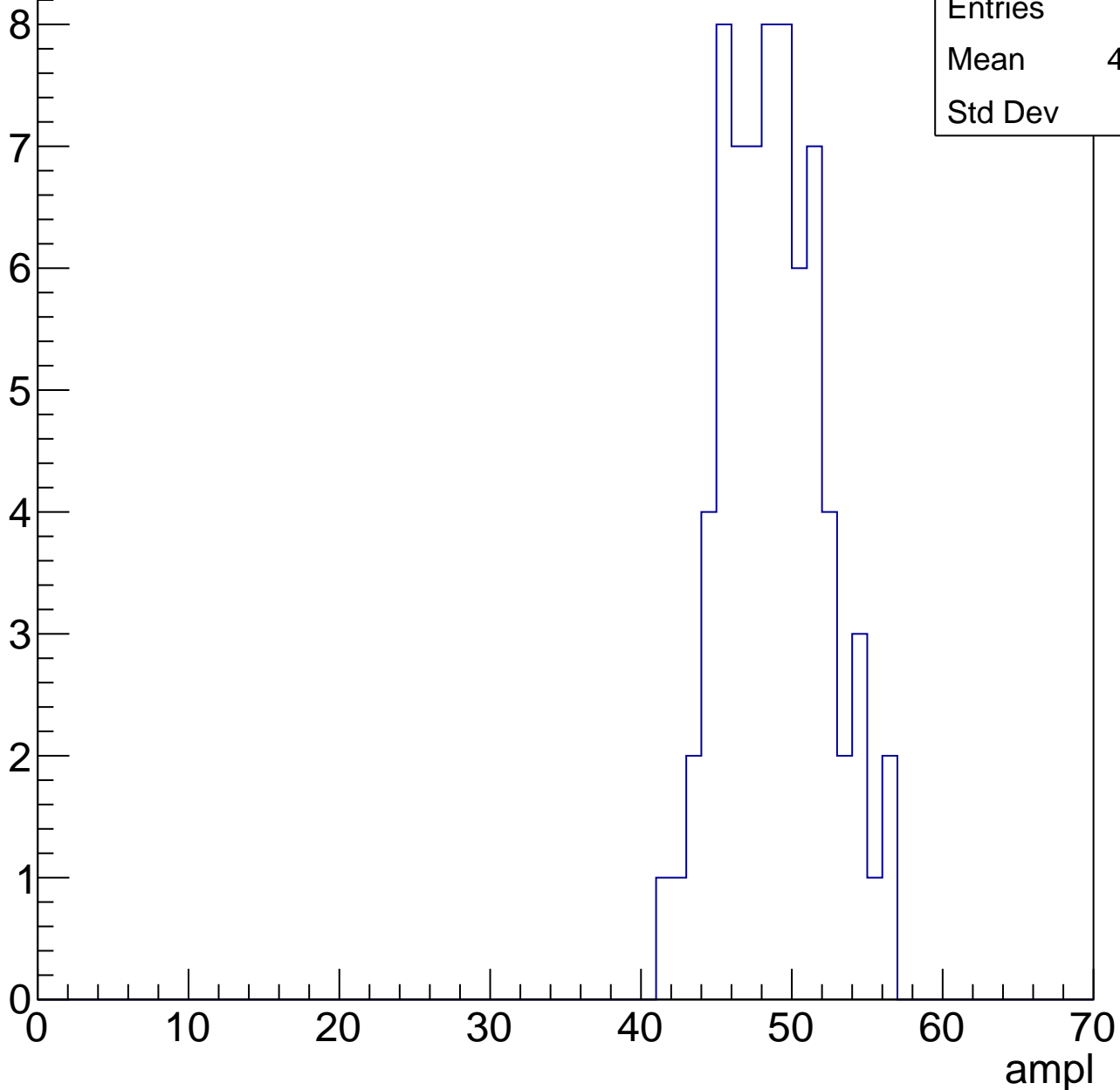


B1L100S, U7-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

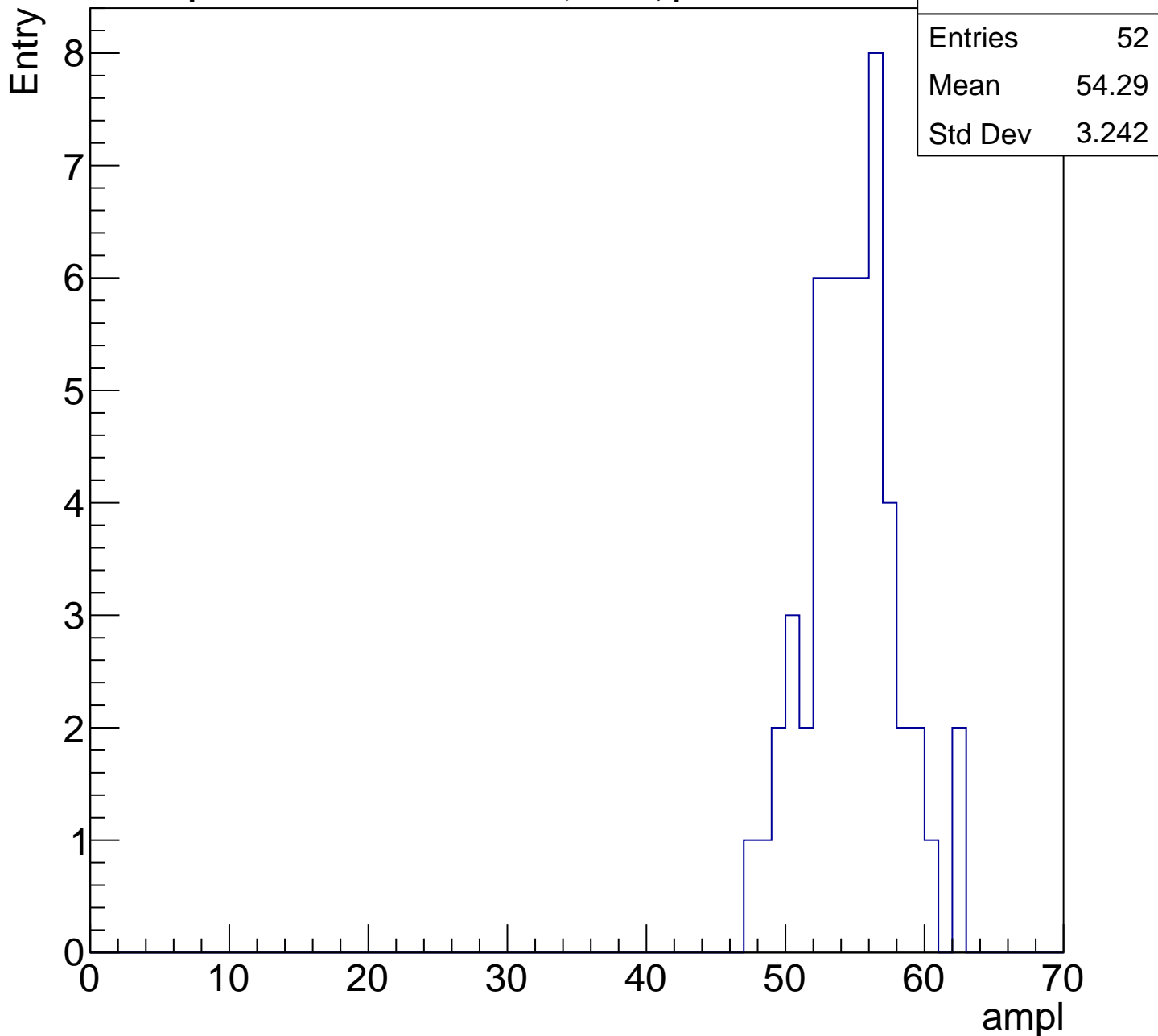
Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 48.34 |
| Std Dev | 3.34 |



B1L100S, U7-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch34, adc5

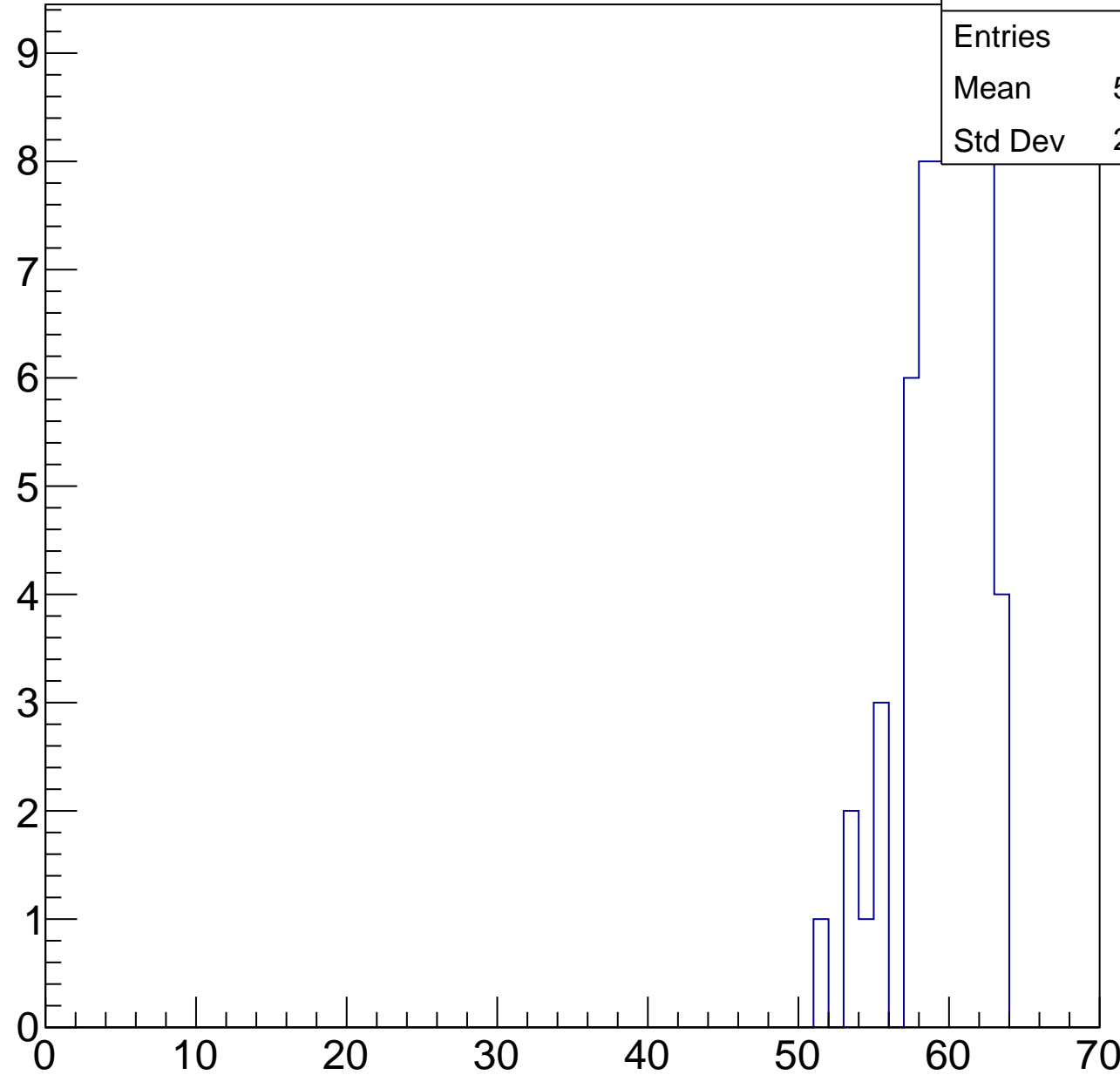
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 59 |
| Mean | 59.17 |
| Std Dev | 2.669 |

ampl

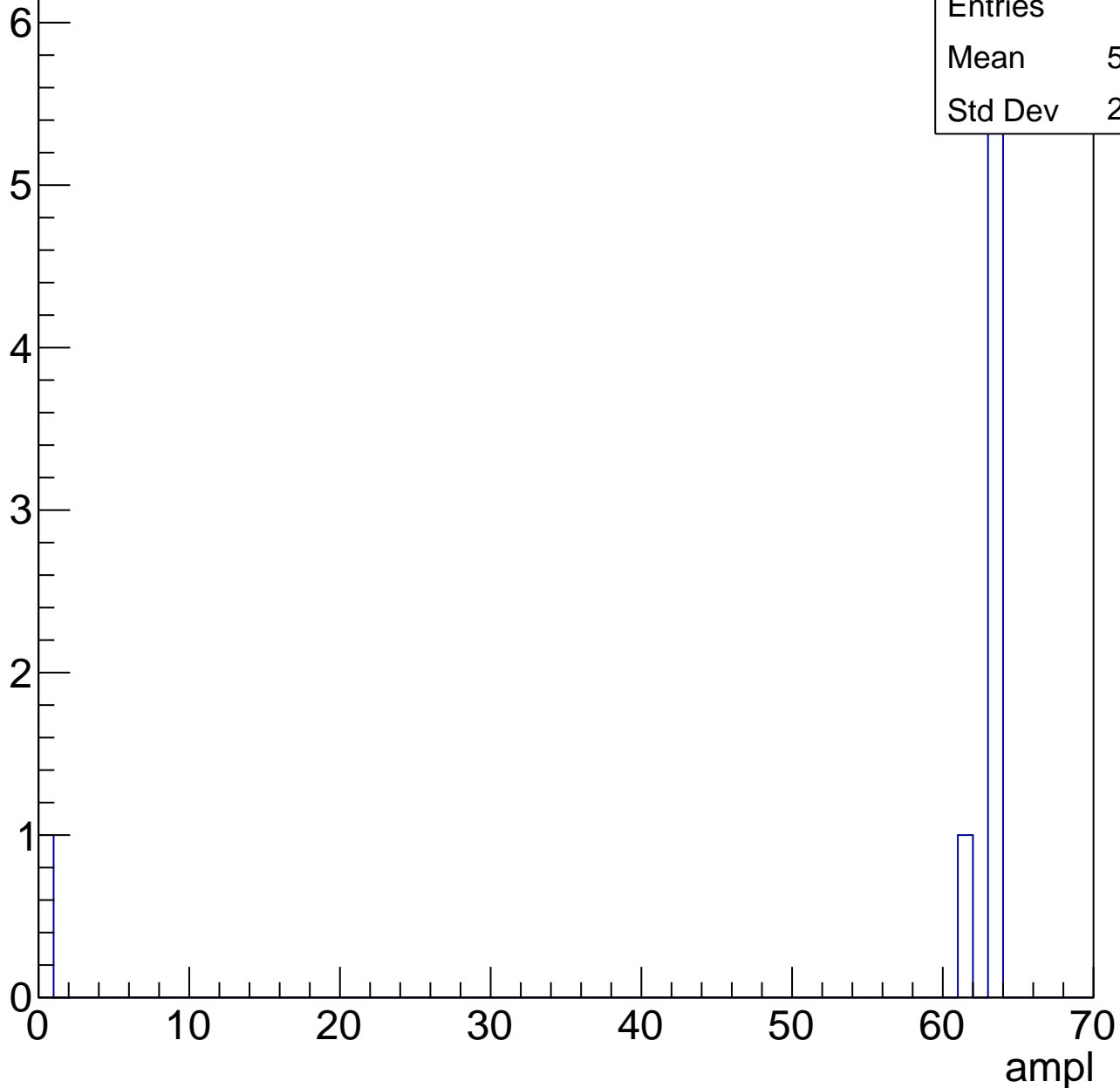


B1L100S, U7-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54.88 |
| Std Dev | 20.75 |



B1L100S, U7-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch35, adc0

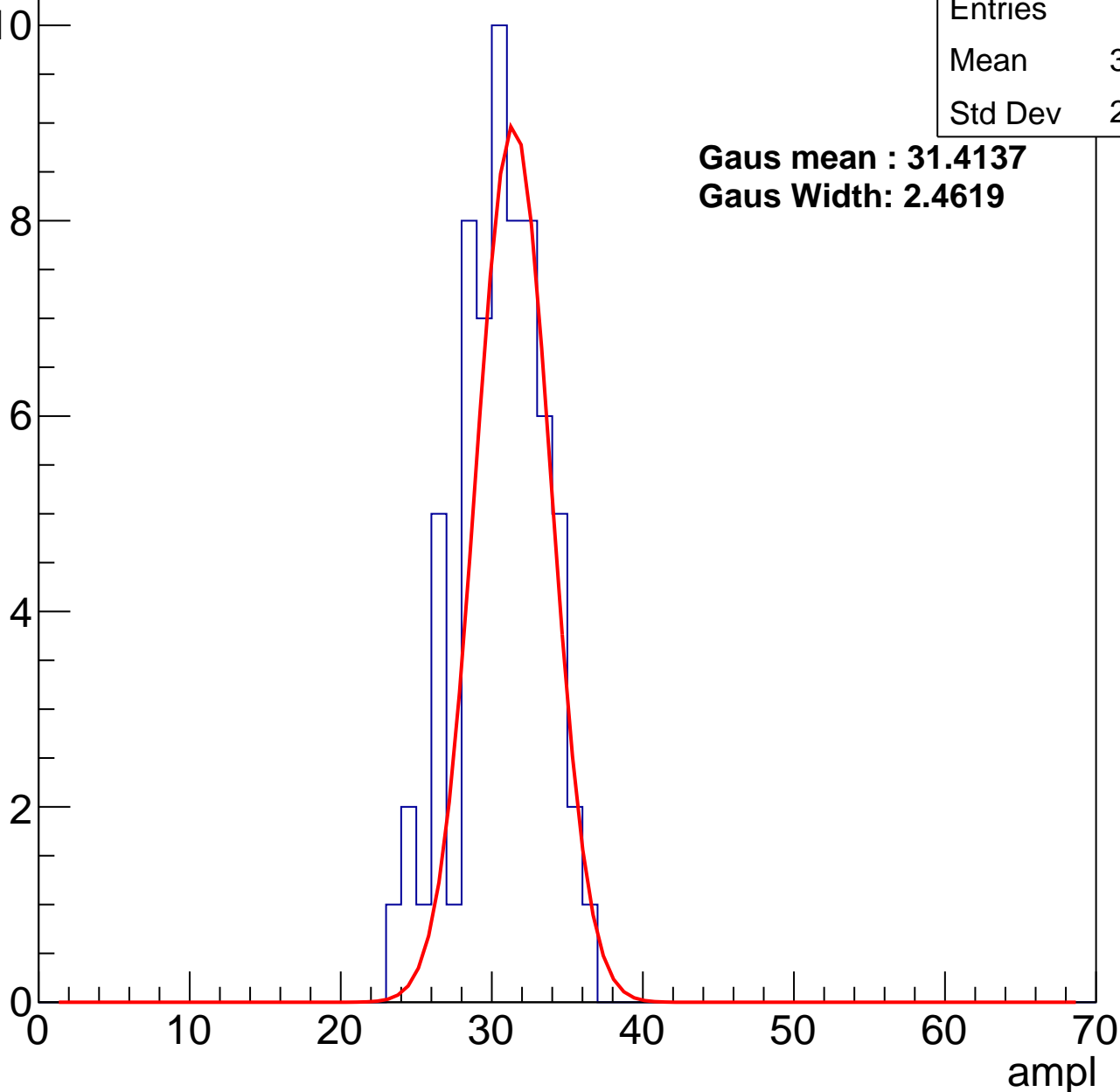
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 30.12 |
| Std Dev | 2.864 |

Gaus mean : 31.4137

Gaus Width: 2.4619



B1L100S, U7-ch35, adc1

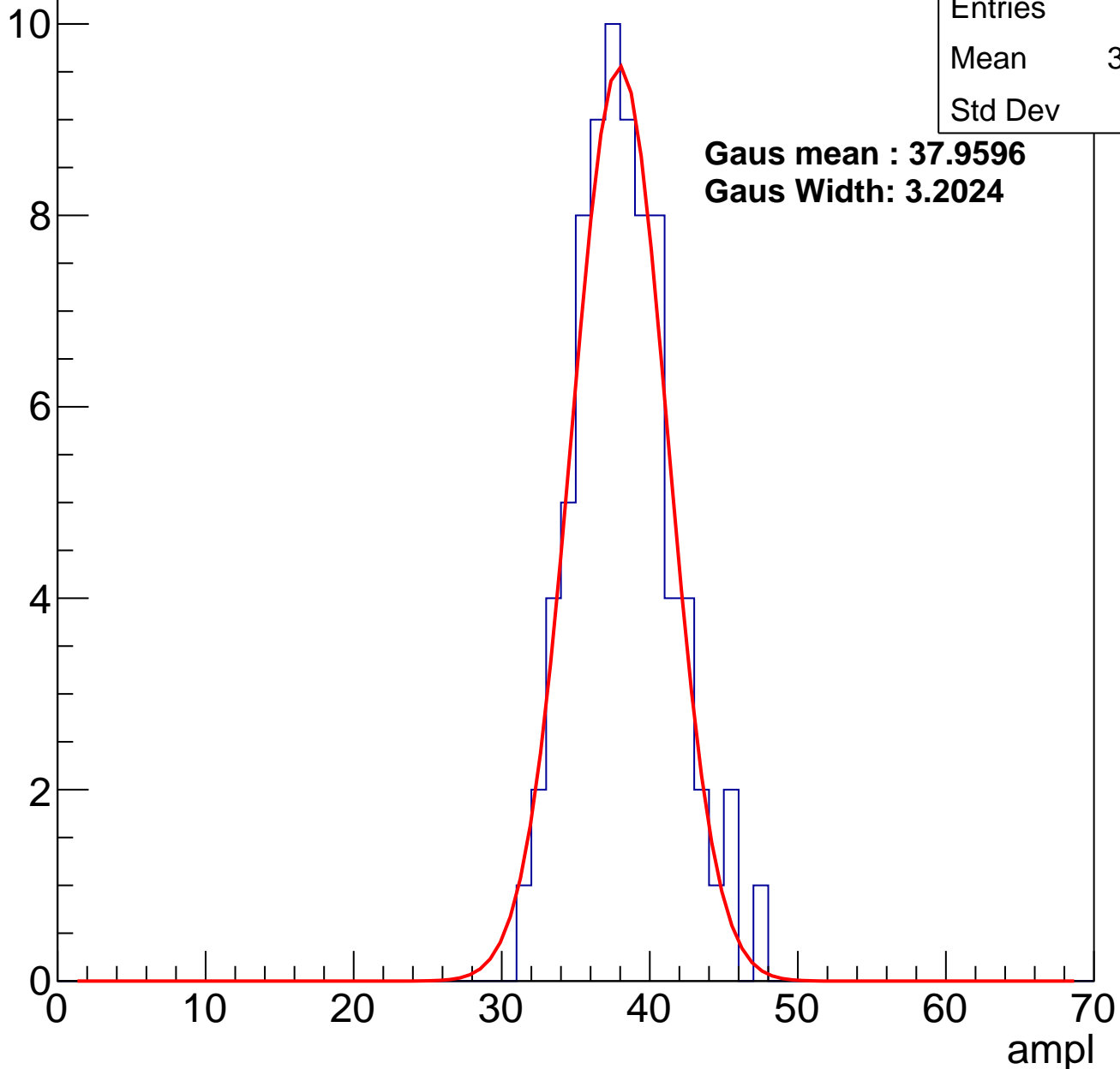
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 37.74 |
| Std Dev | 3.24 |

Gaus mean : 37.9596

Gaus Width: 3.2024

Entry



B1L100S, U7-ch35, adc2

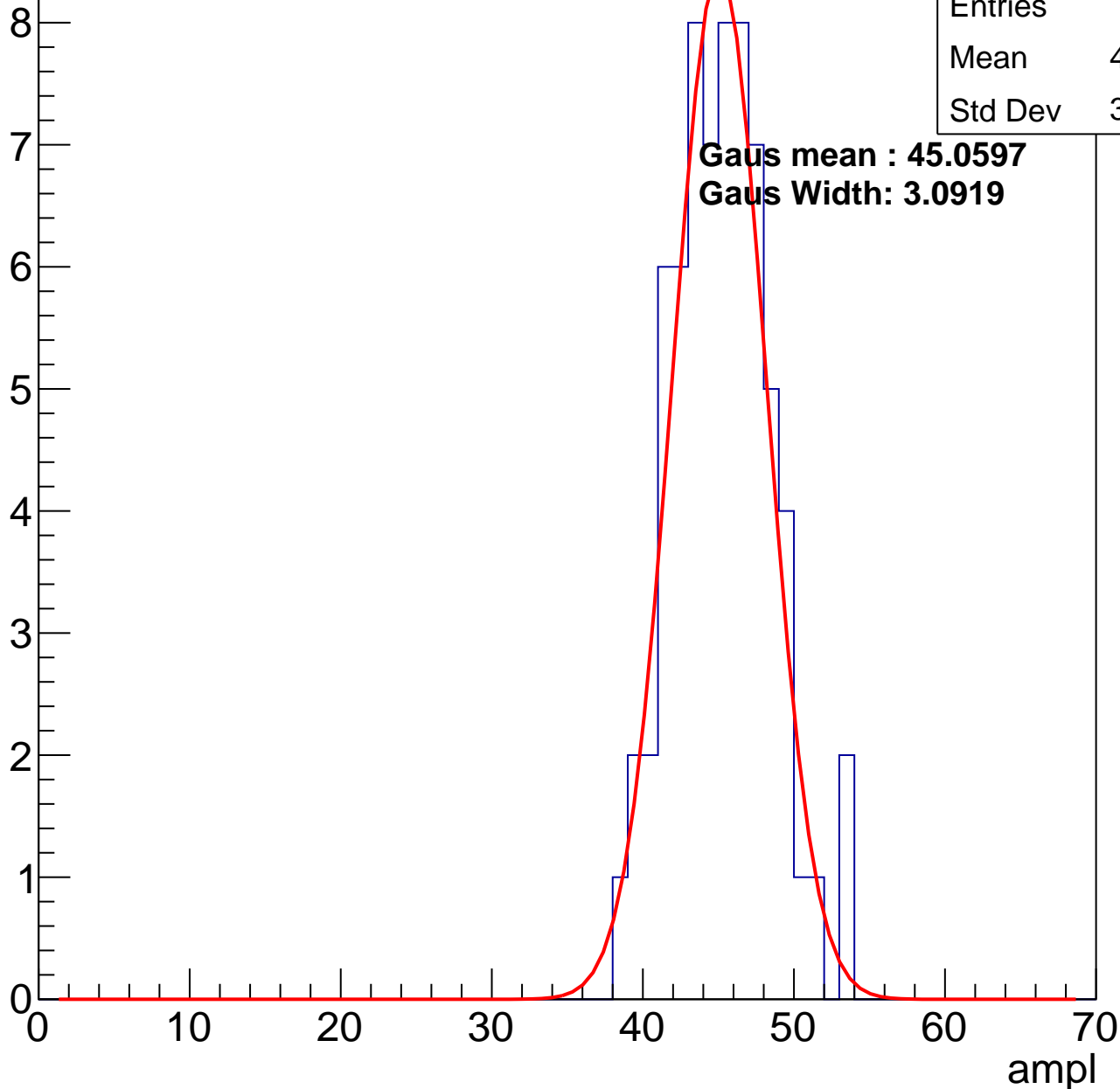
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 44.79 |
| Std Dev | 3.188 |

Gaus mean : 45.0597

Gaus Width: 3.0919

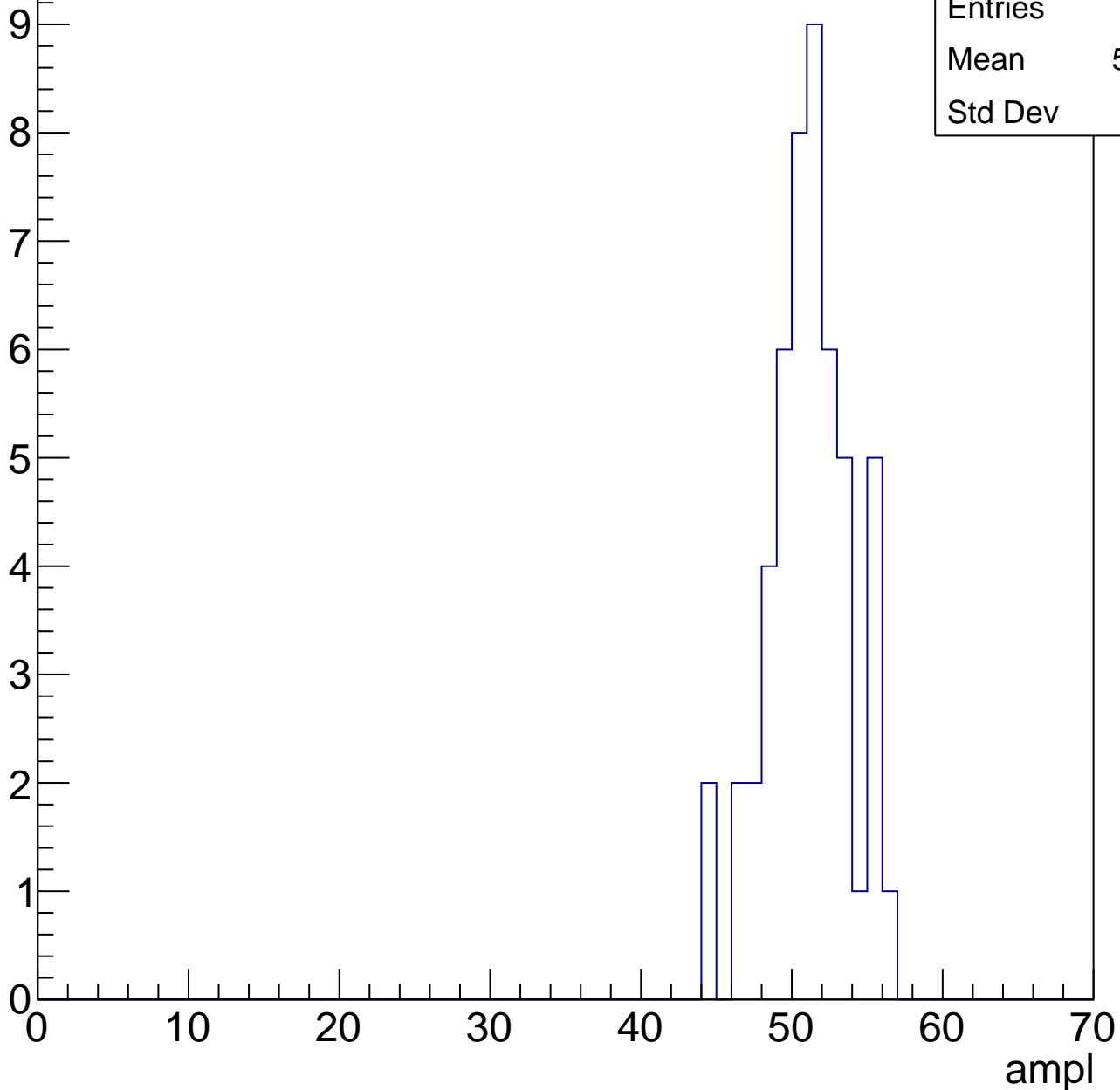


B1L100S, U7-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 50.61 |
| Std Dev | 2.73 |

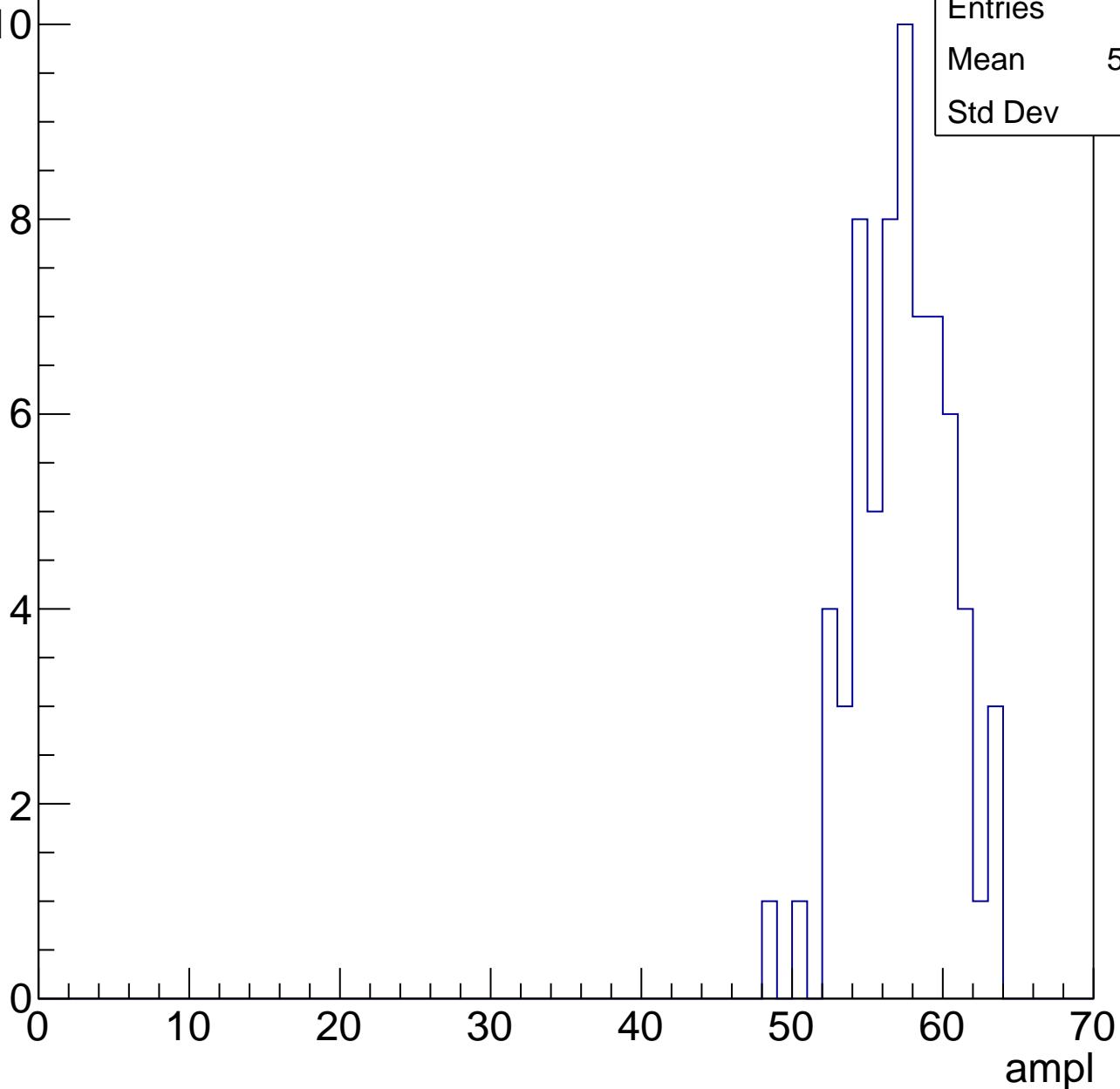


B1L100S, U7-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 56.82 |
| Std Dev | 3.12 |

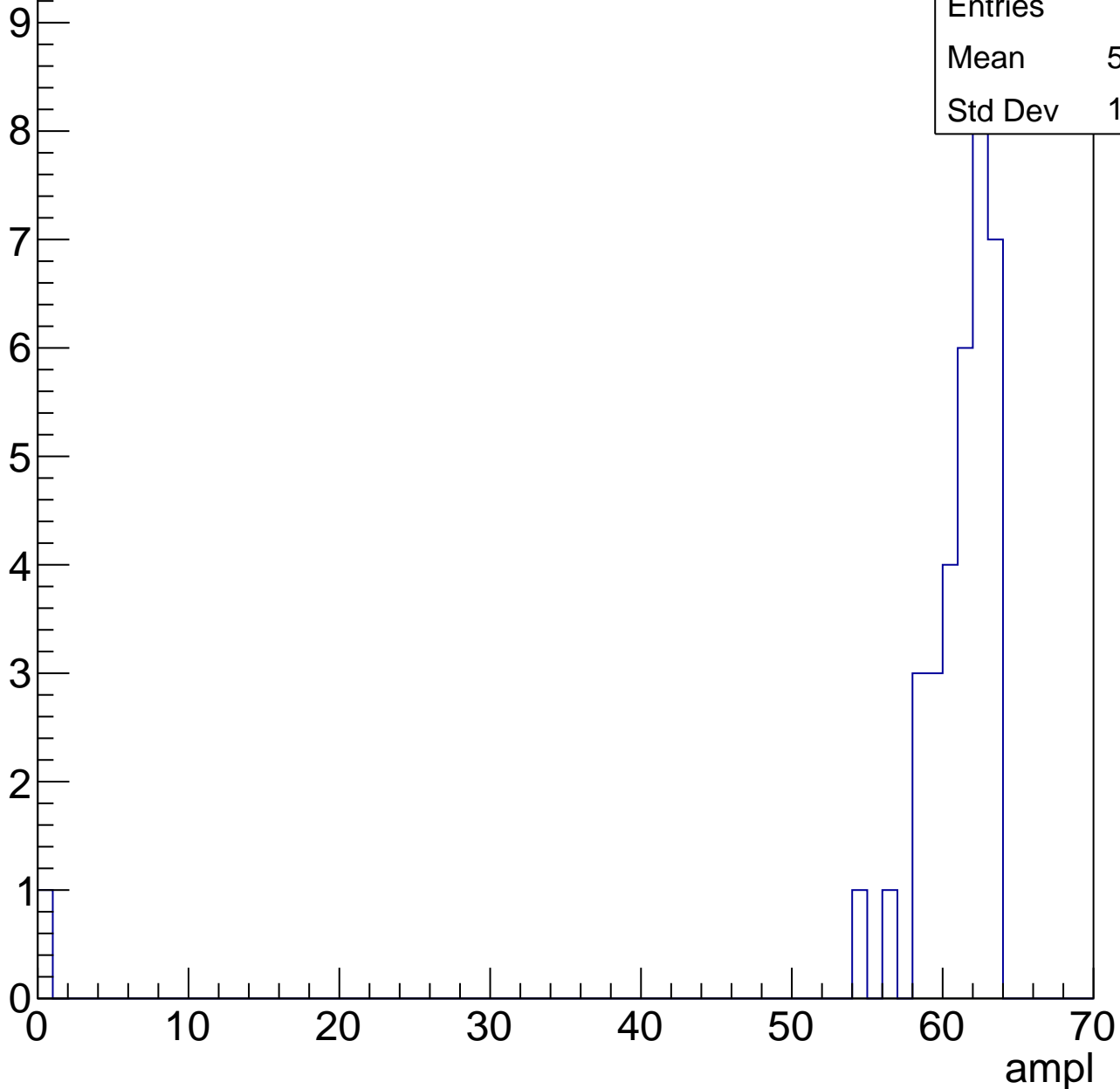


B1L100S, U7-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 59.03 |
| Std Dev | 10.34 |



B1L100S, U7-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch36, adc0

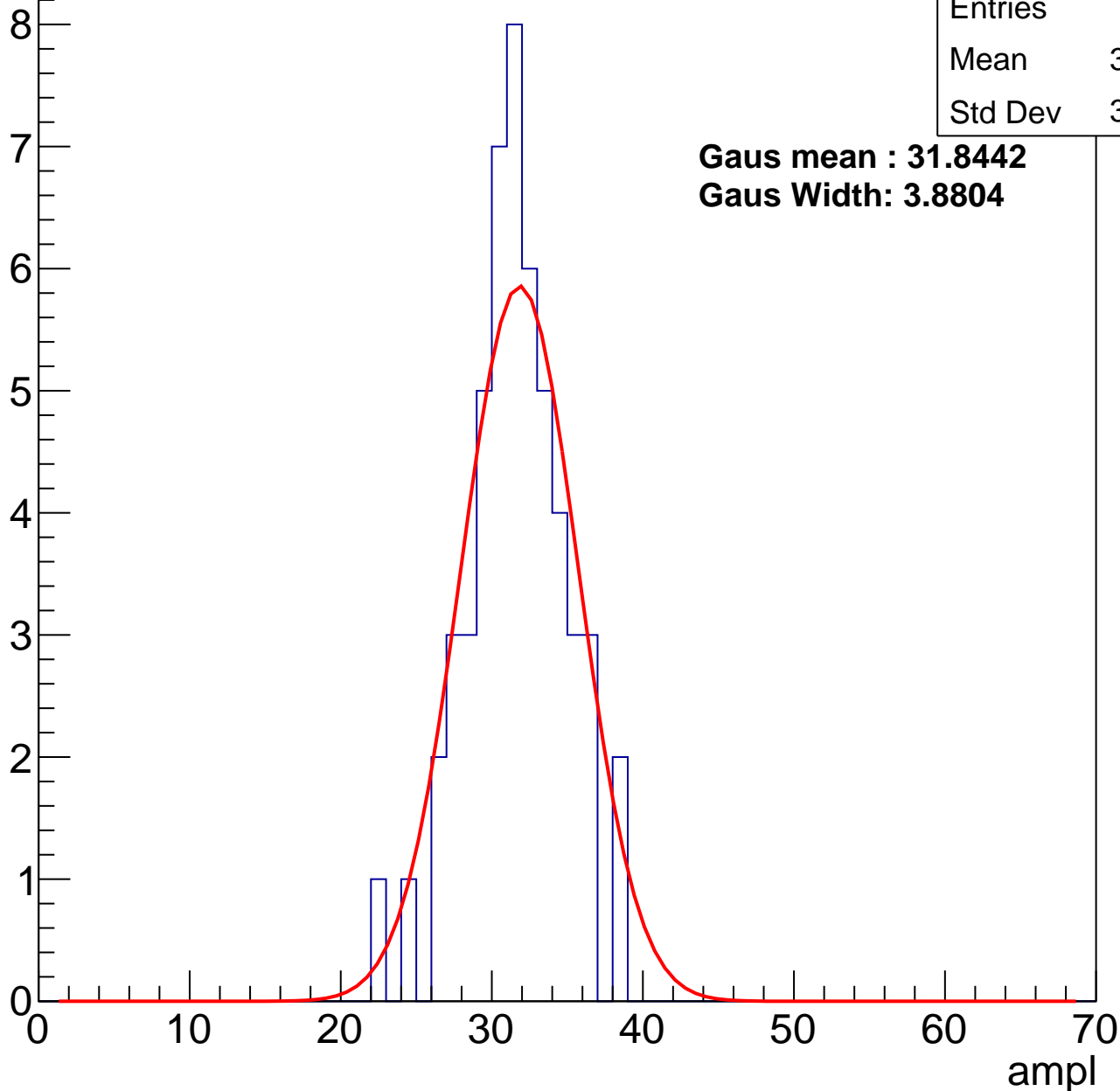
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 31.09 |
| Std Dev | 3.264 |

Gaus mean : 31.8442

Gaus Width: 3.8804



B1L100S, U7-ch36, adc1

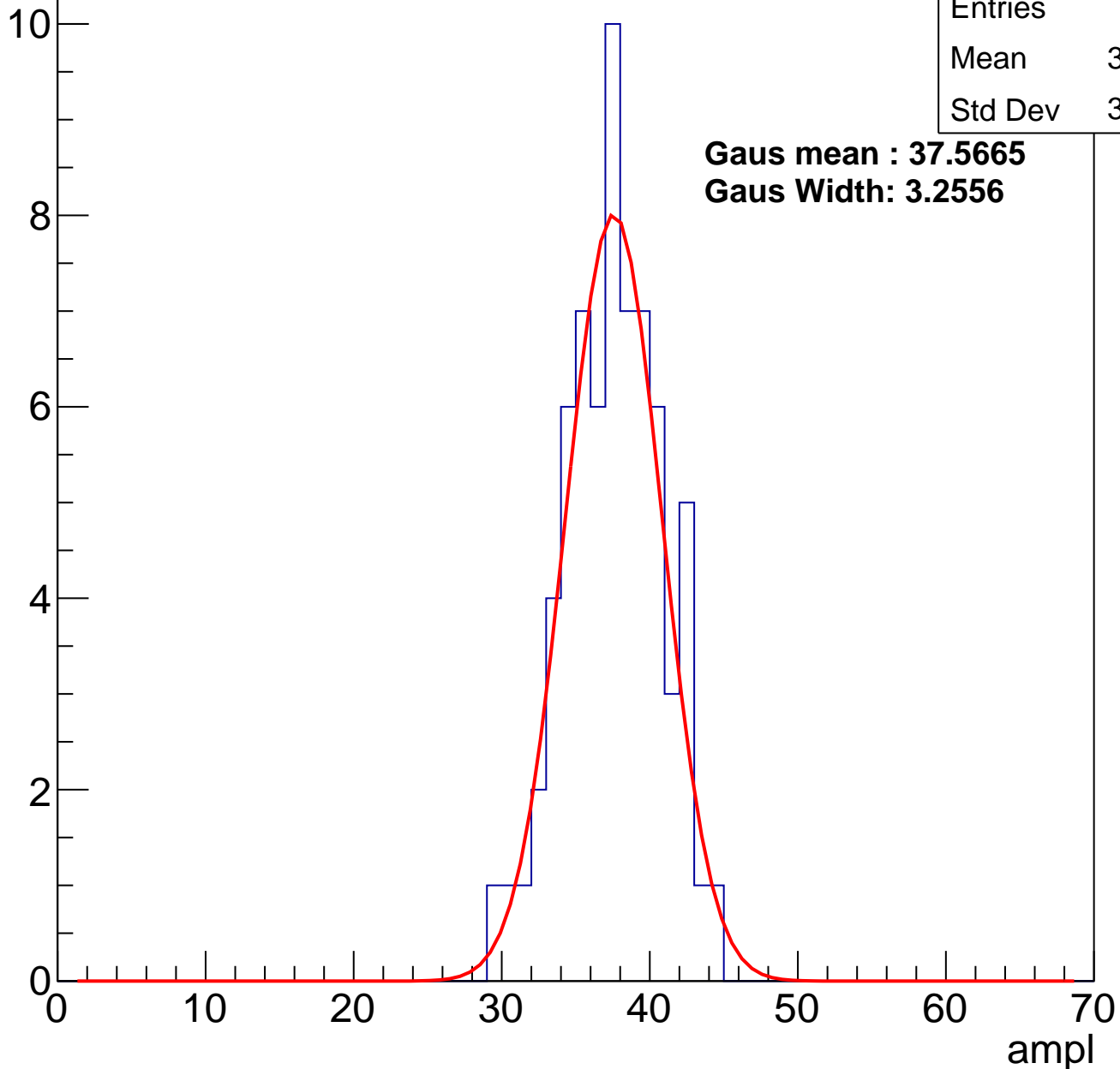
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 68 |
| Mean | 37.06 |
| Std Dev | 3.203 |

Gaus mean : 37.5665

Gaus Width: 3.2556

Entry



B1L100S, U7-ch36, adc2

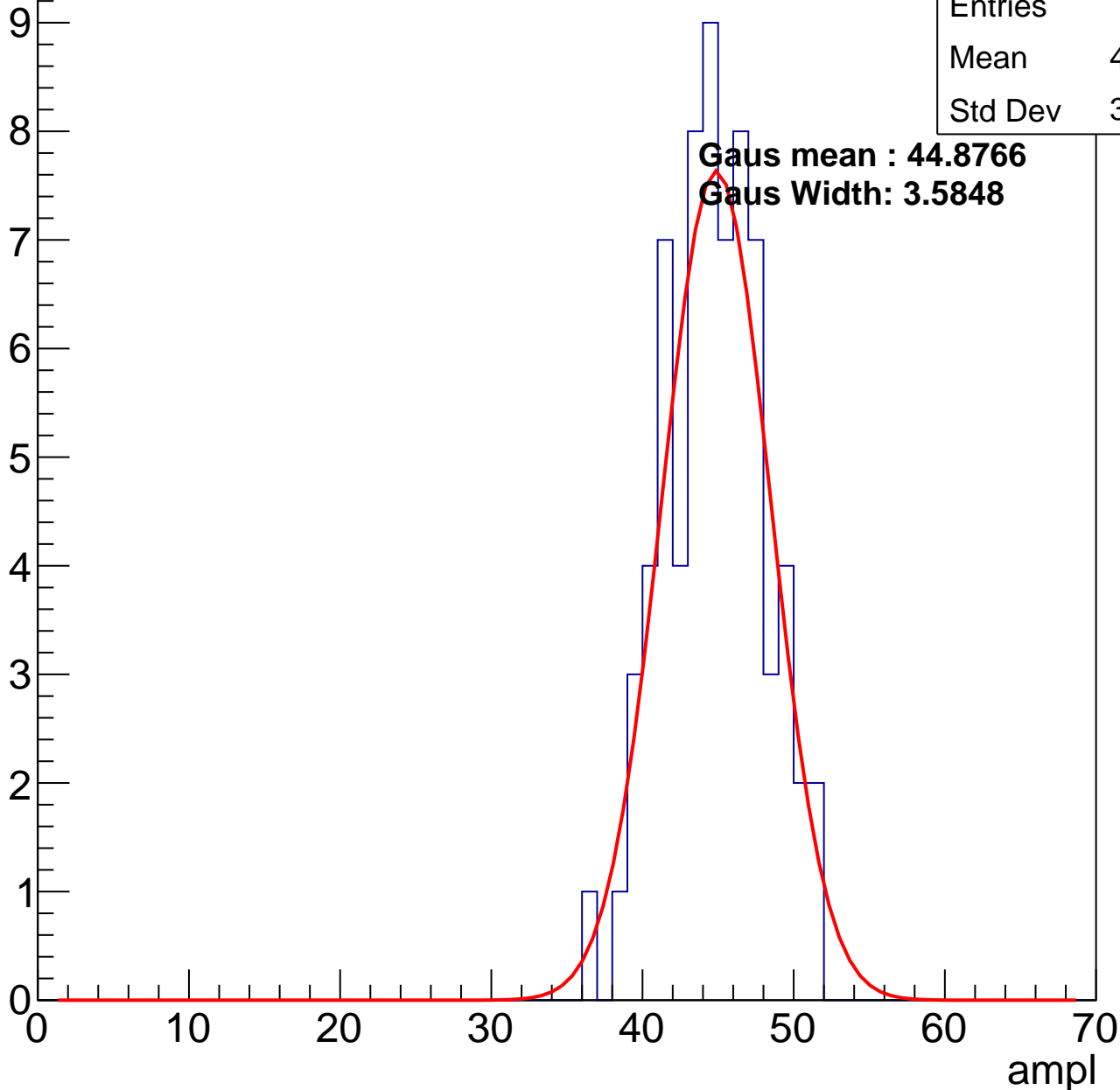
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.29 |
| Std Dev | 3.252 |

Gaus mean : 44.8766

Gaus Width: 3.5848

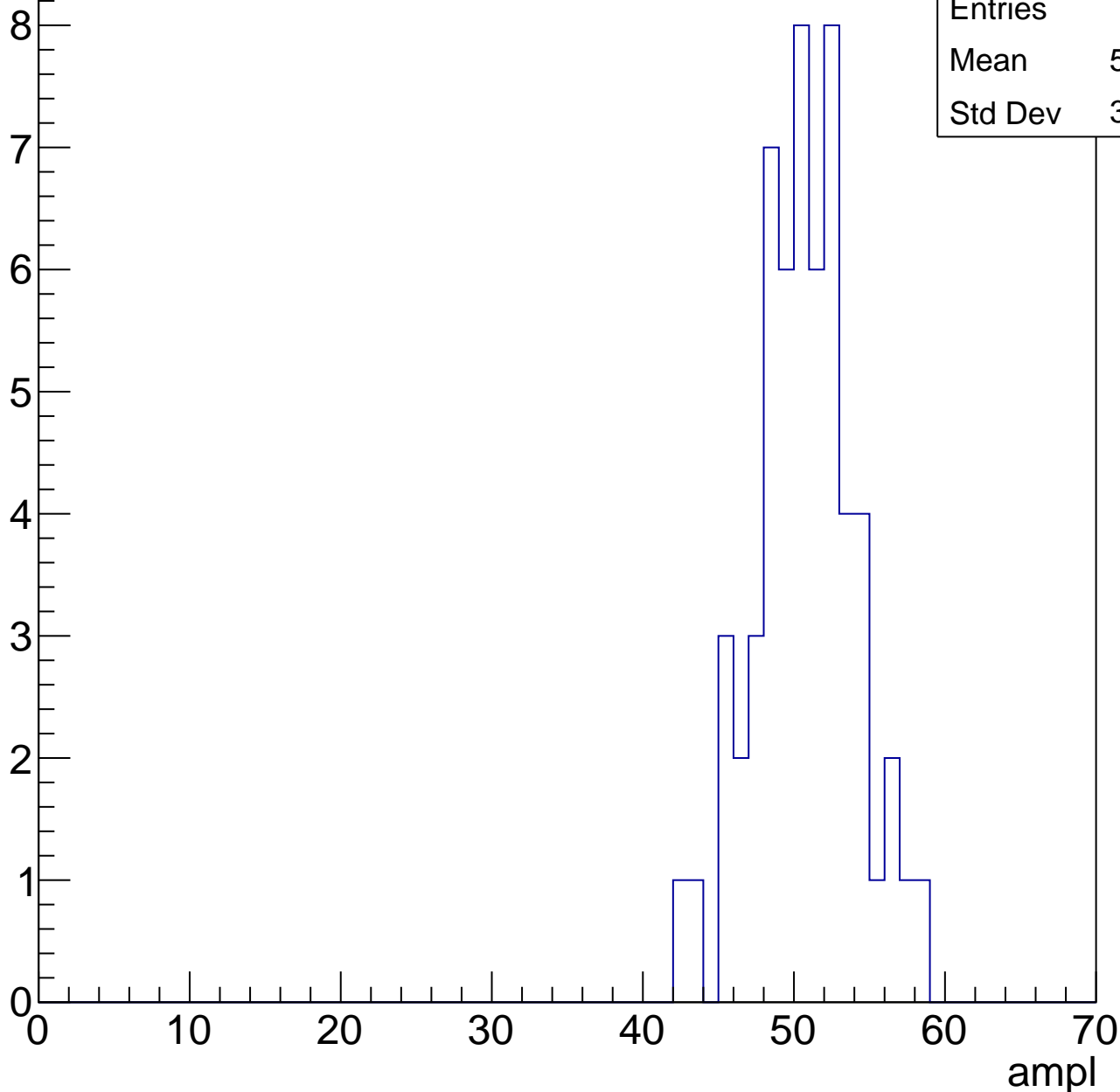


B1L100S, U7-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 50.26 |
| Std Dev | 3.283 |

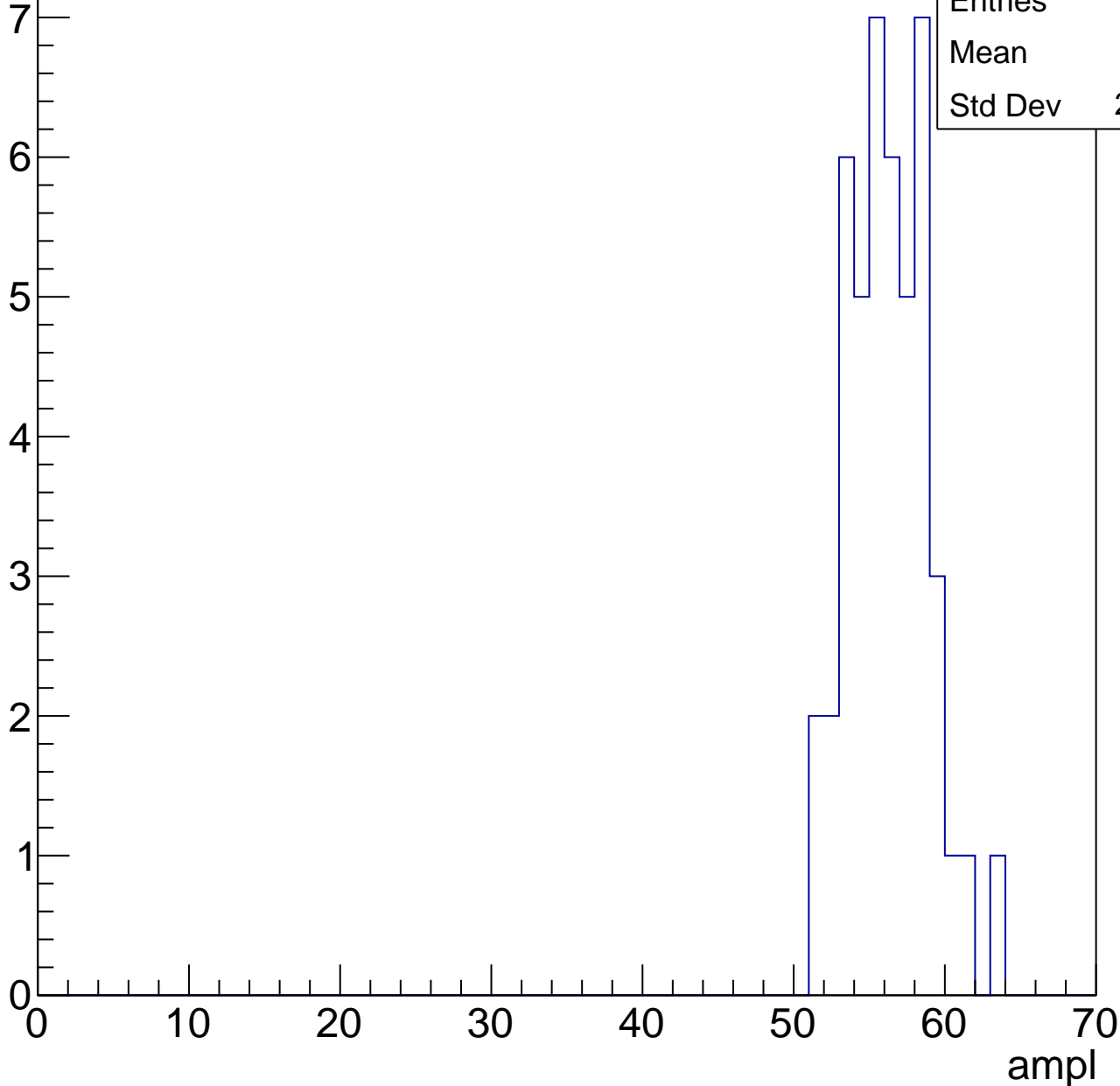


B1L100S, U7-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

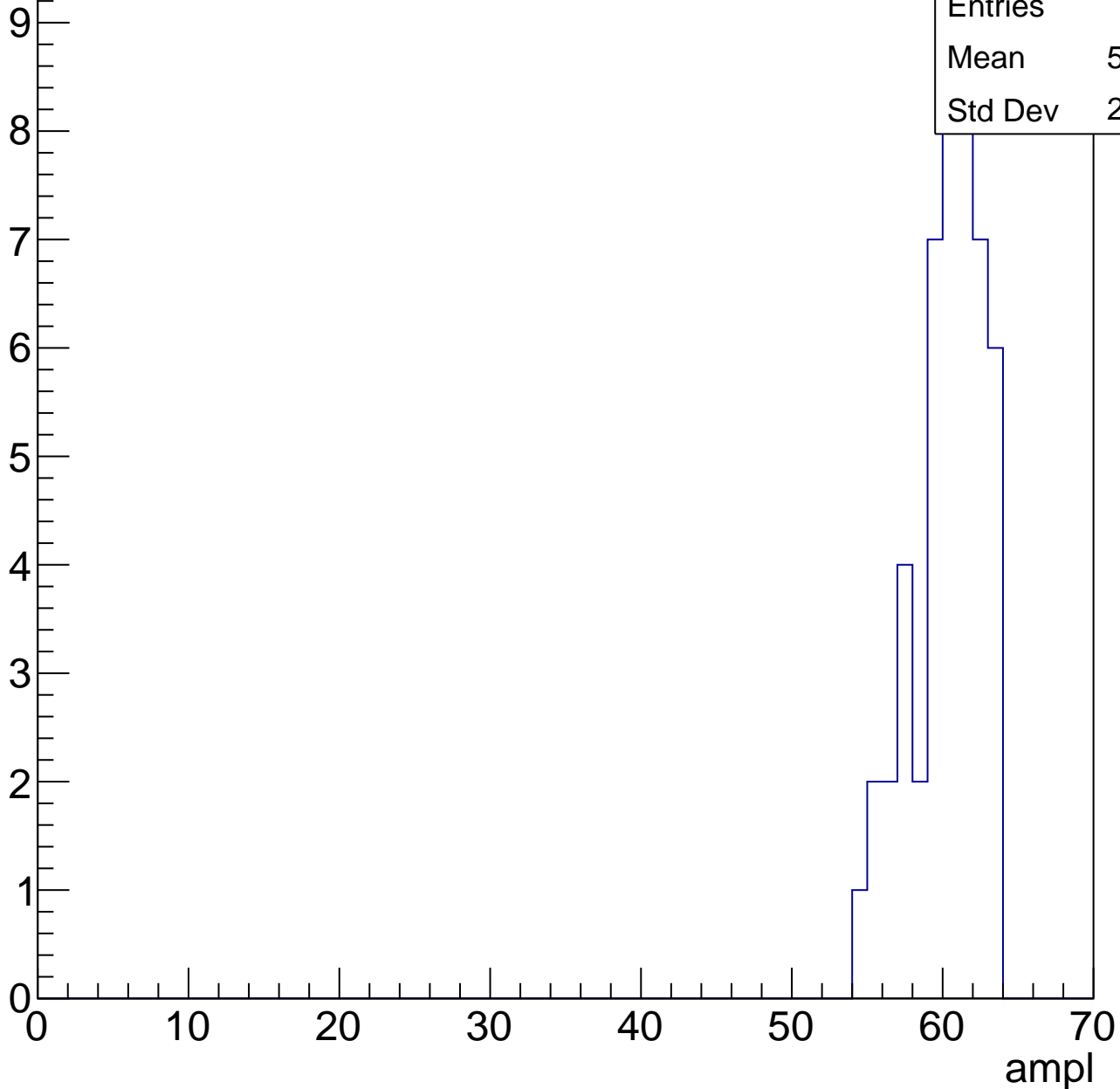
| | |
|---------|-------|
| Entries | 46 |
| Mean | 55.8 |
| Std Dev | 2.601 |



B1L100S, U7-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

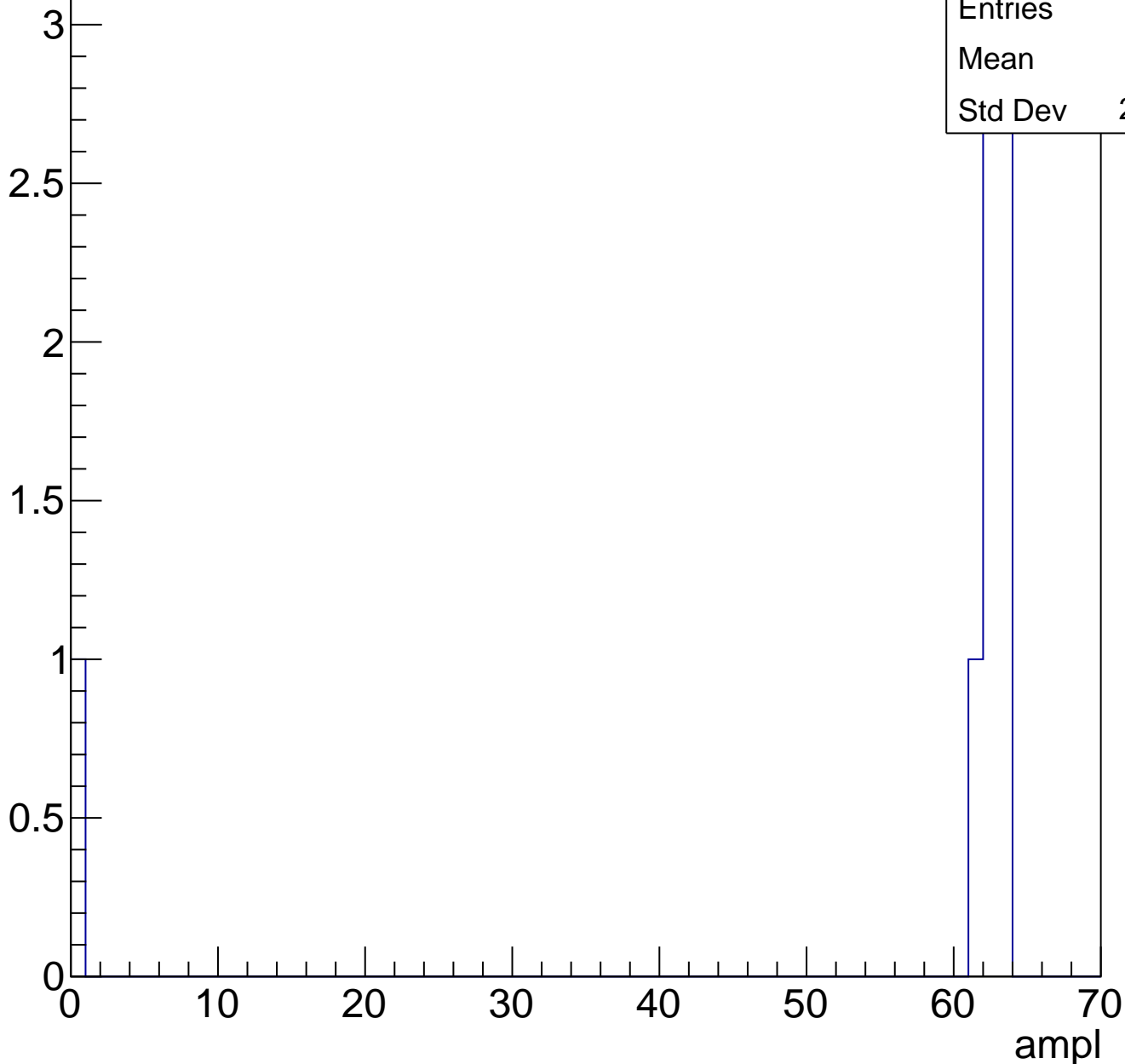
Entry



B1L100S, U7-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch37, adc0

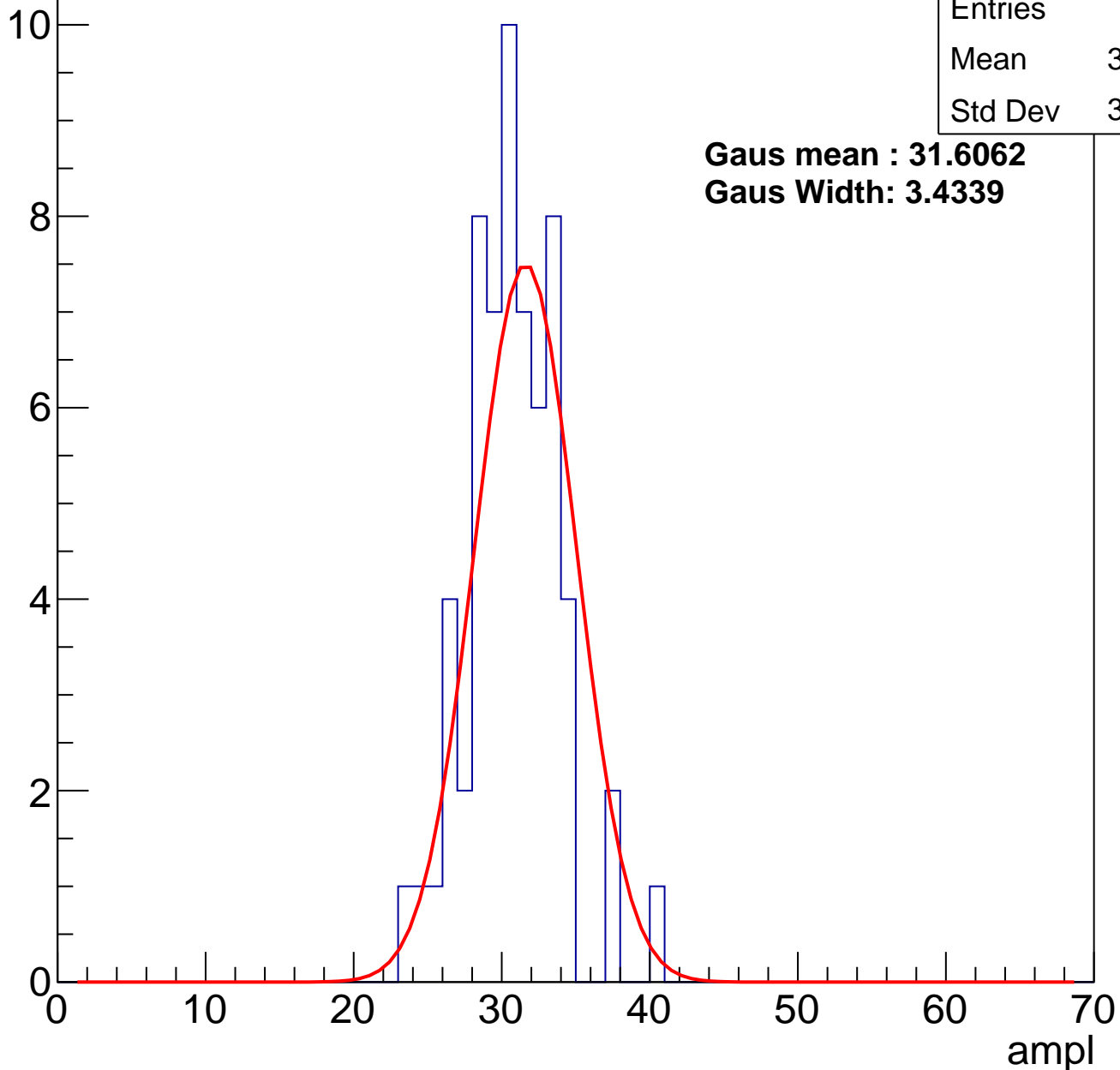
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.32 |
| Std Dev | 3.084 |

Gaus mean : 31.6062

Gaus Width: 3.4339

Entry



B1L100S, U7-ch37, adc1

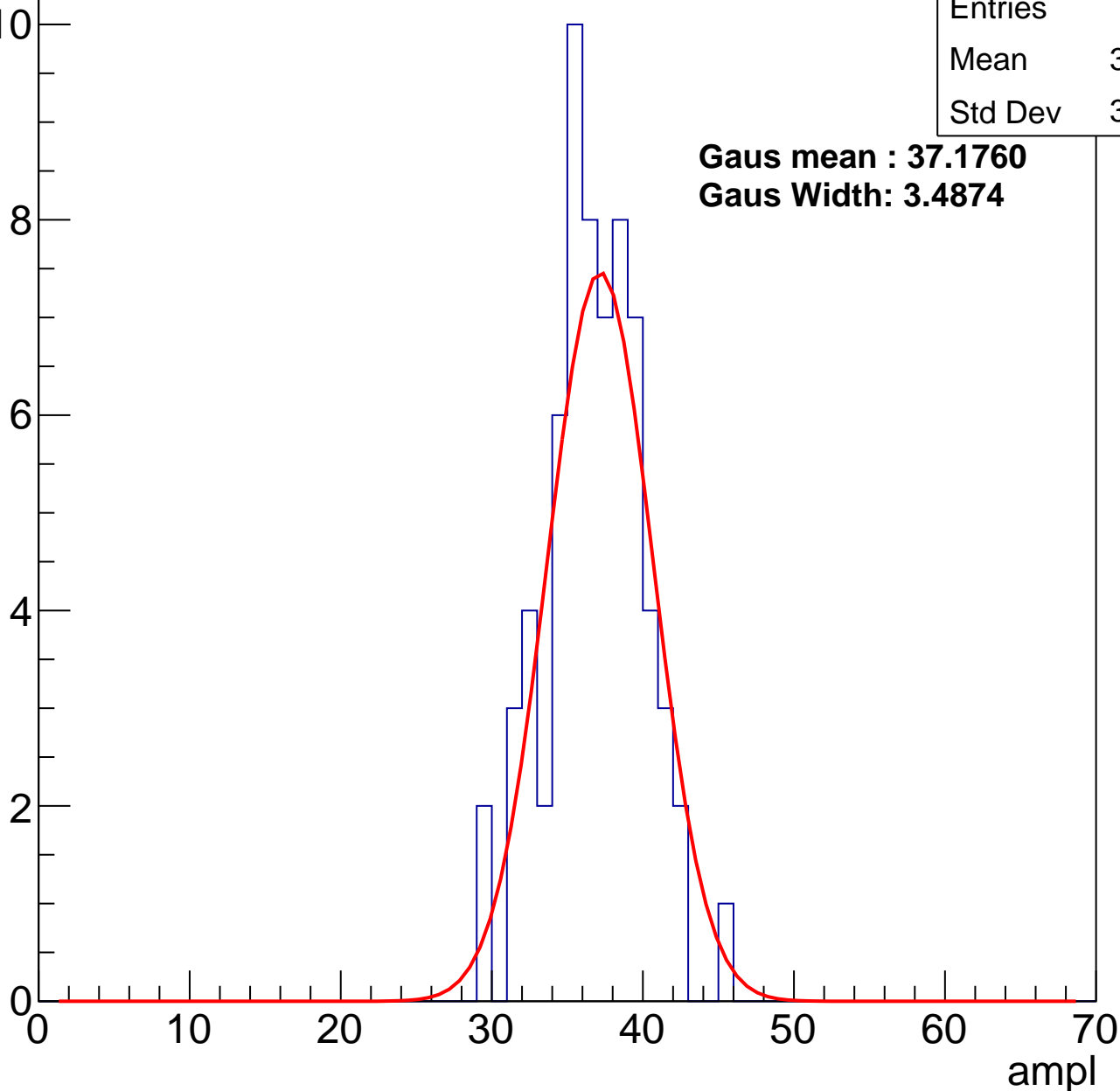
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.34 |
| Std Dev | 3.174 |

Gaus mean : 37.1760

Gaus Width: 3.4874



B1L100S, U7-ch37, adc2

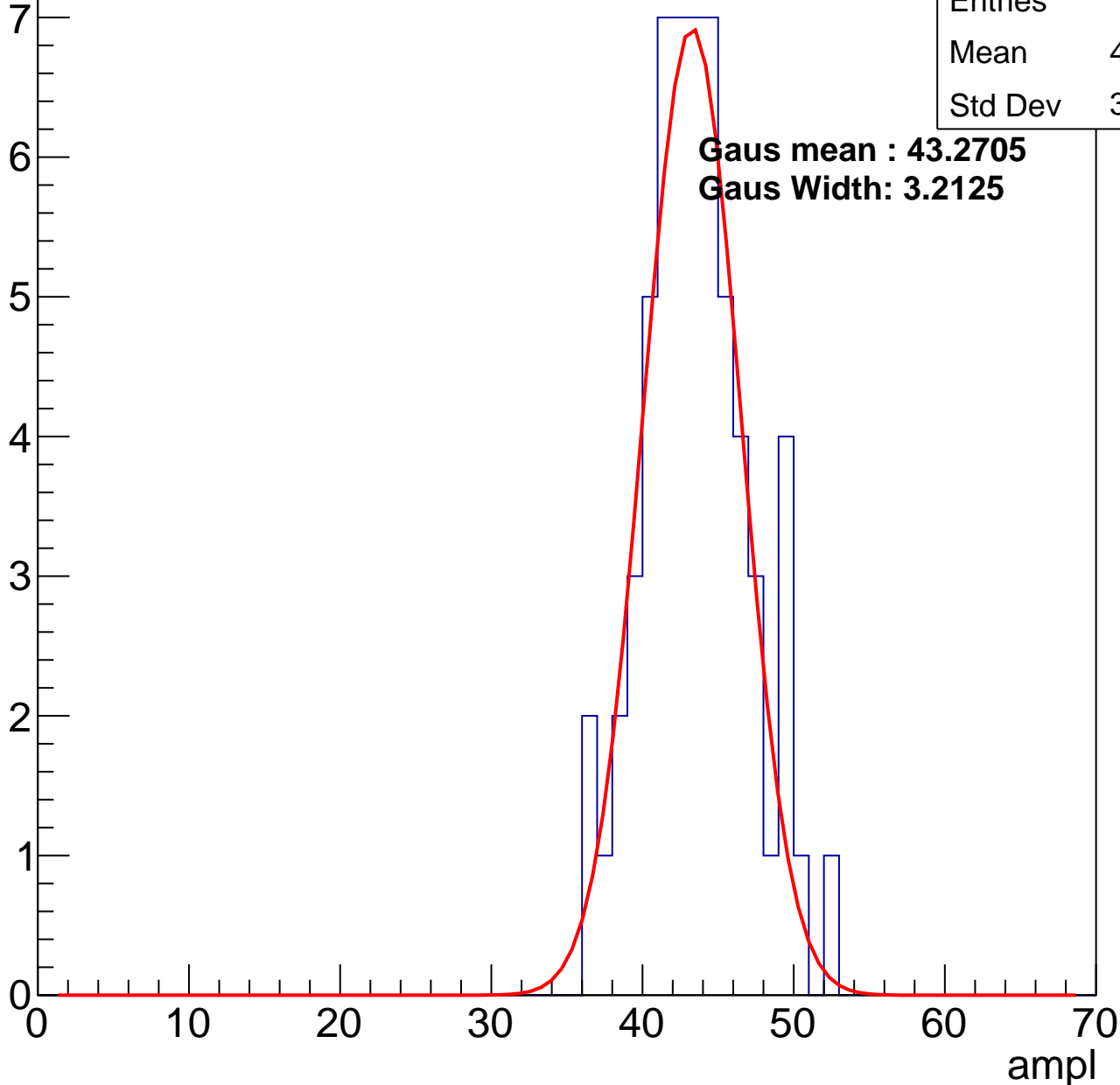
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 43.13 |
| Std Dev | 3.466 |

Gaus mean : 43.2705

Gaus Width: 3.2125

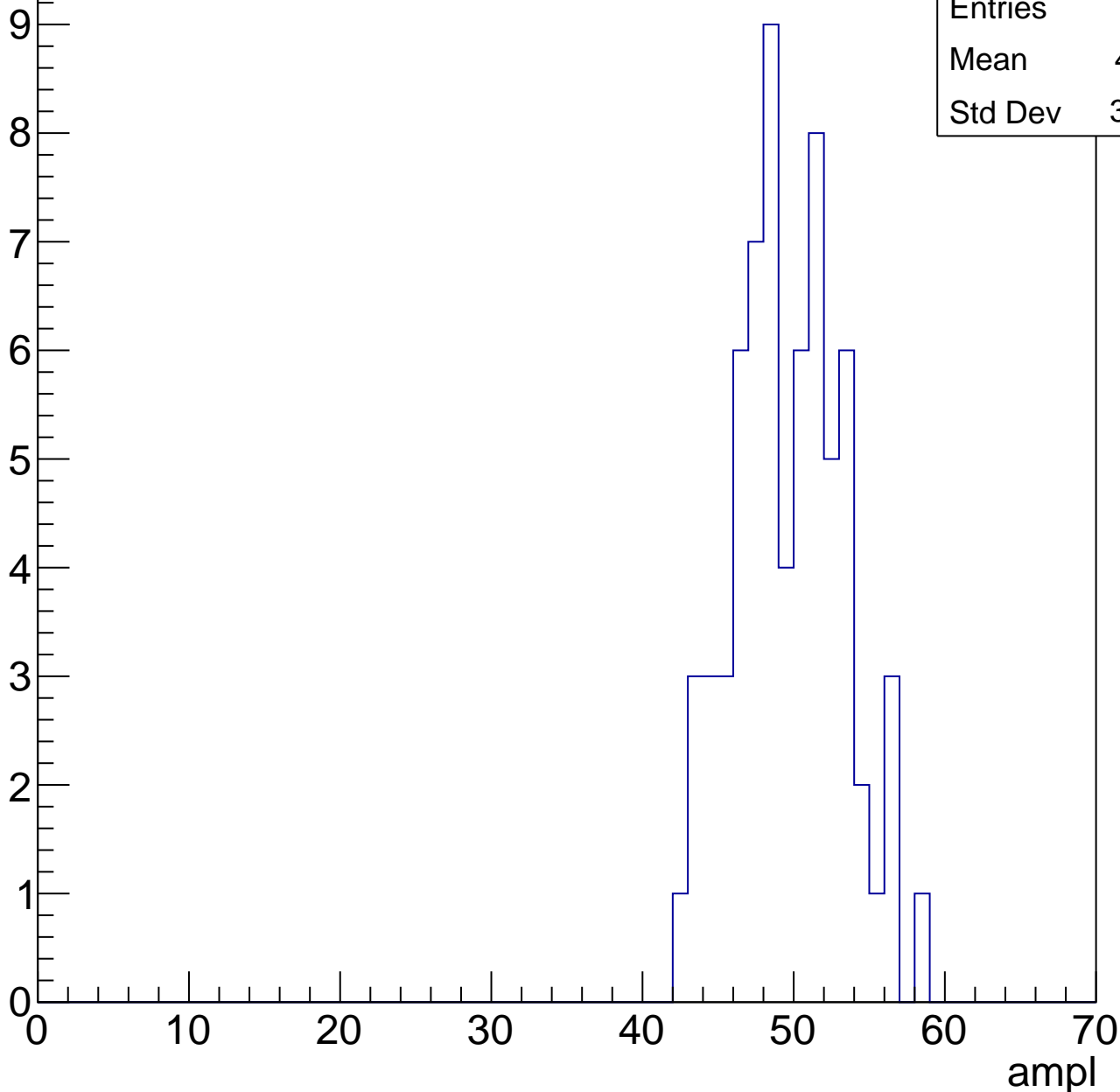


B1L100S, U7-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 49.21 |
| Std Dev | 3.559 |

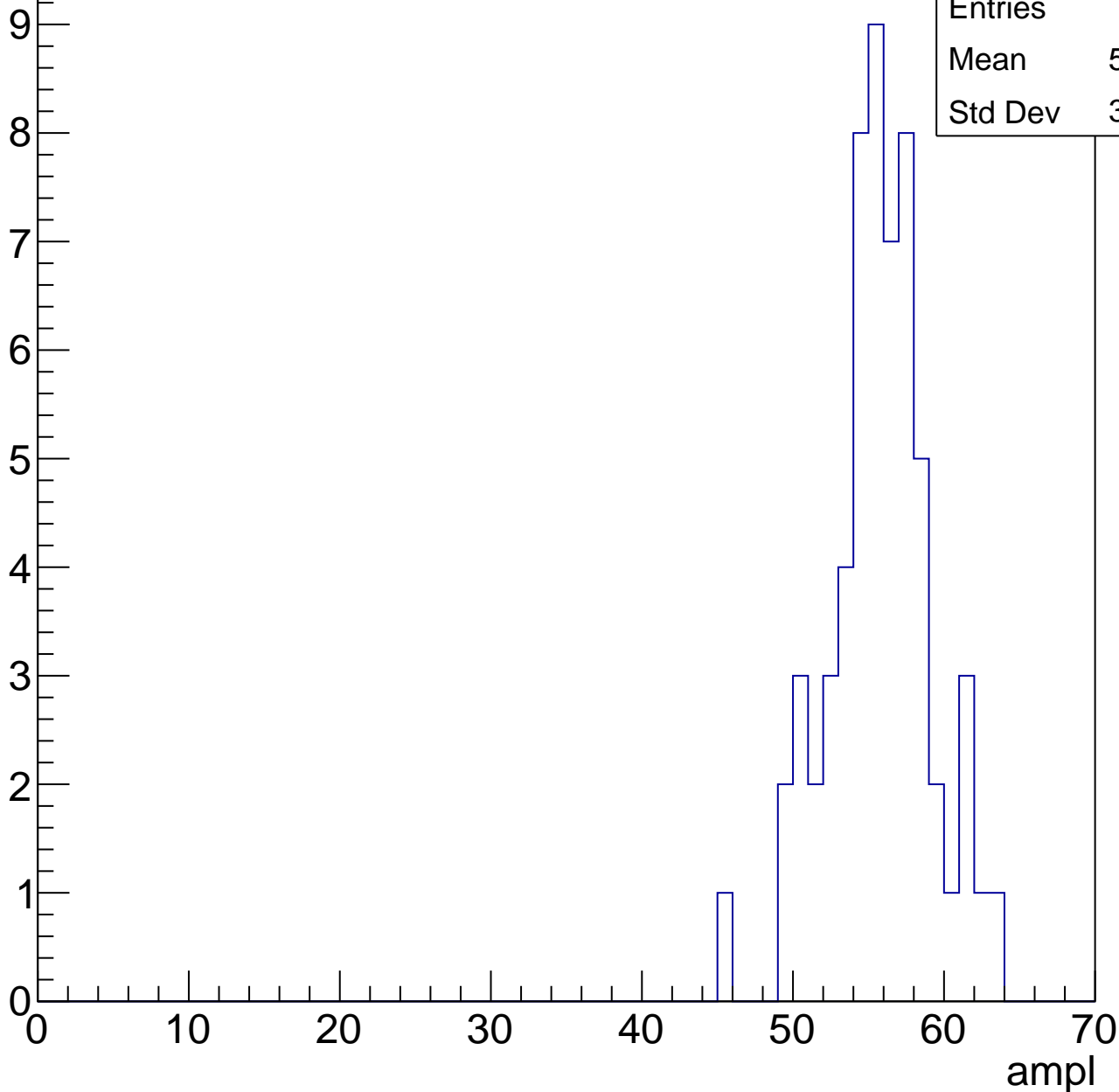


B1L100S, U7-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 55.23 |
| Std Dev | 3.388 |



B1L100S, U7-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

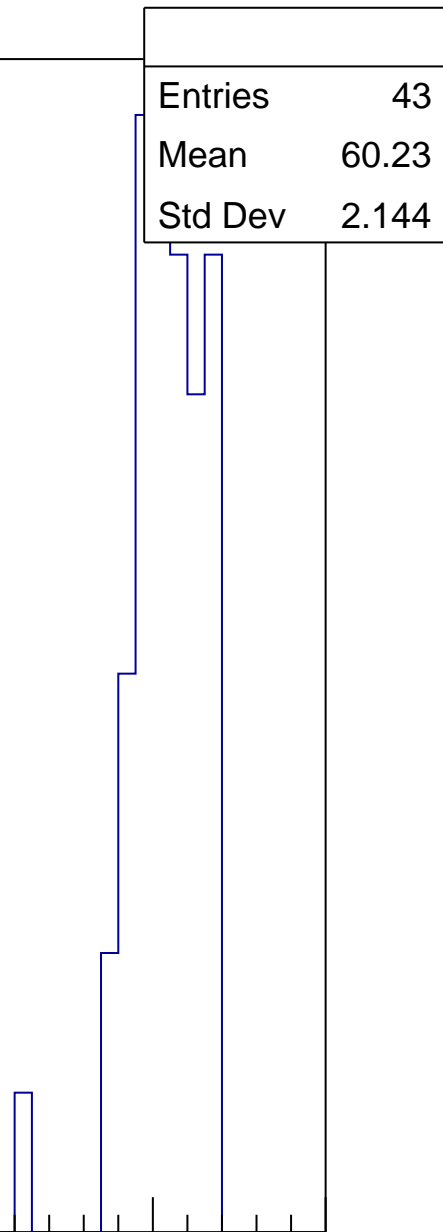
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 43 |
| Mean | 60.23 |
| Std Dev | 2.144 |

ampl

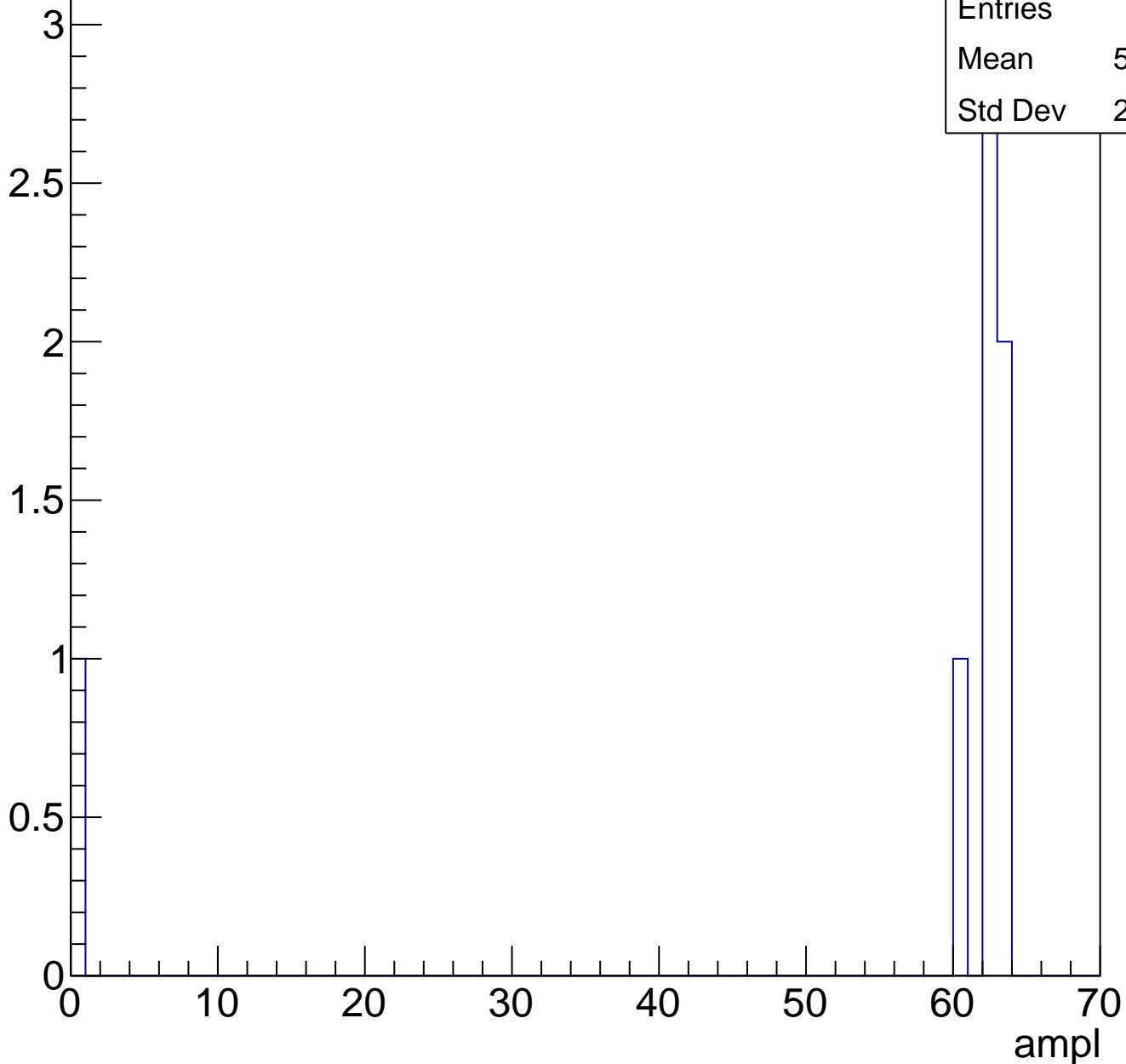
0 10 20 30 40 50 60 70



B1L100S, U7-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch38, adc0

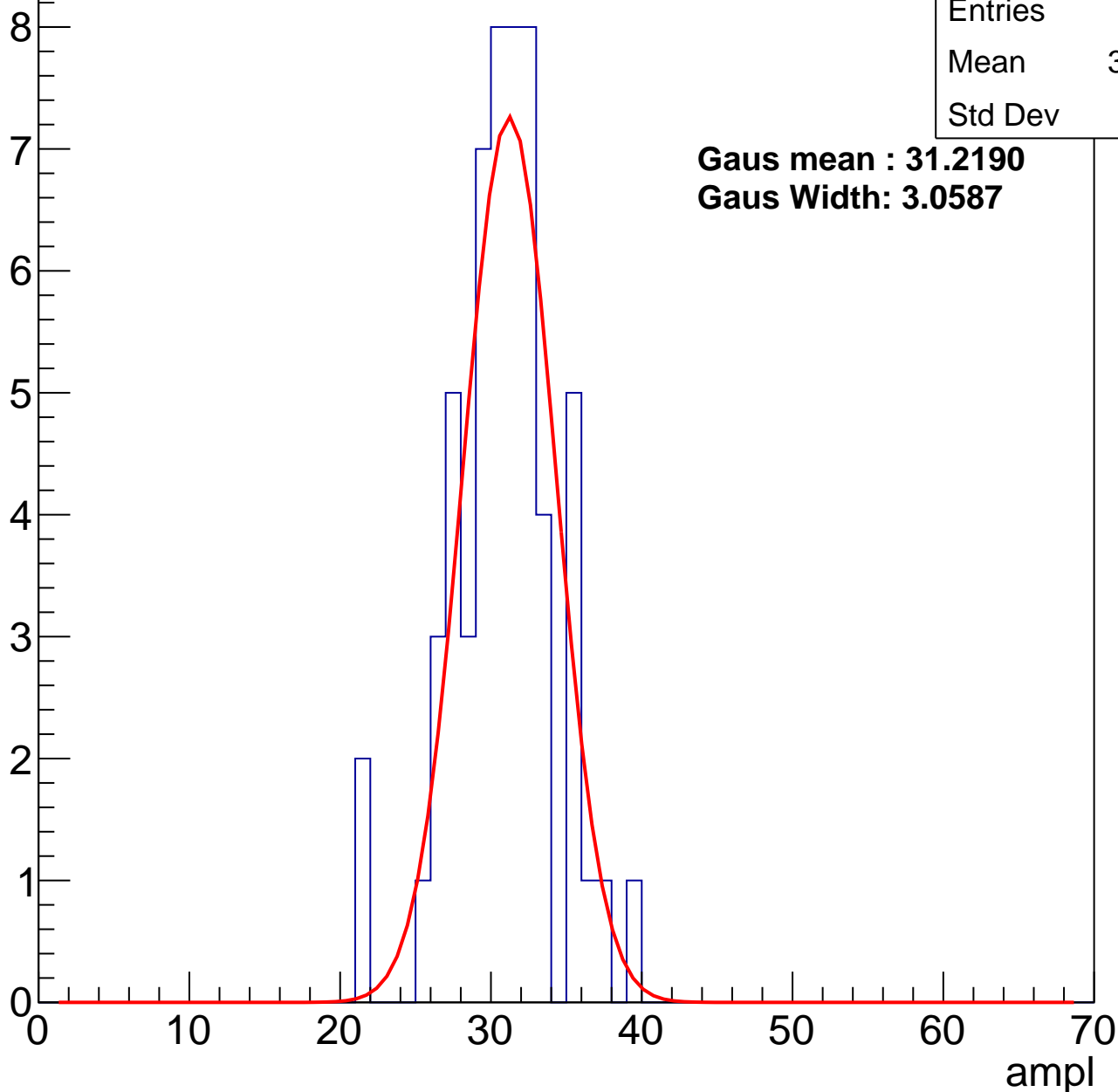
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 30.35 |
| Std Dev | 3.4 |

Gaus mean : 31.2190

Gaus Width: 3.0587



B1L100S, U7-ch38, adc1

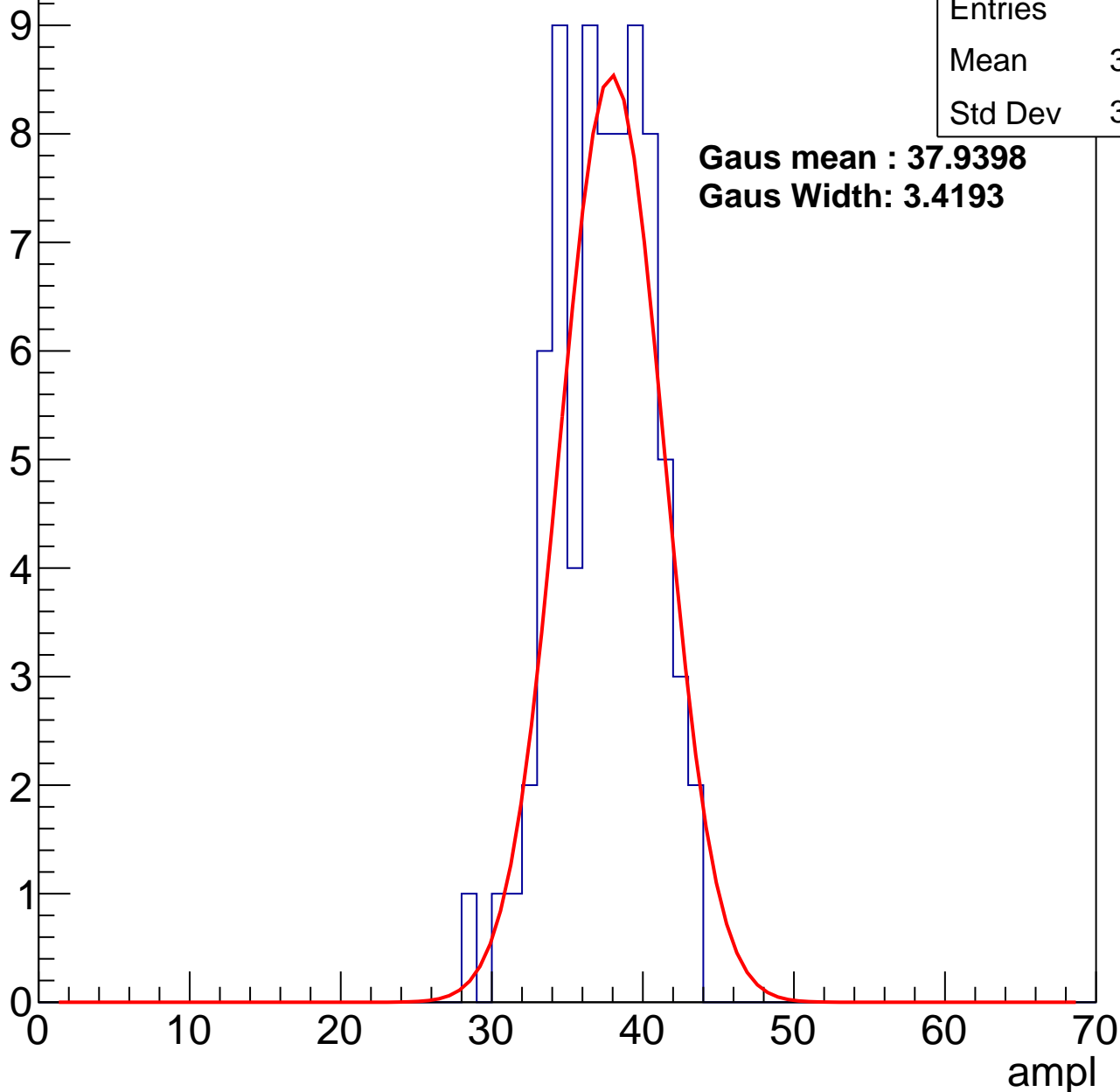
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.96 |
| Std Dev | 3.164 |

Gaus mean : 37.9398

Gaus Width: 3.4193



B1L100S, U7-ch38, adc2

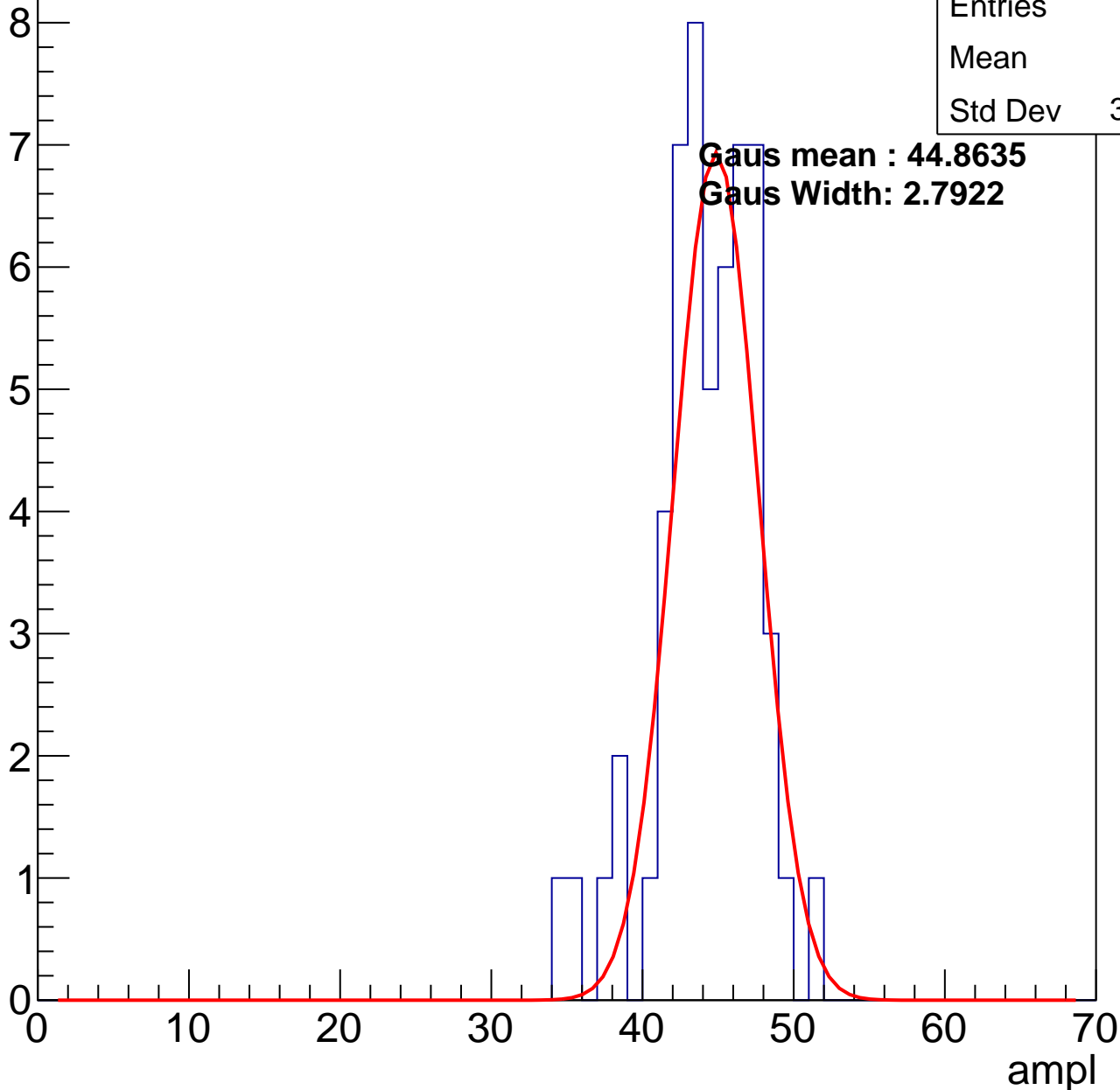
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.8 |
| Std Dev | 3.322 |

Gaus mean : 44.8635

Gaus Width: 2.7922

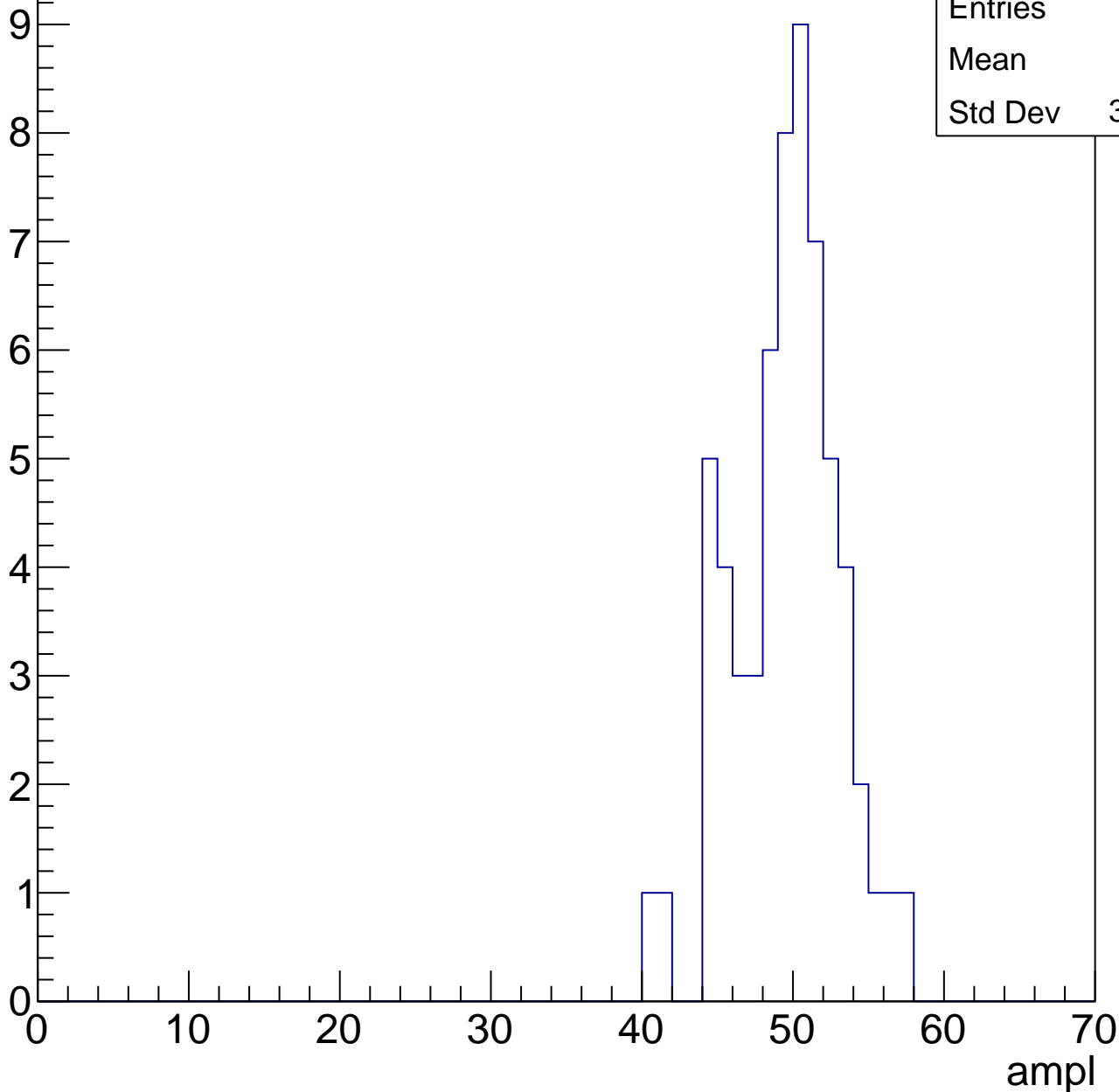


B1L100S, U7-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.1 |
| Std Dev | 3.444 |

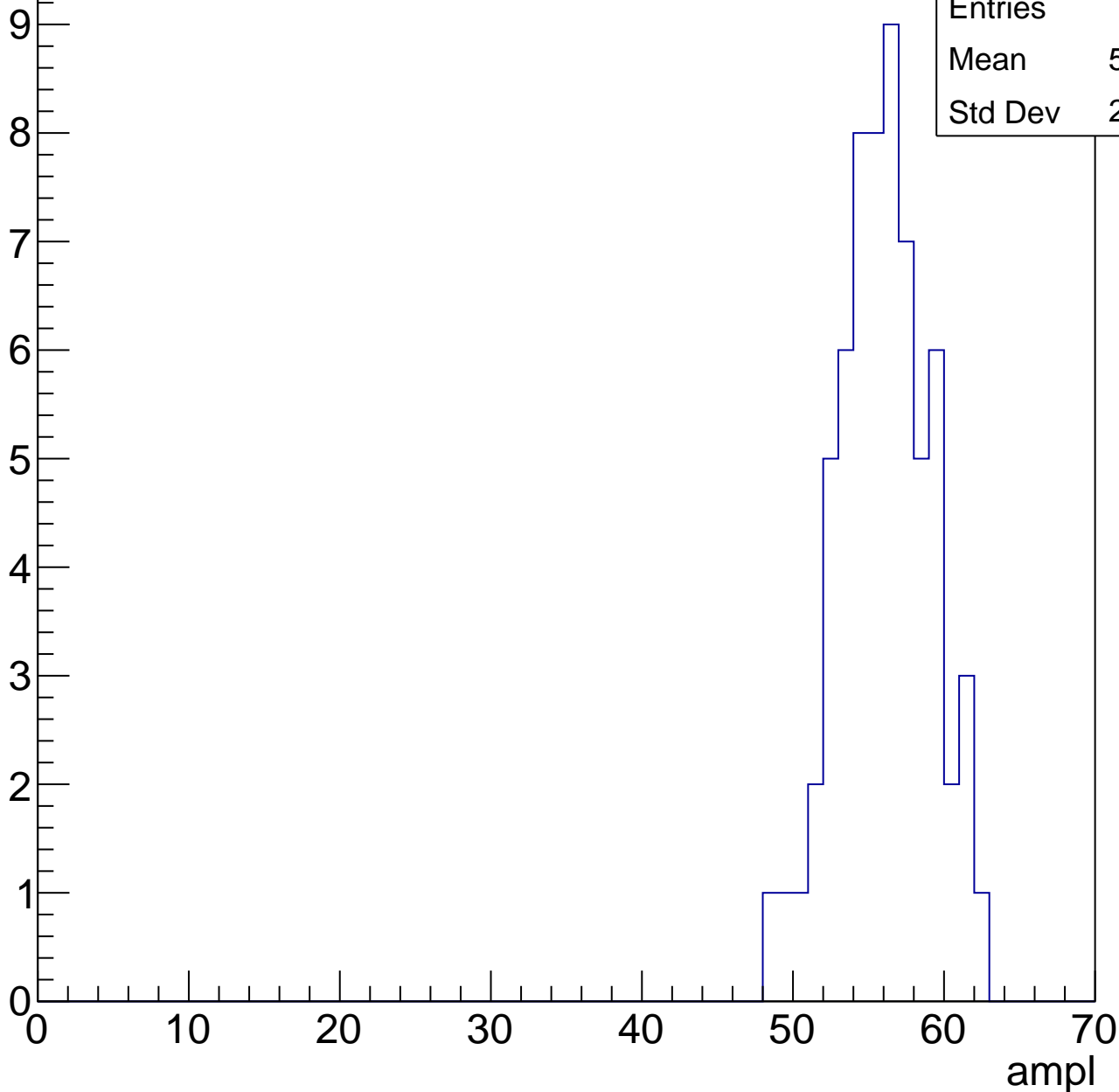


B1L100S, U7-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 55.55 |
| Std Dev | 2.998 |



B1L100S, U7-ch38, adc5

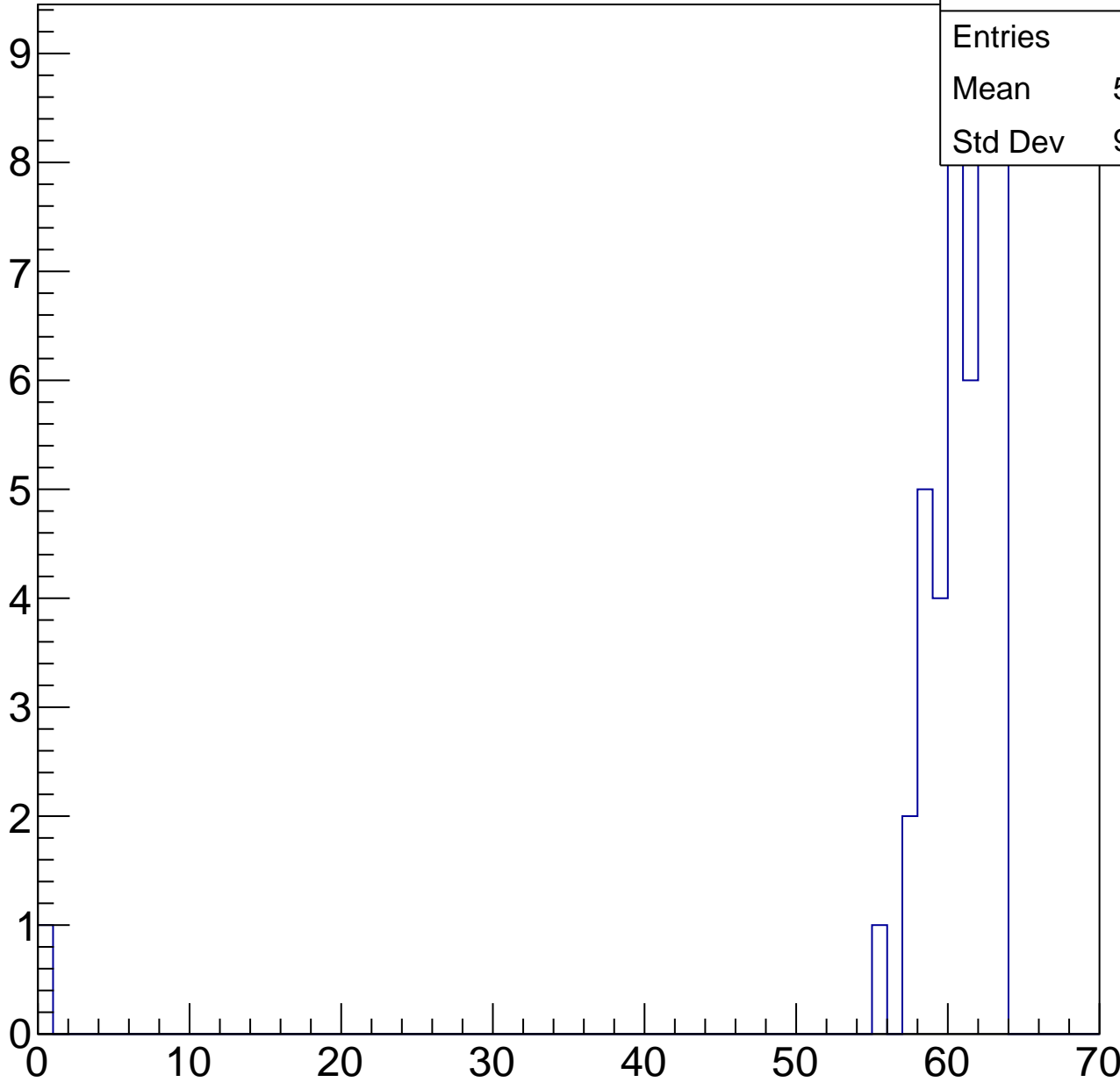
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.16 |
| Std Dev | 9.232 |

ampl



B1L100S, U7-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

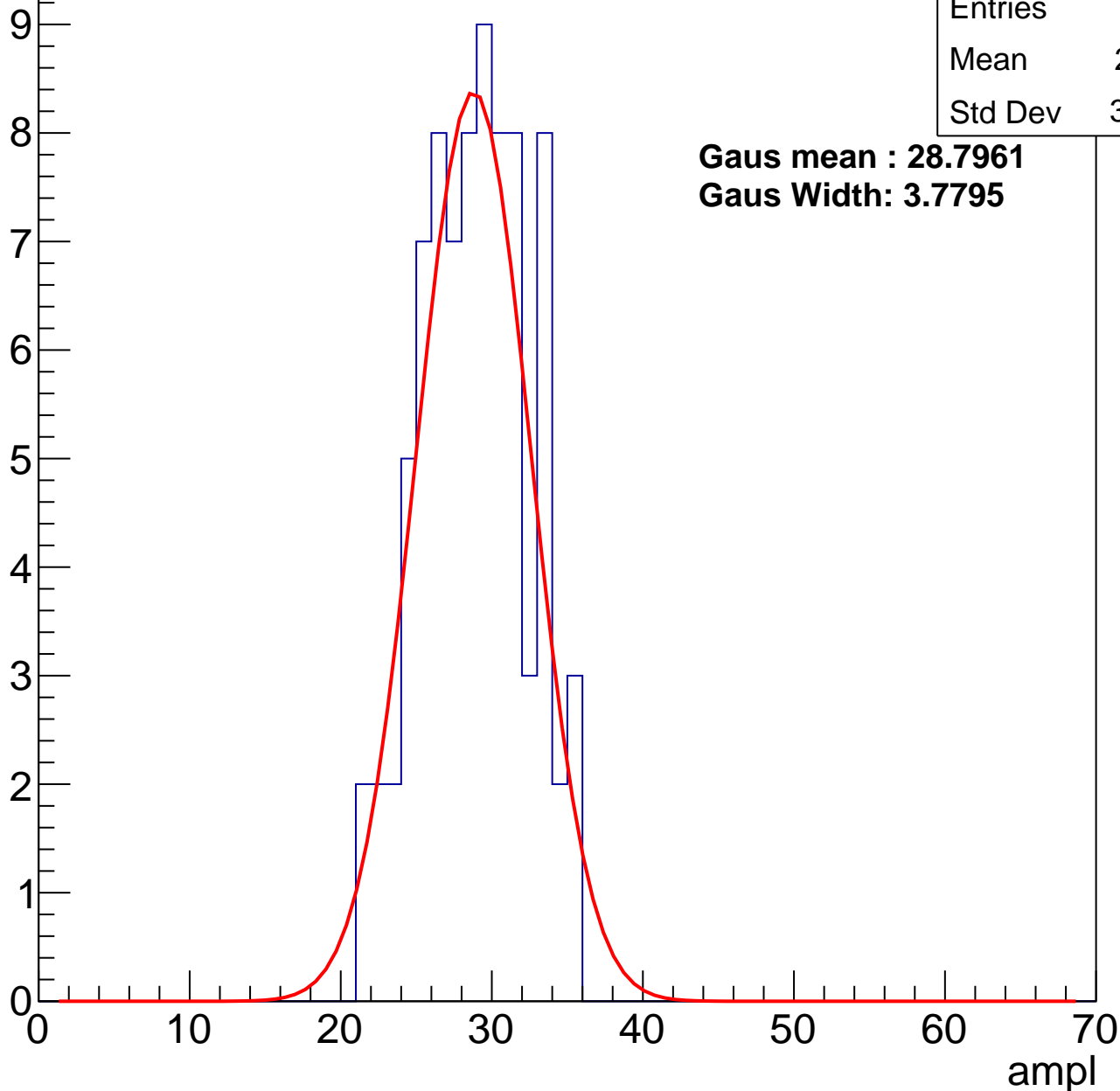
Entry



B1L100S, U7-ch39, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch39, adc1

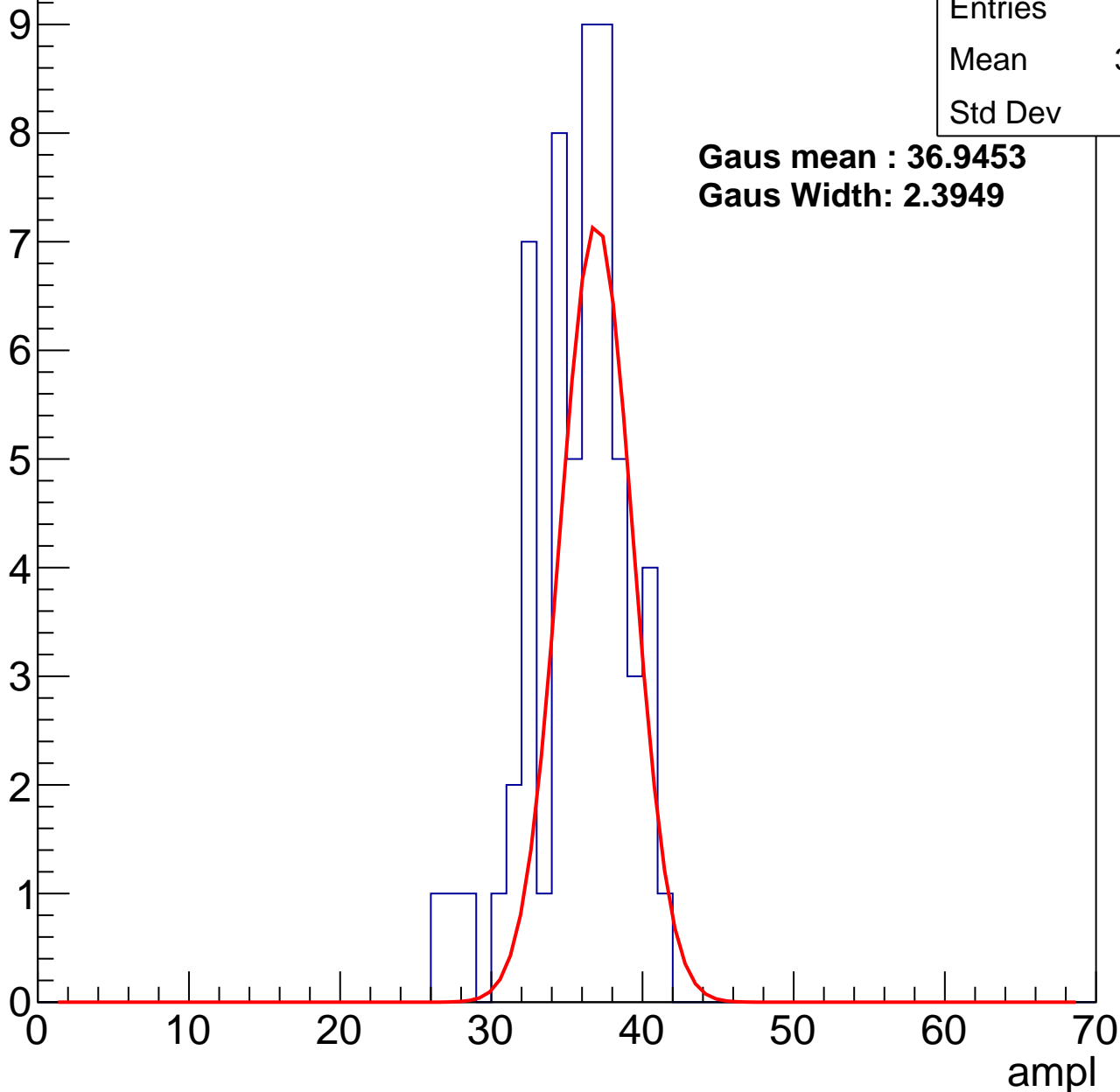
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 35.21 |
| Std Dev | 3.21 |

Gaus mean : 36.9453

Gaus Width: 2.3949



B1L100S, U7-ch39, adc2

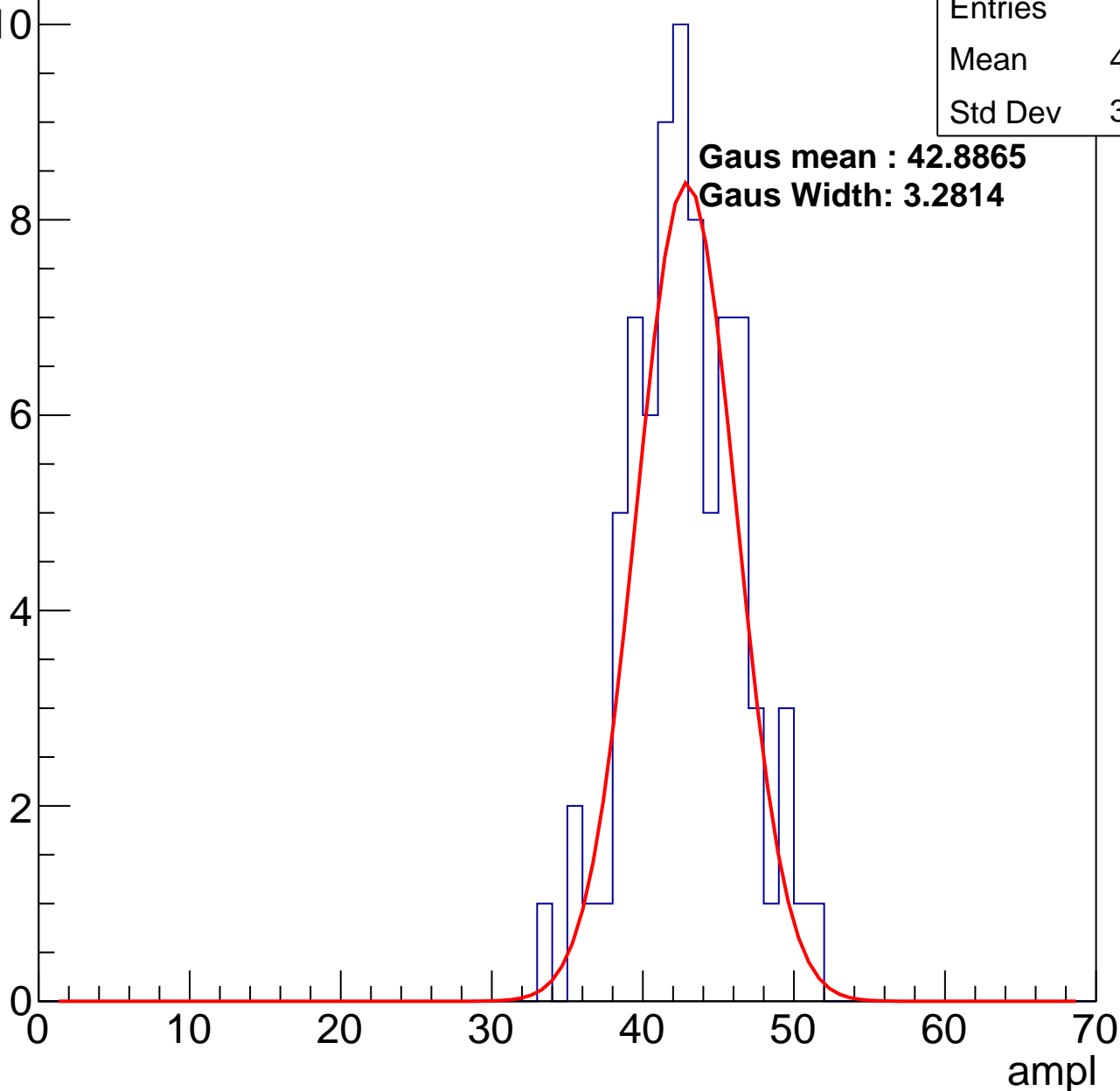
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.38 |
| Std Dev | 3.599 |

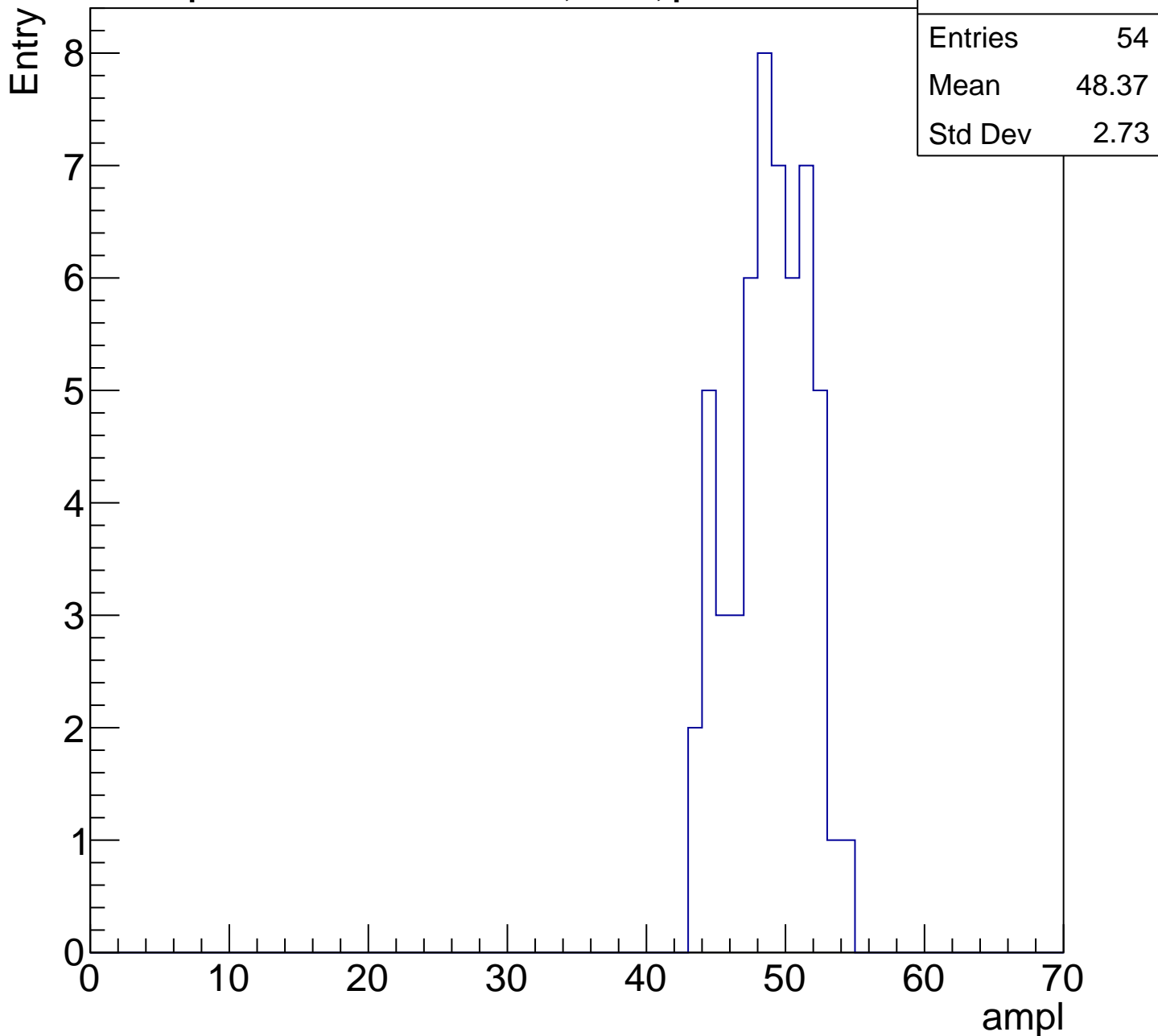
Gaus mean : 42.8865

Gaus Width: 3.2814



B1L100S, U7-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

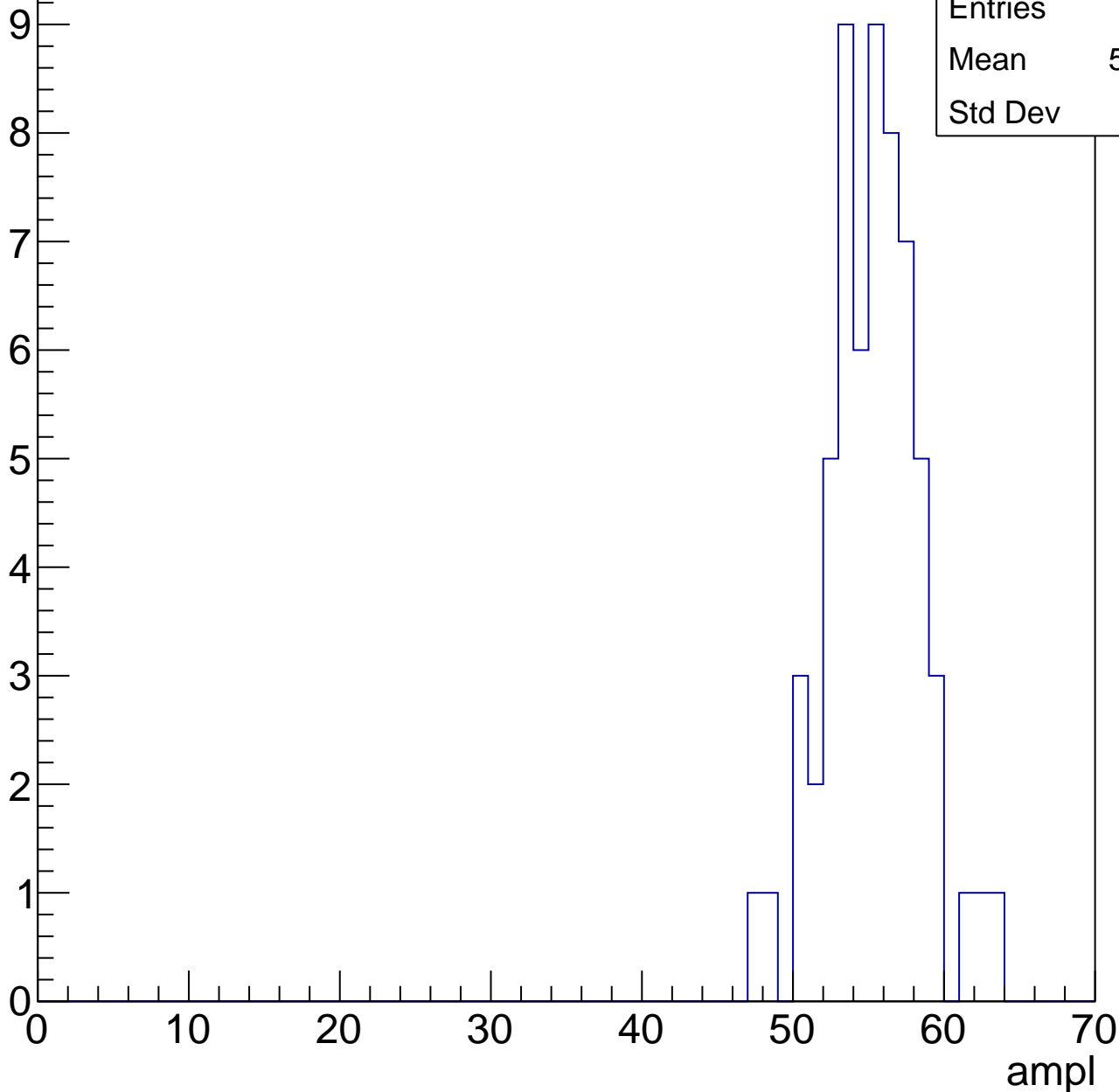


B1L100S, U7-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 54.89 |
| Std Dev | 3.07 |



B1L100S, U7-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.67 |
| Std Dev | 2.439 |

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L100S, U7-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 7 |
| Mean | 53 |
| Std Dev | 21.66 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U7-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch40, adc0

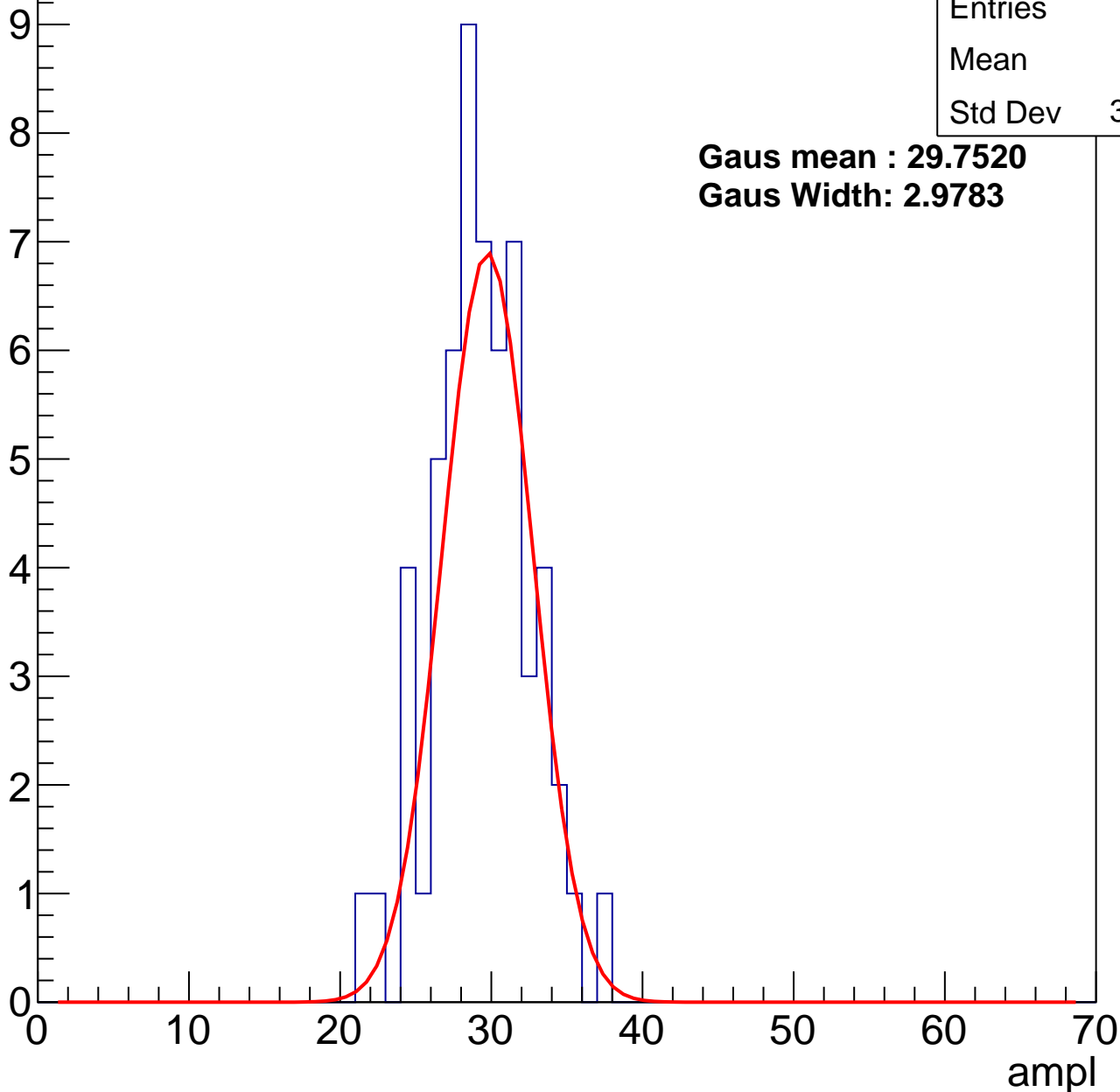
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 28.9 |
| Std Dev | 3.166 |

Gaus mean : 29.7520

Gaus Width: 2.9783



B1L100S, U7-ch40, adc1

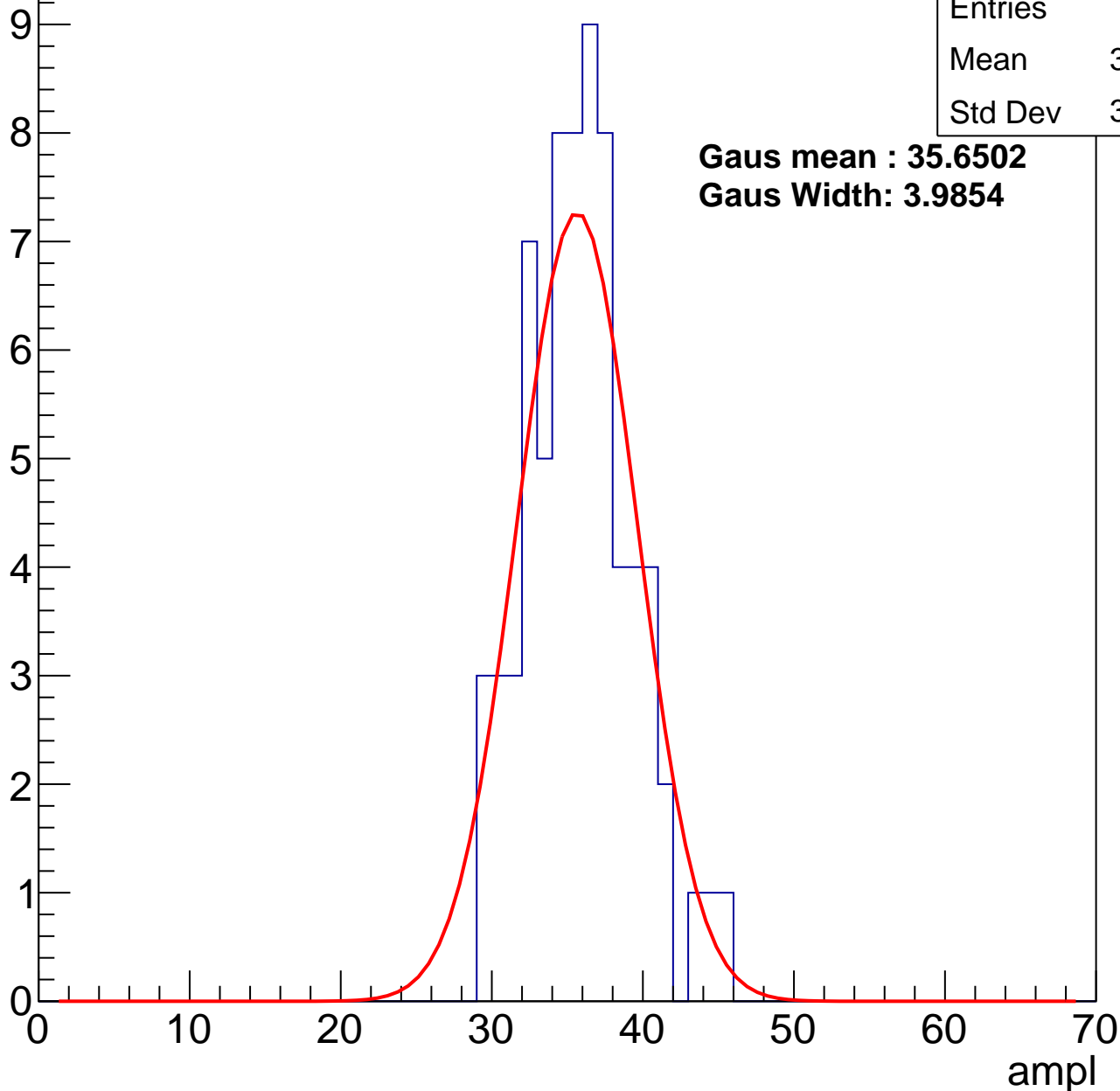
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.39 |
| Std Dev | 3.498 |

Gaus mean : 35.6502

Gaus Width: 3.9854



B1L100S, U7-ch40, adc2

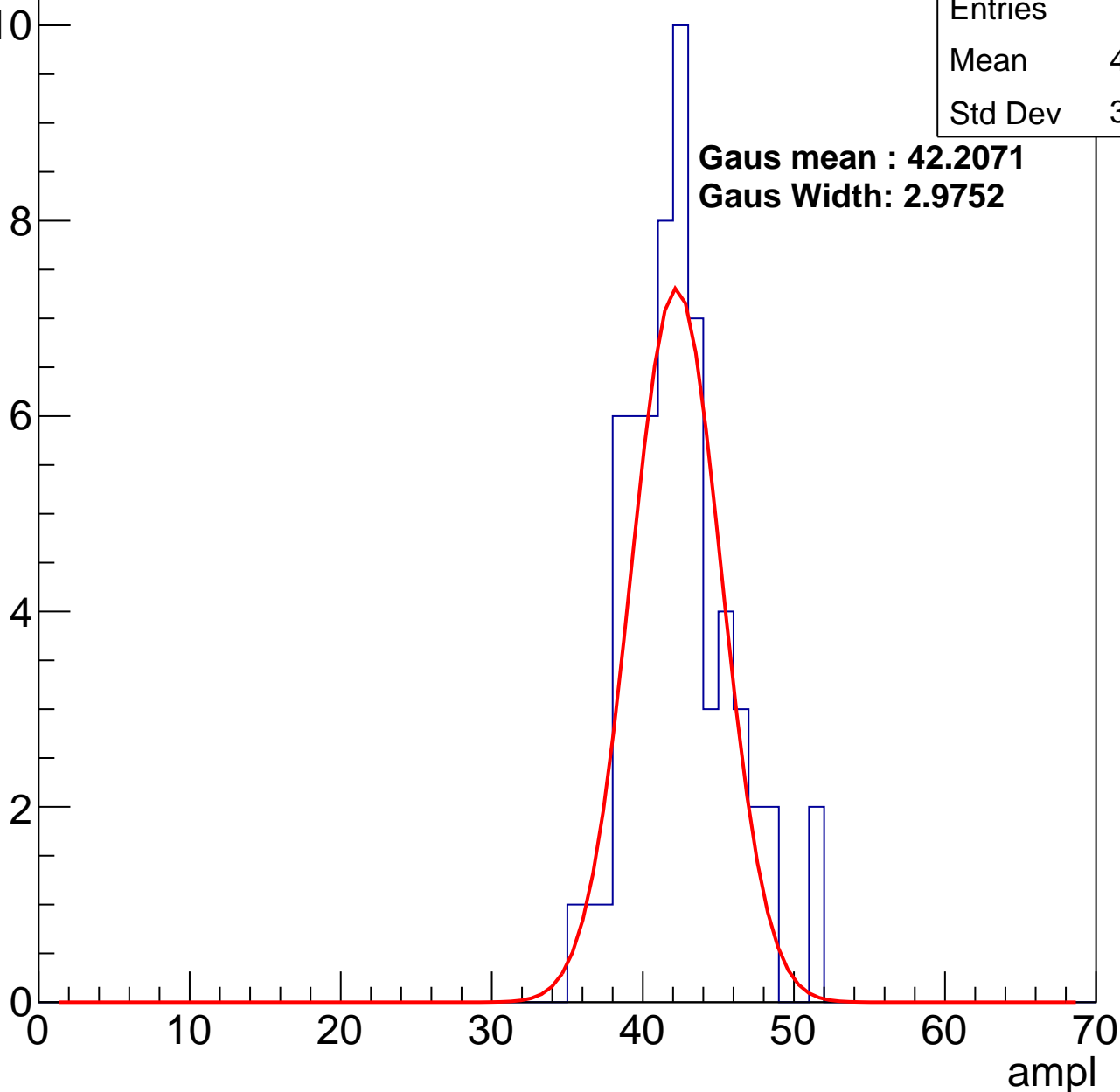
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.95 |
| Std Dev | 3.309 |

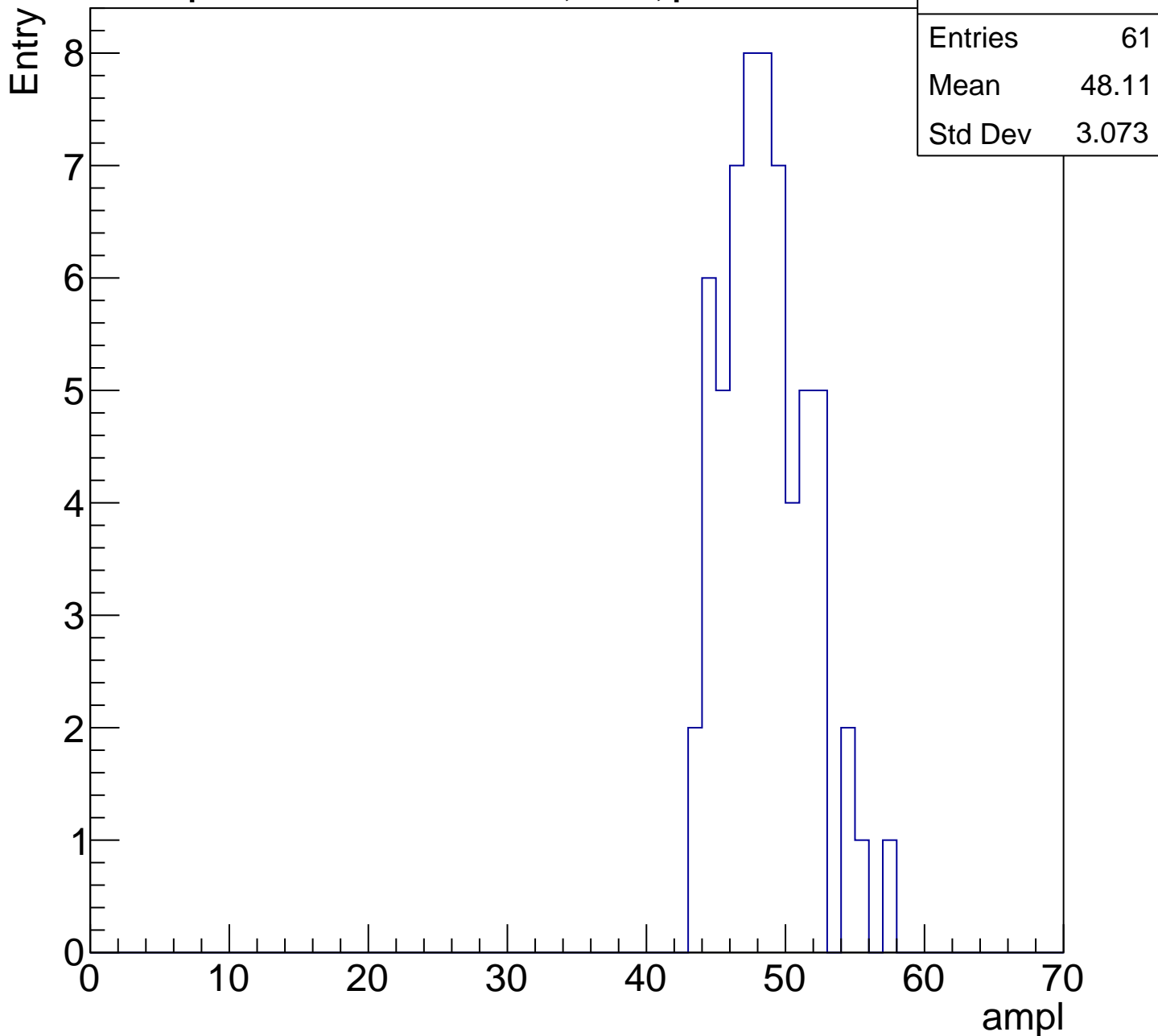
Gaus mean : 42.2071

Gaus Width: 2.9752



B1L100S, U7-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

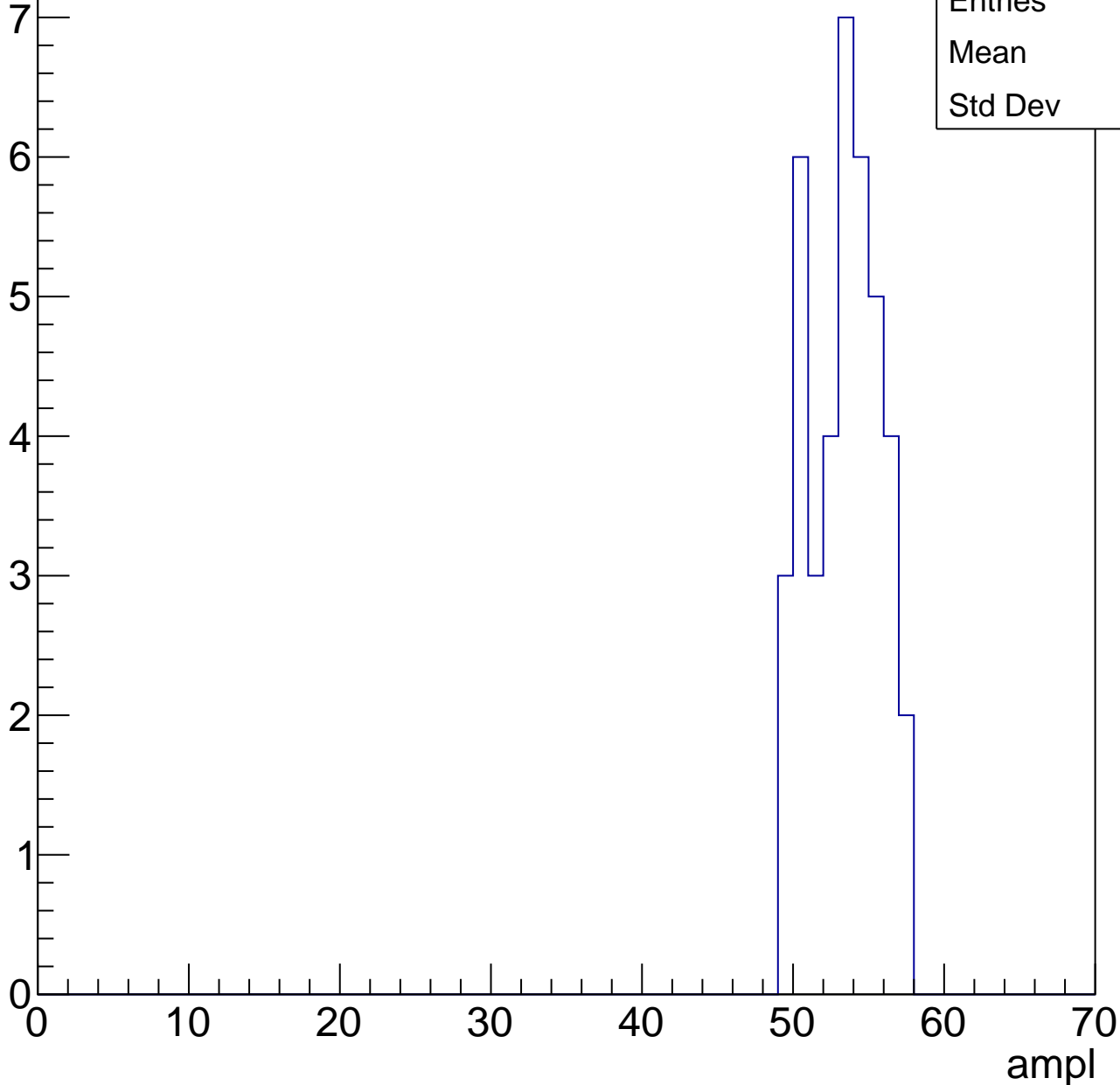


B1L100S, U7-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 40 |
| Mean | 52.9 |
| Std Dev | 2.3 |



B1L100S, U7-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 73

Mean 57.93

Std Dev 7.375

8

6

4

2

0

0

10

20

30

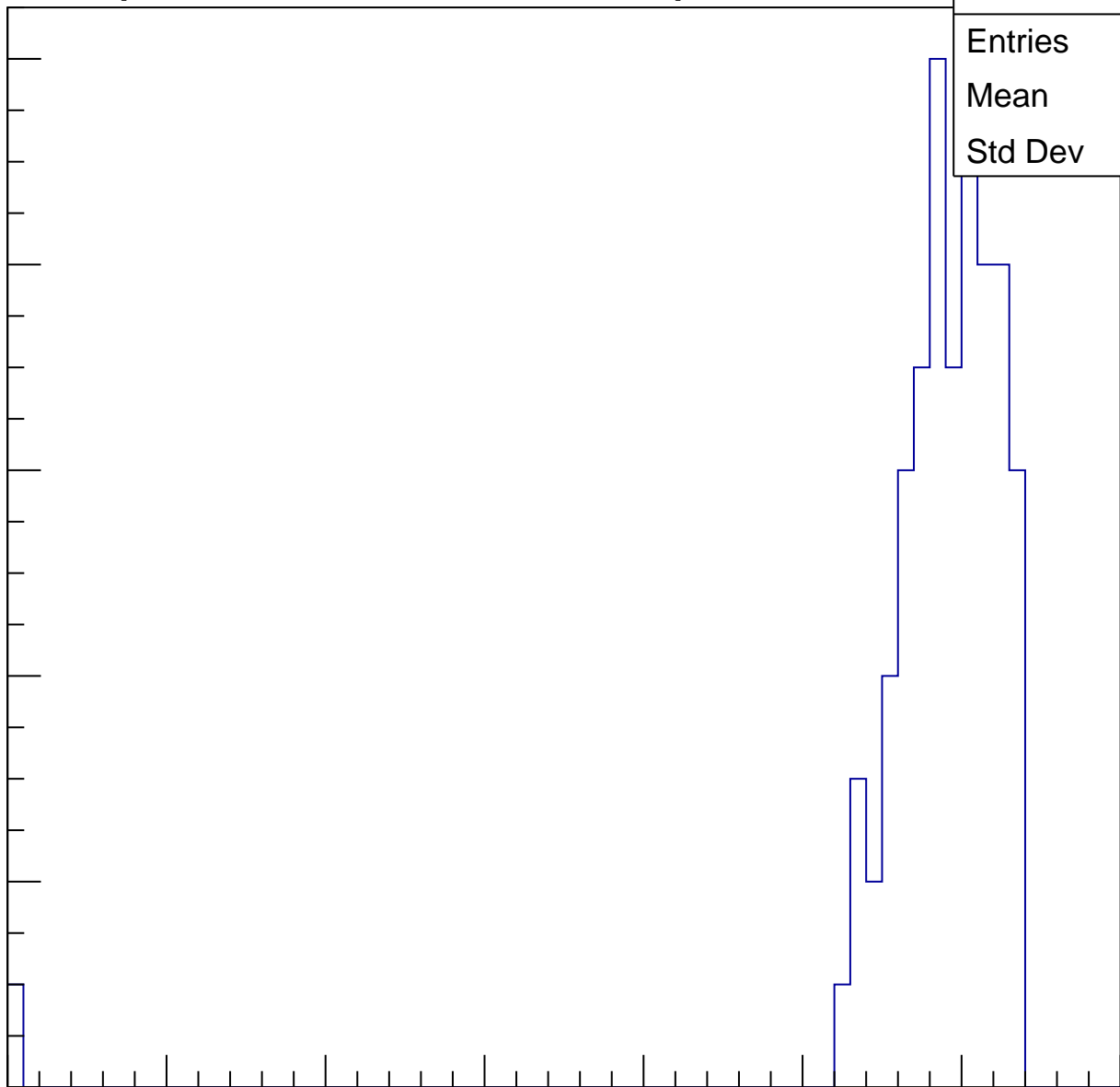
40

50

60

70

ampl



B1L100S, U7-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.11

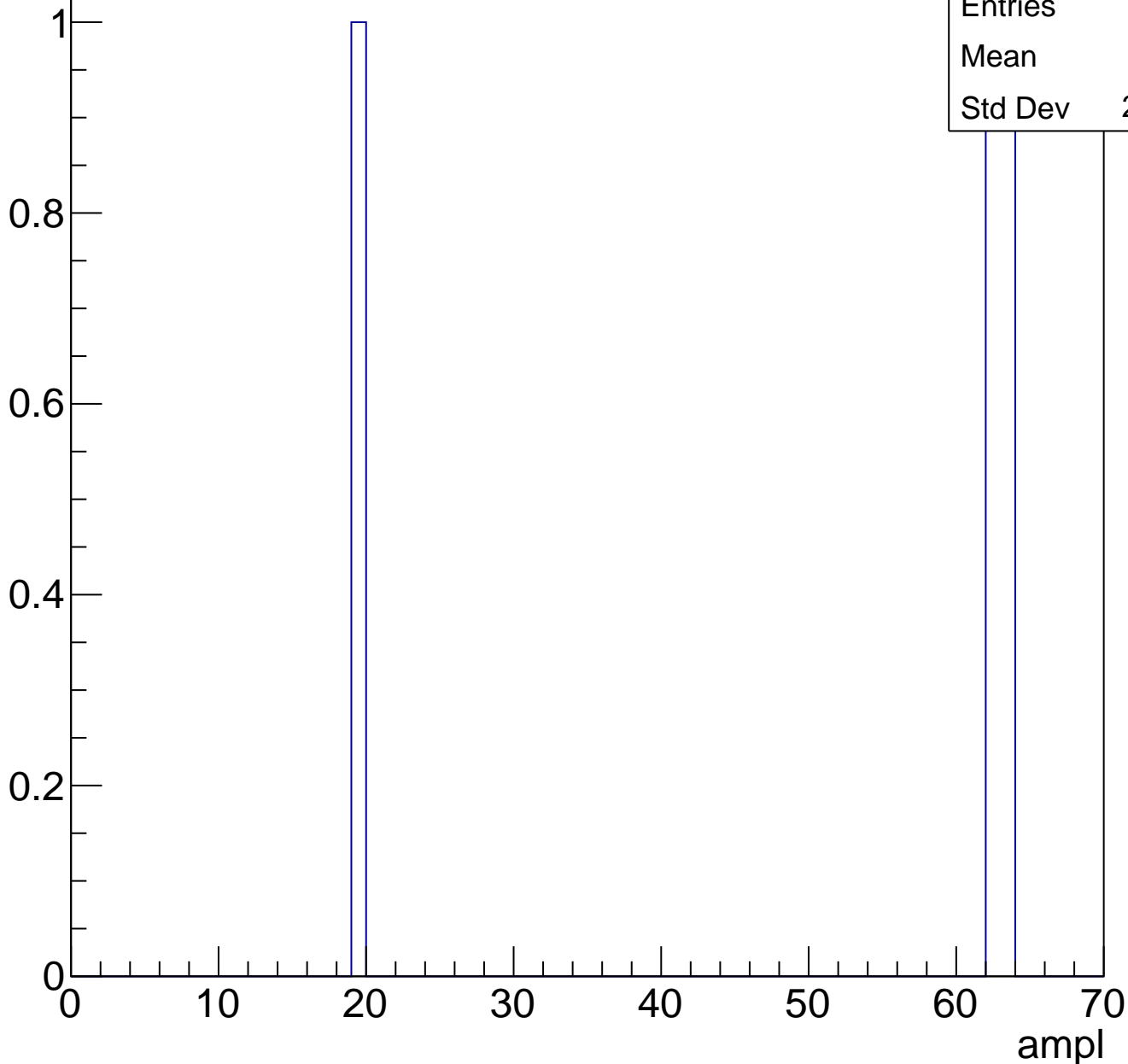
Std Dev

1.663

B1L100S, U7-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch41, adc0

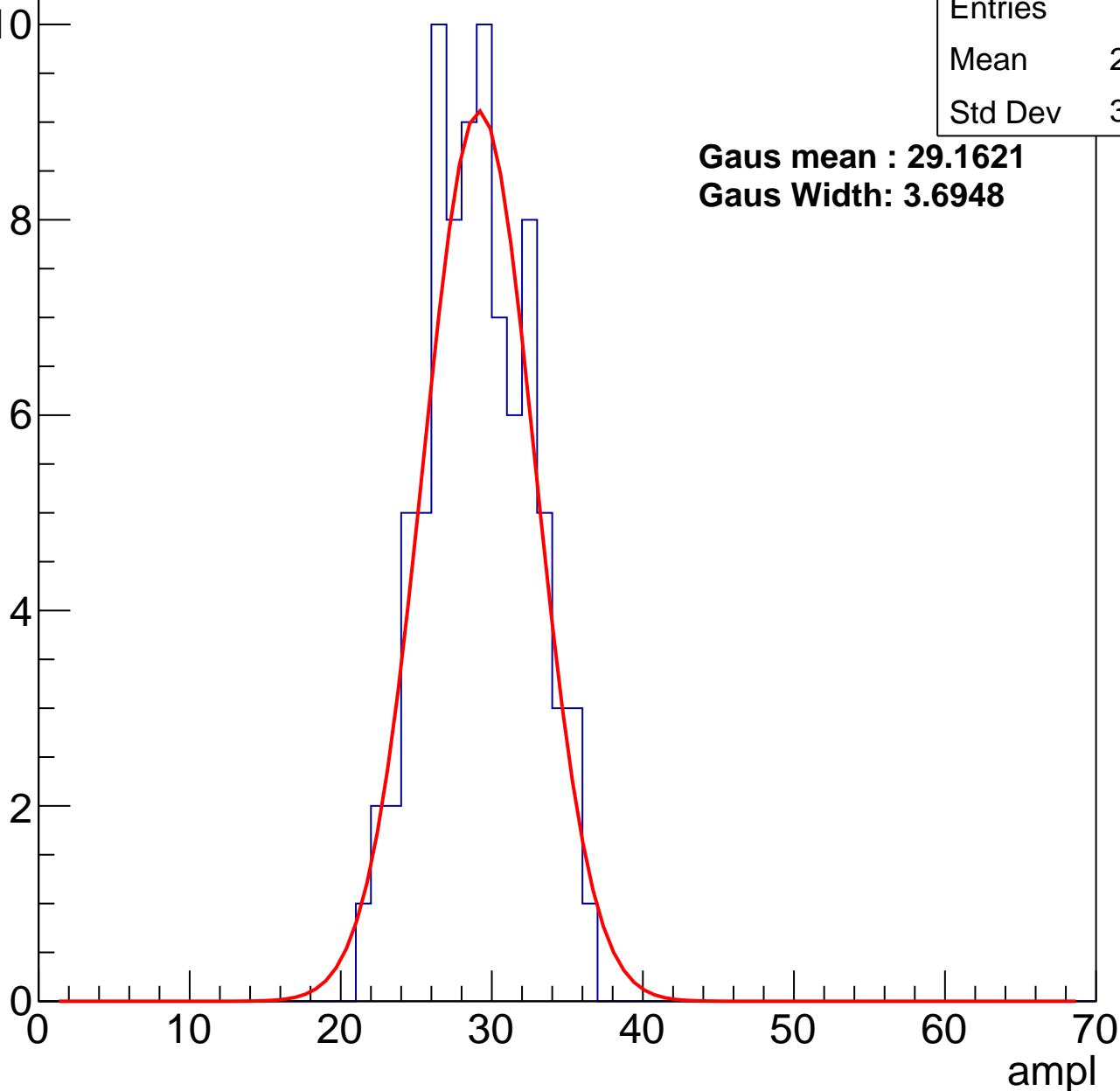
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 28.64 |
| Std Dev | 3.385 |

Gaus mean : 29.1621

Gaus Width: 3.6948



B1L100S, U7-ch41, adc1

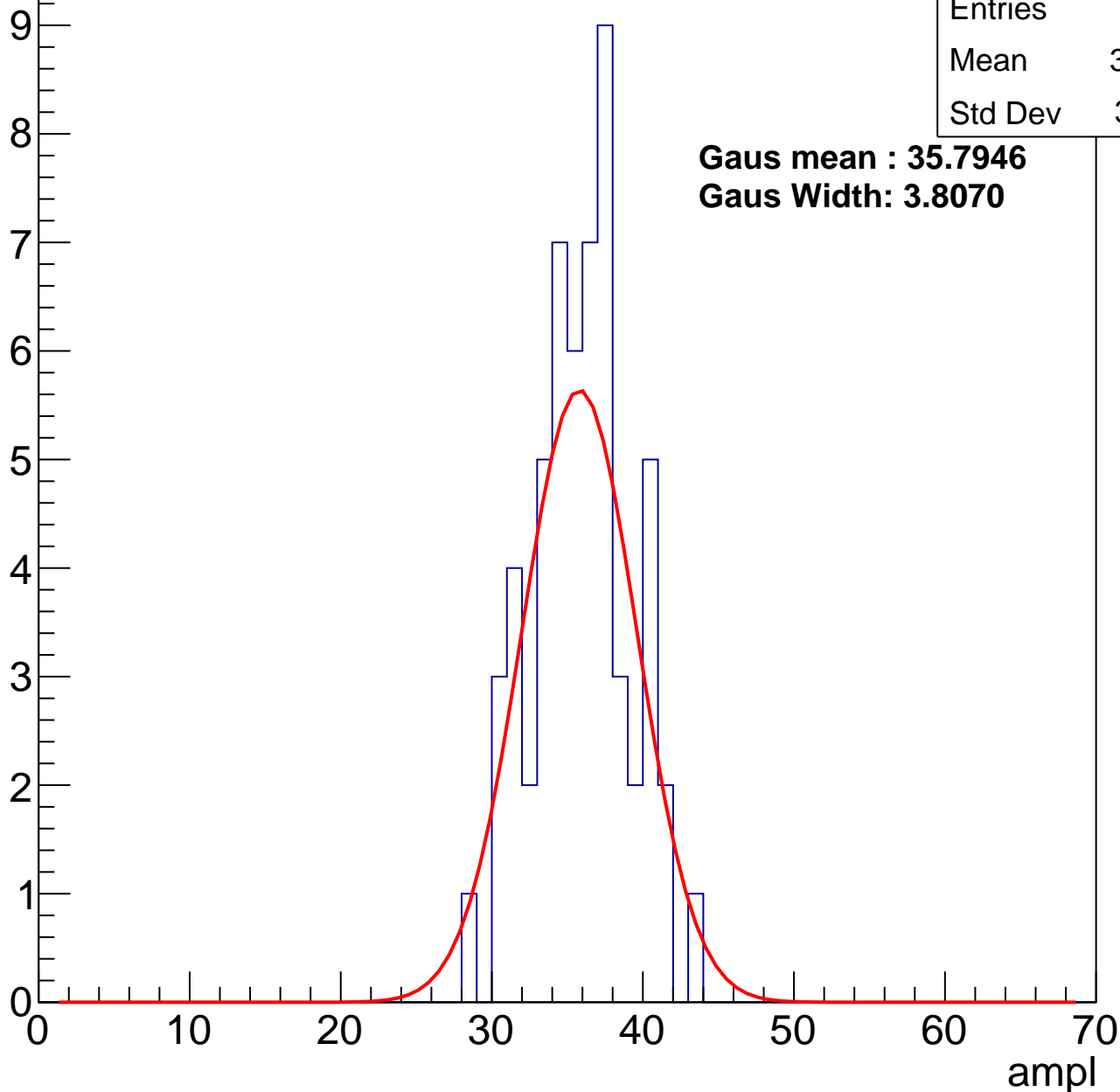
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 35.46 |
| Std Dev | 3.201 |

Gaus mean : 35.7946

Gaus Width: 3.8070



B1L100S, U7-ch41, adc2

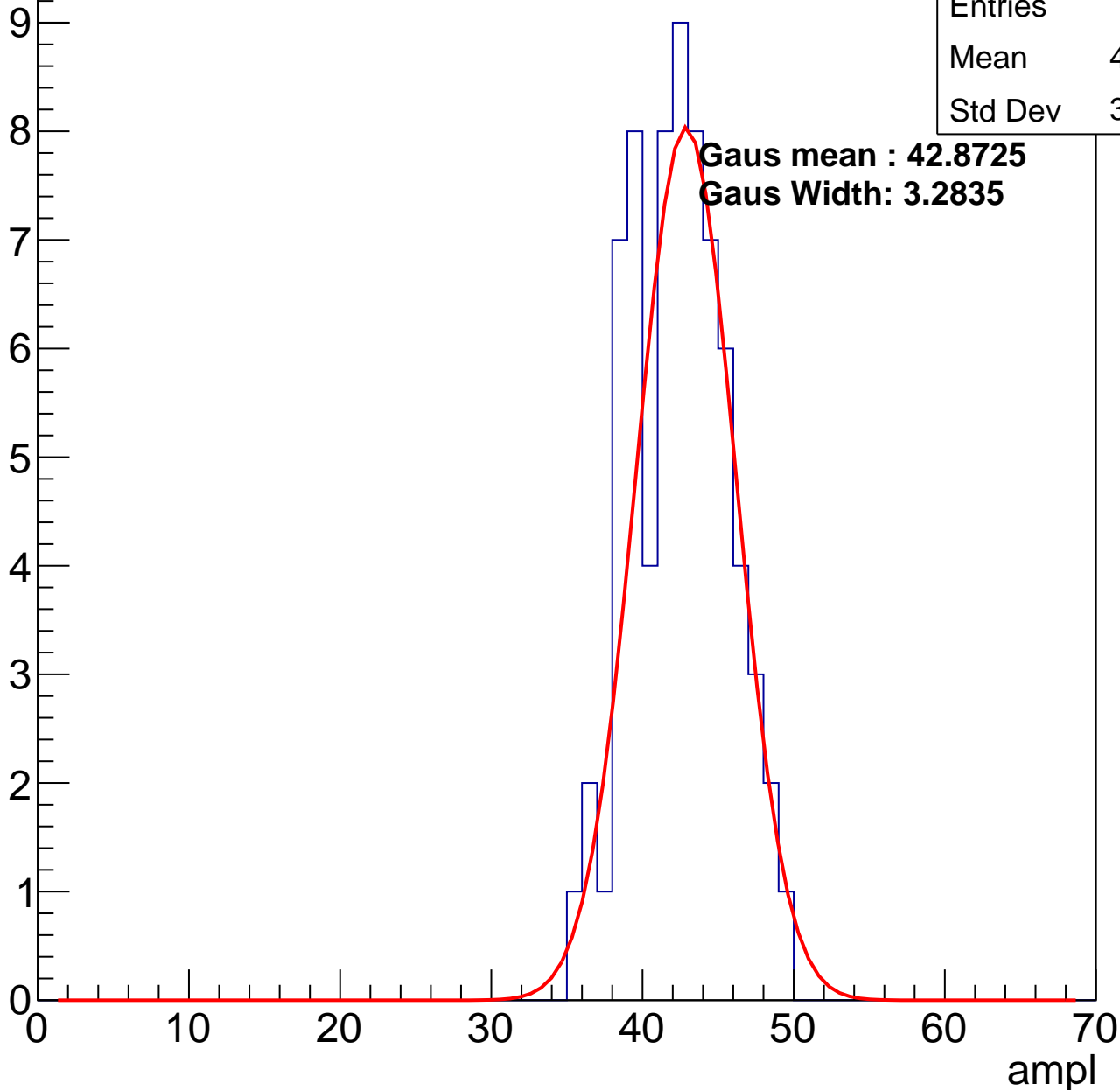
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 41.97 |
| Std Dev | 3.149 |

Gaus mean : 42.8725

Gaus Width: 3.2835

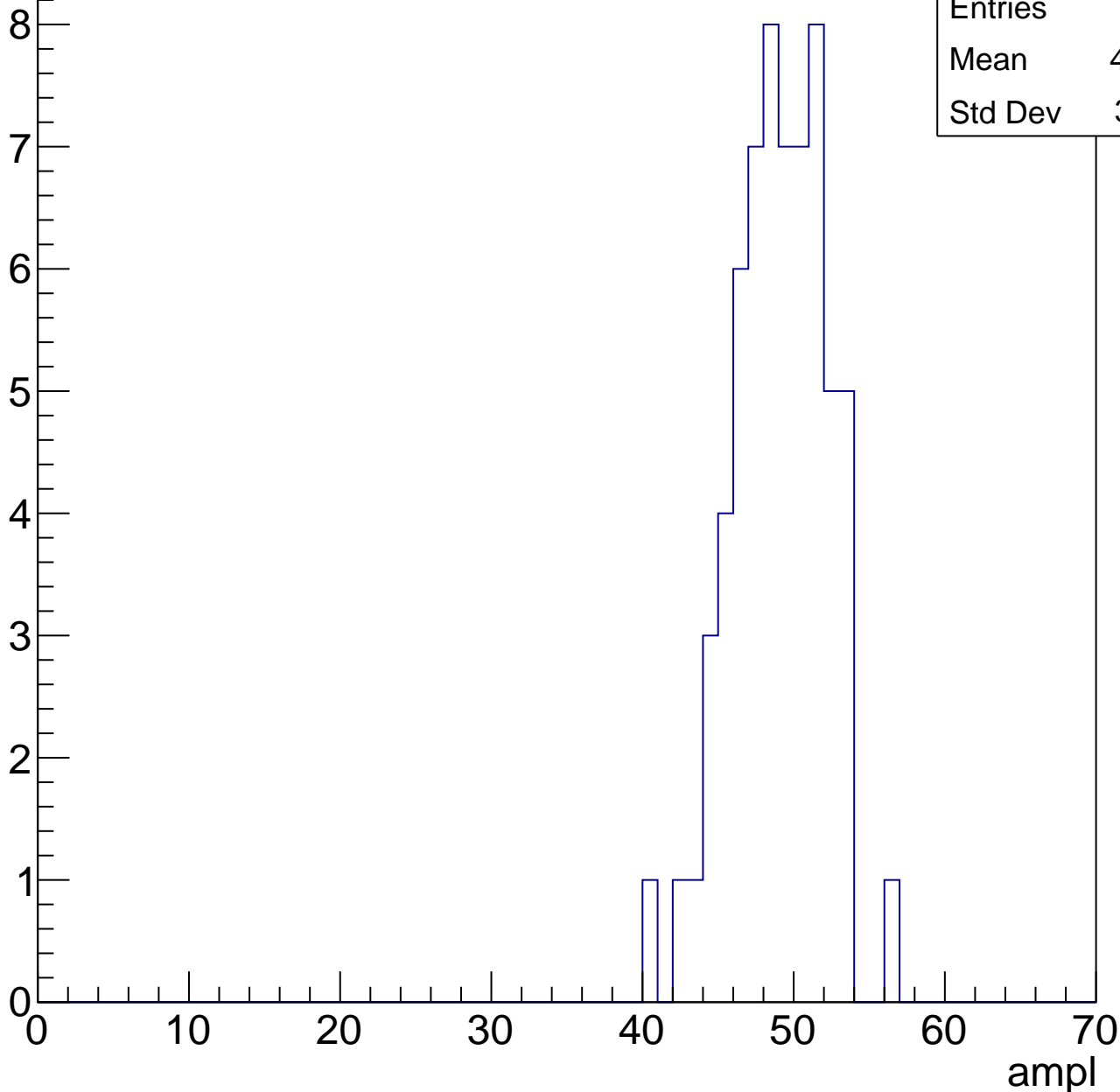


B1L100S, U7-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

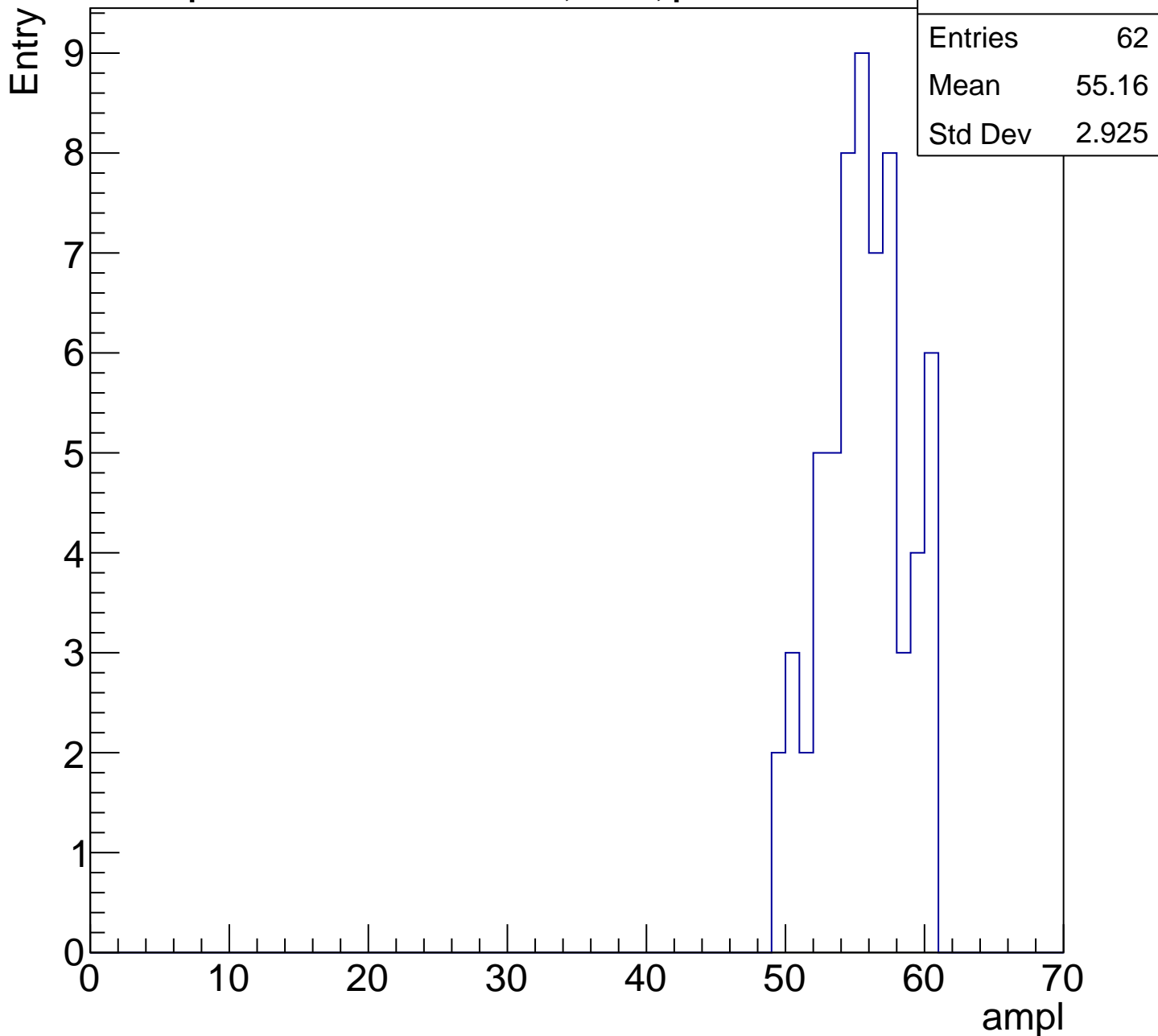
Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.56 |
| Std Dev | 3.051 |



B1L100S, U7-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch41, adc5

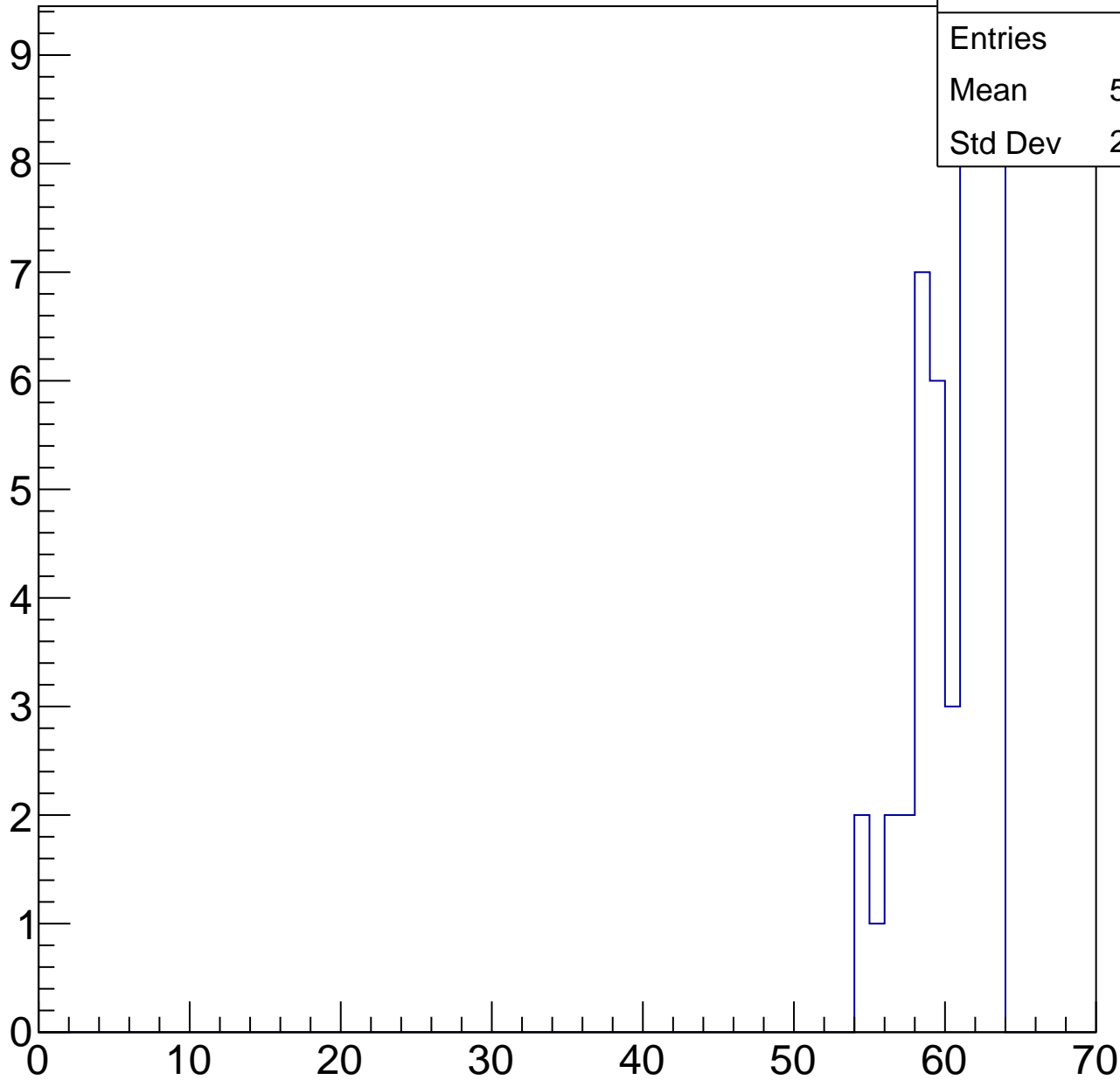
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.98 |
| Std Dev | 2.487 |

ampl



B1L100S, U7-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

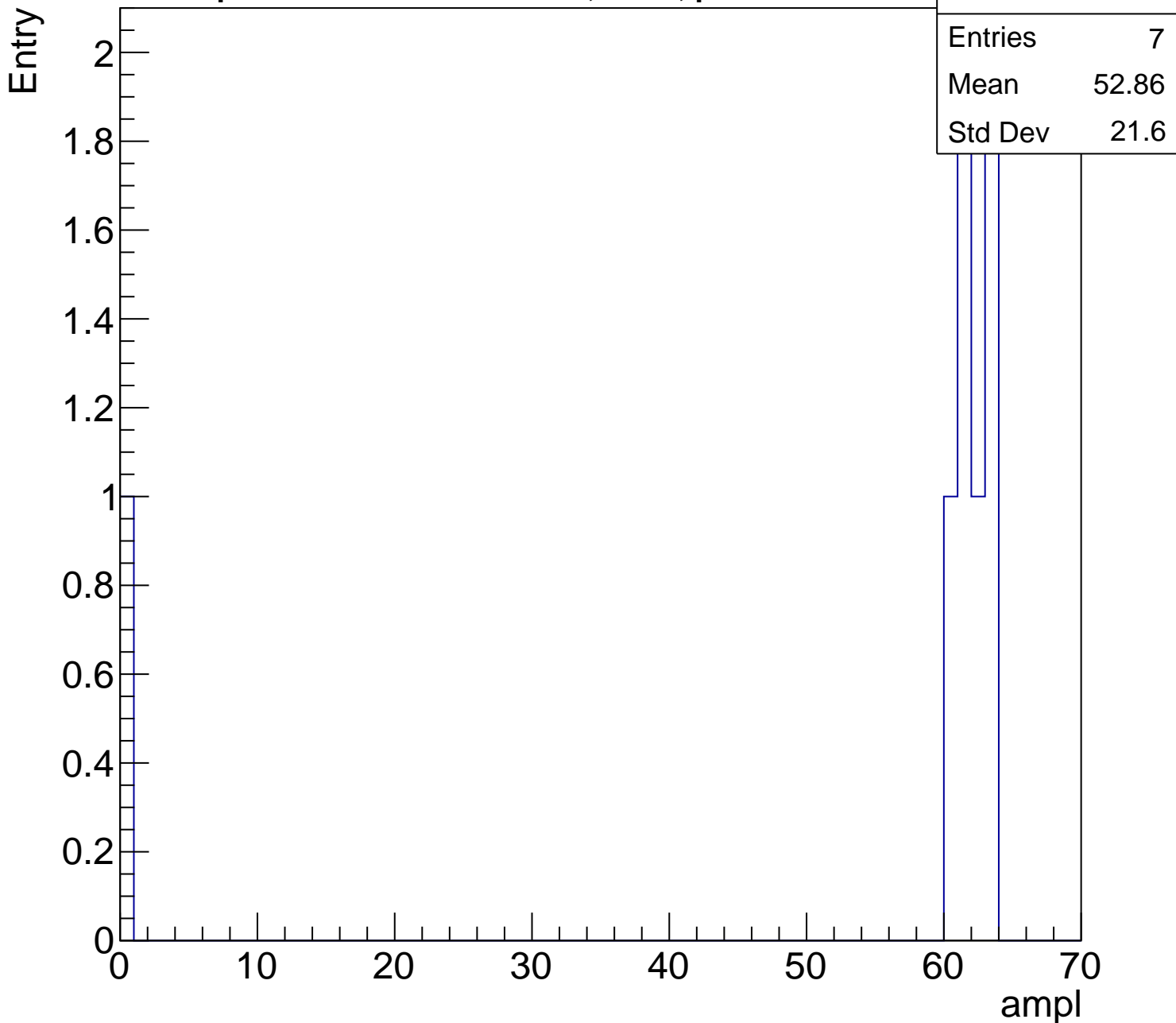
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 7 |
| Mean | 52.86 |
| Std Dev | 21.6 |

0 10 20 30 40 50 60 70

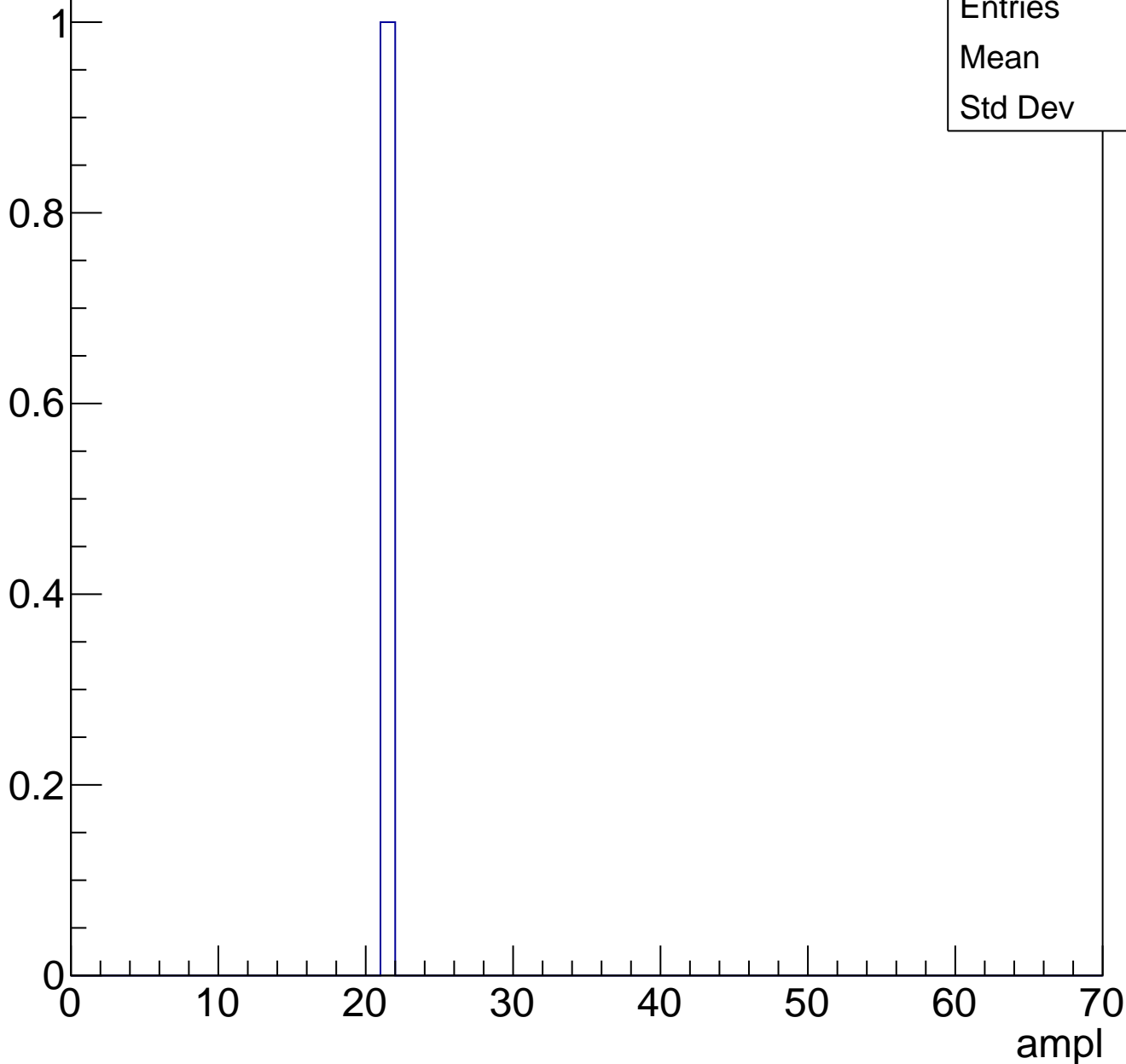
ampl



B1L100S, U7-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch42, adc0

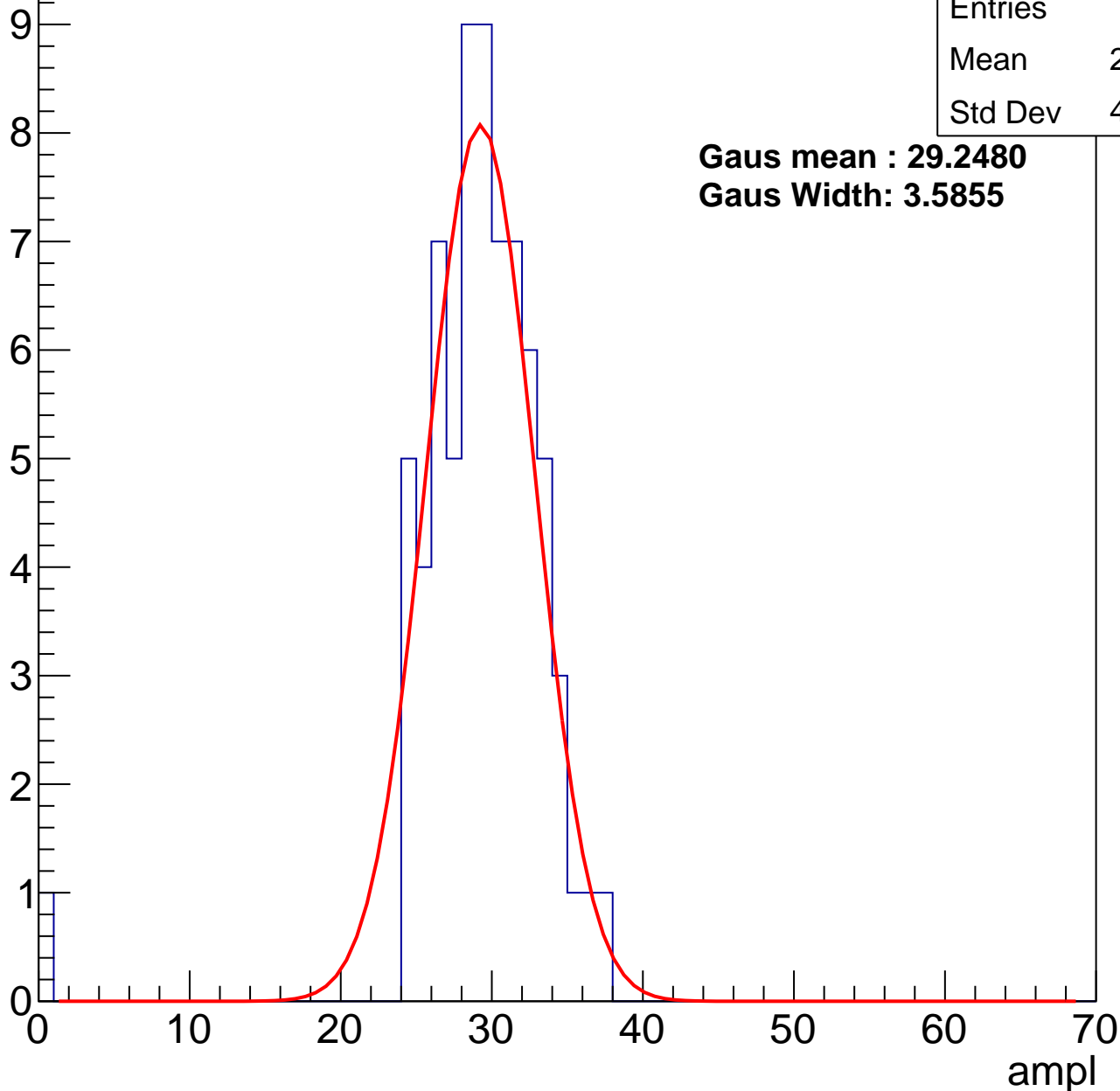
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.79 |
| Std Dev | 4.614 |

Gaus mean : 29.2480

Gaus Width: 3.5855



B1L100S, U7-ch42, adc1

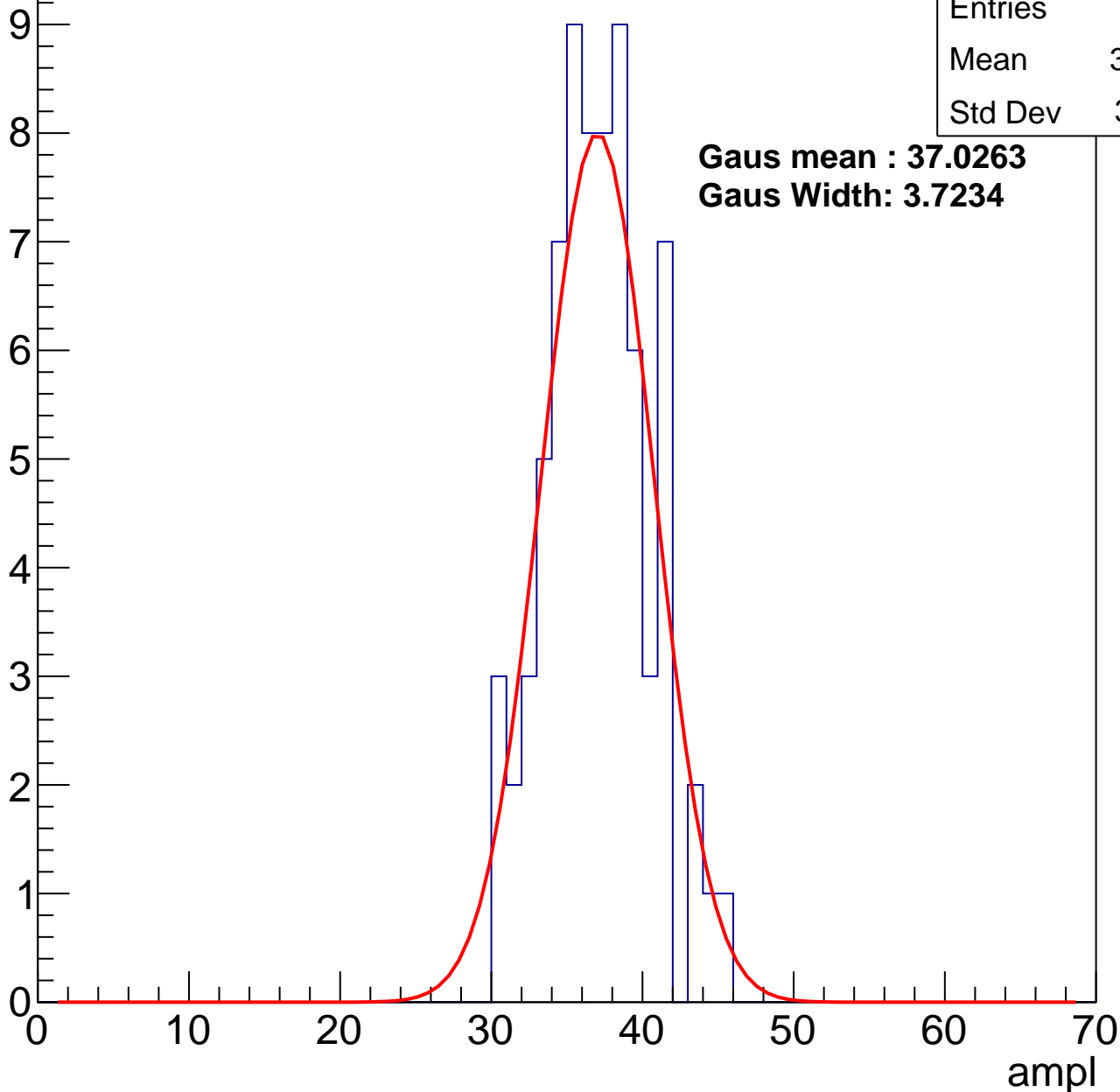
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.59 |
| Std Dev | 3.341 |

Gaus mean : 37.0263

Gaus Width: 3.7234



B1L100S, U7-ch42, adc2

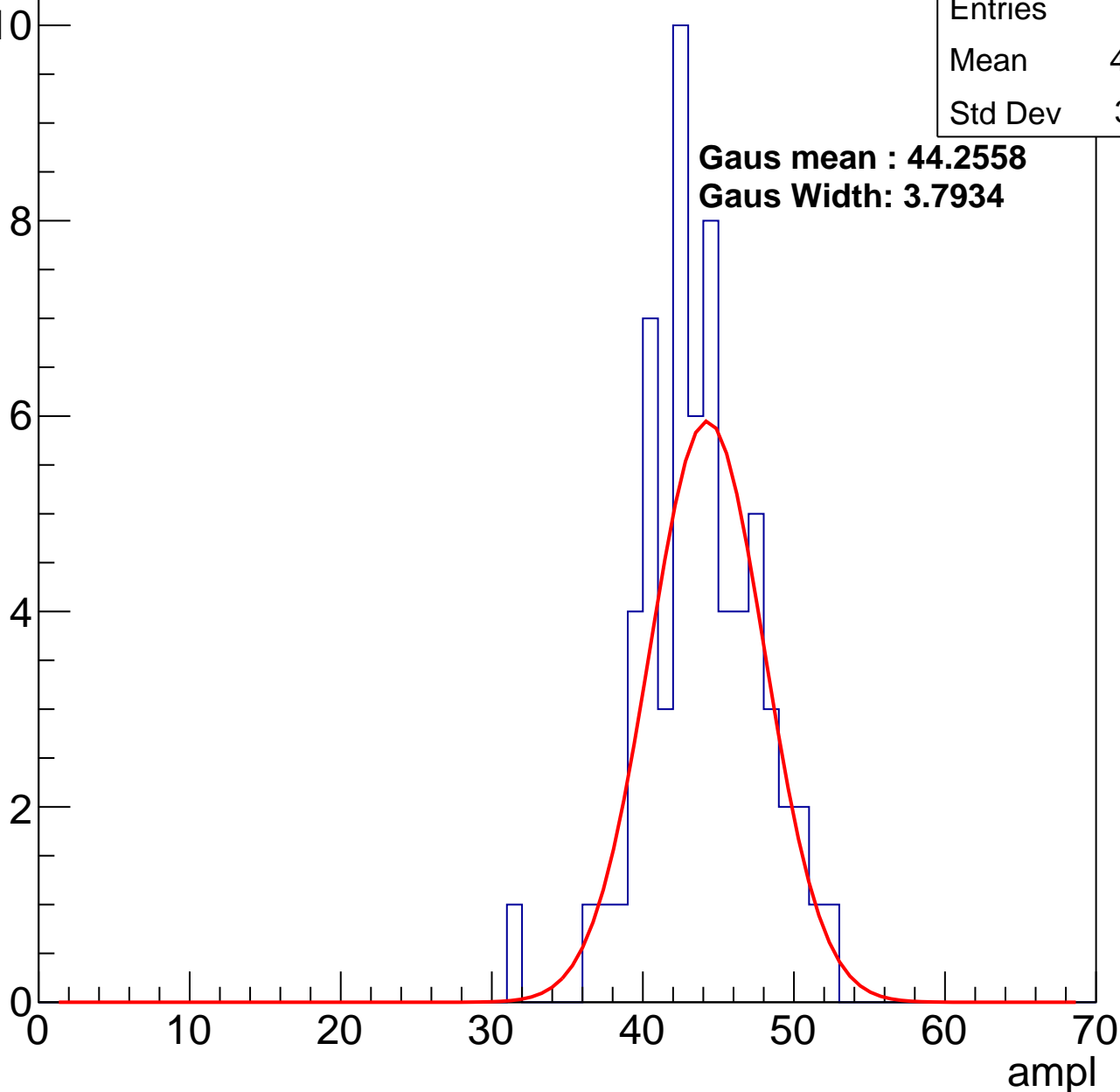
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.36 |
| Std Dev | 3.801 |

Gaus mean : 44.2558

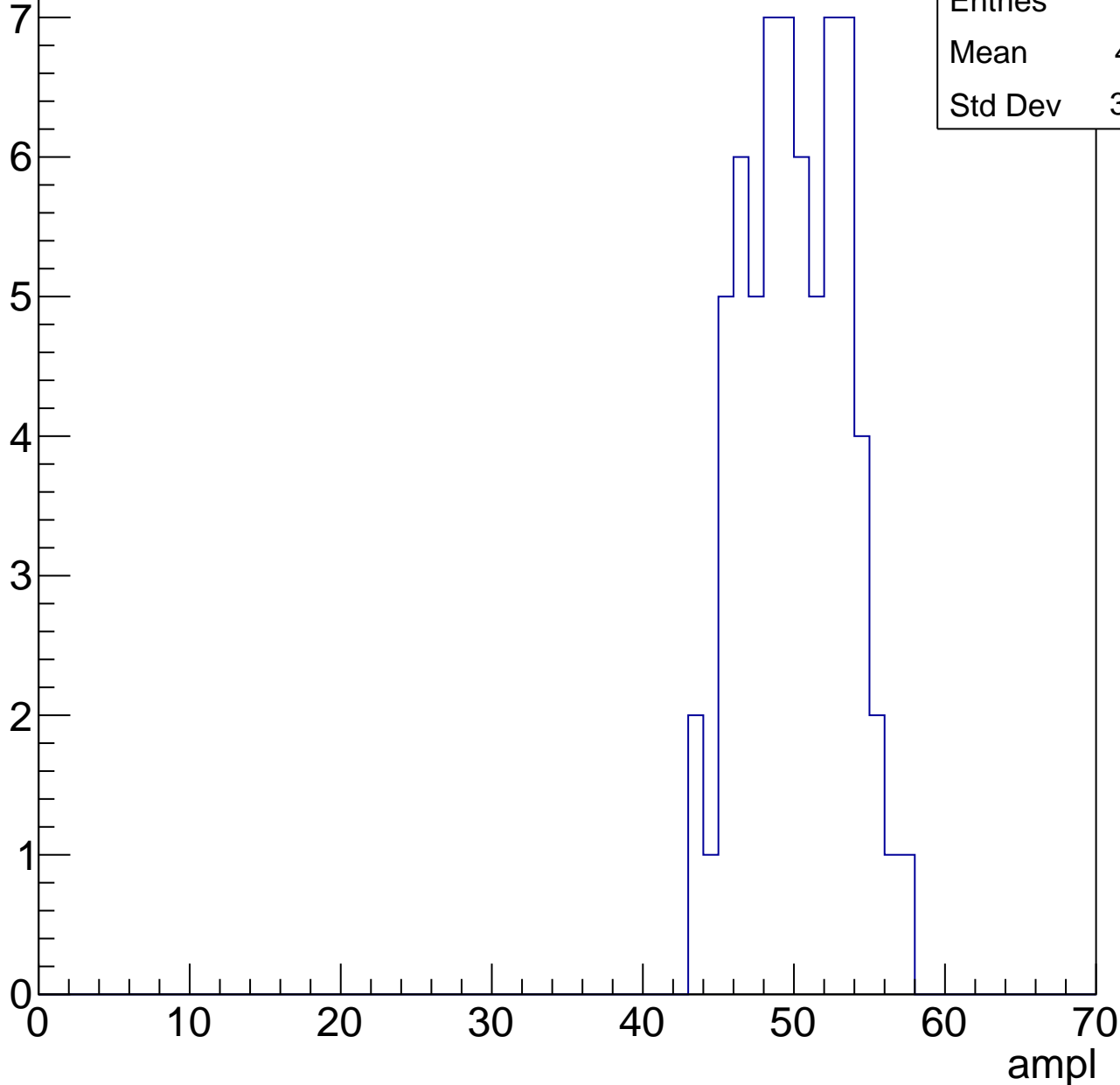
Gaus Width: 3.7934



B1L100S, U7-ch42, adc3

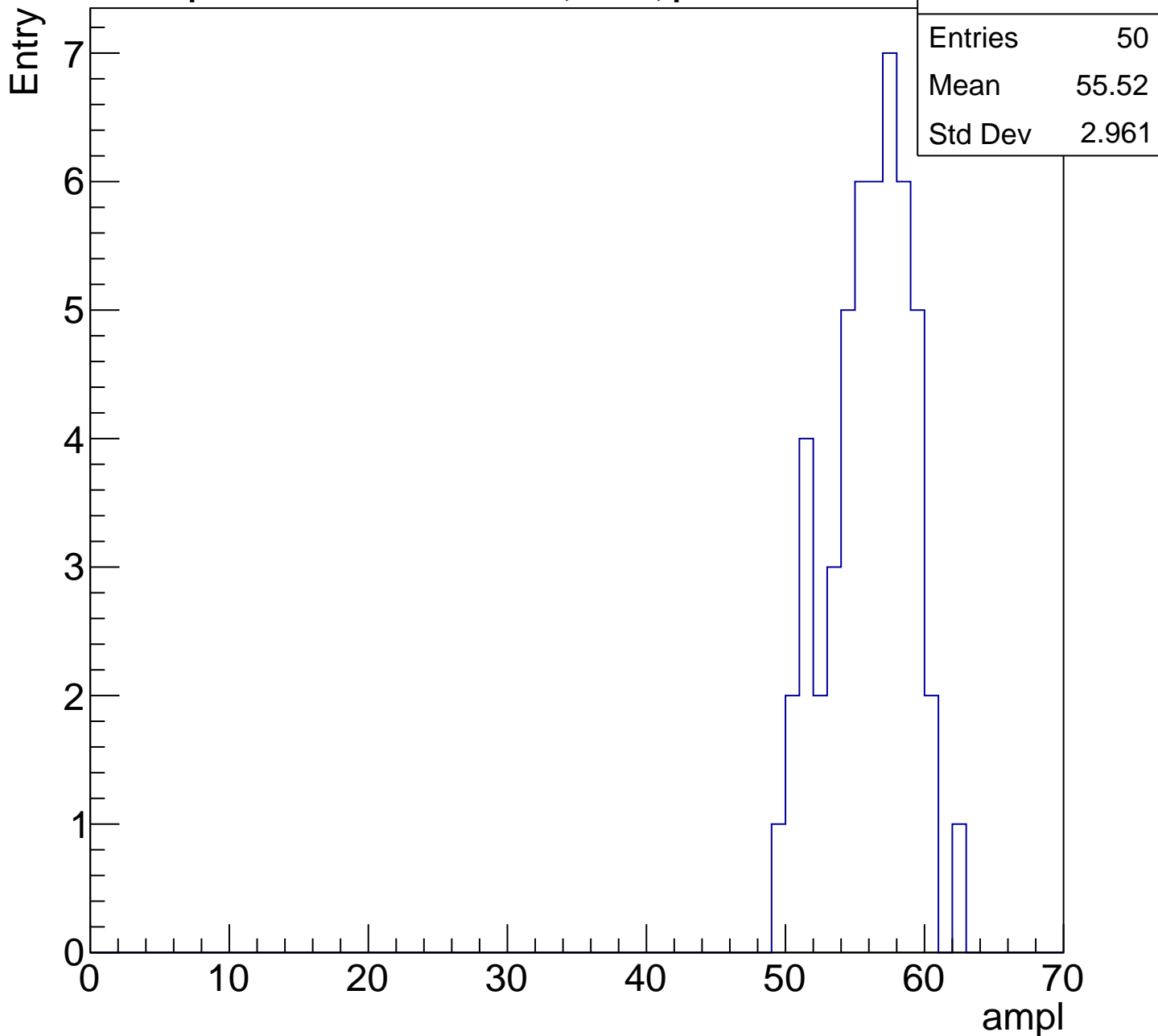
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

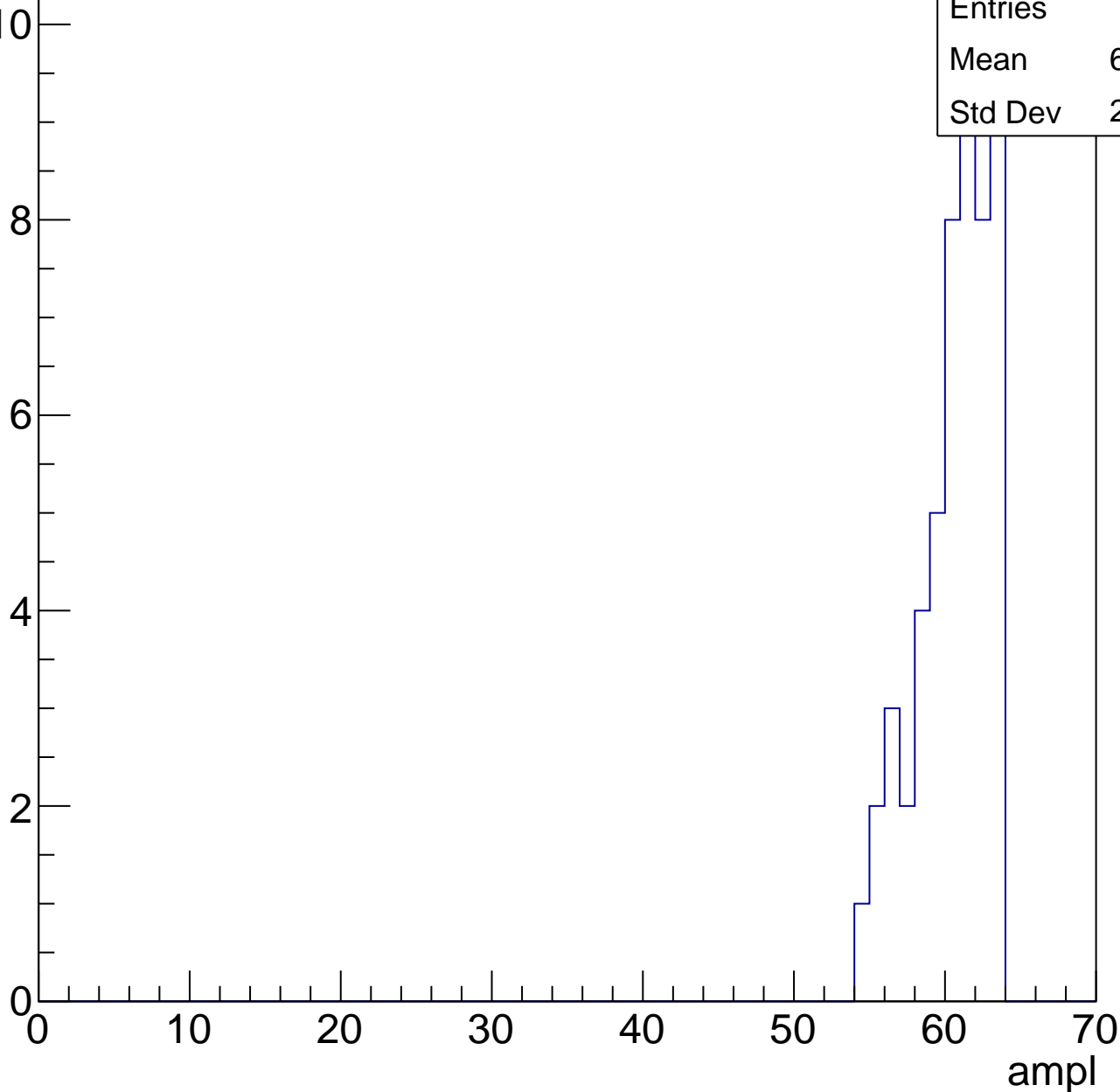


B1L100S, U7-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

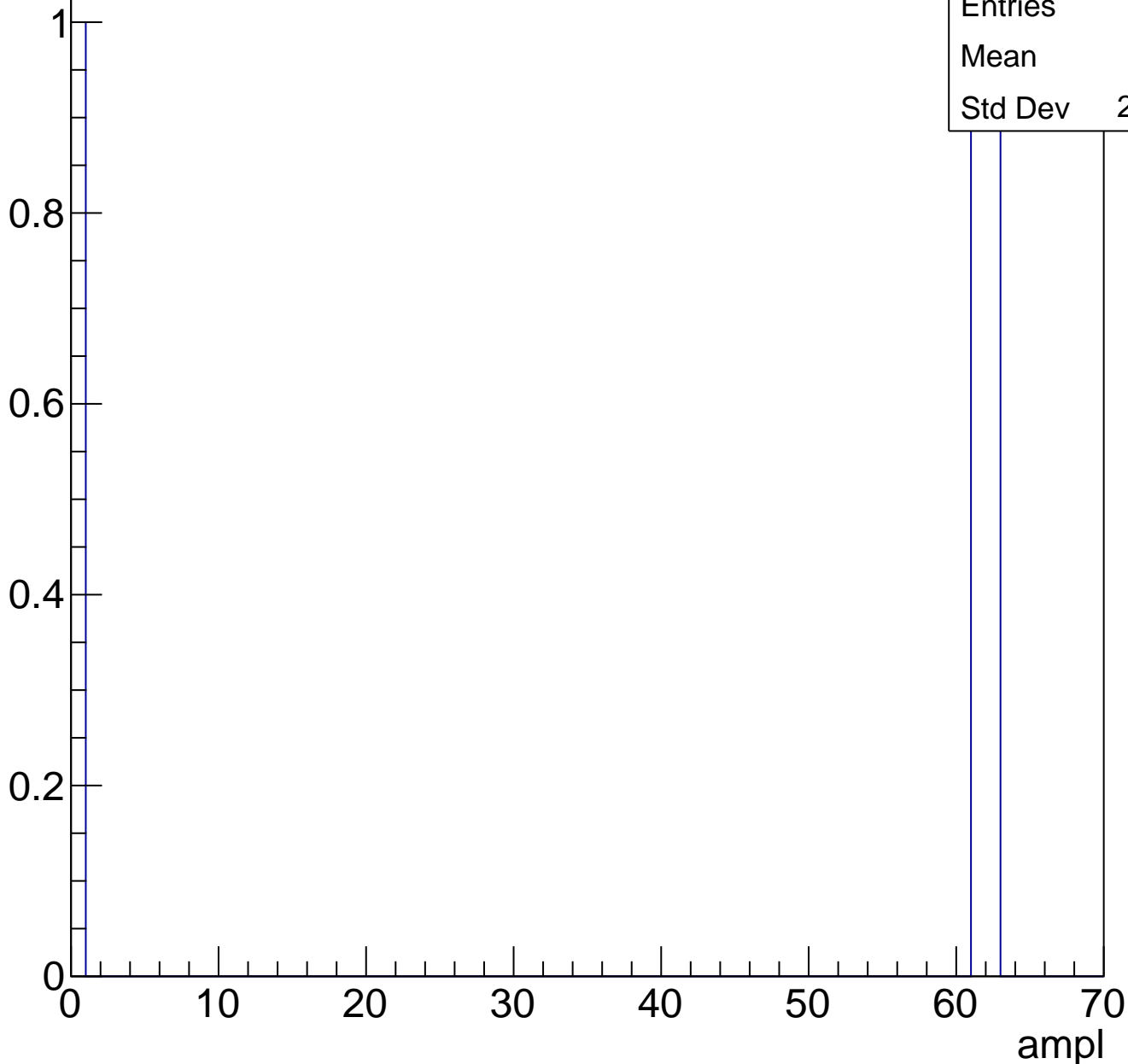
| | |
|---------|-------|
| Entries | 52 |
| Mean | 60.15 |
| Std Dev | 2.413 |



B1L100S, U7-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch43, adc0

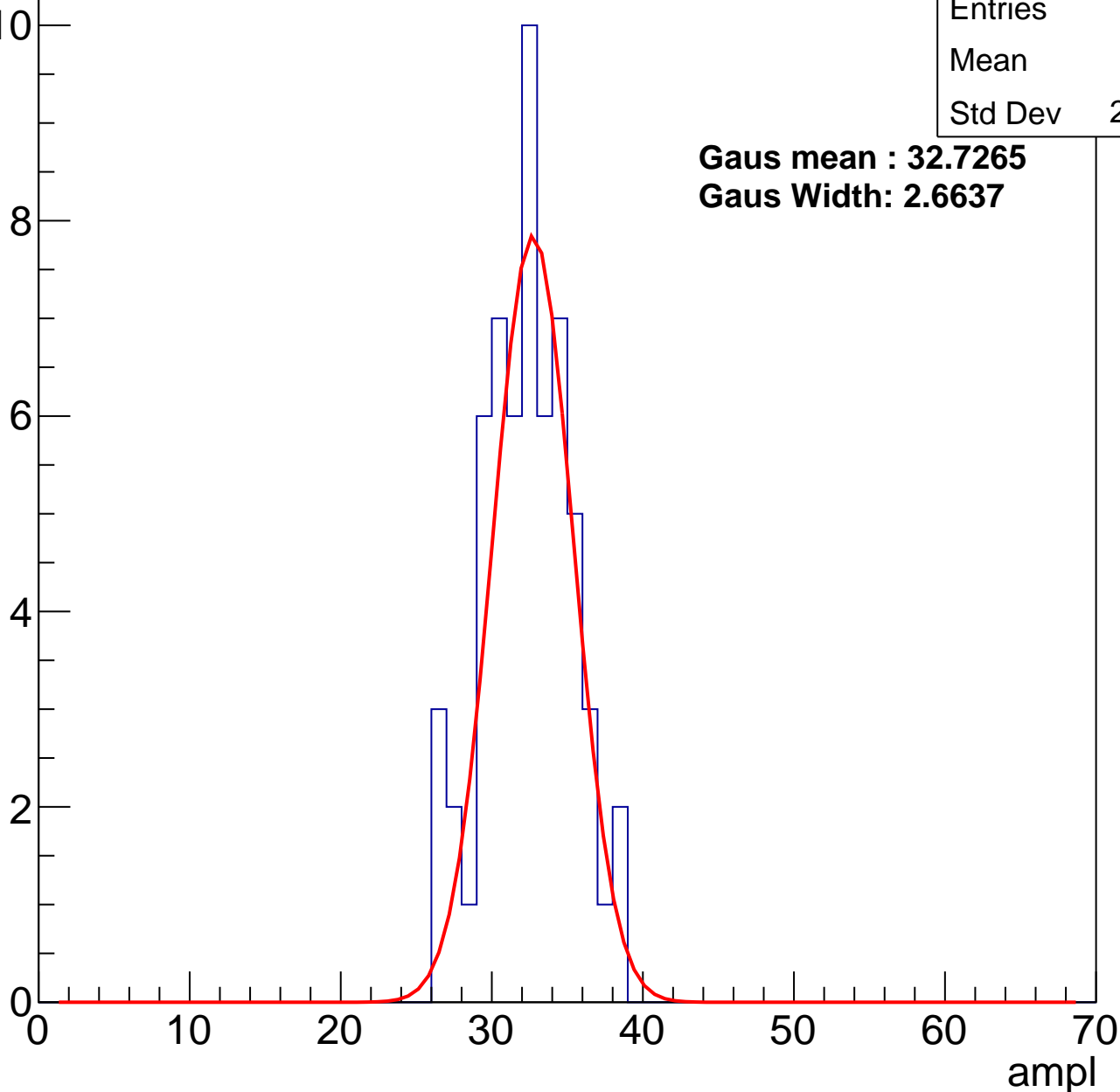
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 31.9 |
| Std Dev | 2.868 |

Gaus mean : 32.7265

Gaus Width: 2.6637



B1L100S, U7-ch43, adc1

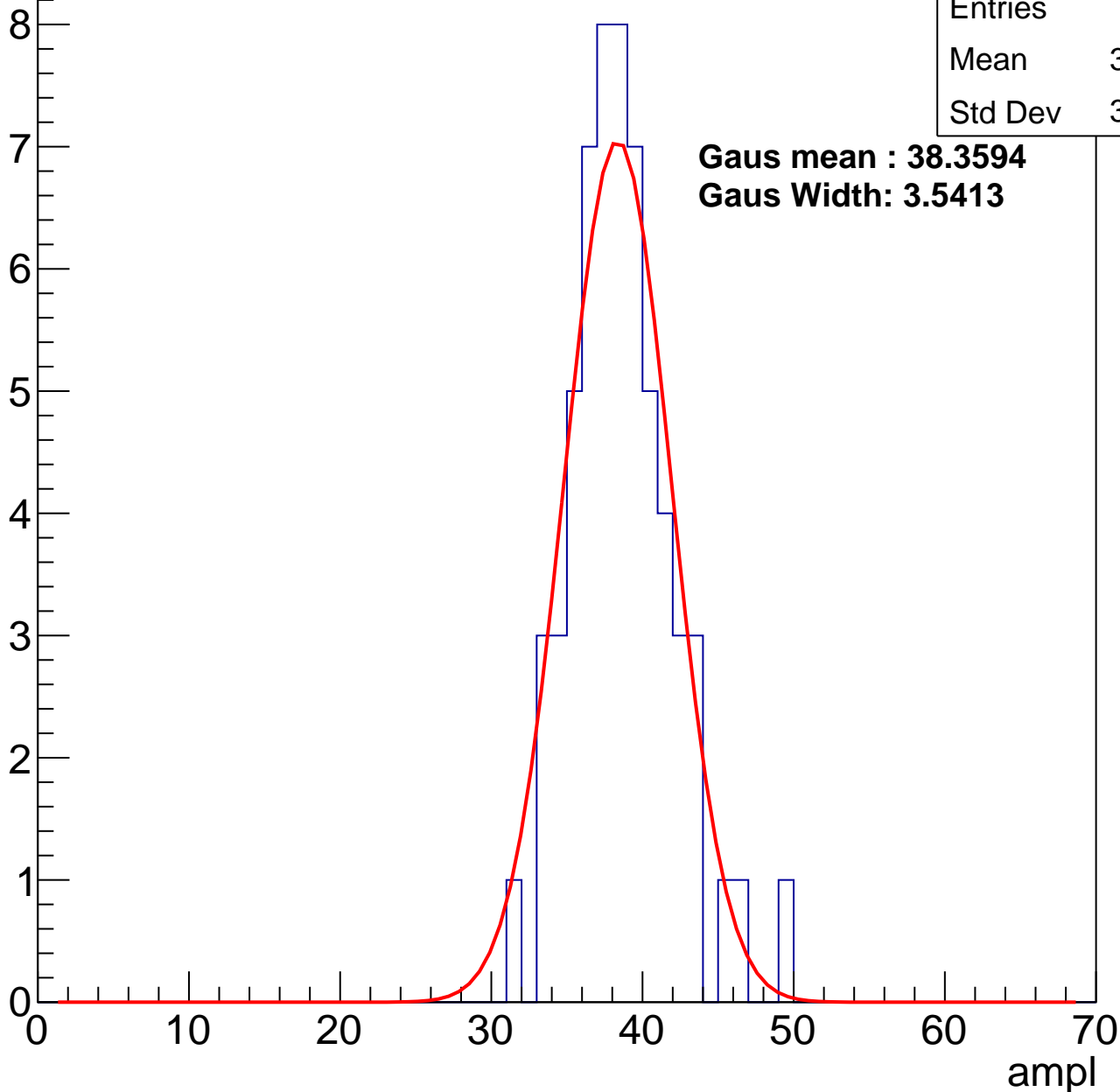
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 38.18 |
| Std Dev | 3.344 |

Gaus mean : 38.3594

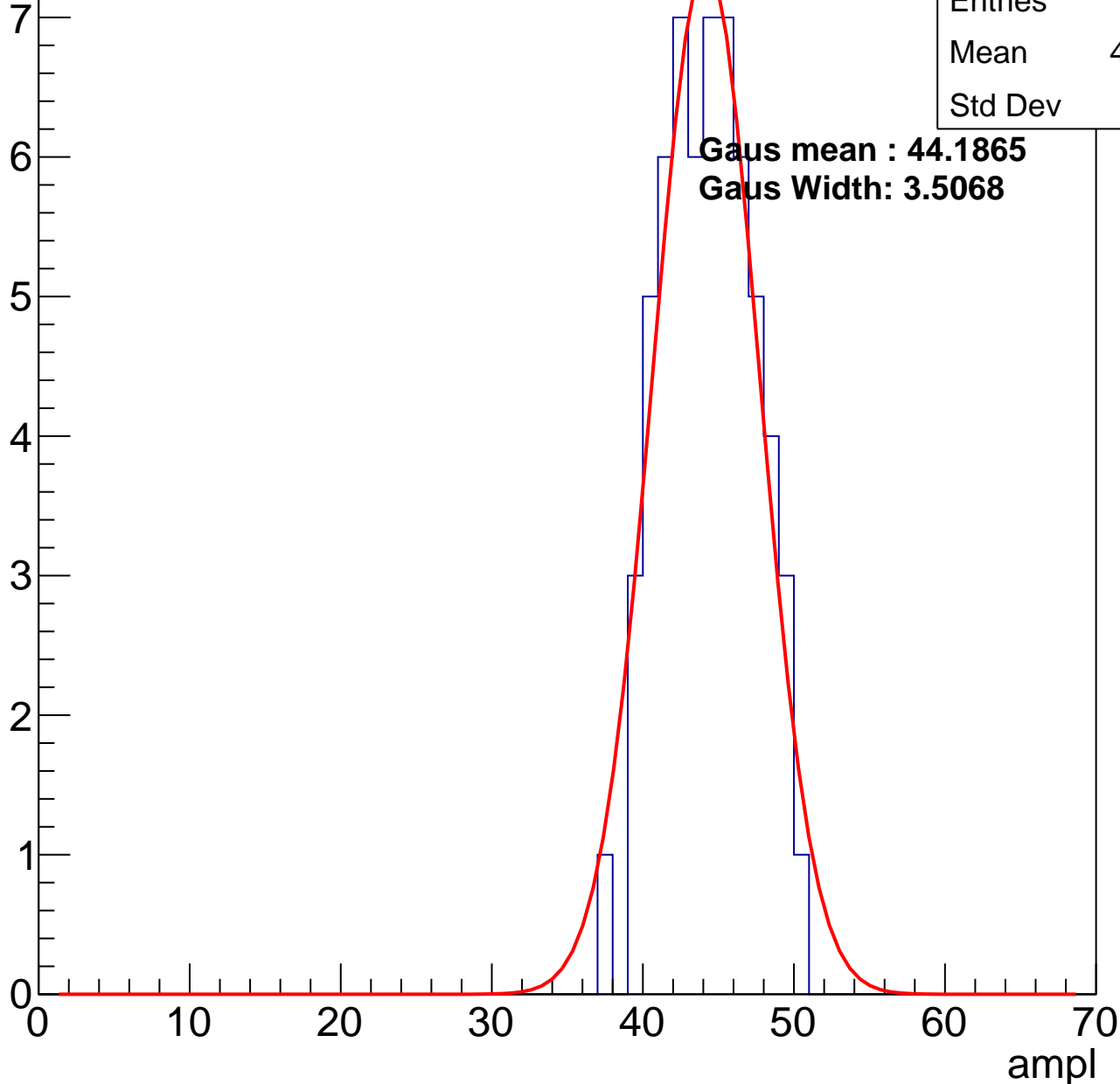
Gaus Width: 3.5413



B1L100S, U7-ch43, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

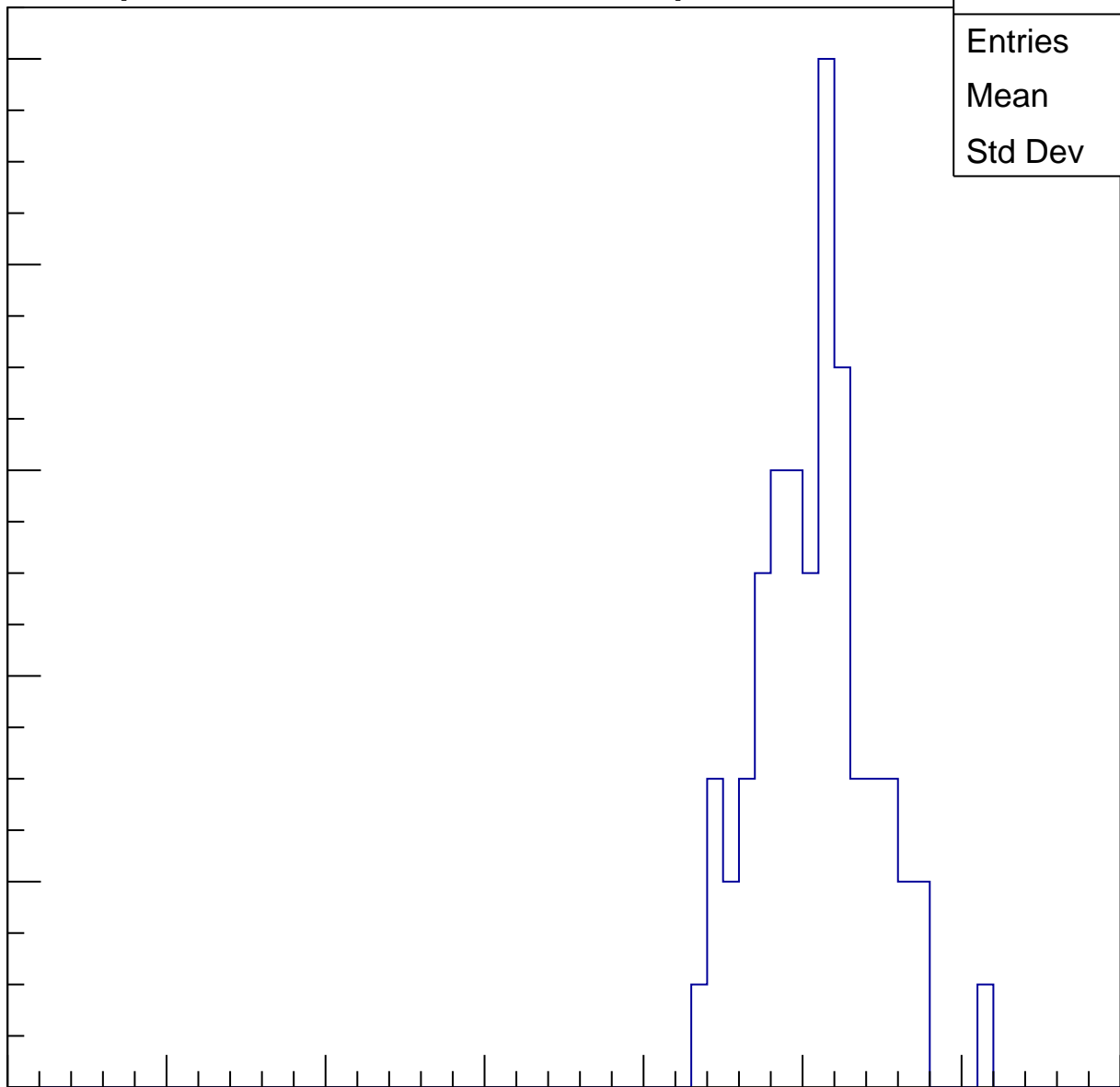
| | |
|---------|-------|
| Entries | 62 |
| Mean | 50.27 |
| Std Dev | 3.602 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

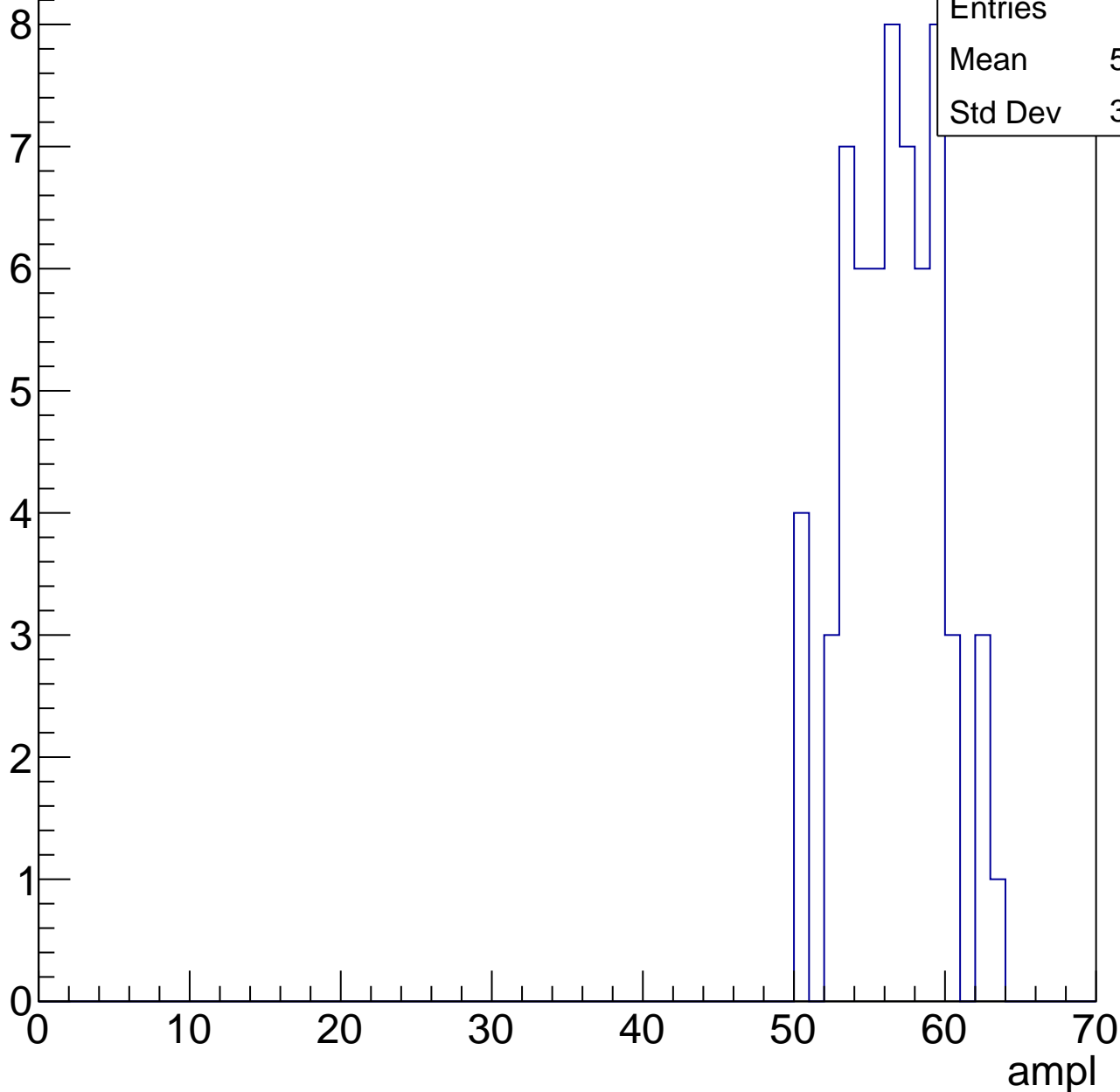


B1L100S, U7-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 56.08 |
| Std Dev | 3.092 |



B1L100S, U7-ch43, adc5

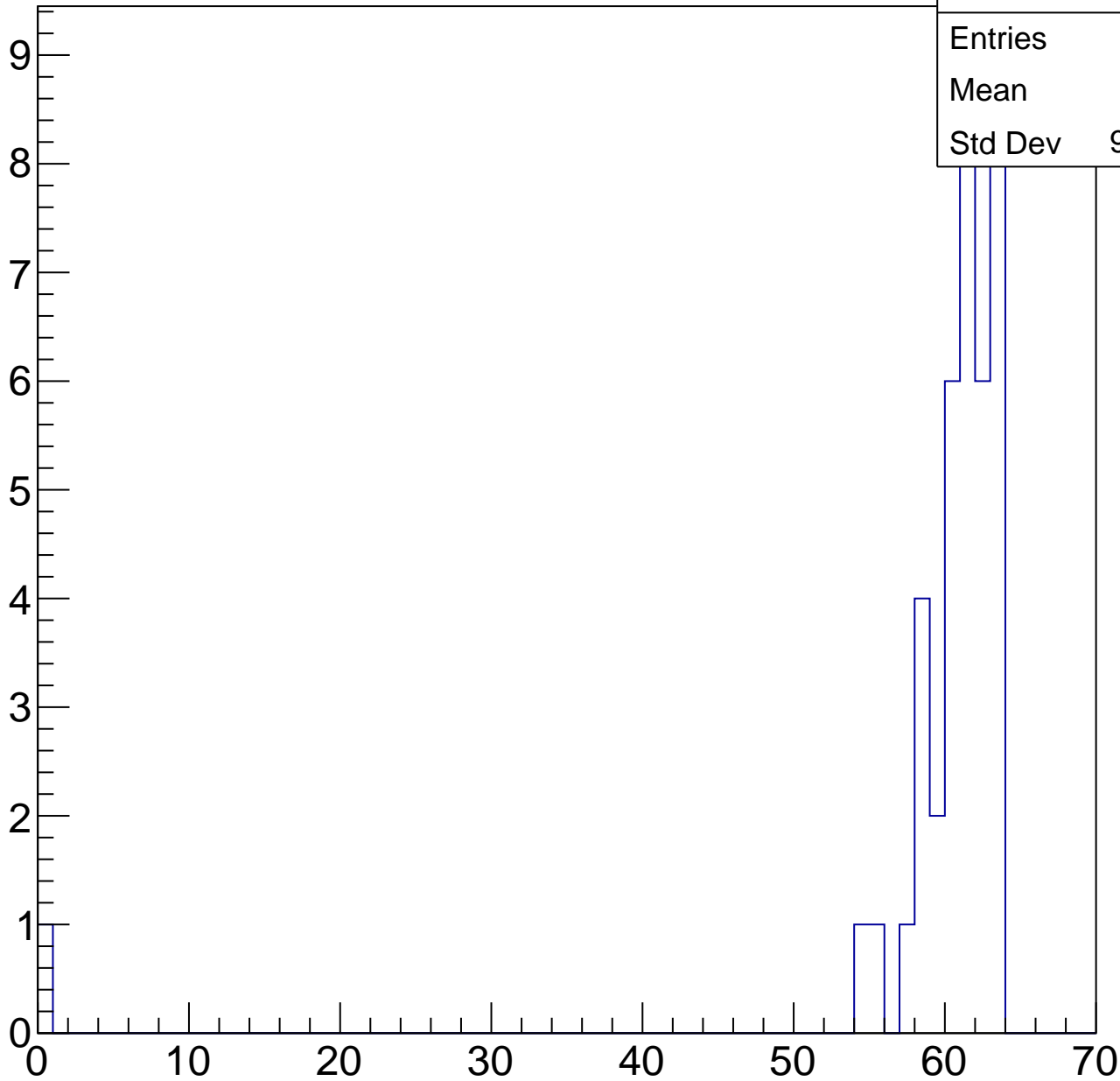
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.1 |
| Std Dev | 9.708 |

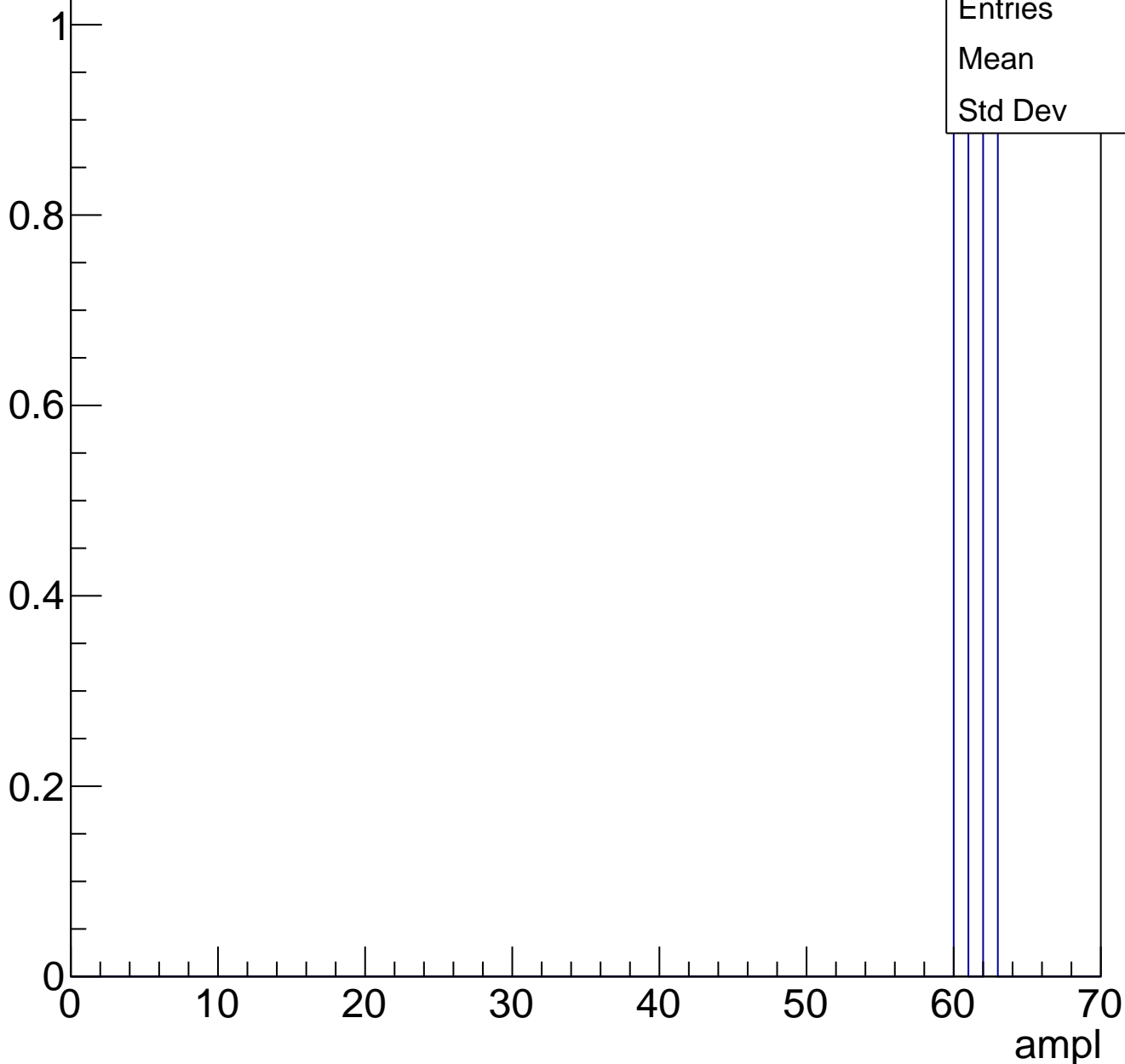
ampl



B1L100S, U7-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch44, adc0

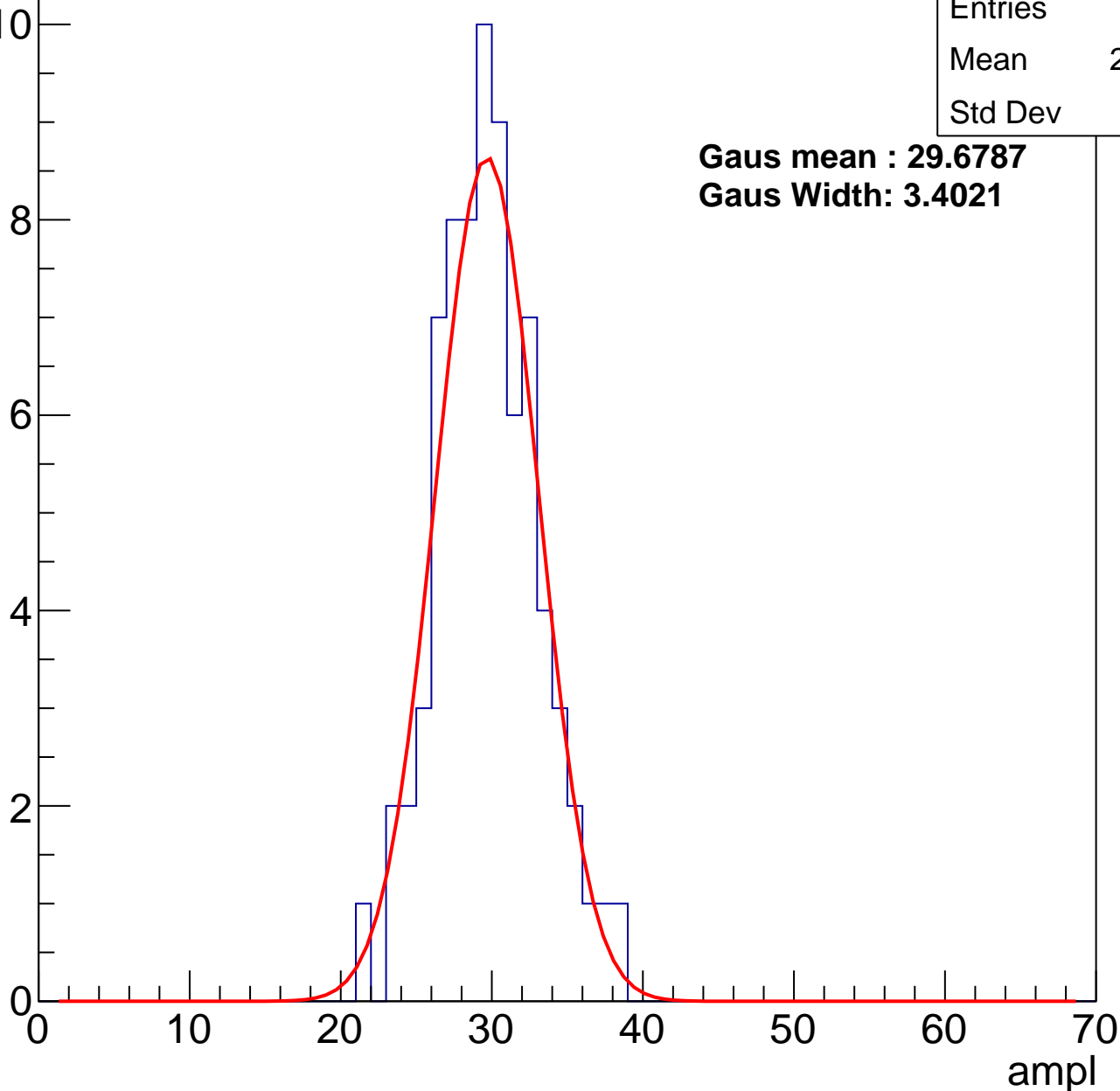
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.29 |
| Std Dev | 3.33 |

Gaus mean : 29.6787

Gaus Width: 3.4021



B1L100S, U7-ch44, adc1

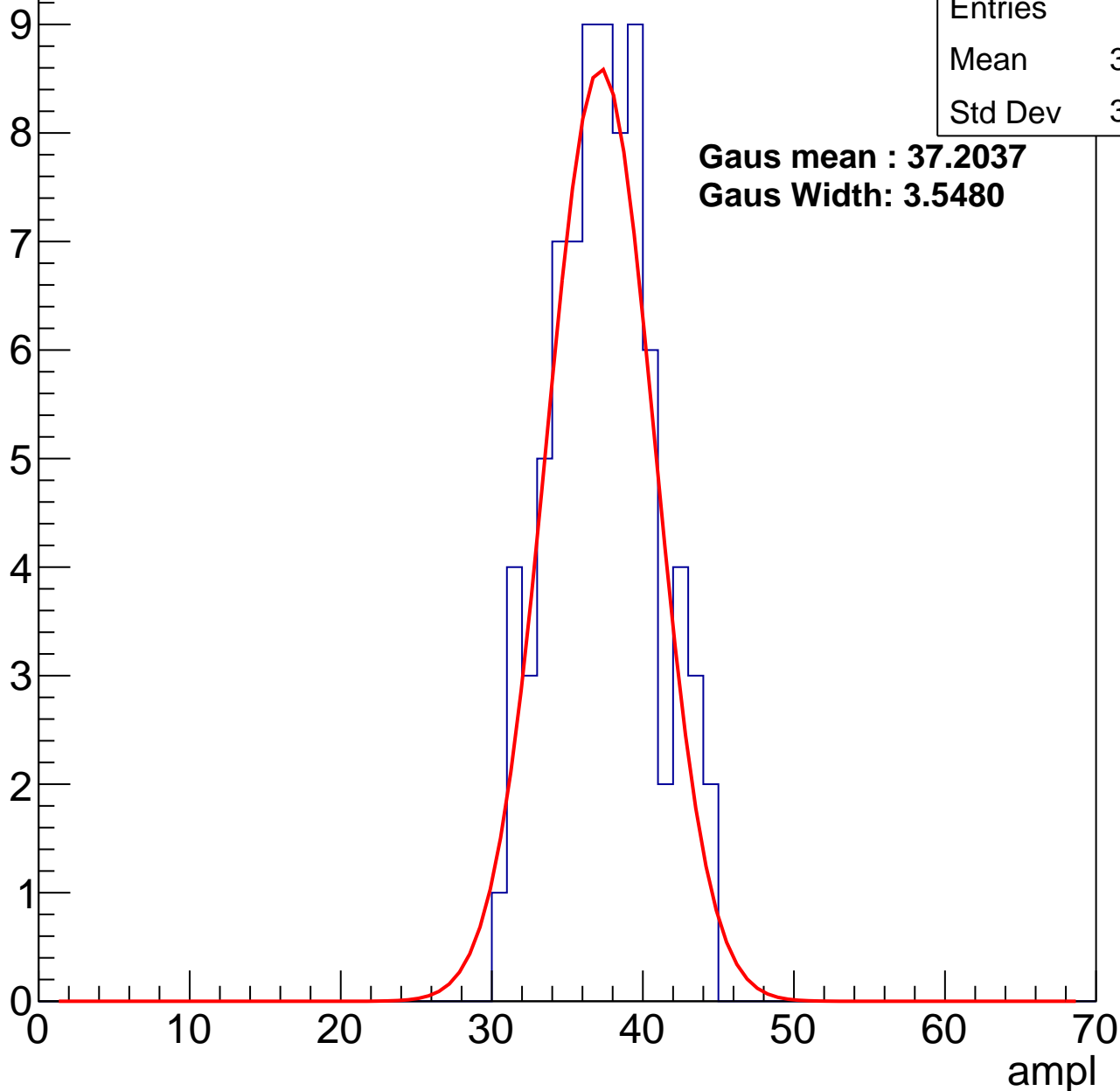
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 36.92 |
| Std Dev | 3.344 |

Gaus mean : 37.2037

Gaus Width: 3.5480



B1L100S, U7-ch44, adc2

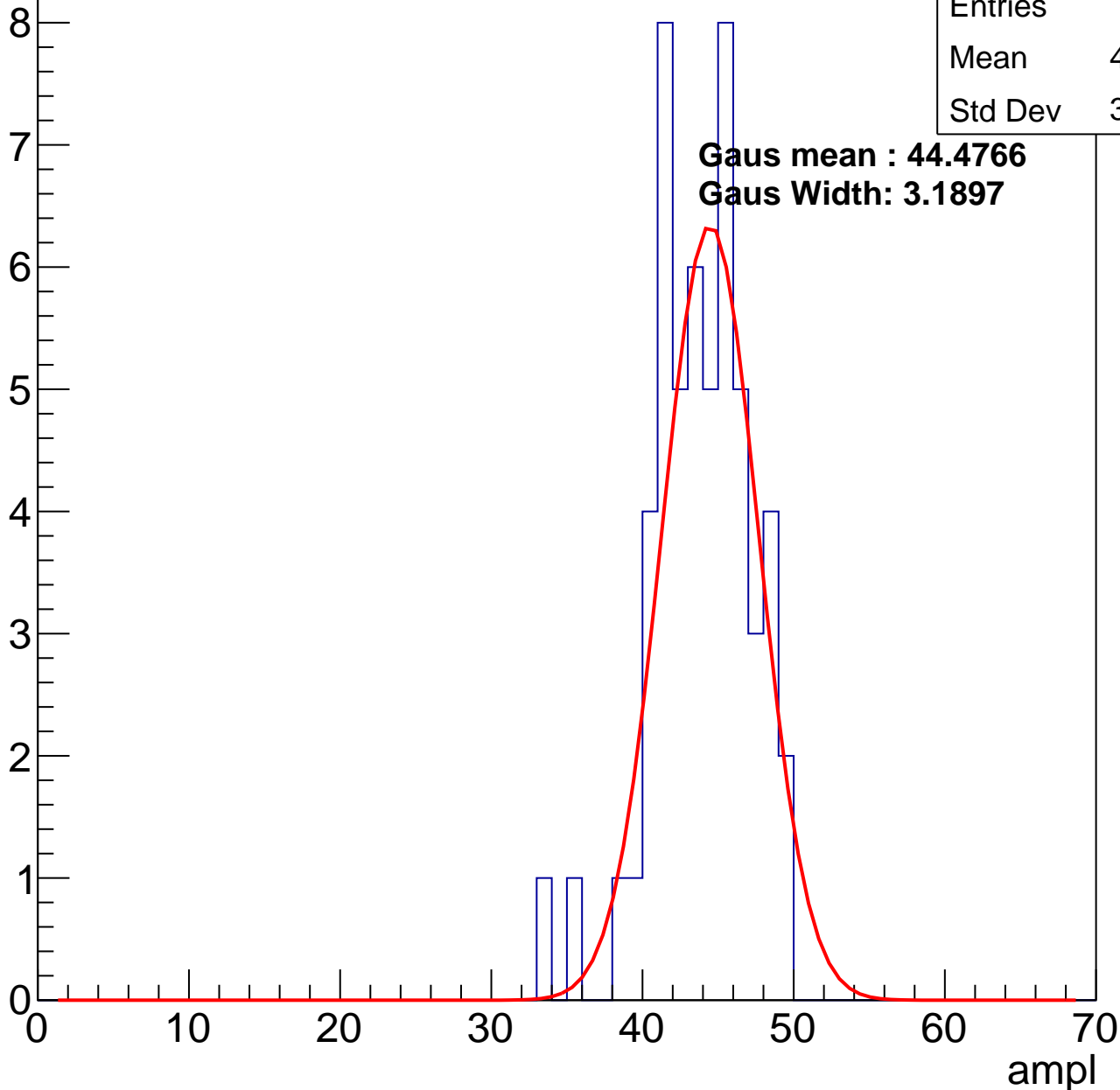
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.37 |
| Std Dev | 3.256 |

Gaus mean : 44.4766

Gaus Width: 3.1897

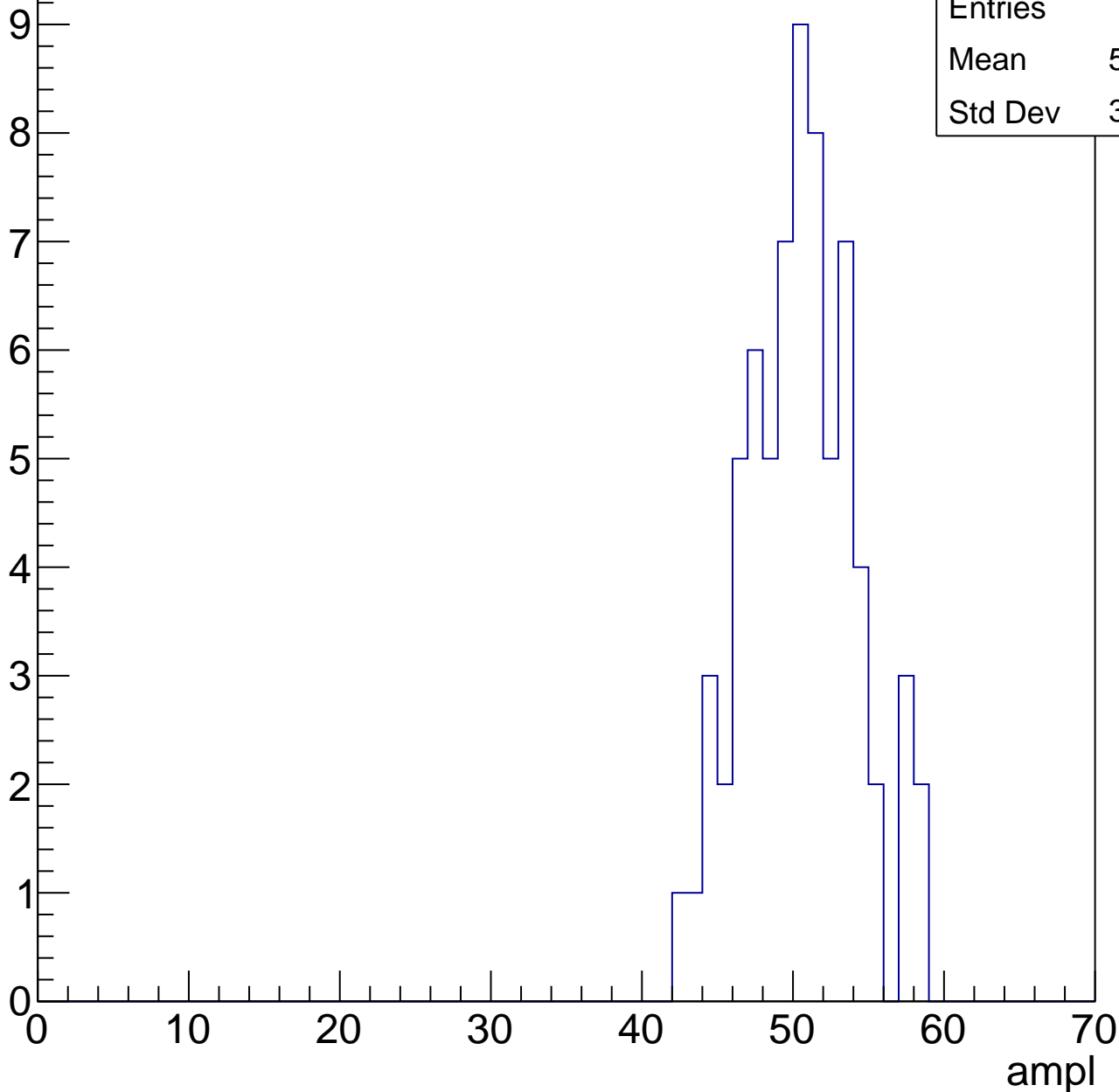


B1L100S, U7-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 50.06 |
| Std Dev | 3.609 |

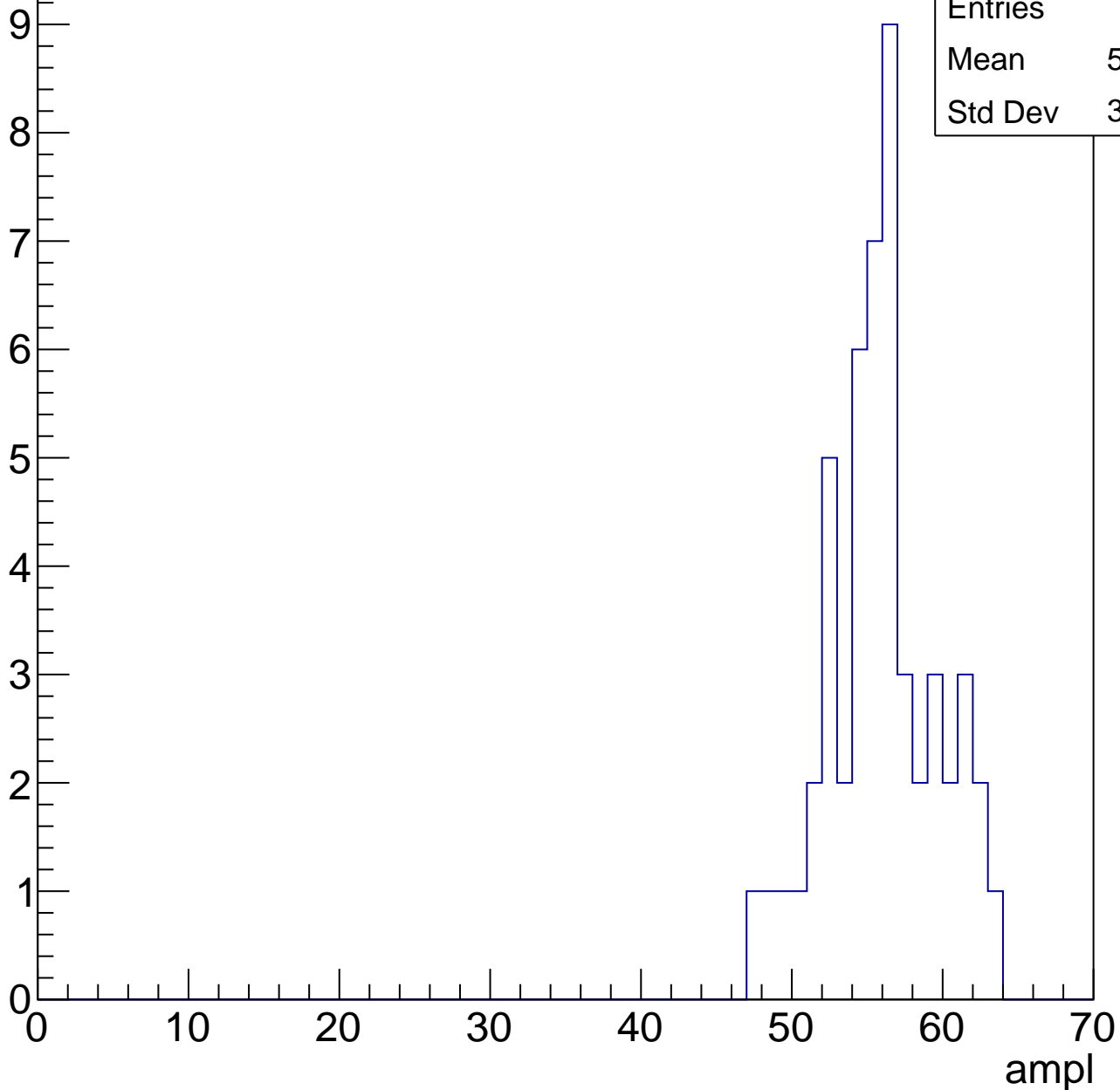


B1L100S, U7-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 55.47 |
| Std Dev | 3.599 |



B1L100S, U7-ch44, adc5

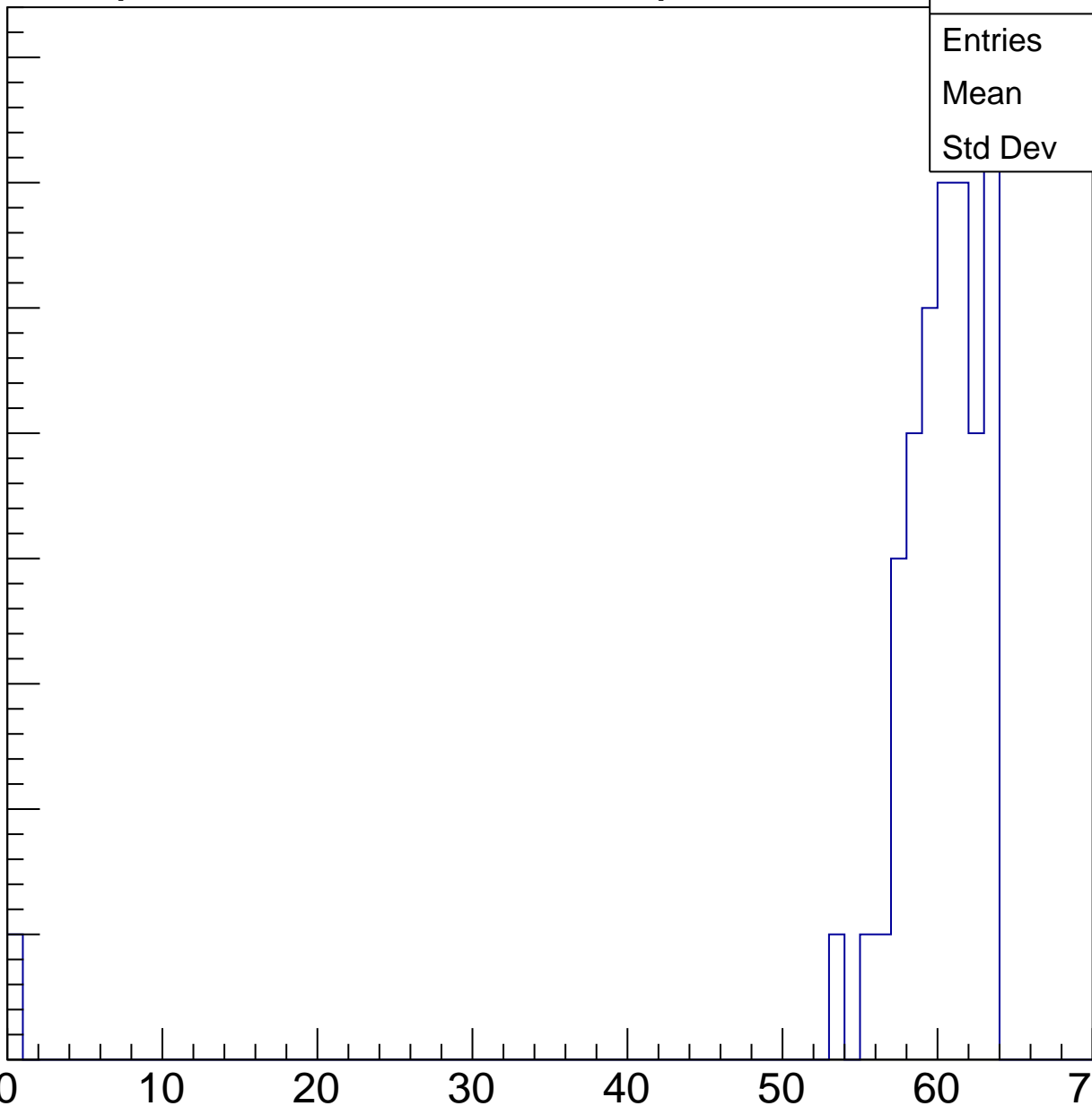
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.63 |
| Std Dev | 9.047 |

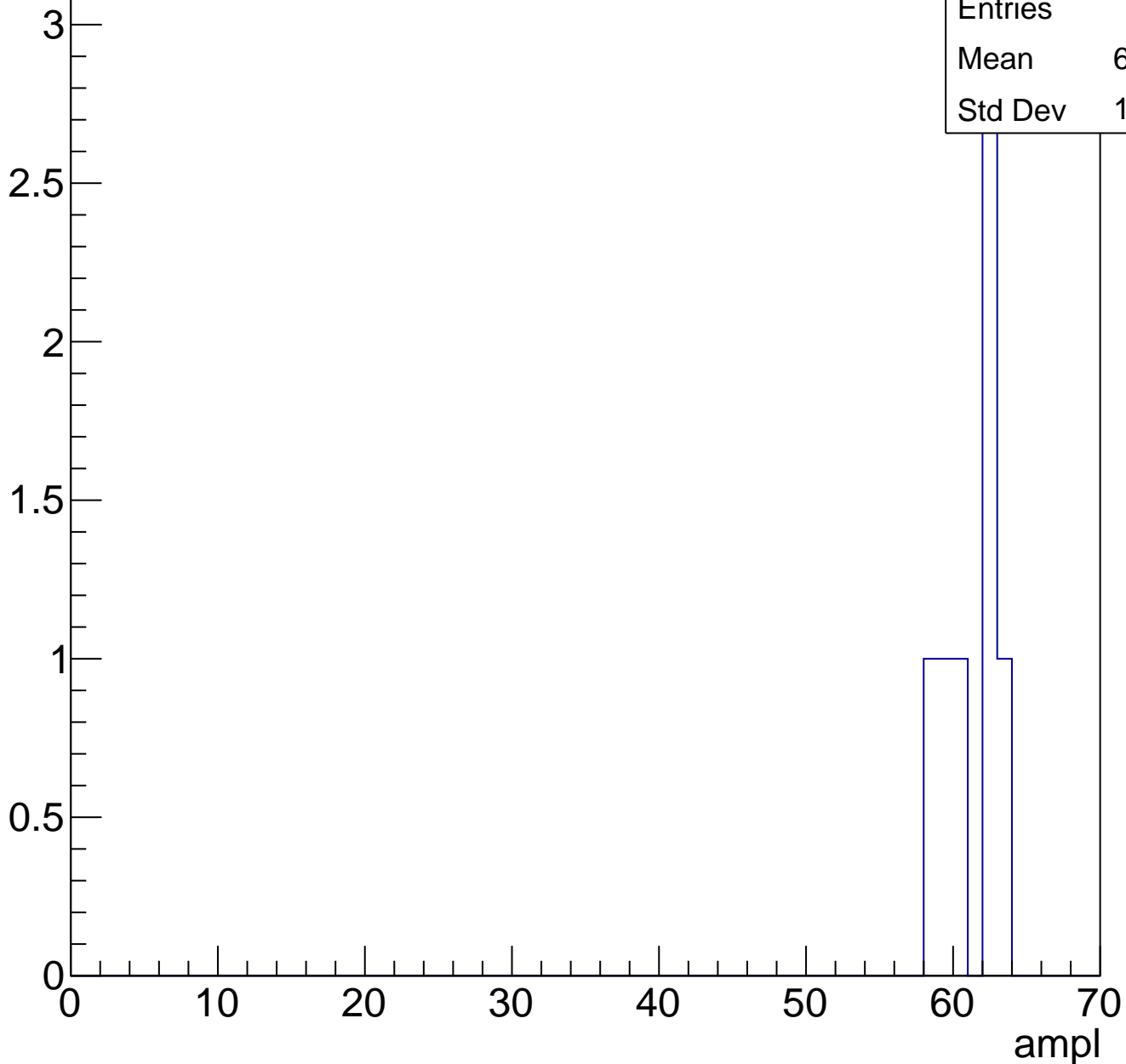
ampl



B1L100S, U7-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch45, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.39 |
| Std Dev | 3.434 |

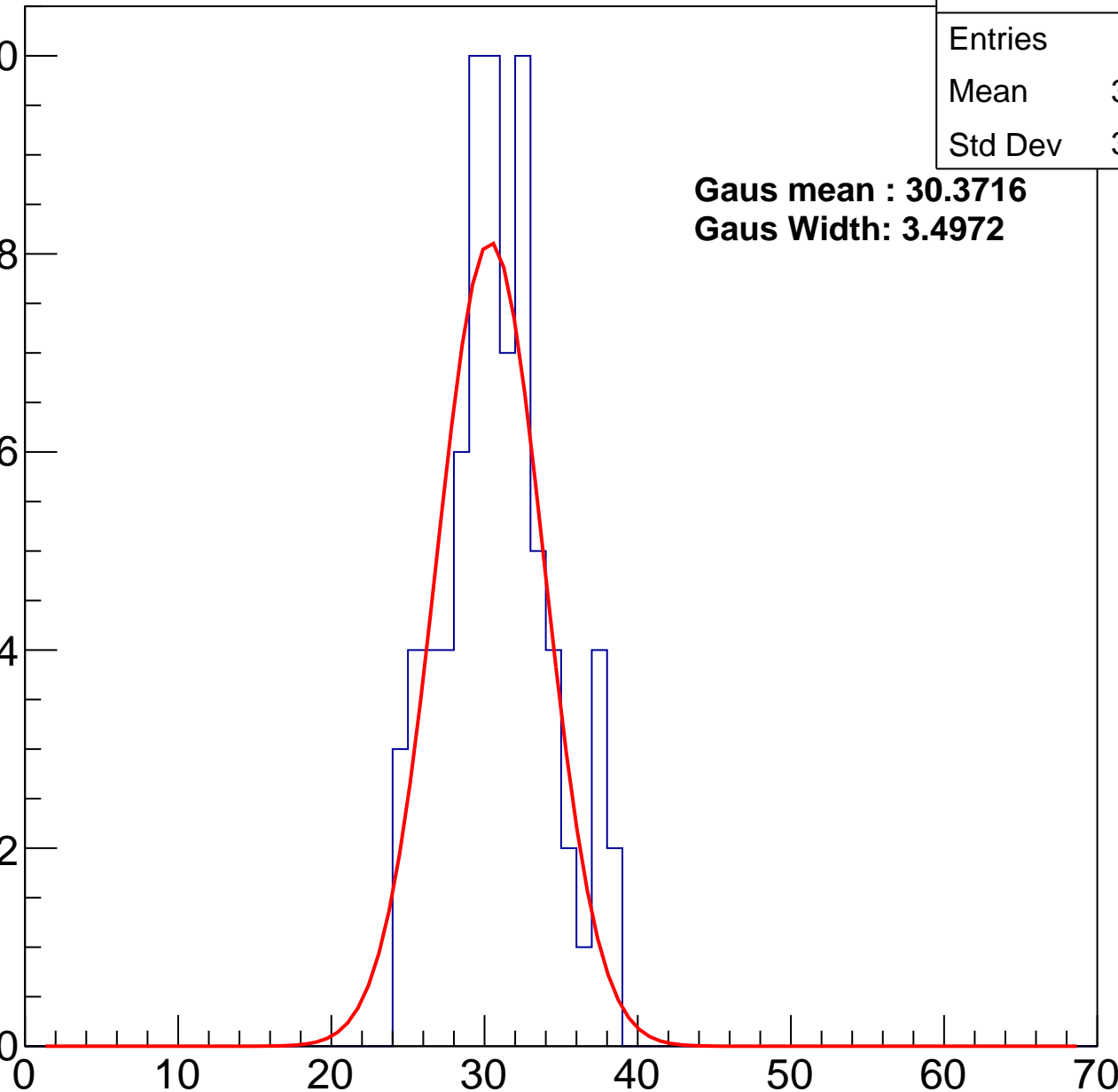
Gaus mean : 30.3716

Gaus Width: 3.4972

Entry

10
8
6
4
2
0

ampl



B1L100S, U7-ch45, adc1

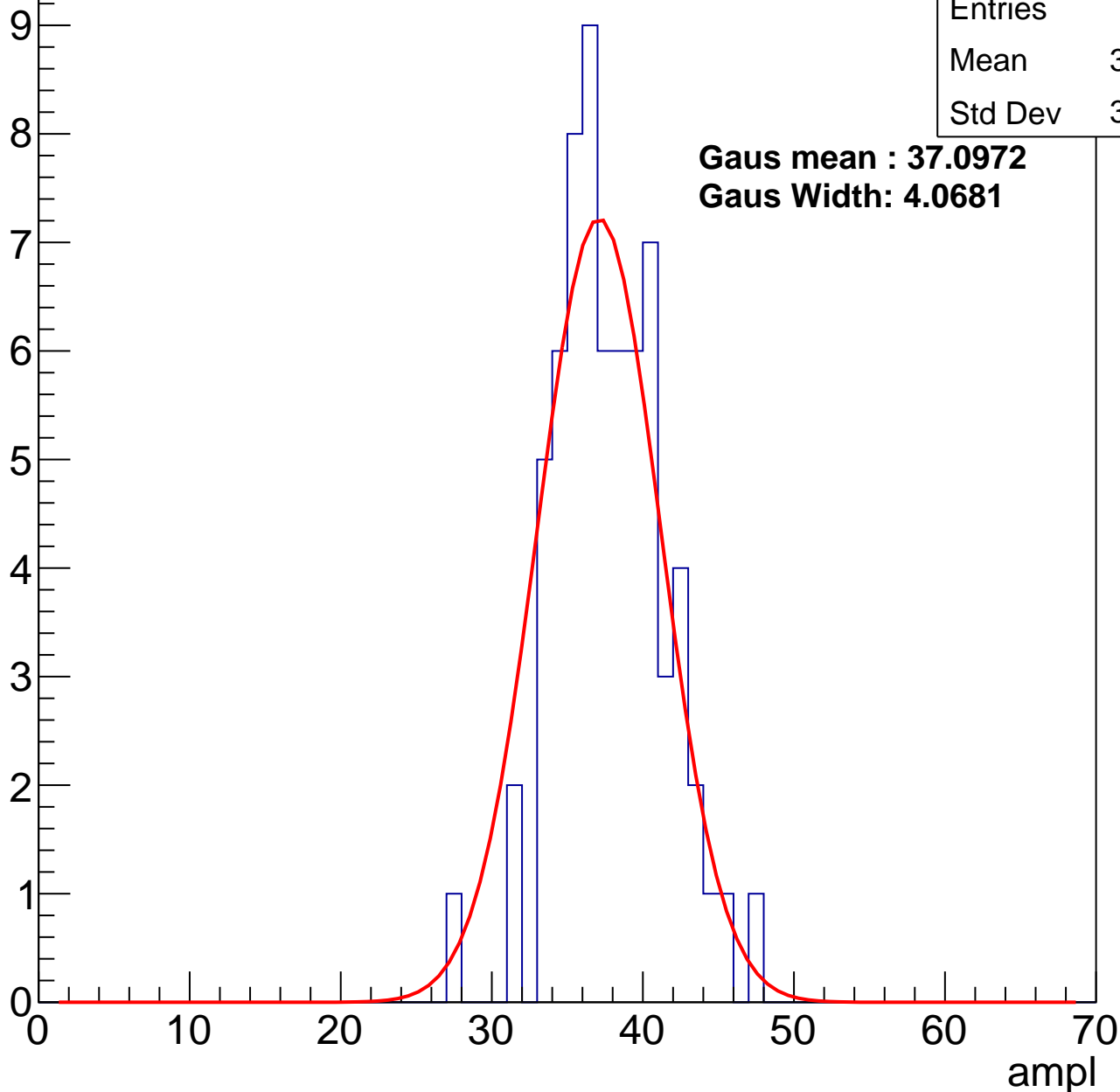
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 37.34 |
| Std Dev | 3.567 |

Gaus mean : 37.0972

Gaus Width: 4.0681



B1L100S, U7-ch45, adc2

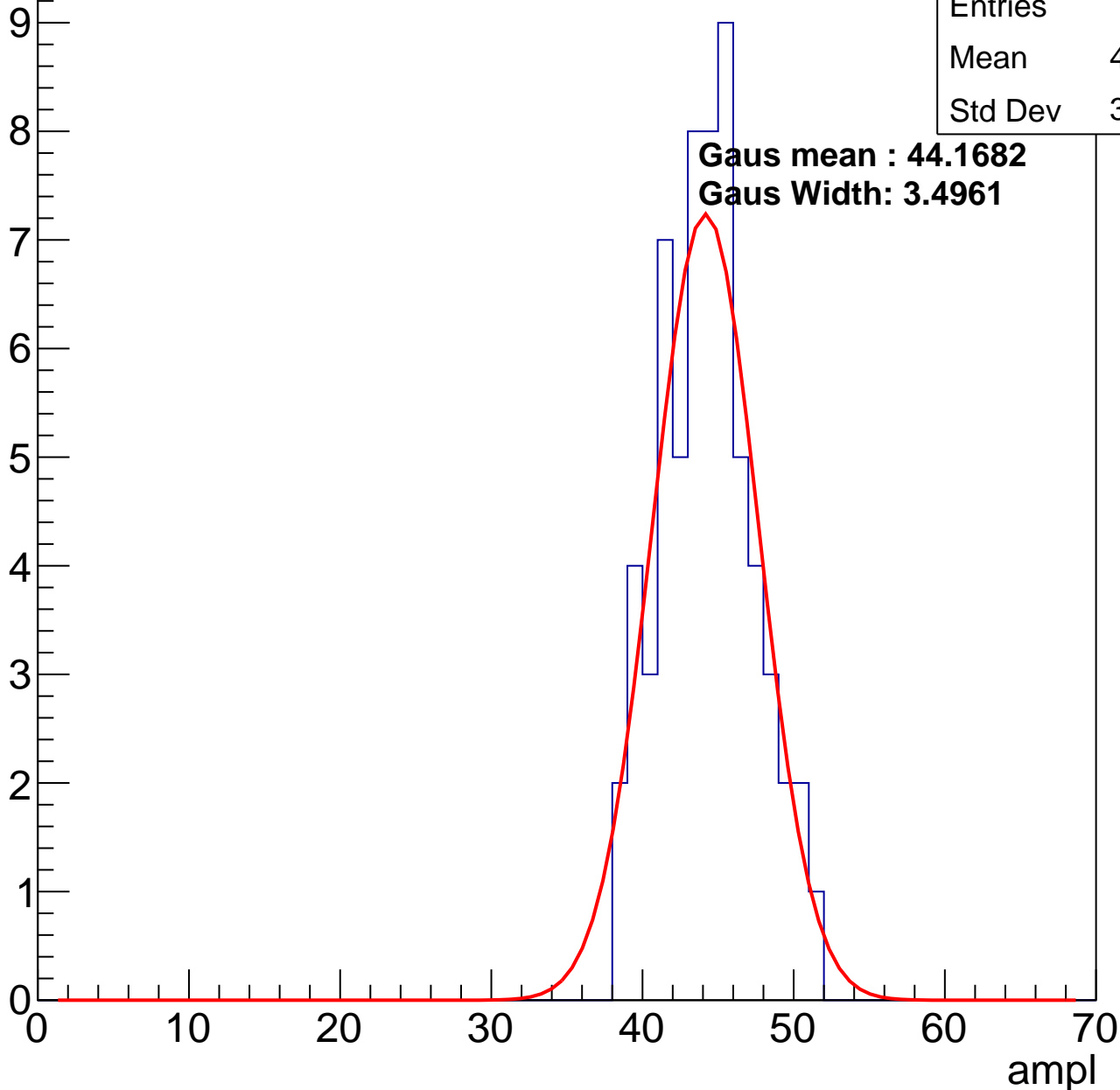
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.83 |
| Std Dev | 3.068 |

Gaus mean : 44.1682

Gaus Width: 3.4961

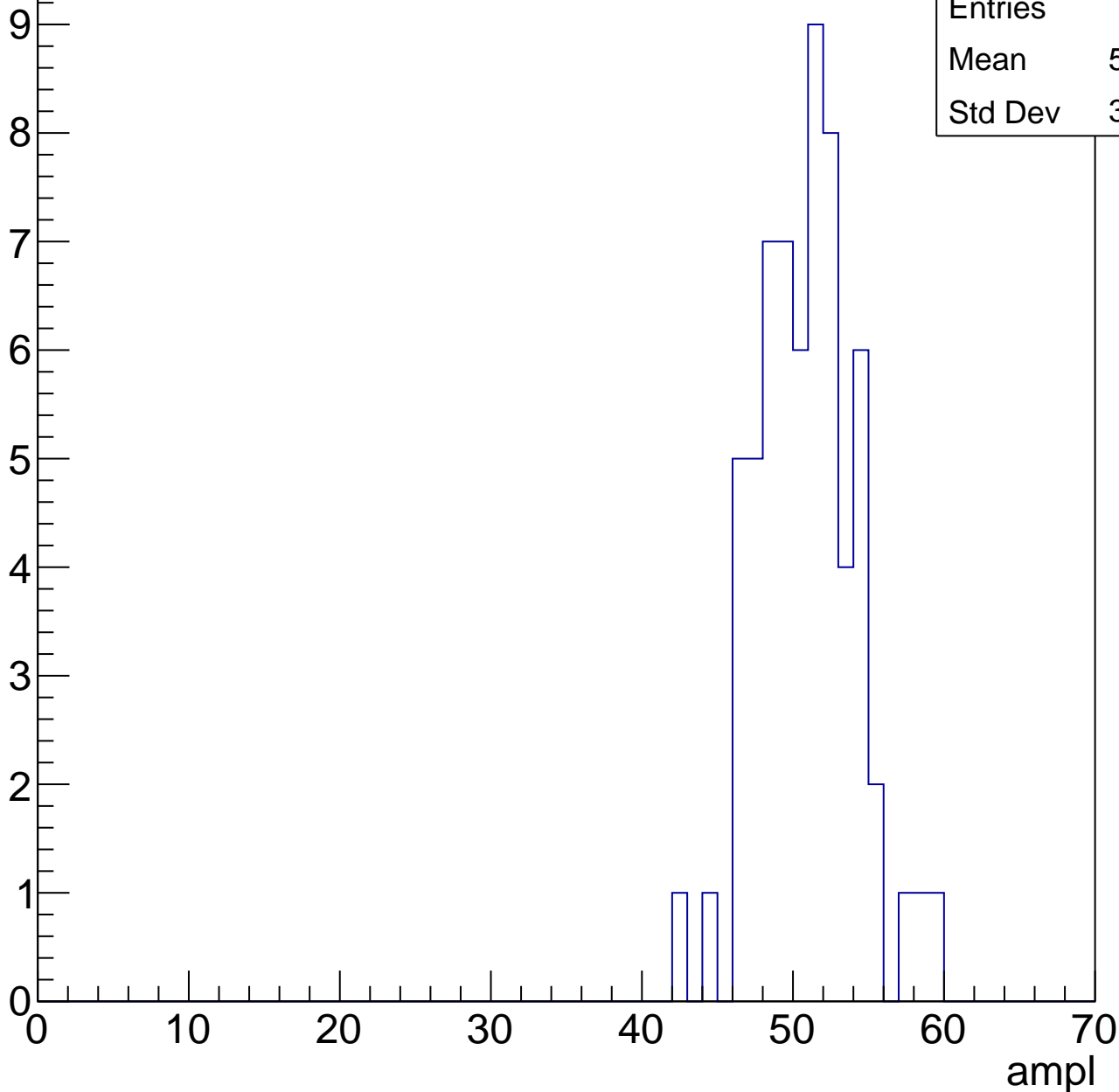


B1L100S, U7-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.39 |
| Std Dev | 3.229 |



B1L100S, U7-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

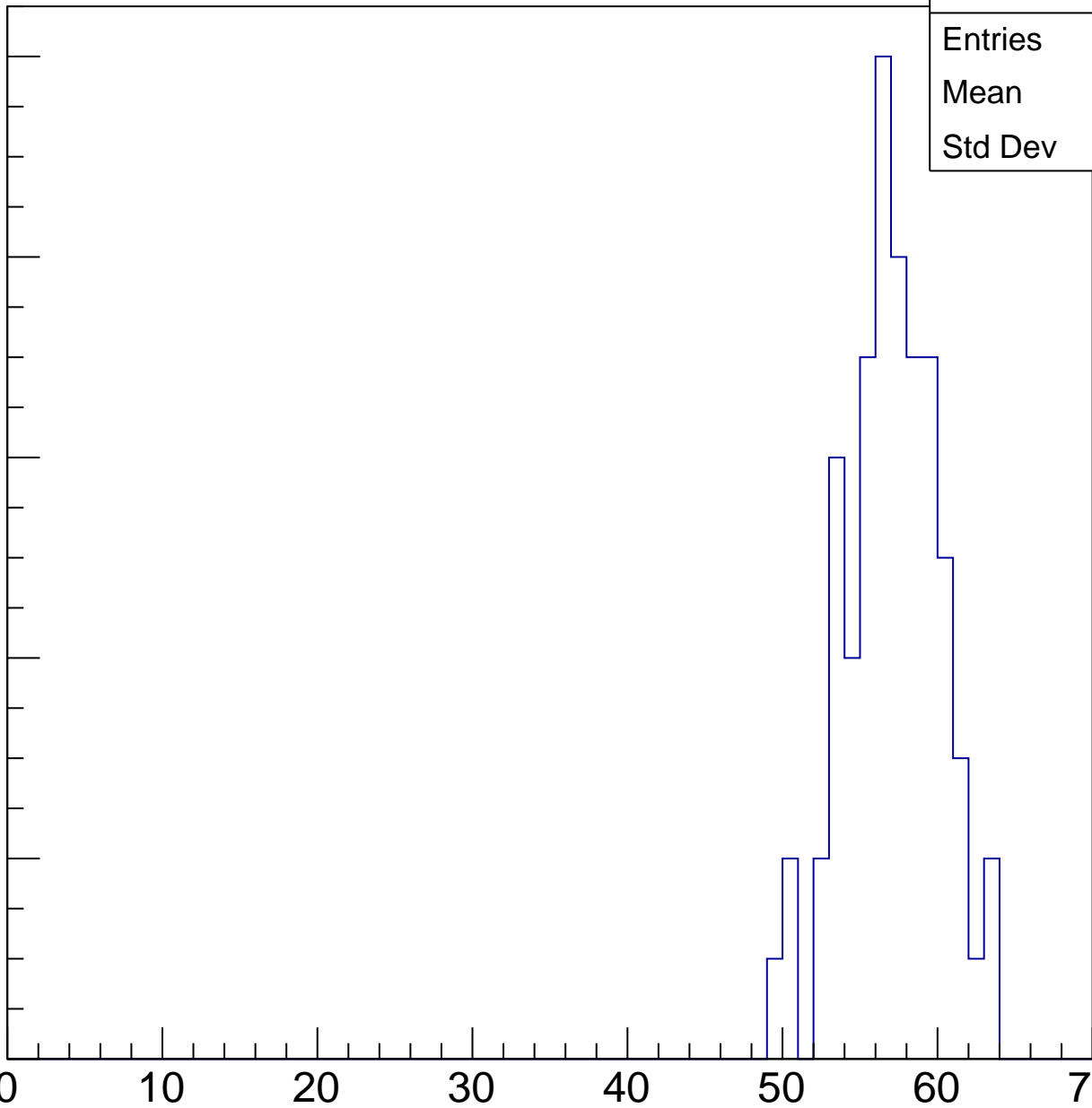
| | |
|---------|-------|
| Entries | 65 |
| Mean | 56.58 |
| Std Dev | 3.028 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

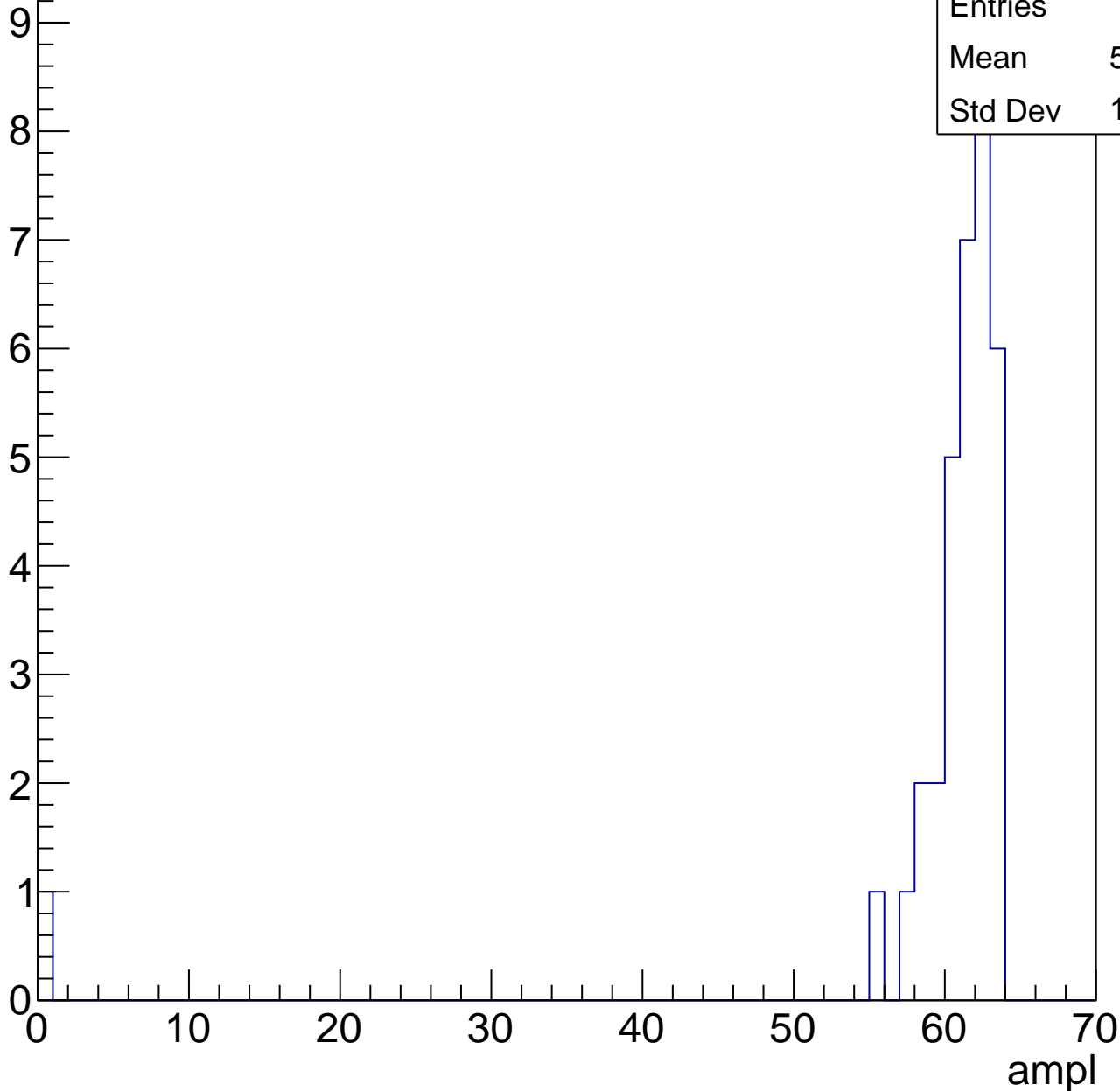


B1L100S, U7-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 59.09 |
| Std Dev | 10.45 |



B1L100S, U7-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

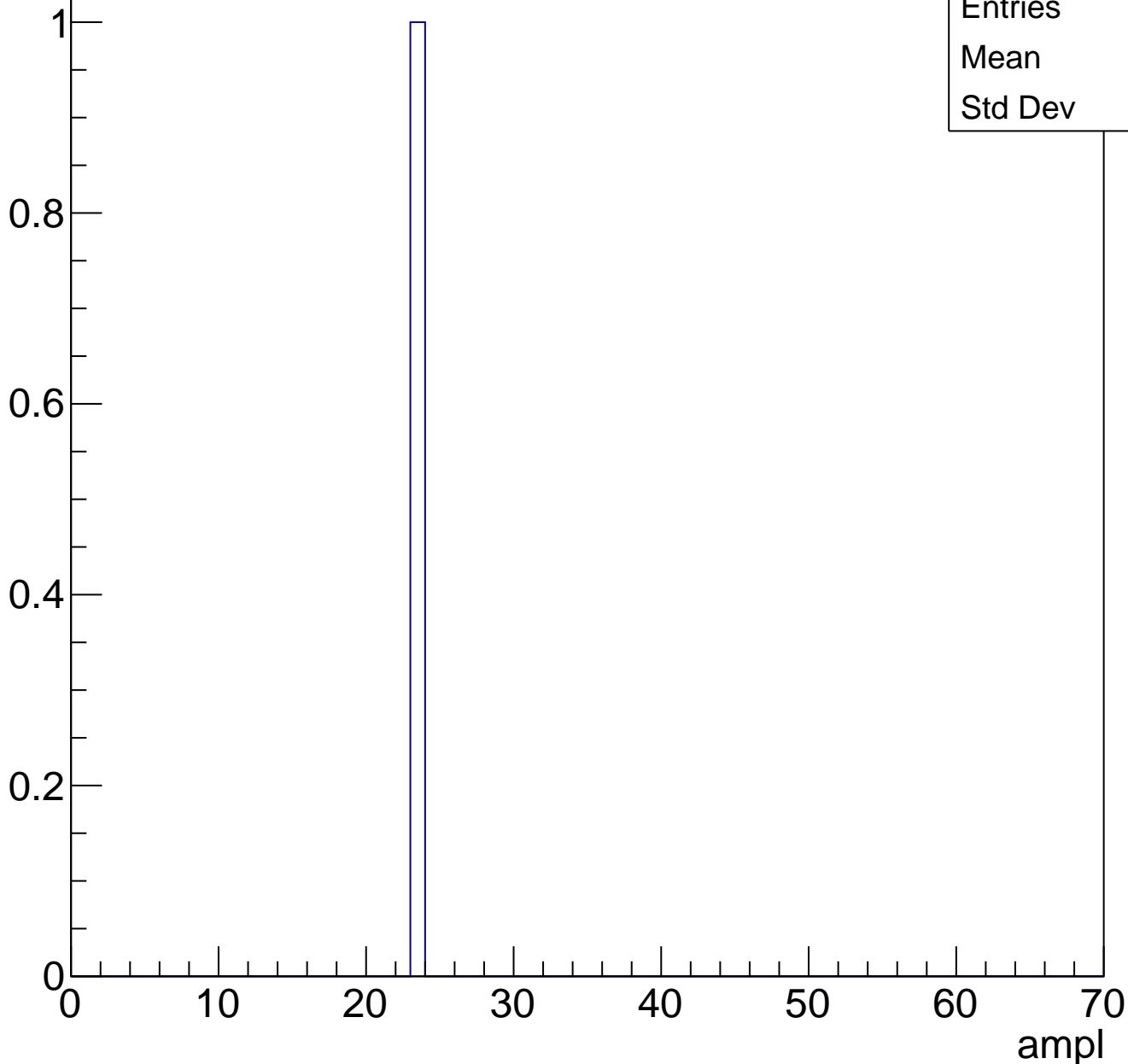
Entry



B1L100S, U7-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch46, adc0

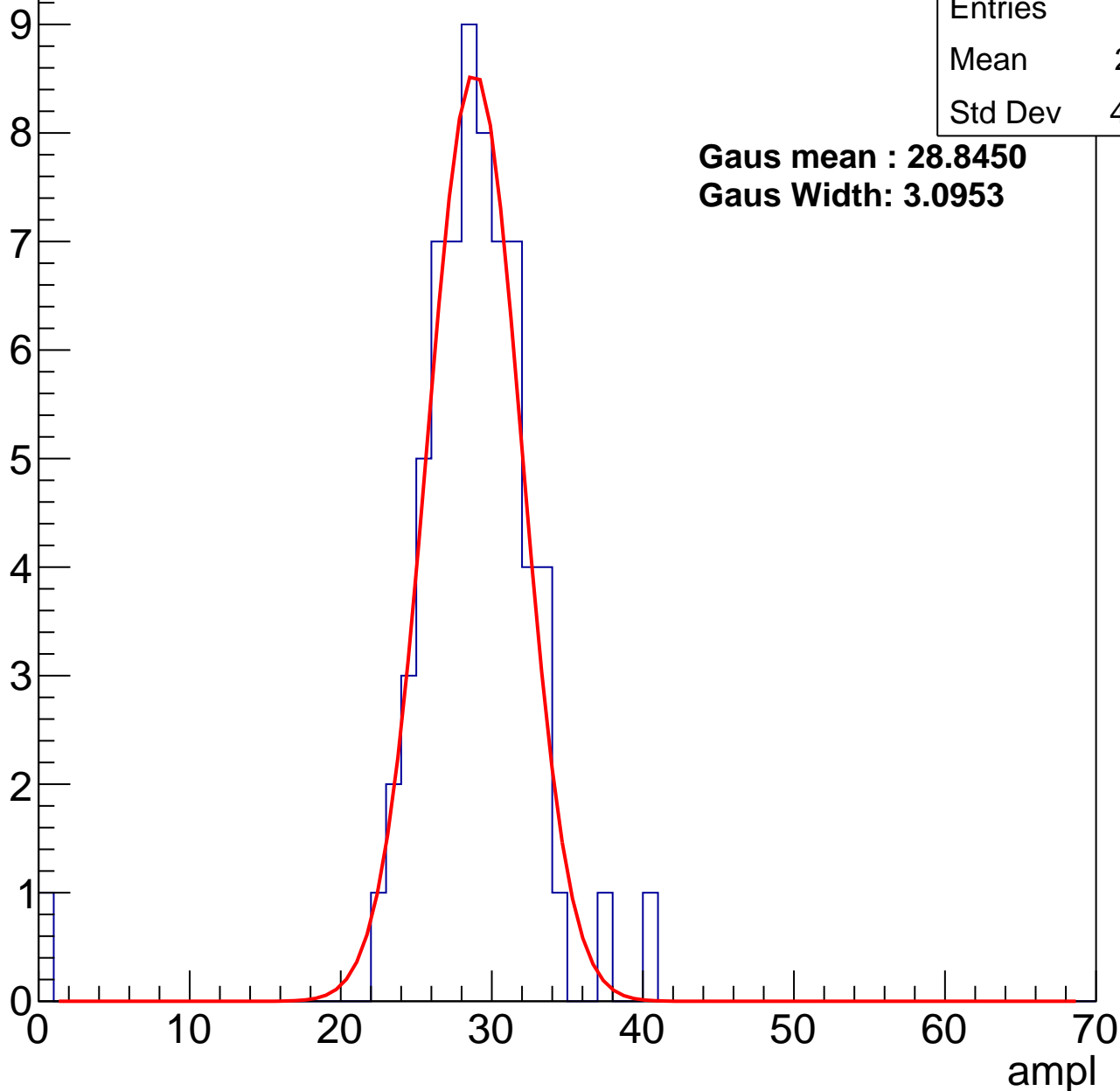
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.21 |
| Std Dev | 4.727 |

Gaus mean : 28.8450

Gaus Width: 3.0953



B1L100S, U7-ch46, adc1

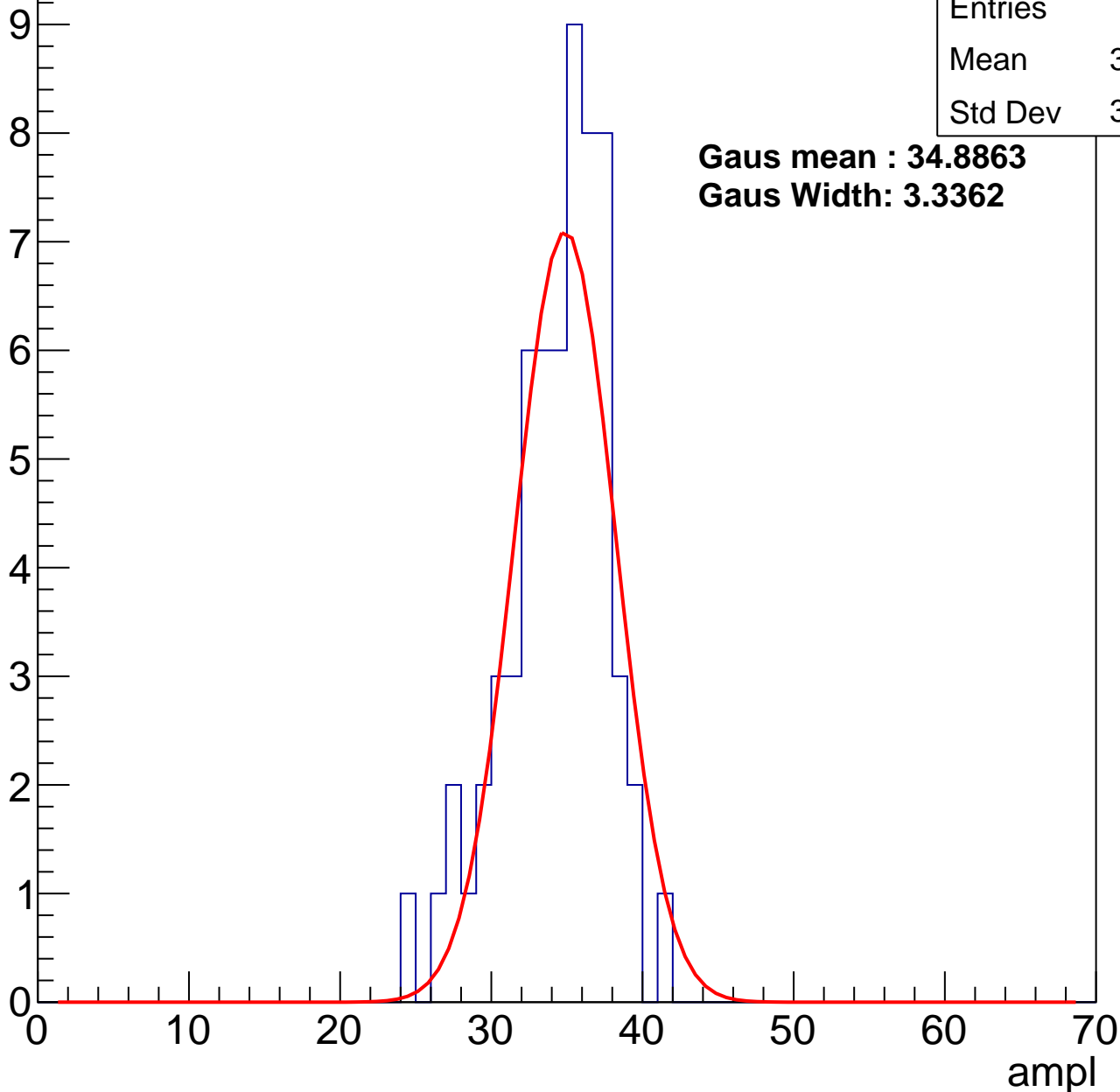
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 33.85 |
| Std Dev | 3.388 |

Gaus mean : 34.8863

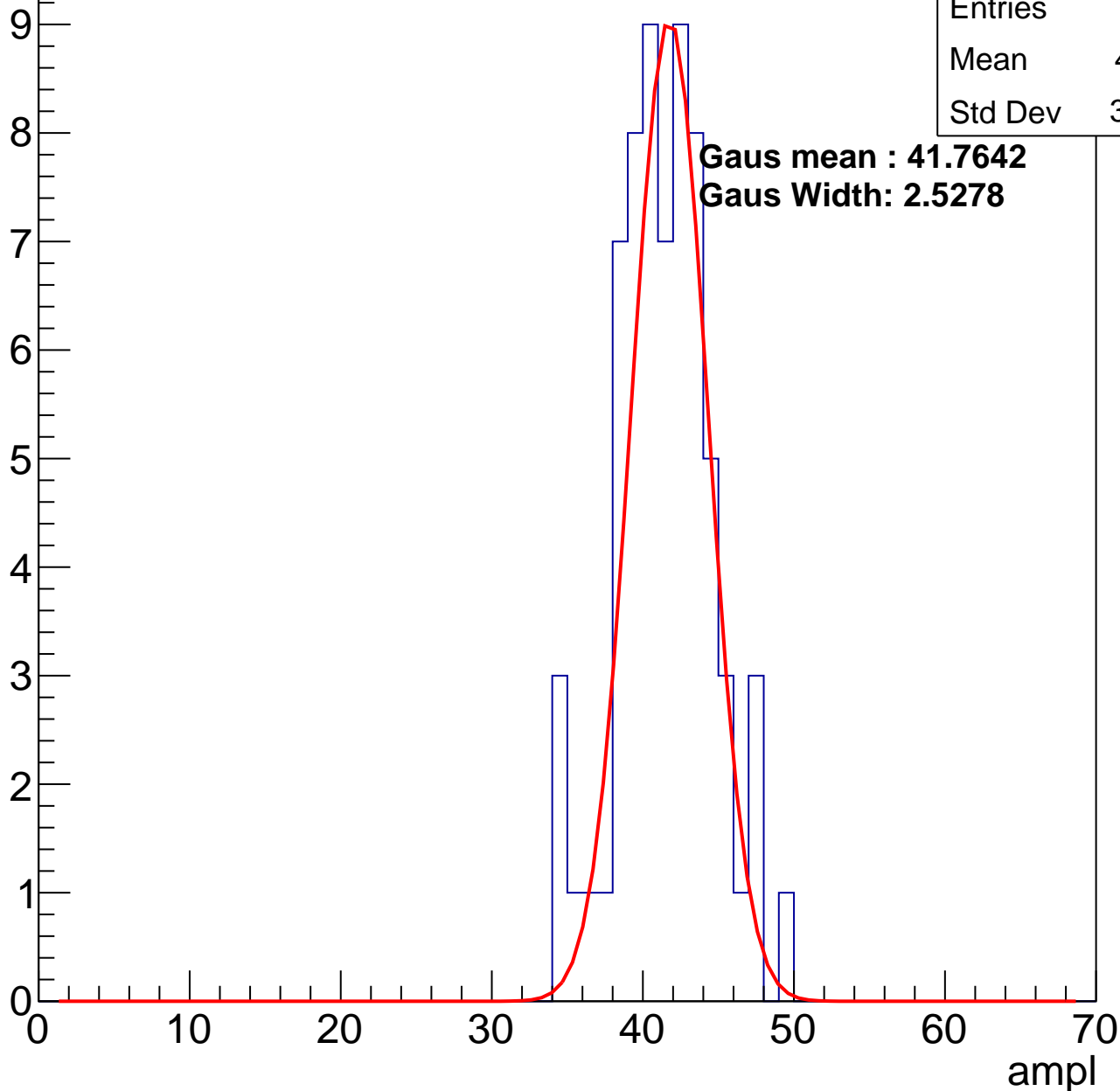
Gaus Width: 3.3362



B1L100S, U7-ch46, adc2

calib_packv5_042523_0143.root, FC#4, port A2

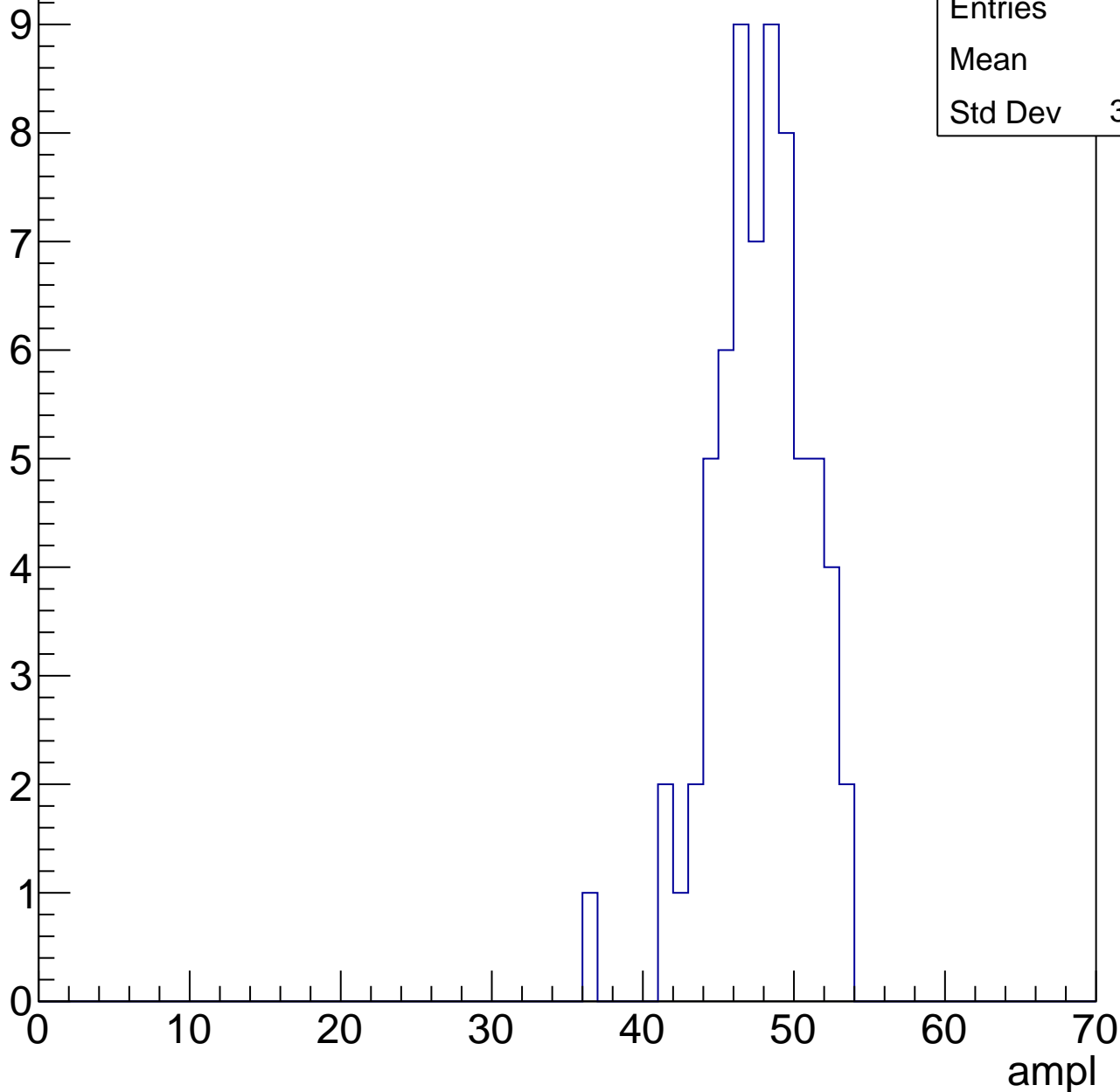
Entry



B1L100S, U7-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

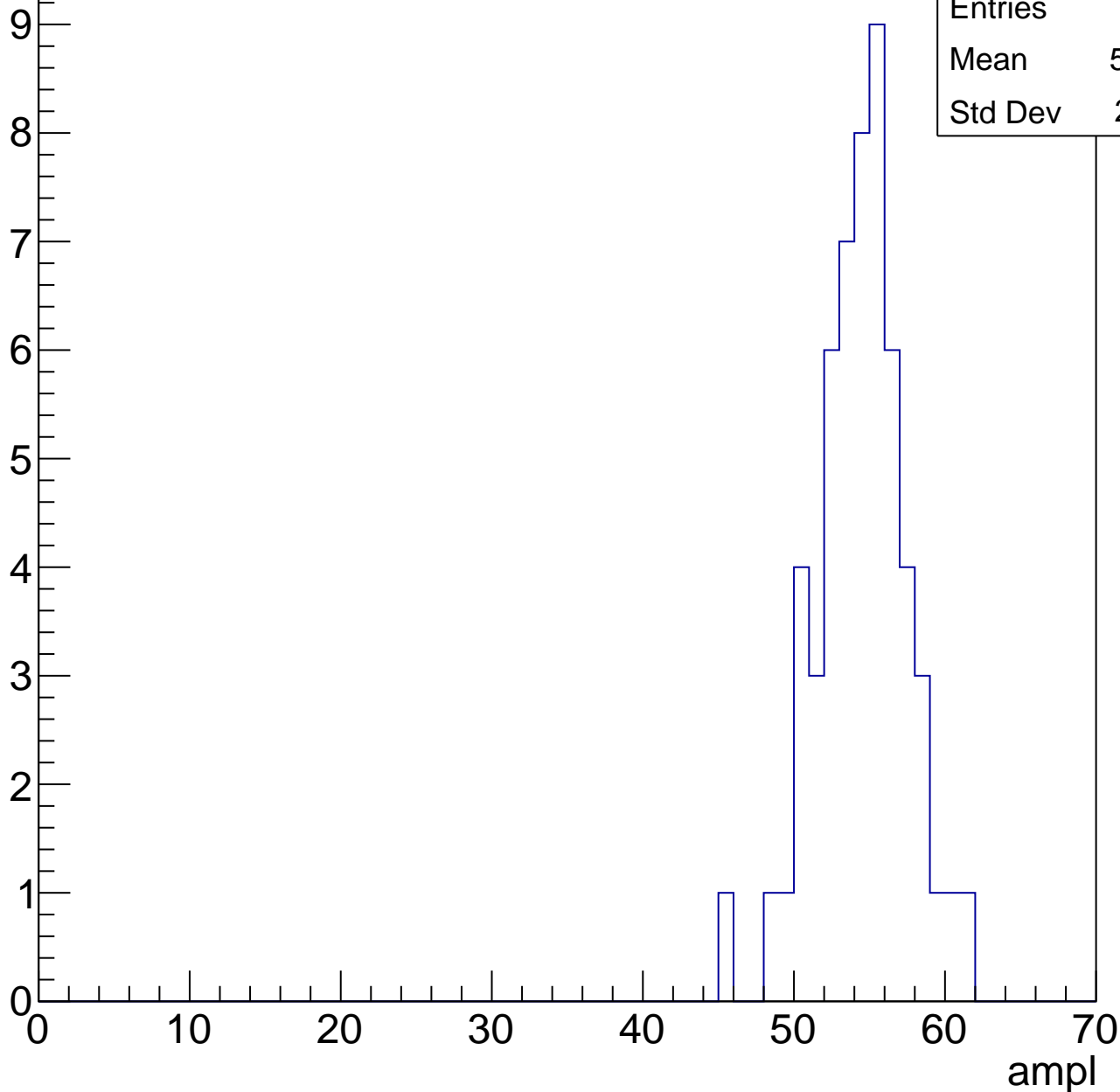


| | |
|---------|-------|
| Entries | 66 |
| Mean | 47.3 |
| Std Dev | 3.162 |

B1L100S, U7-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

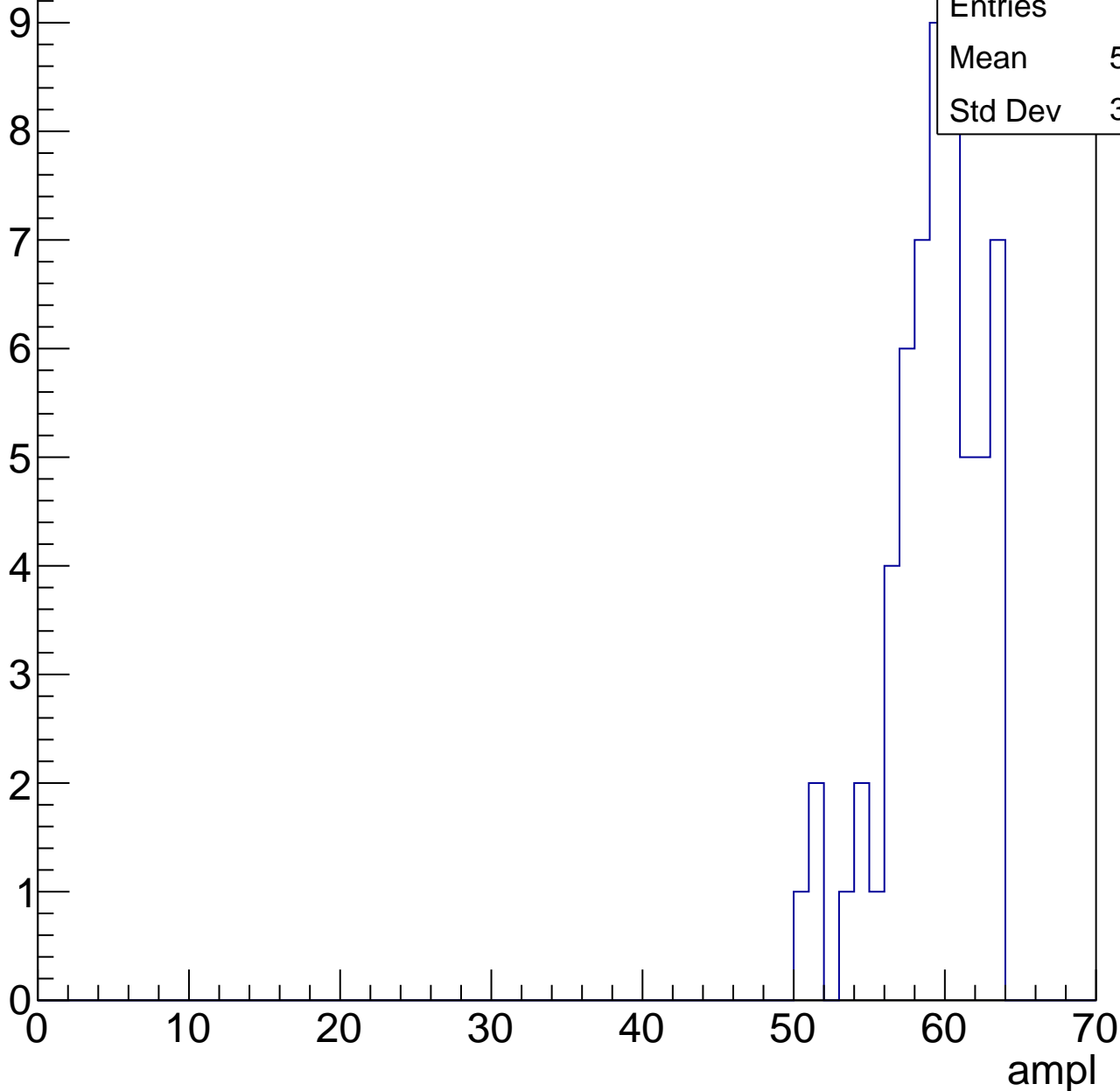


B1L100S, U7-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.76 |
| Std Dev | 3.083 |

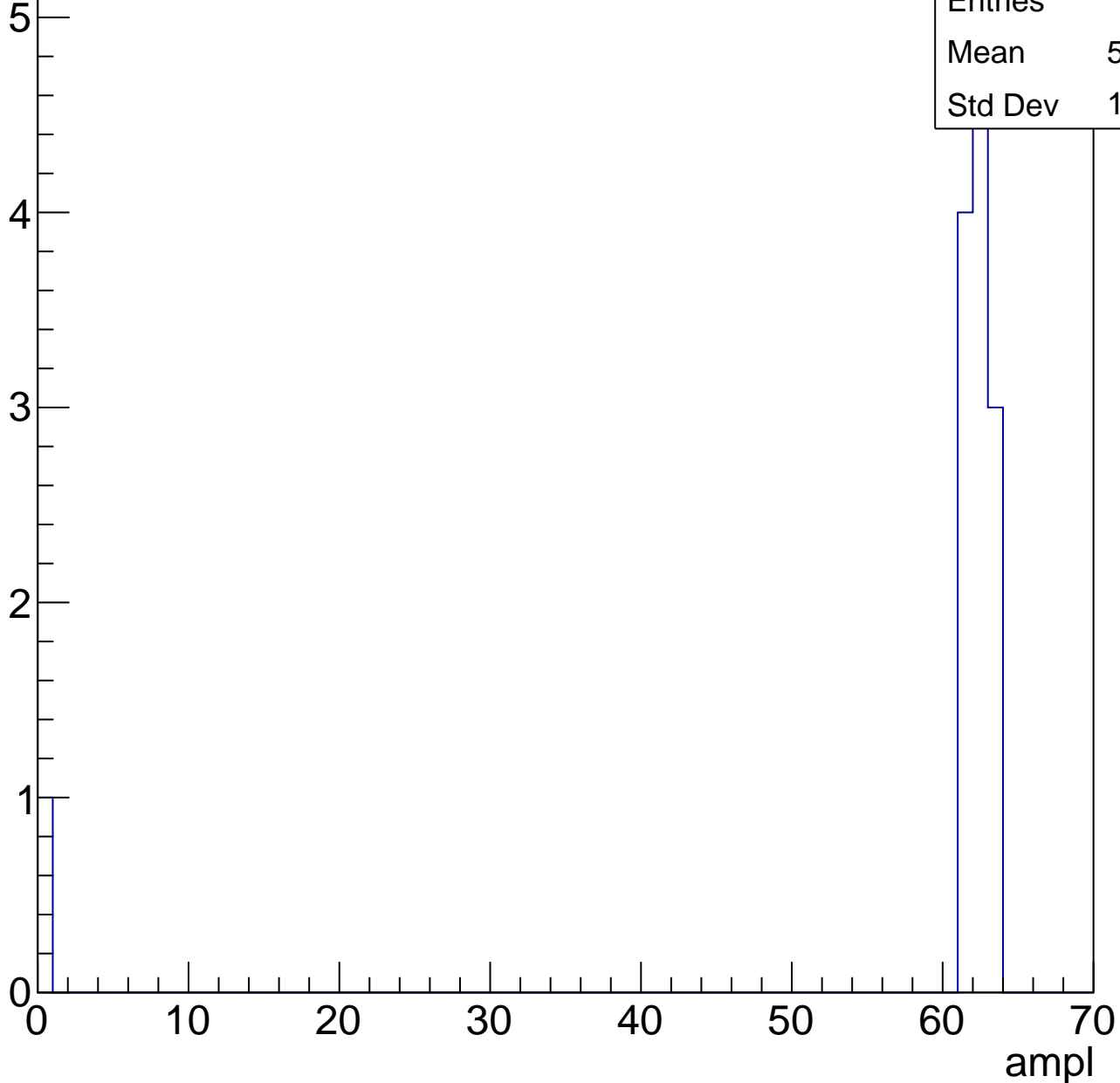


B1L100S, U7-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 57.15 |
| Std Dev | 16.52 |



B1L100S, U7-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch47, adc0

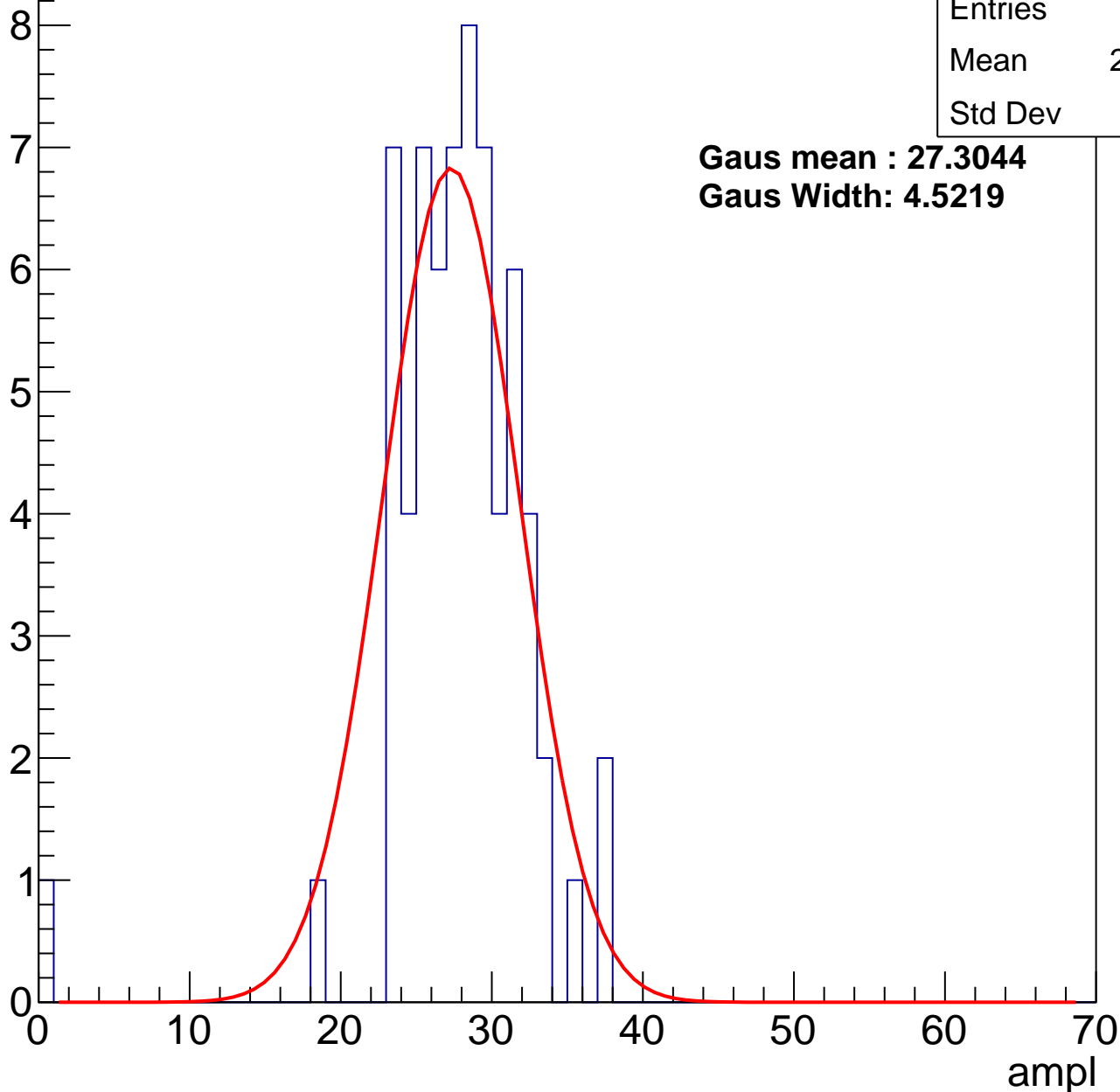
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.33 |
| Std Dev | 4.87 |

Gaus mean : 27.3044

Gaus Width: 4.5219



B1L100S, U7-ch47, adc1

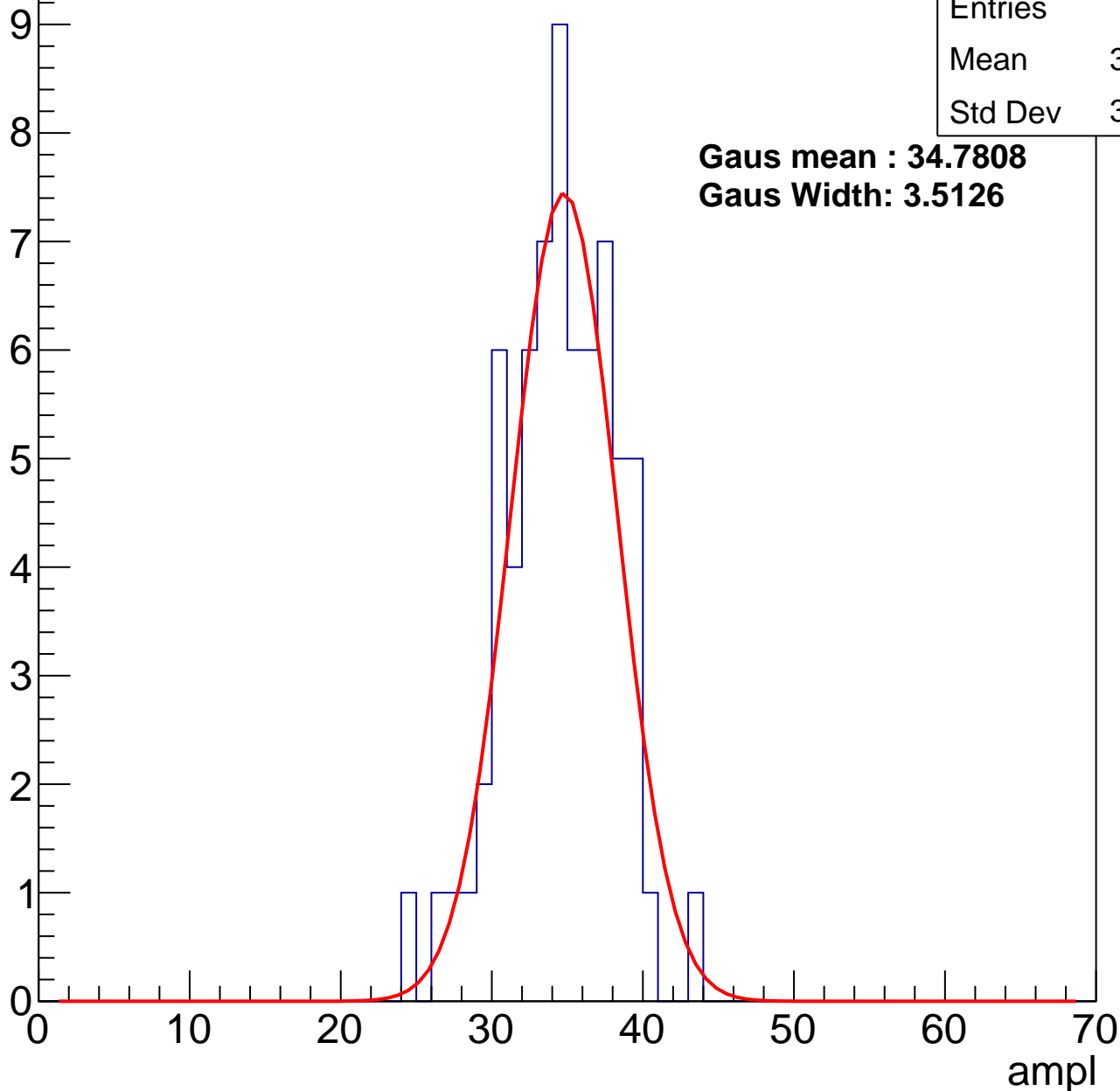
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.04 |
| Std Dev | 3.565 |

Gaus mean : 34.7808

Gaus Width: 3.5126



B1L100S, U7-ch47, adc2

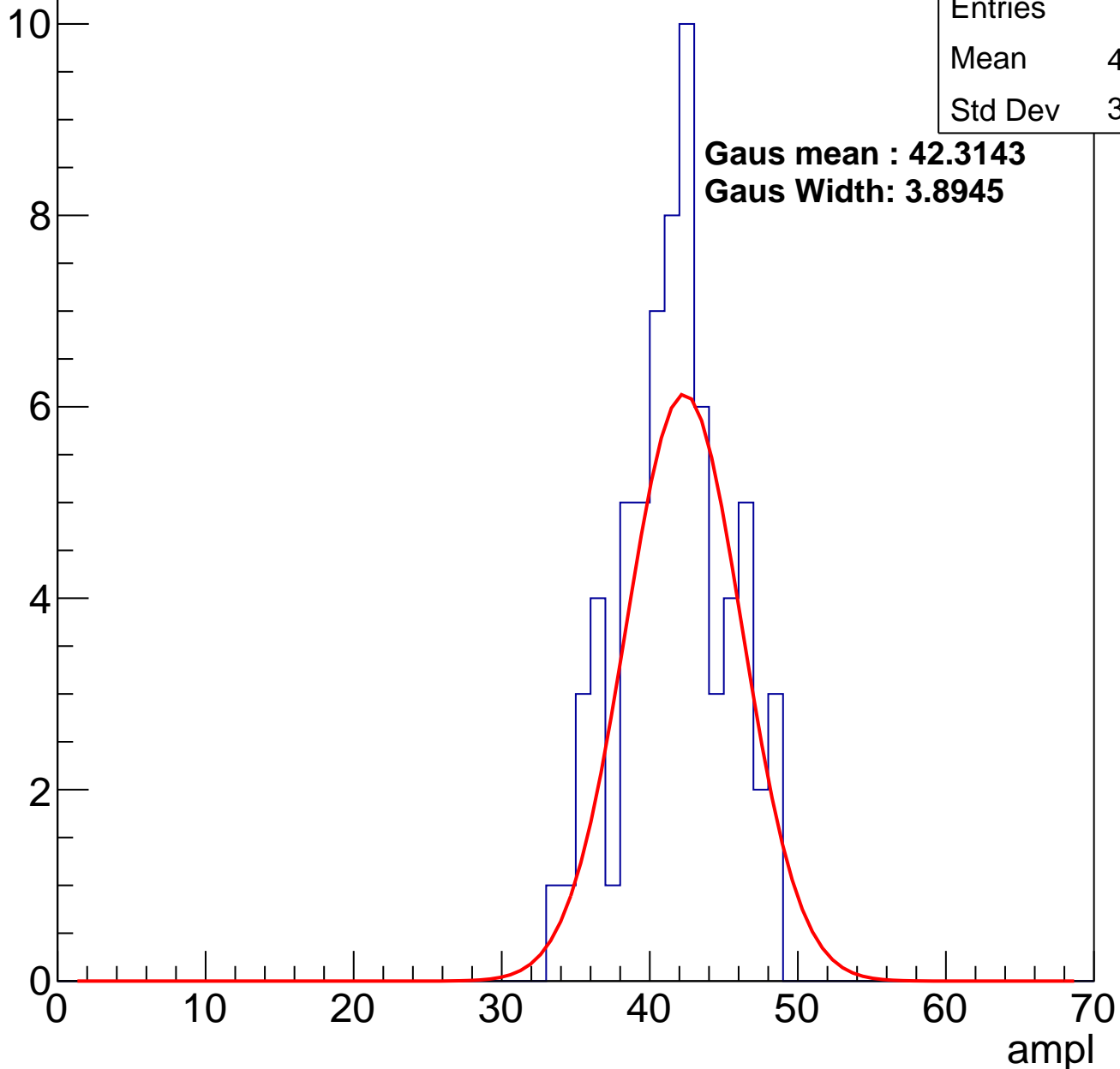
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 68 |
| Mean | 41.24 |
| Std Dev | 3.586 |

Gaus mean : 42.3143

Gaus Width: 3.8945

Entry

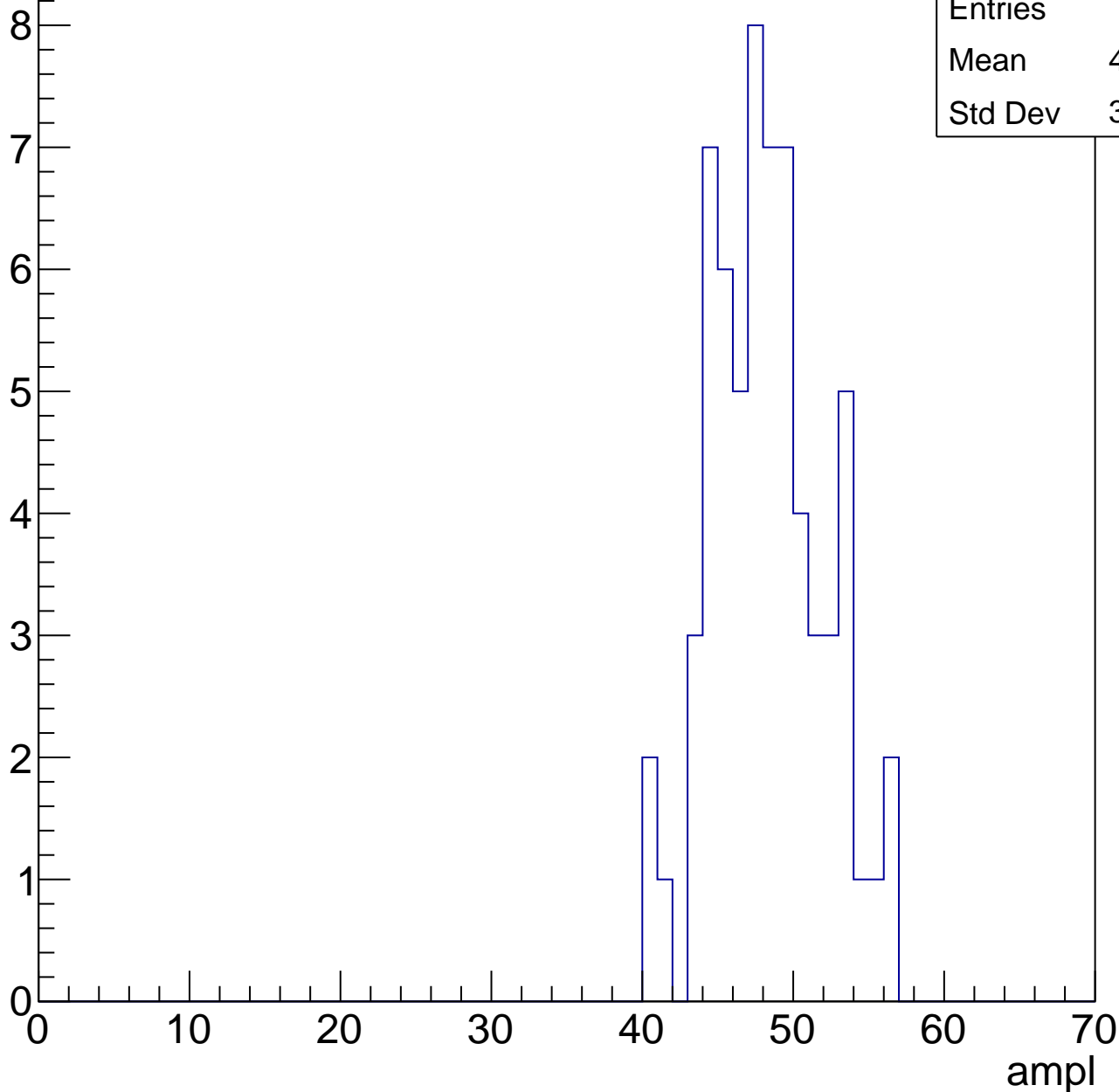


B1L100S, U7-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

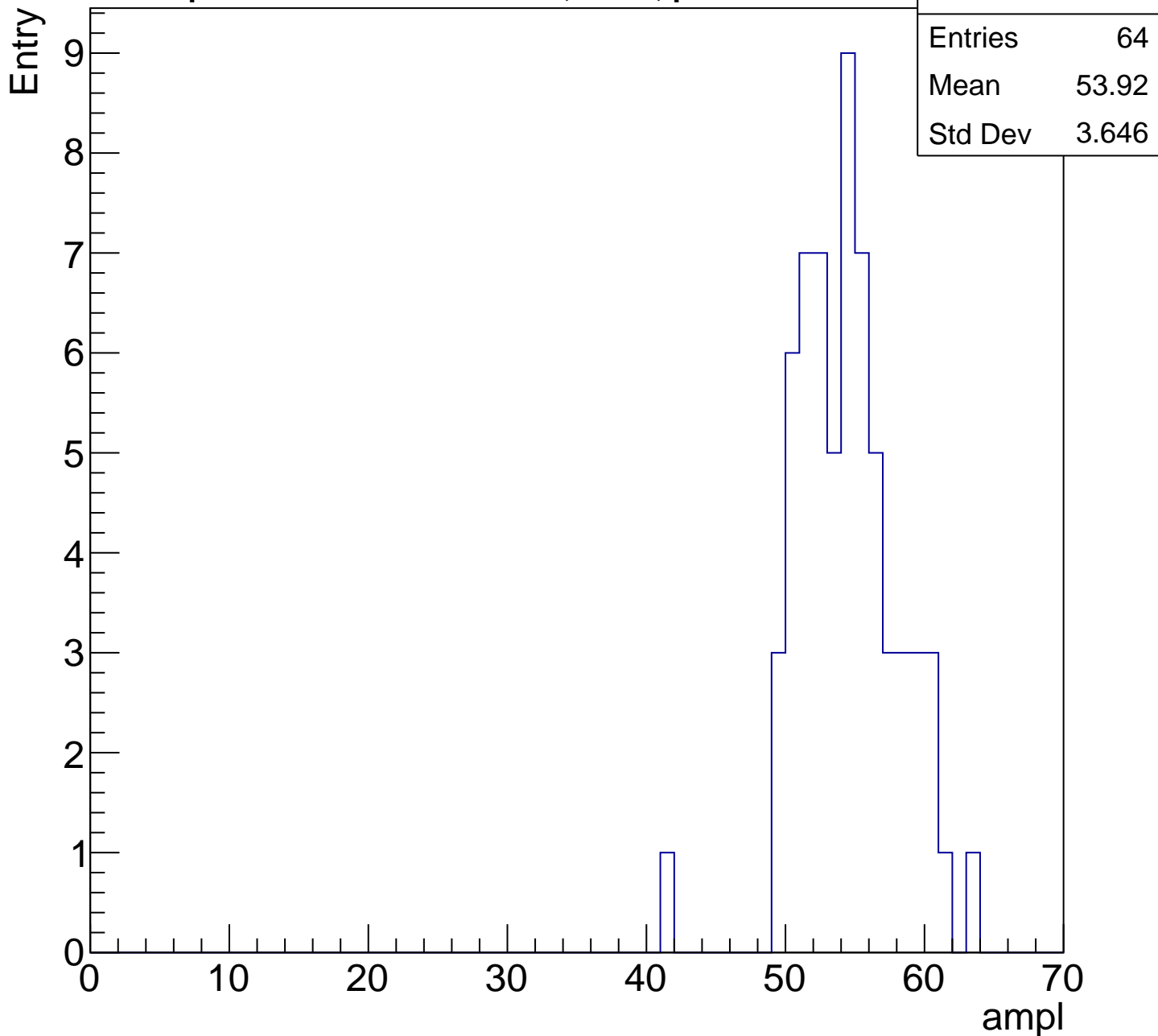
Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 47.82 |
| Std Dev | 3.679 |



B1L100S, U7-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

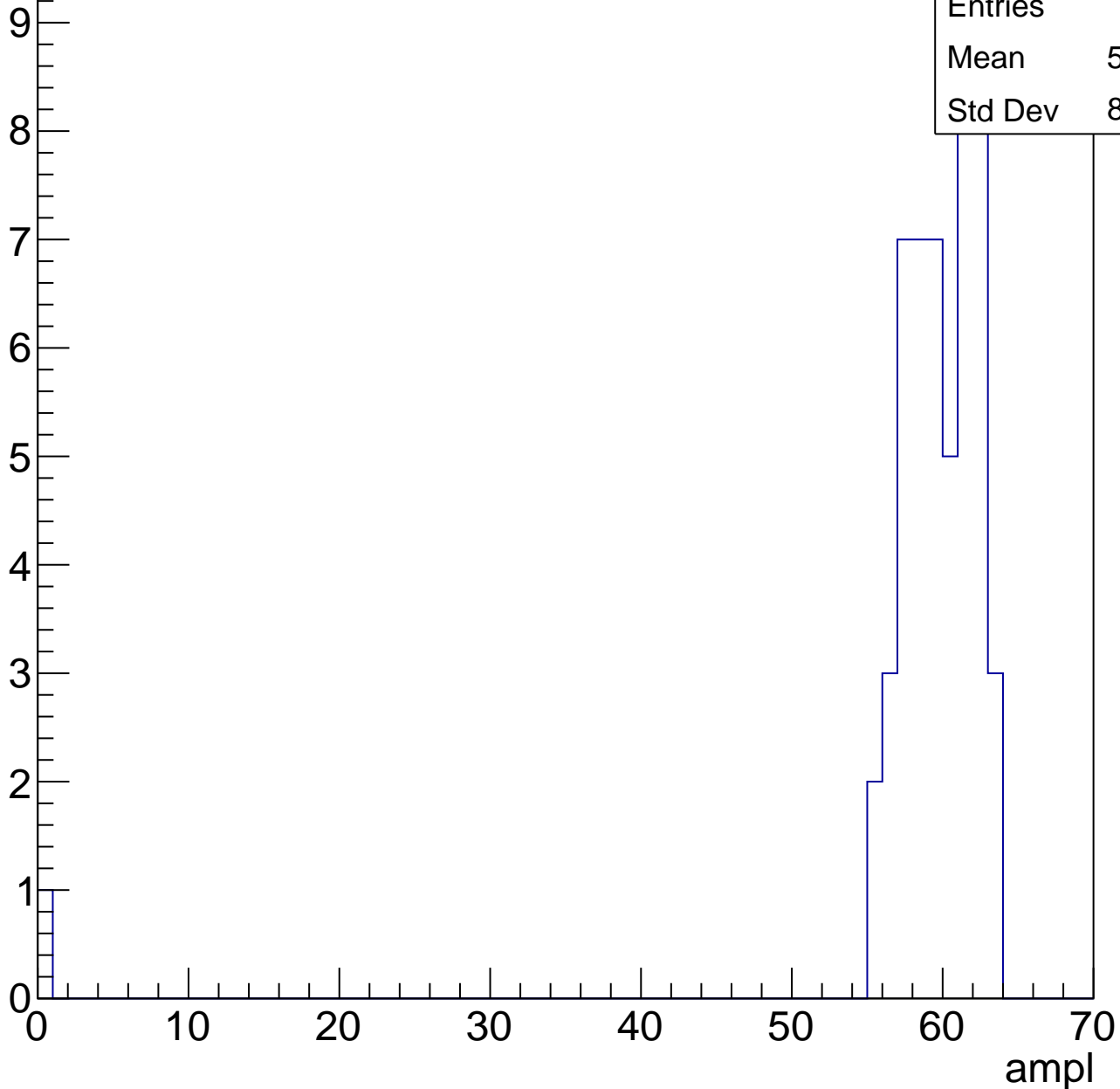


B1L100S, U7-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.29 |
| Std Dev | 8.452 |

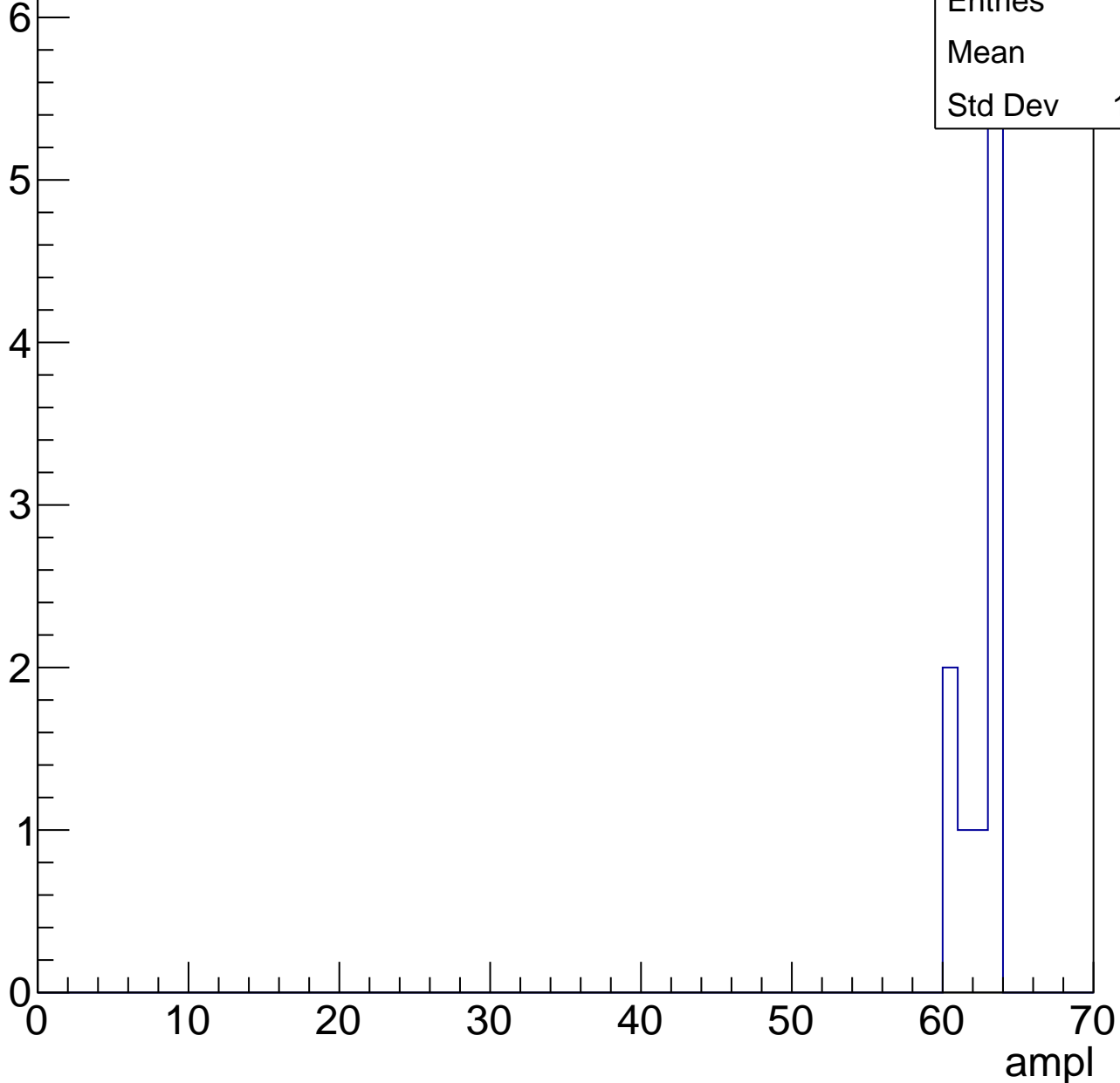


B1L100S, U7-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 62.1 |
| Std Dev | 1.221 |



B1L100S, U7-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch48, adc0

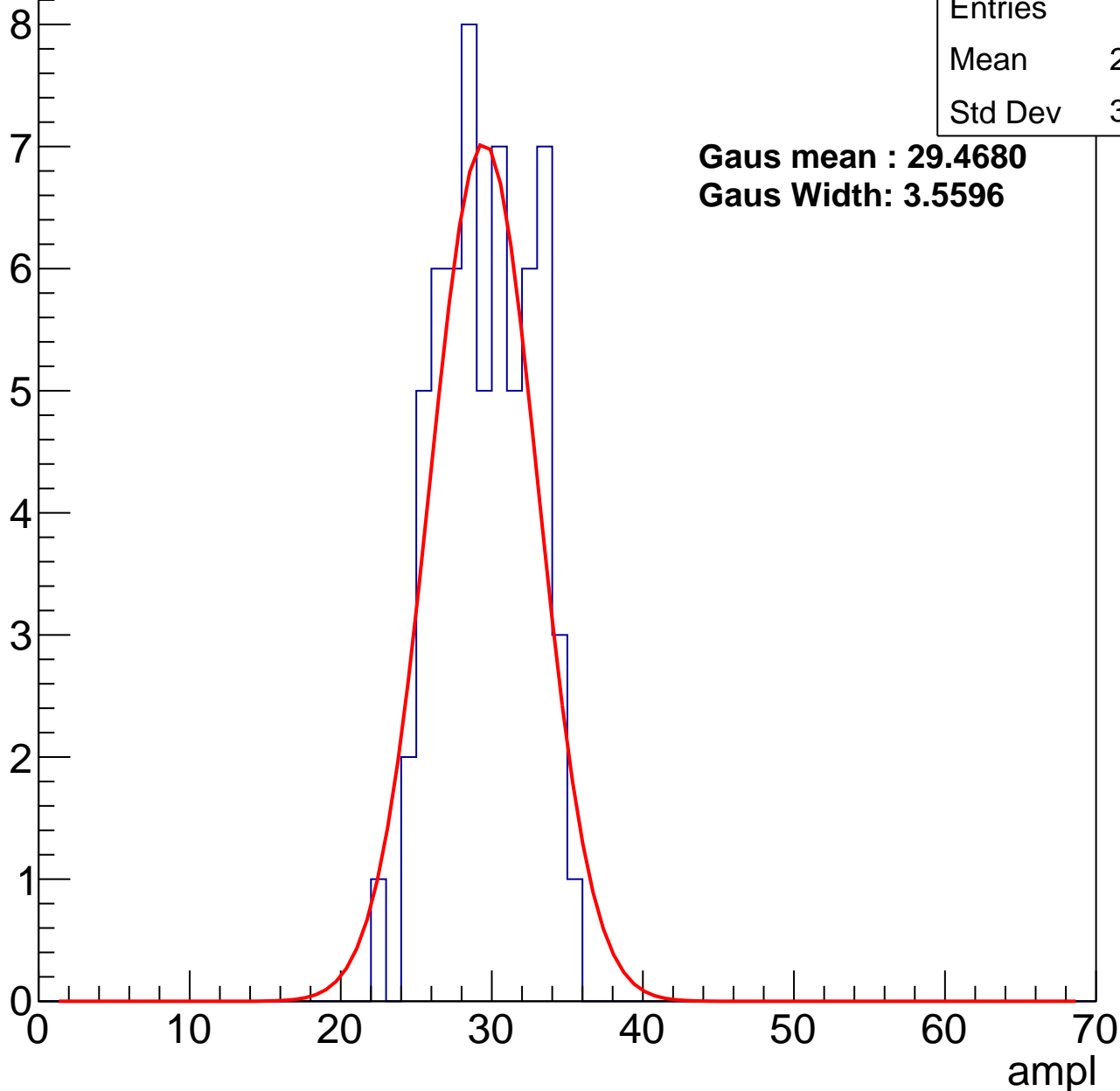
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 29.15 |
| Std Dev | 3.026 |

Gaus mean : 29.4680

Gaus Width: 3.5596



B1L100S, U7-ch48, adc1

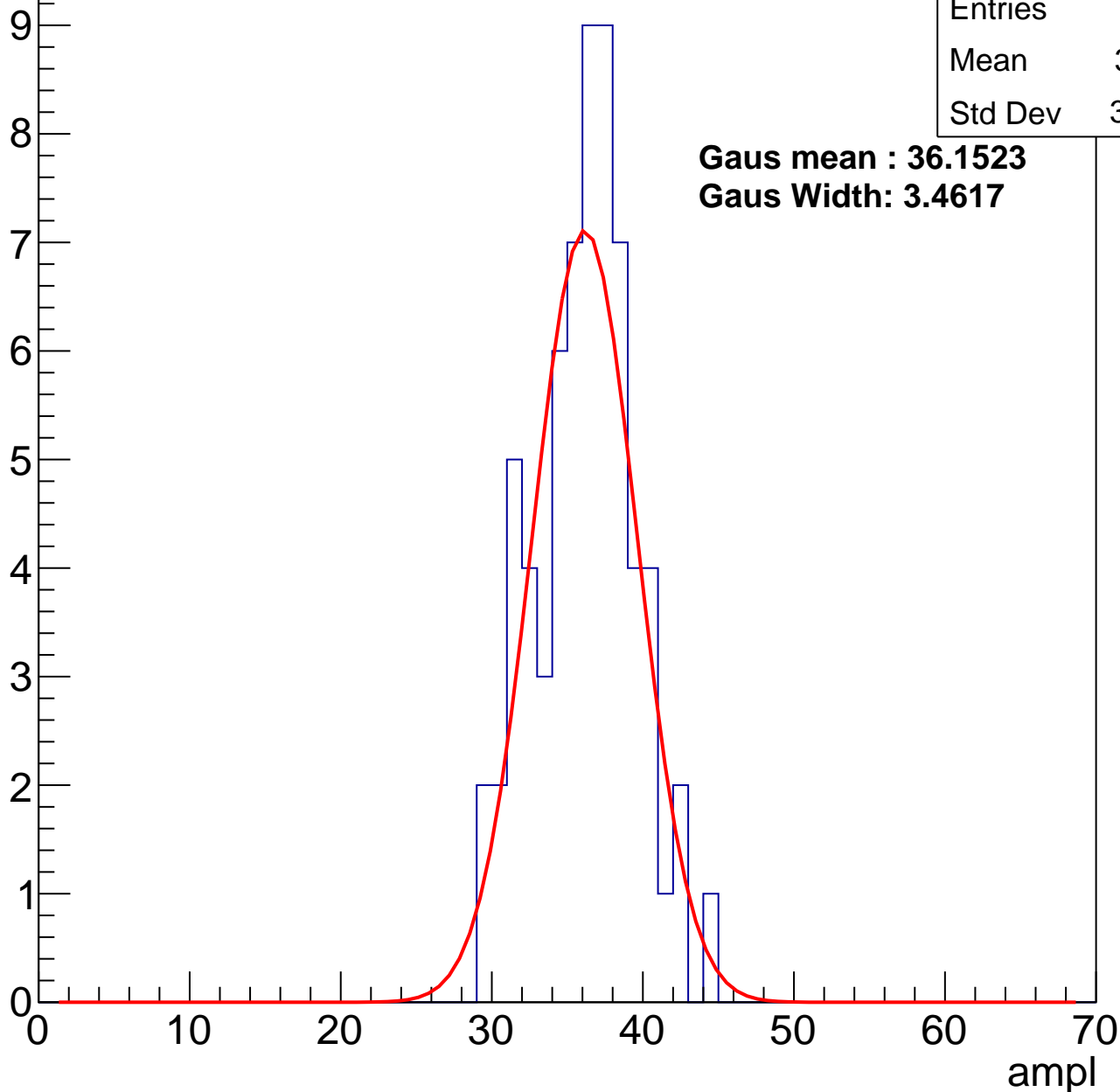
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 35.71 |
| Std Dev | 3.279 |

Gaus mean : 36.1523

Gaus Width: 3.4617



B1L100S, U7-ch48, adc2

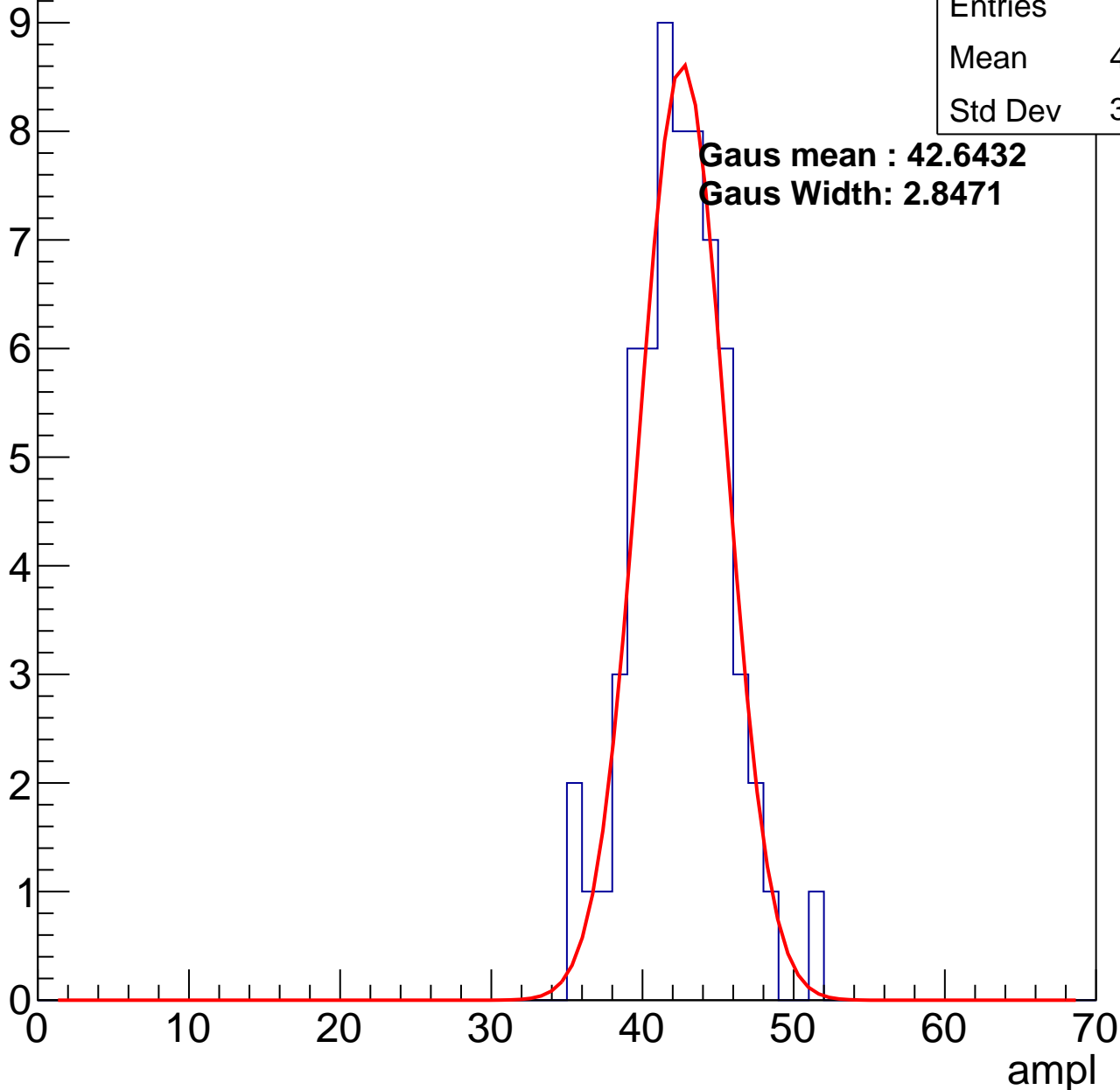
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.02 |
| Std Dev | 3.059 |

Gaus mean : 42.6432

Gaus Width: 2.8471

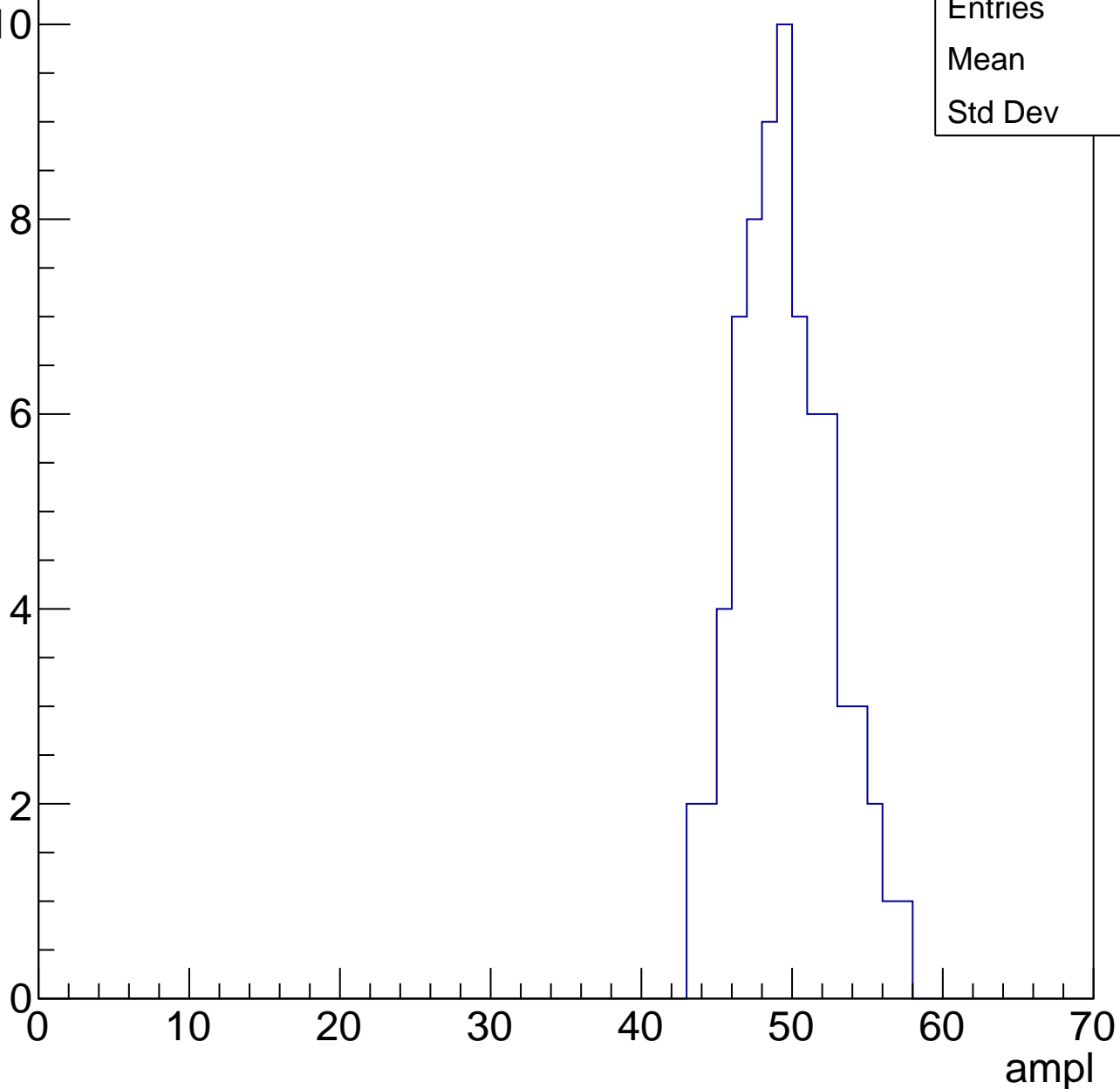


B1L100S, U7-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

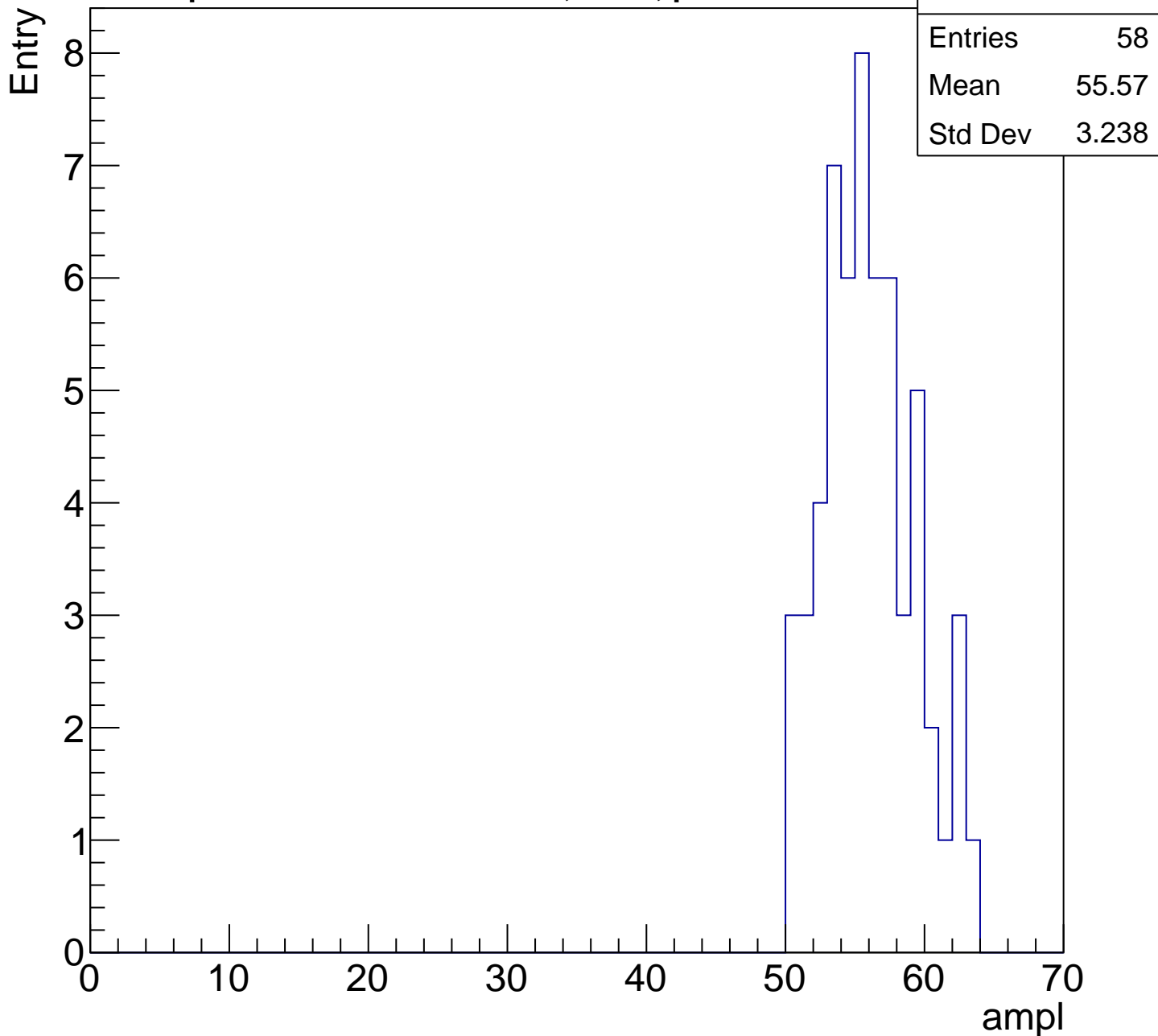
Entry

| | |
|---------|------|
| Entries | 71 |
| Mean | 49.1 |
| Std Dev | 3.1 |



B1L100S, U7-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

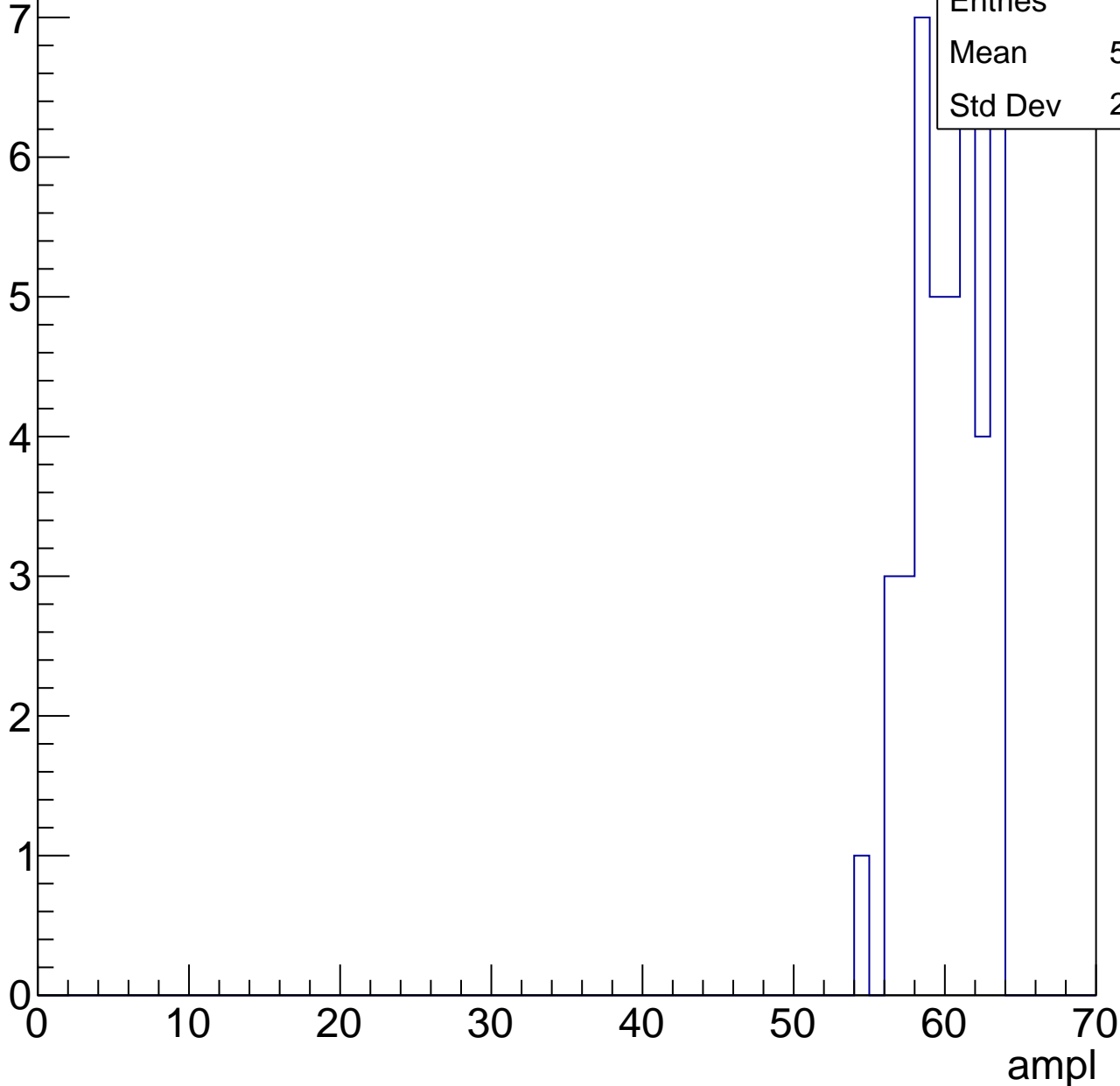


B1L100S, U7-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

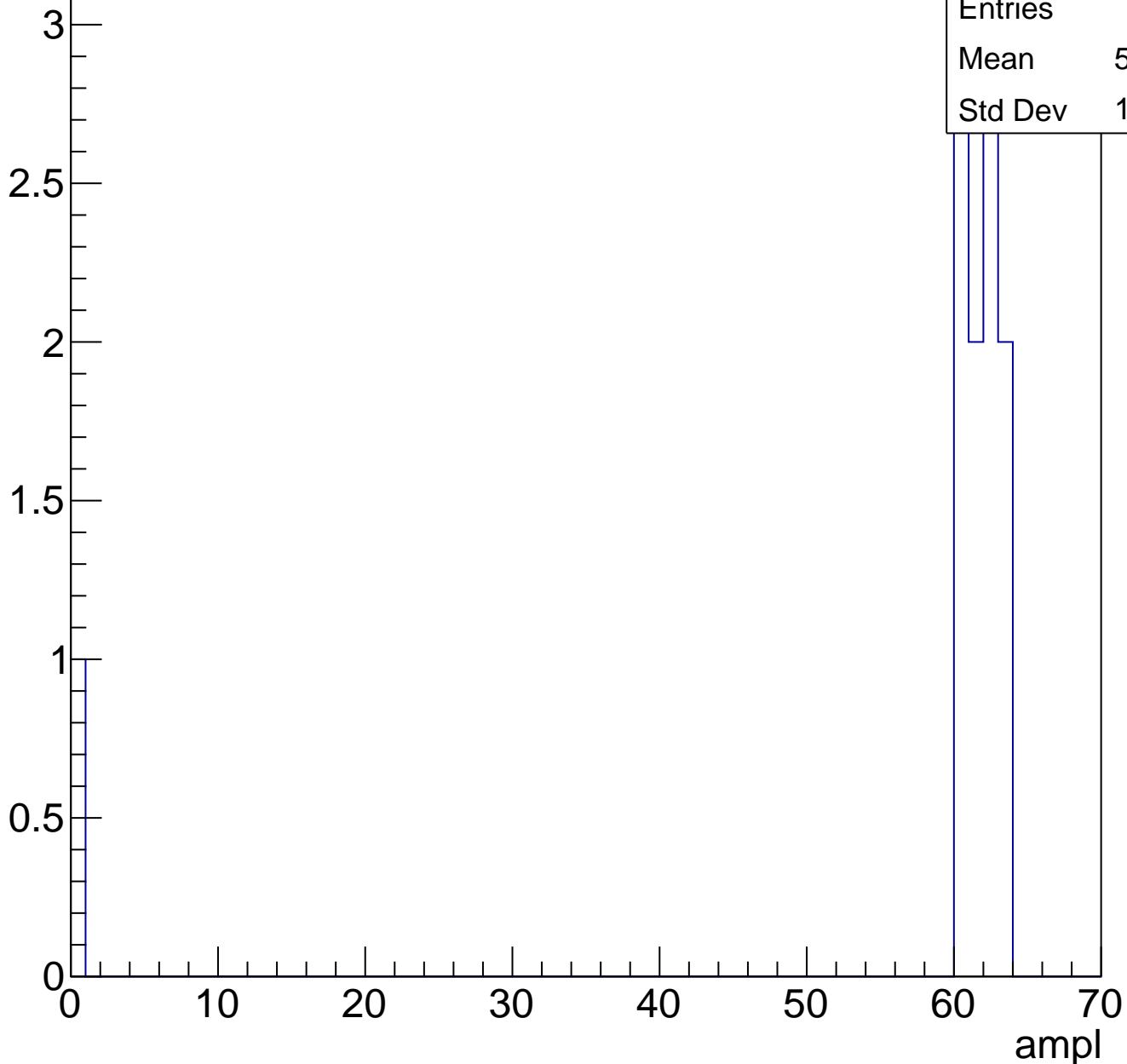
| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.76 |
| Std Dev | 2.328 |



B1L100S, U7-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

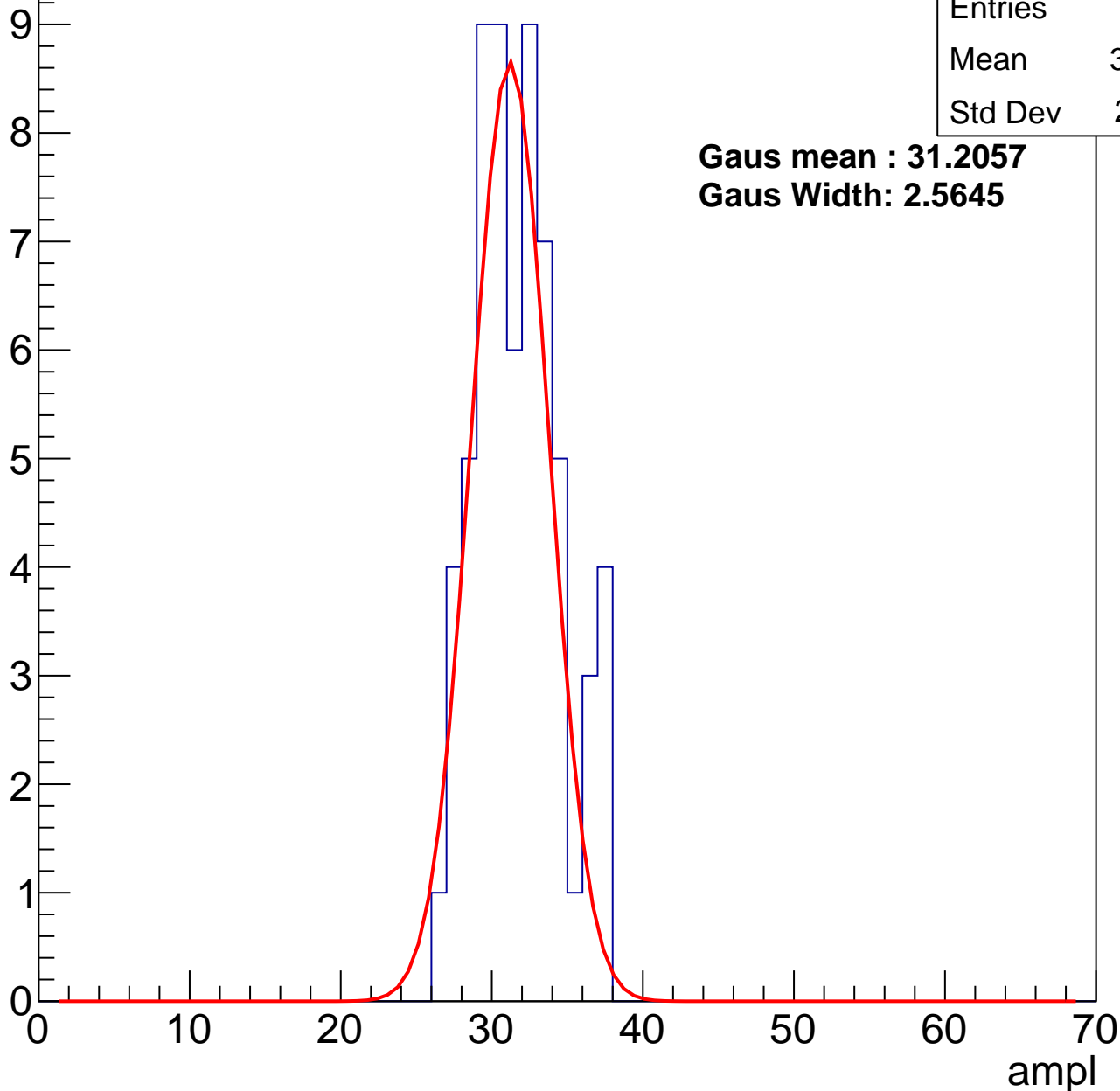


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch49, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 63 |
| Mean | 31.29 |
| Std Dev | 2.791 |

B1L100S, U7-ch49, adc1

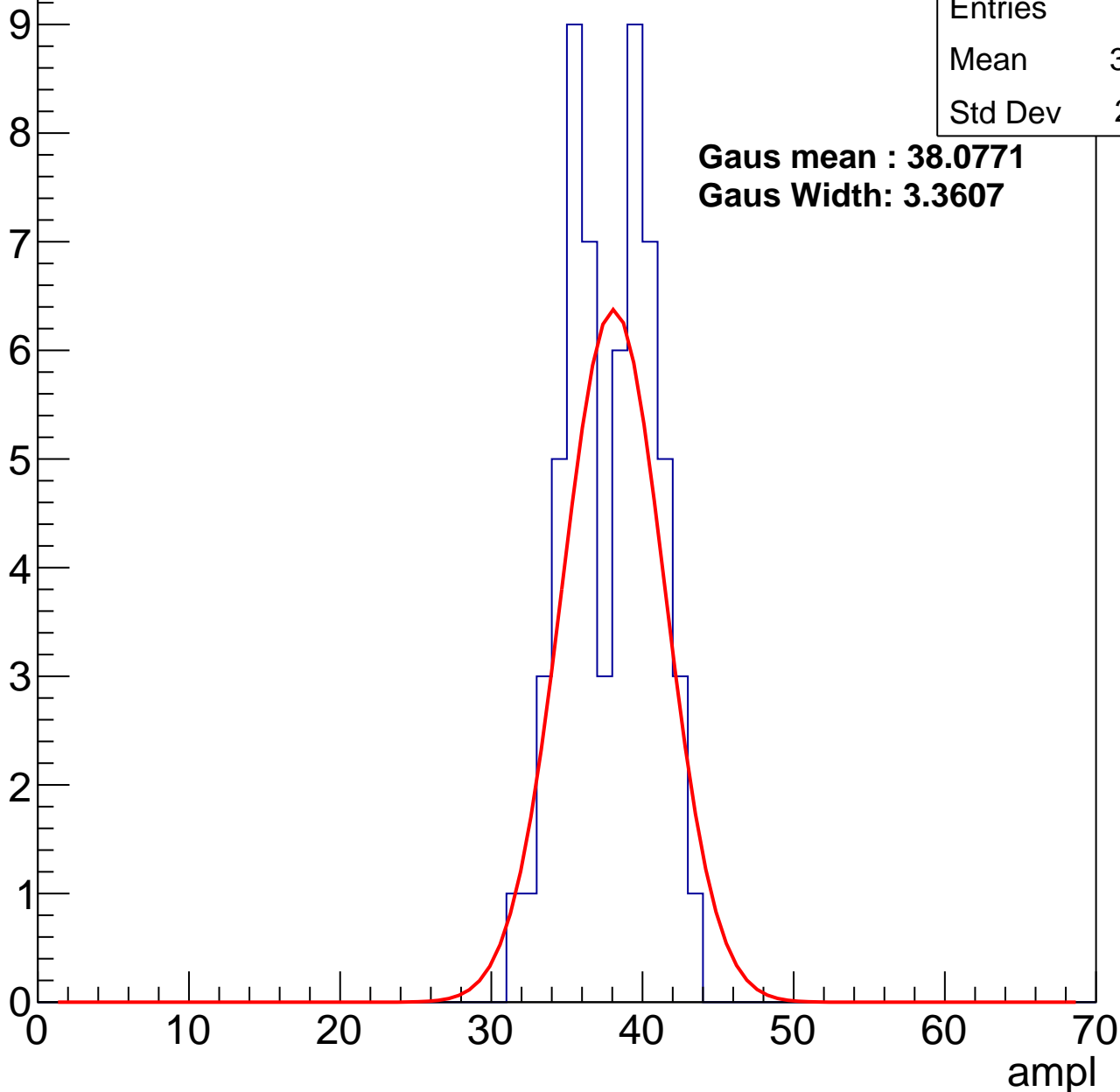
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 37.38 |
| Std Dev | 2.841 |

Gaus mean : 38.0771

Gaus Width: 3.3607



B1L100S, U7-ch49, adc2

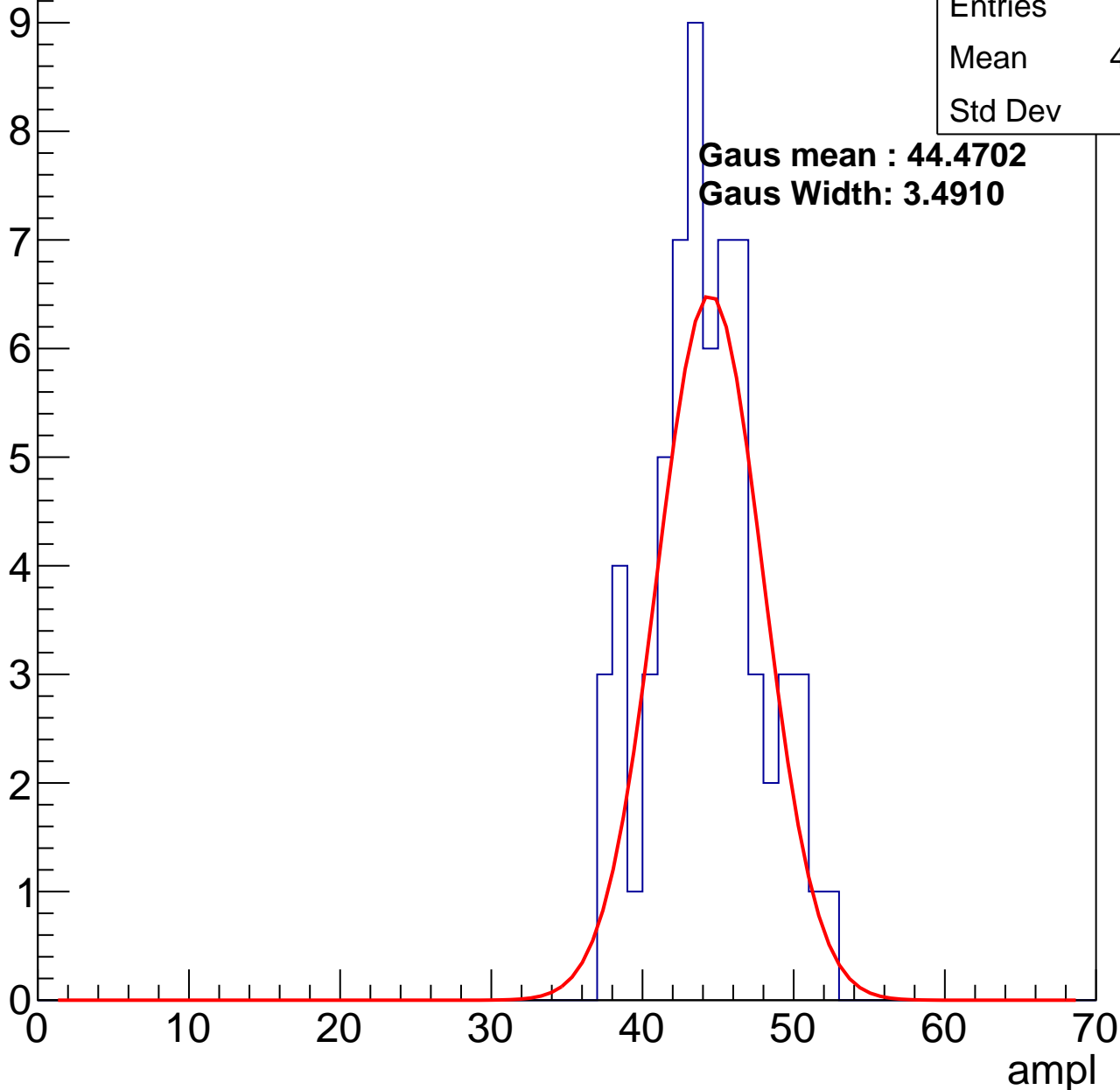
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 43.78 |
| Std Dev | 3.58 |

Gaus mean : 44.4702

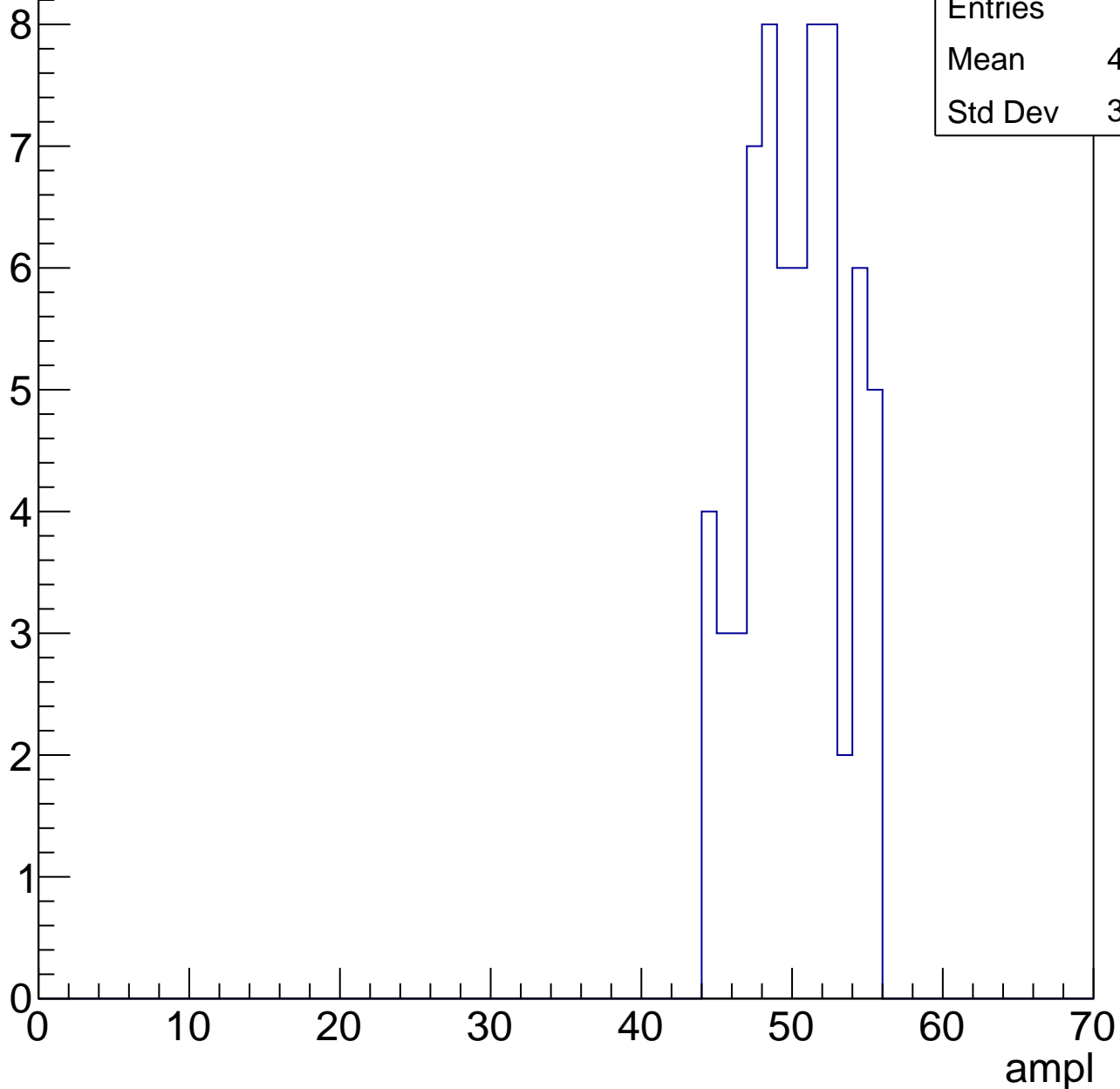
Gaus Width: 3.4910



B1L100S, U7-ch49, adc3

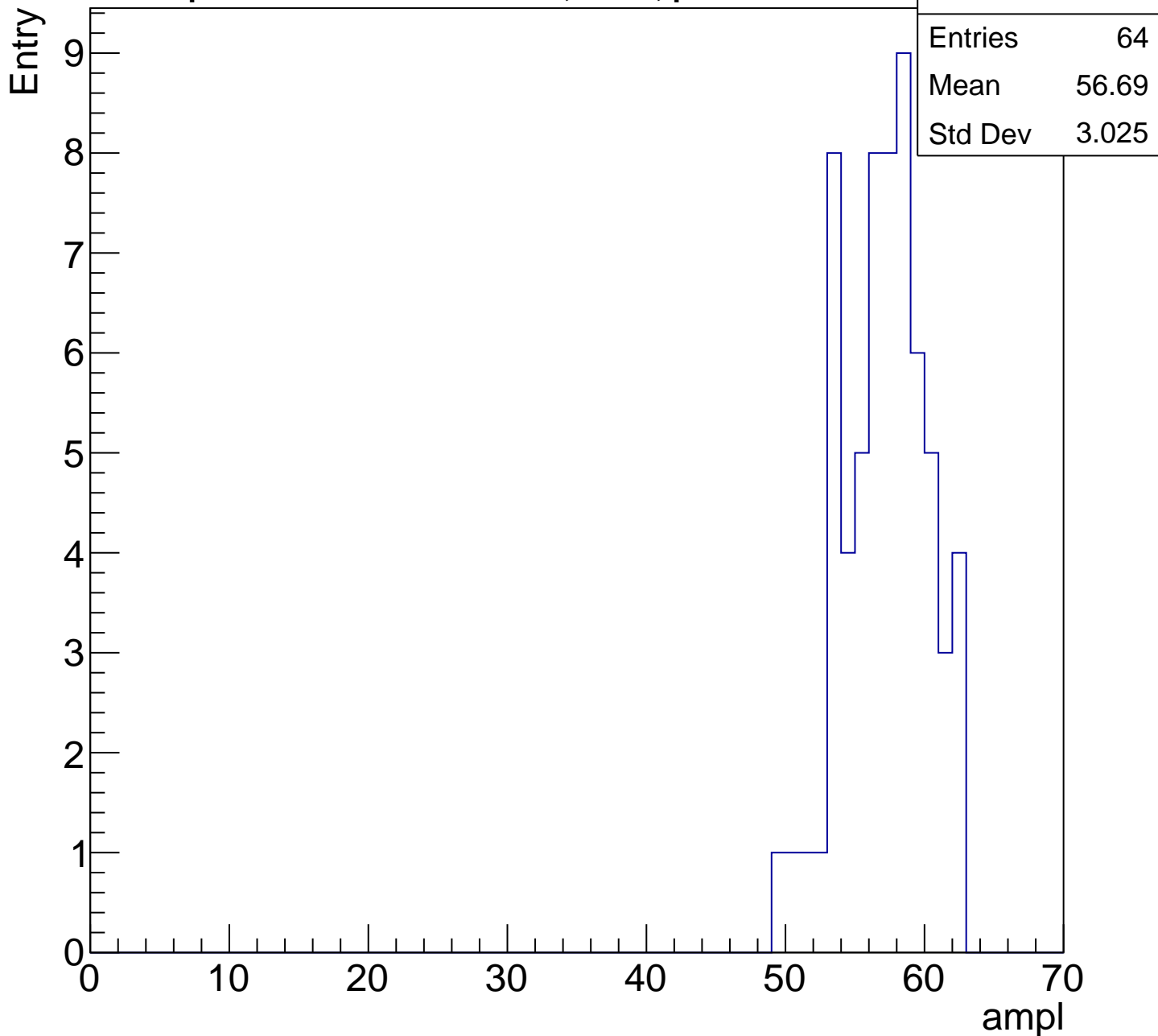
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch49, adc5

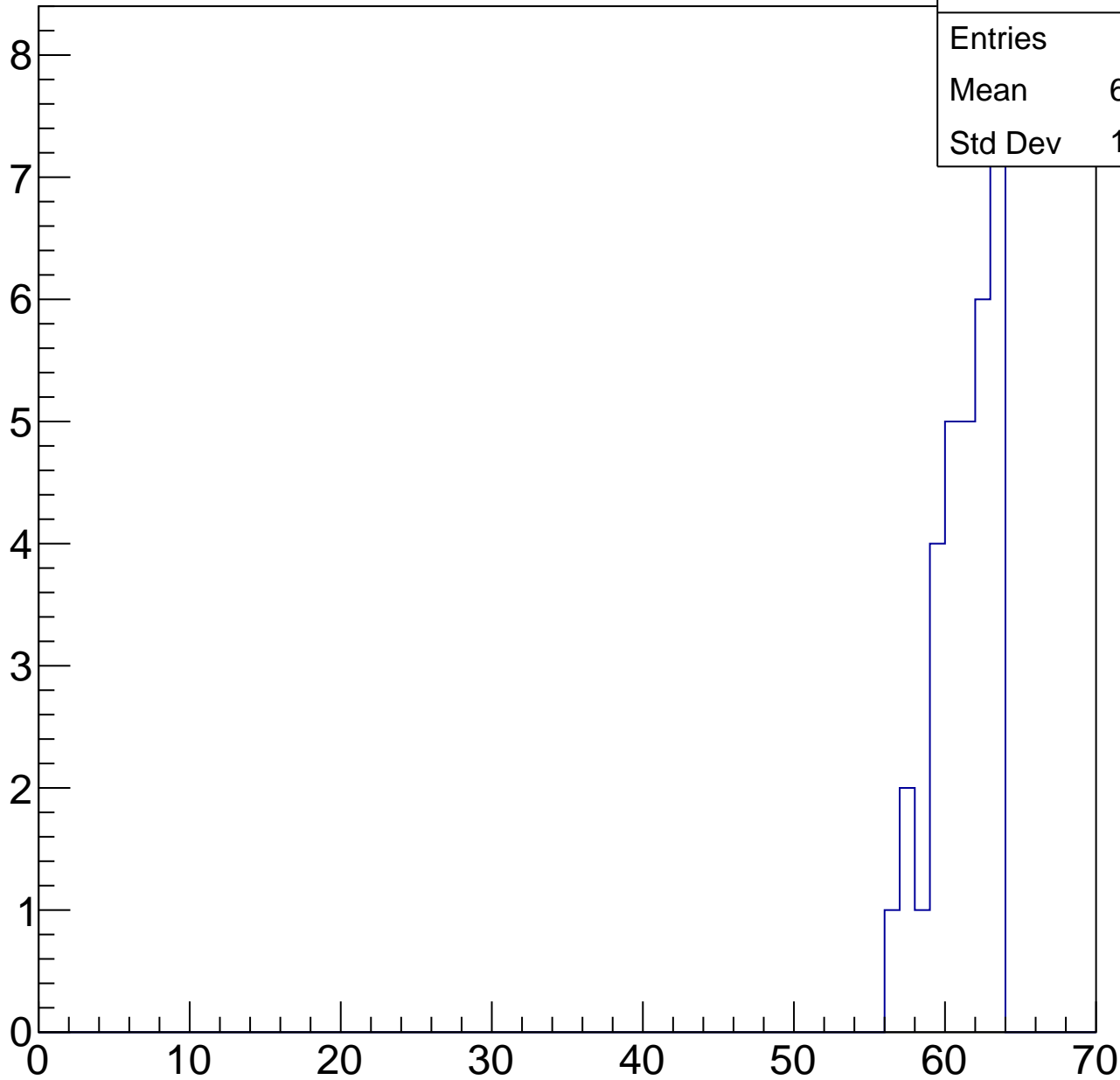
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 32 |
| Mean | 60.78 |
| Std Dev | 1.964 |

ampl



B1L100S, U7-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

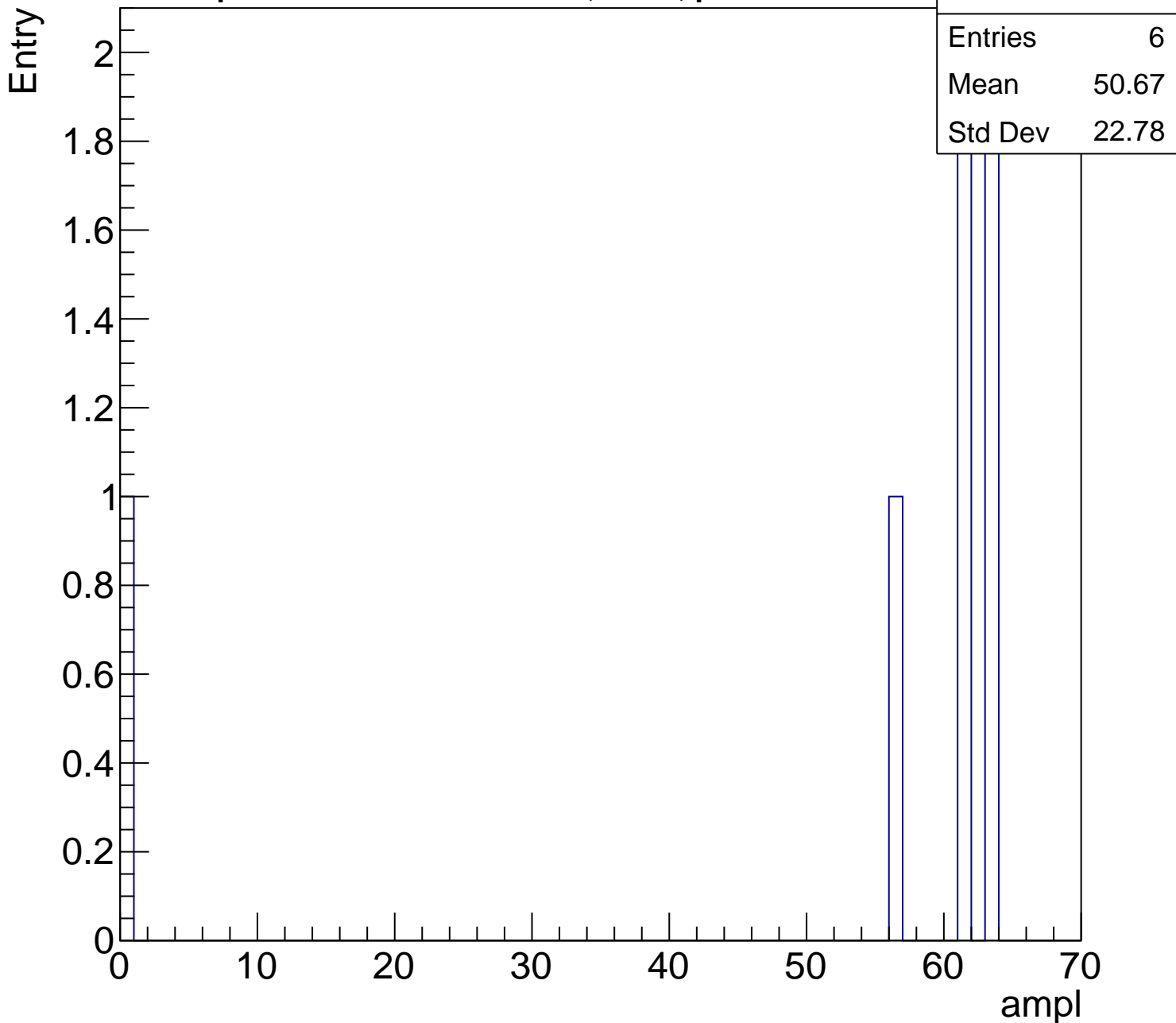
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 50.67 |
| Std Dev | 22.78 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 26 |
| Std Dev | 0 |

ampl

B1L100S, U7-ch50, adc0

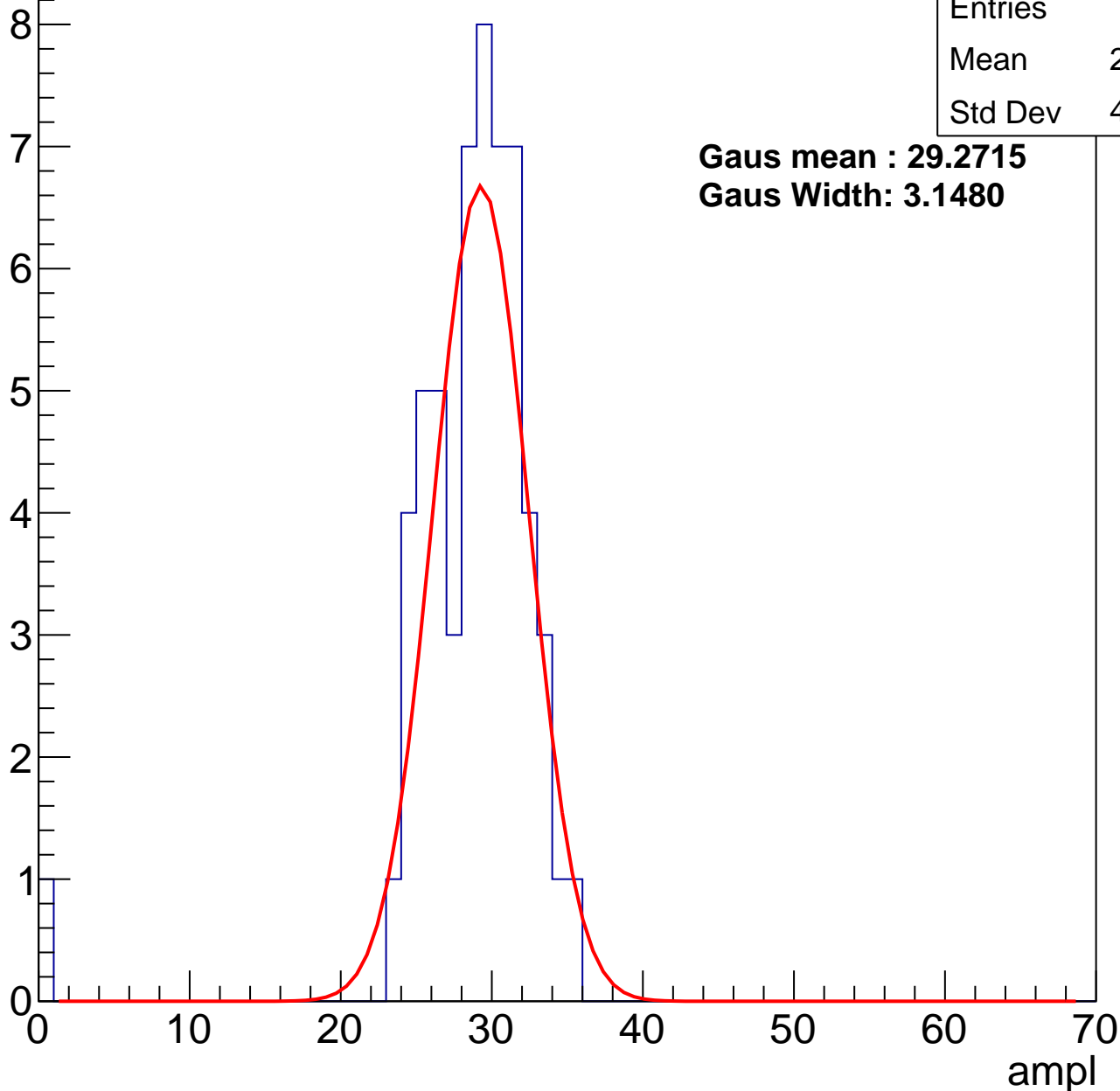
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 28.18 |
| Std Dev | 4.706 |

Gaus mean : 29.2715

Gaus Width: 3.1480



B1L100S, U7-ch50, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 35.99 |
| Std Dev | 3.347 |

Gaus mean : 36.2027

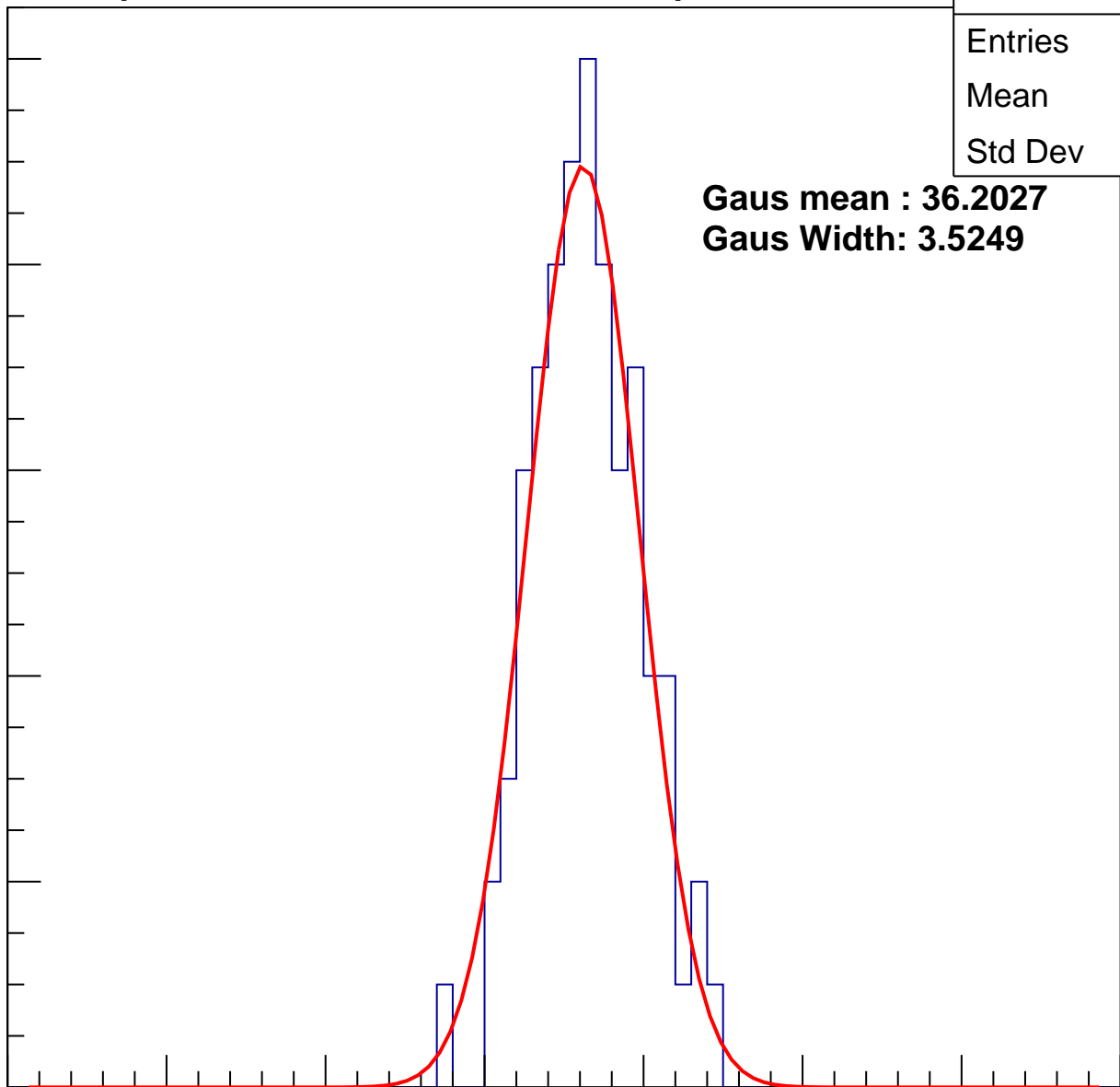
Gaus Width: 3.5249

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch50, adc2

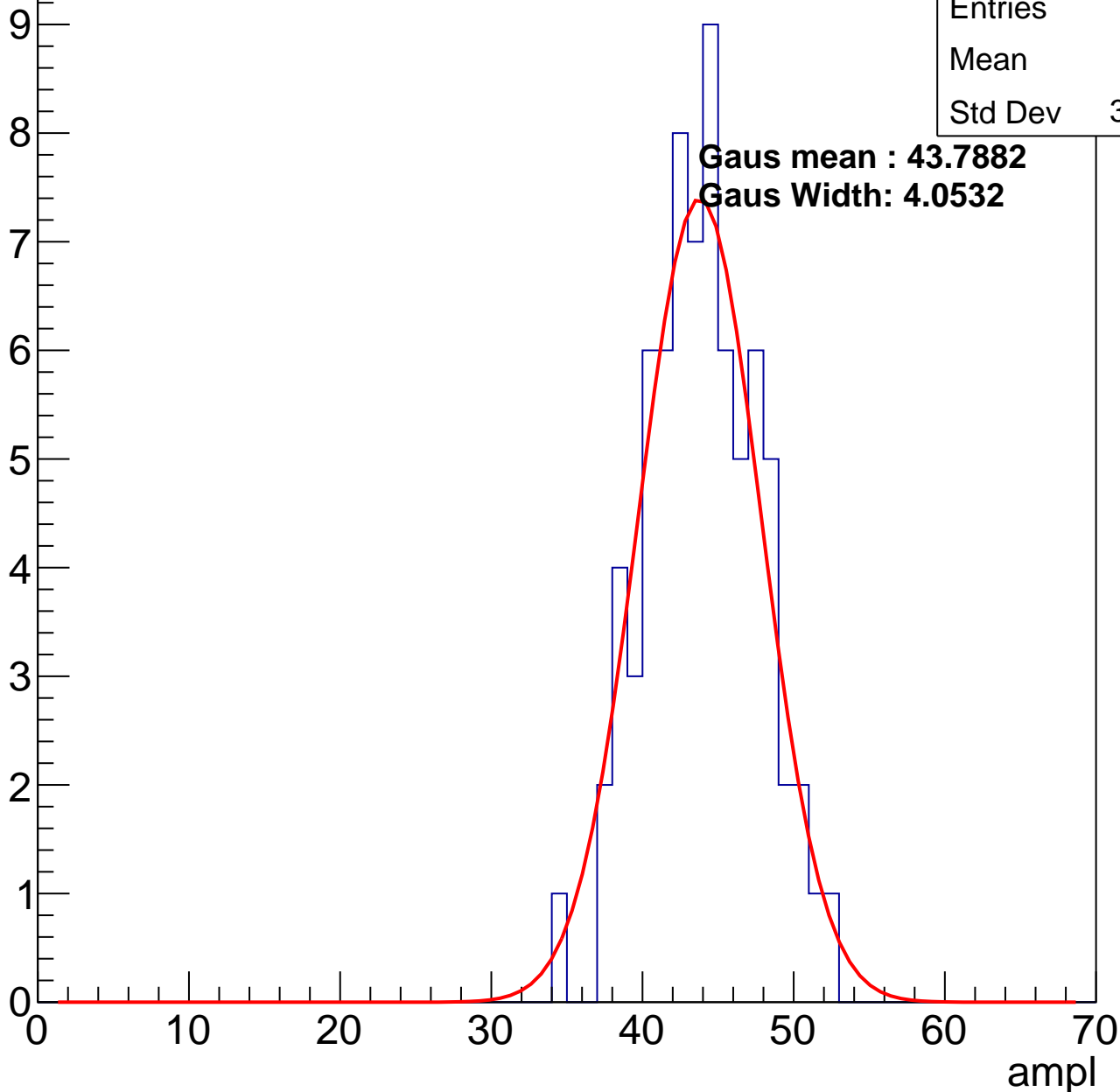
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 43.5 |
| Std Dev | 3.644 |

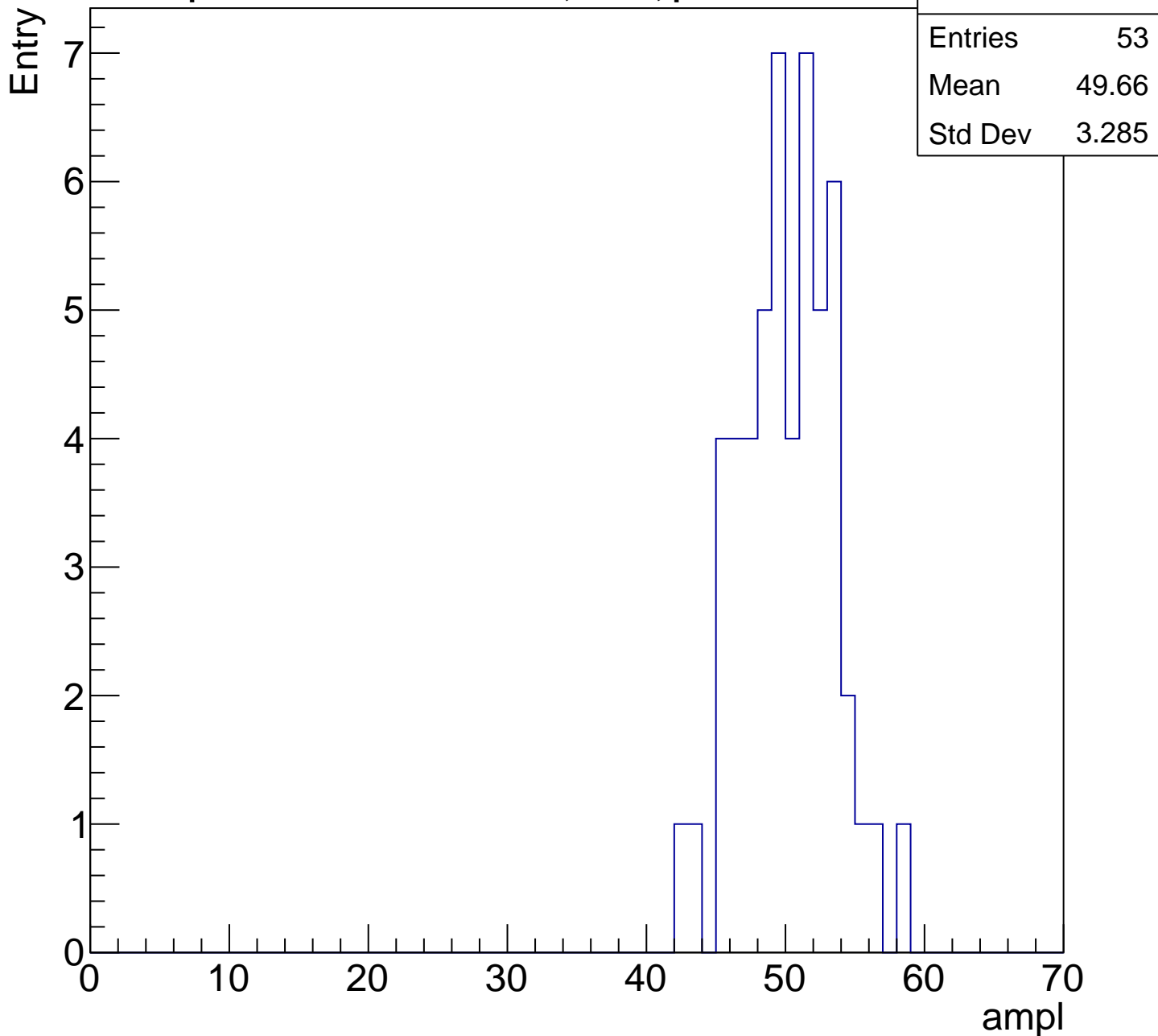
Gaus mean : 43.7882

Gaus Width: 4.0532



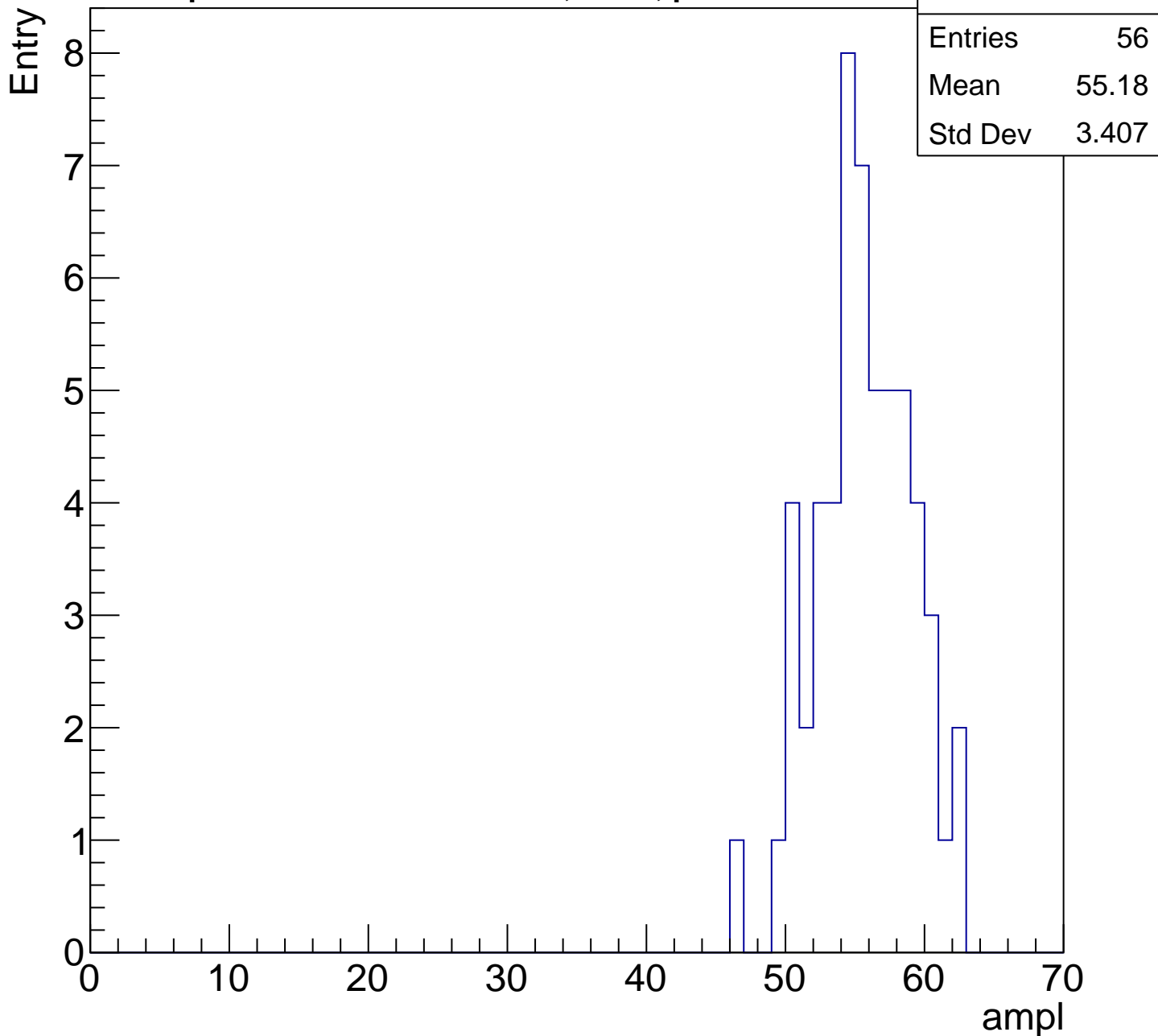
B1L100S, U7-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

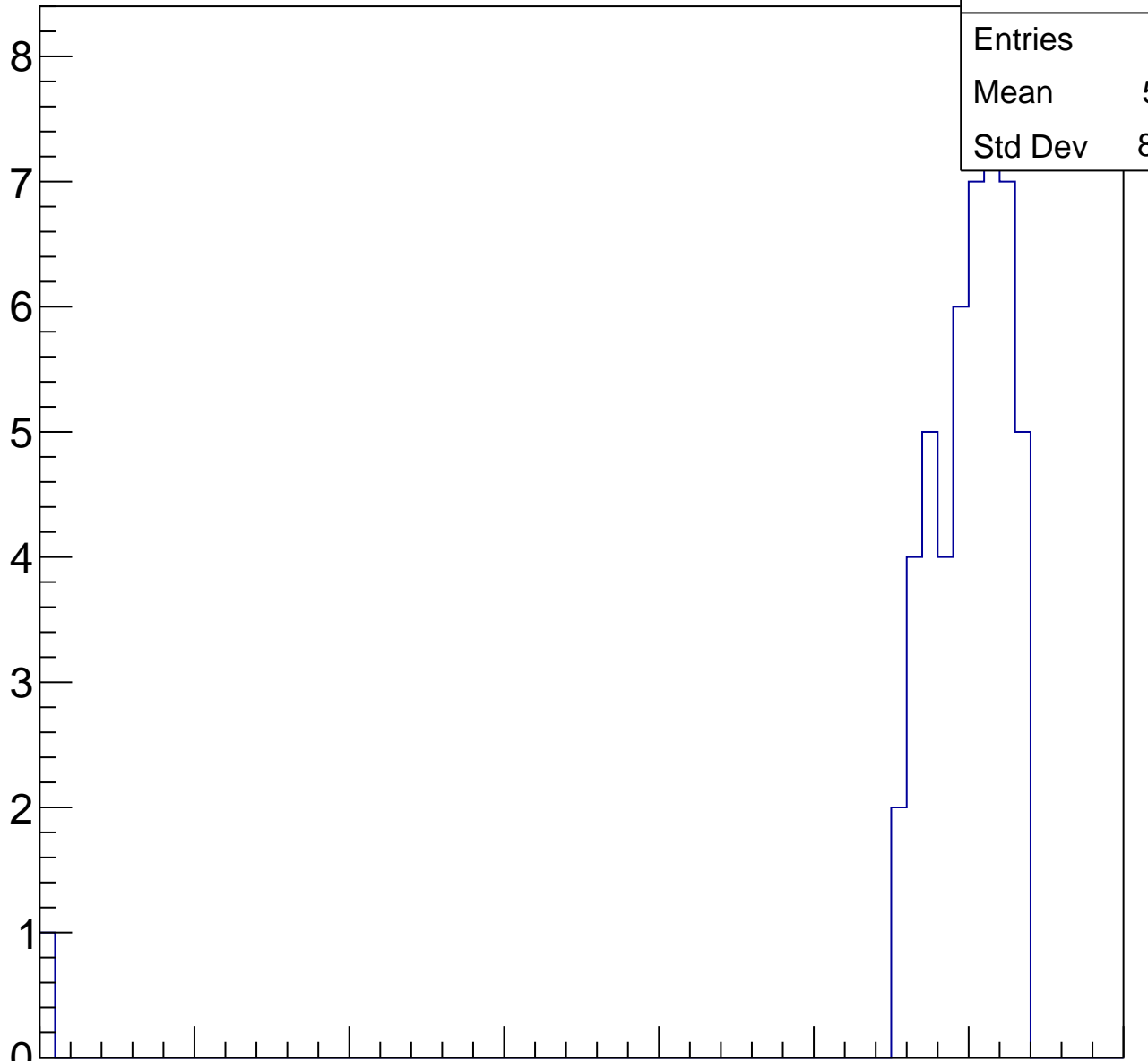
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.41 |
| Std Dev | 8.734 |

ampl

0 10 20 30 40 50 60 70

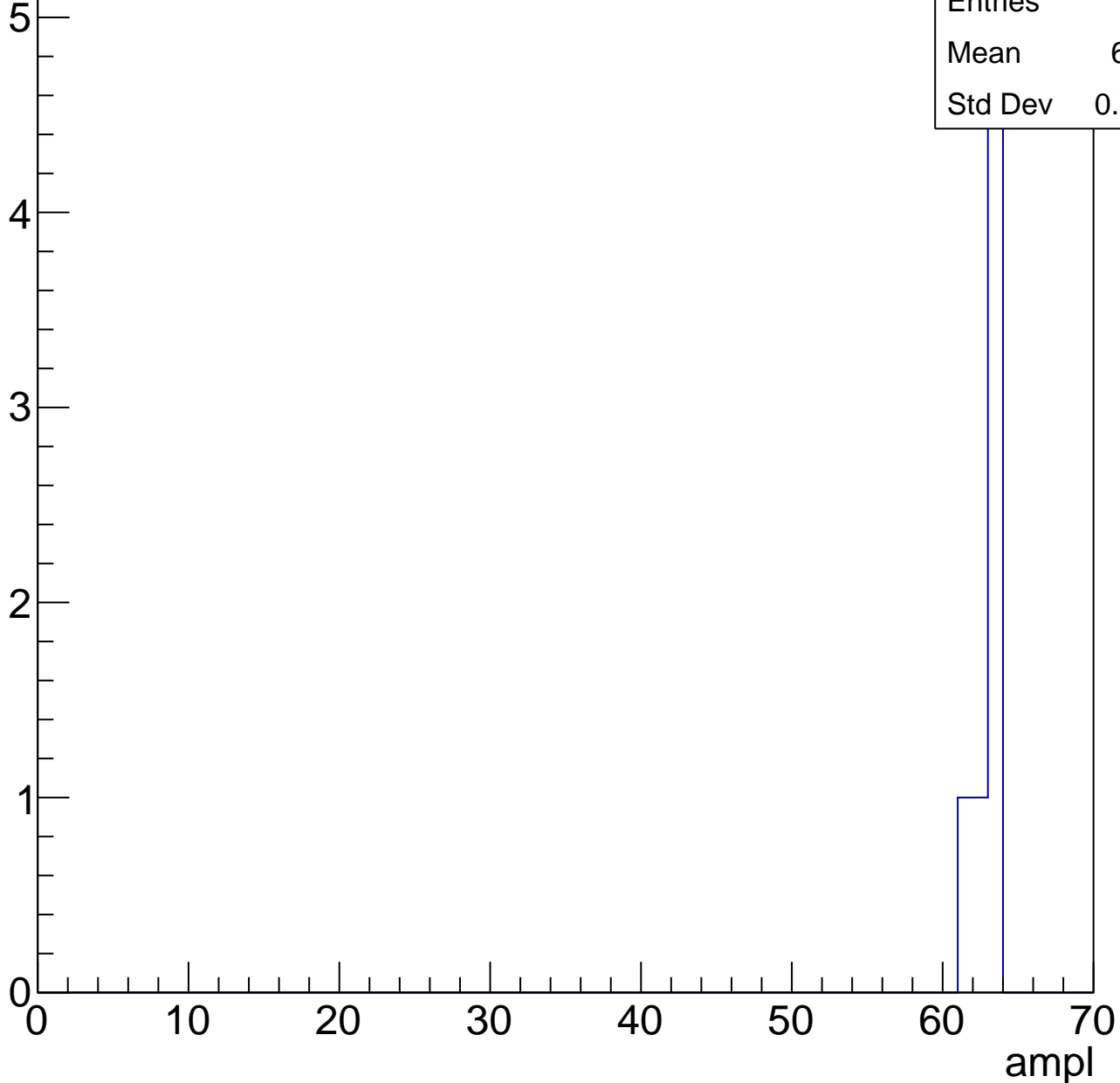


B1L100S, U7-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|--------|
| Entries | 7 |
| Mean | 62.57 |
| Std Dev | 0.7284 |



B1L100S, U7-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch51, adc0

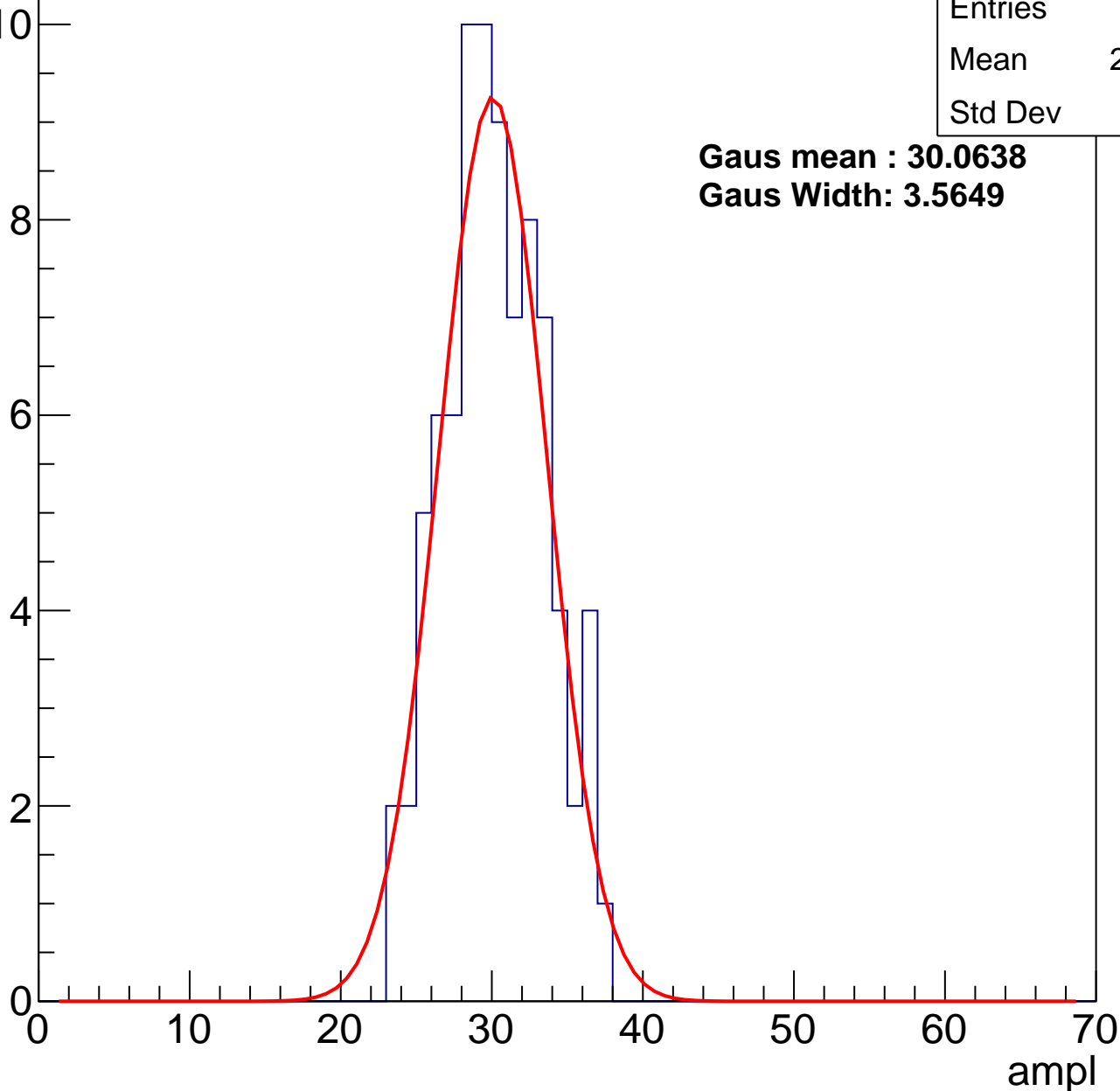
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 29.73 |
| Std Dev | 3.29 |

Gaus mean : 30.0638

Gaus Width: 3.5649



B1L100S, U7-ch51, adc1

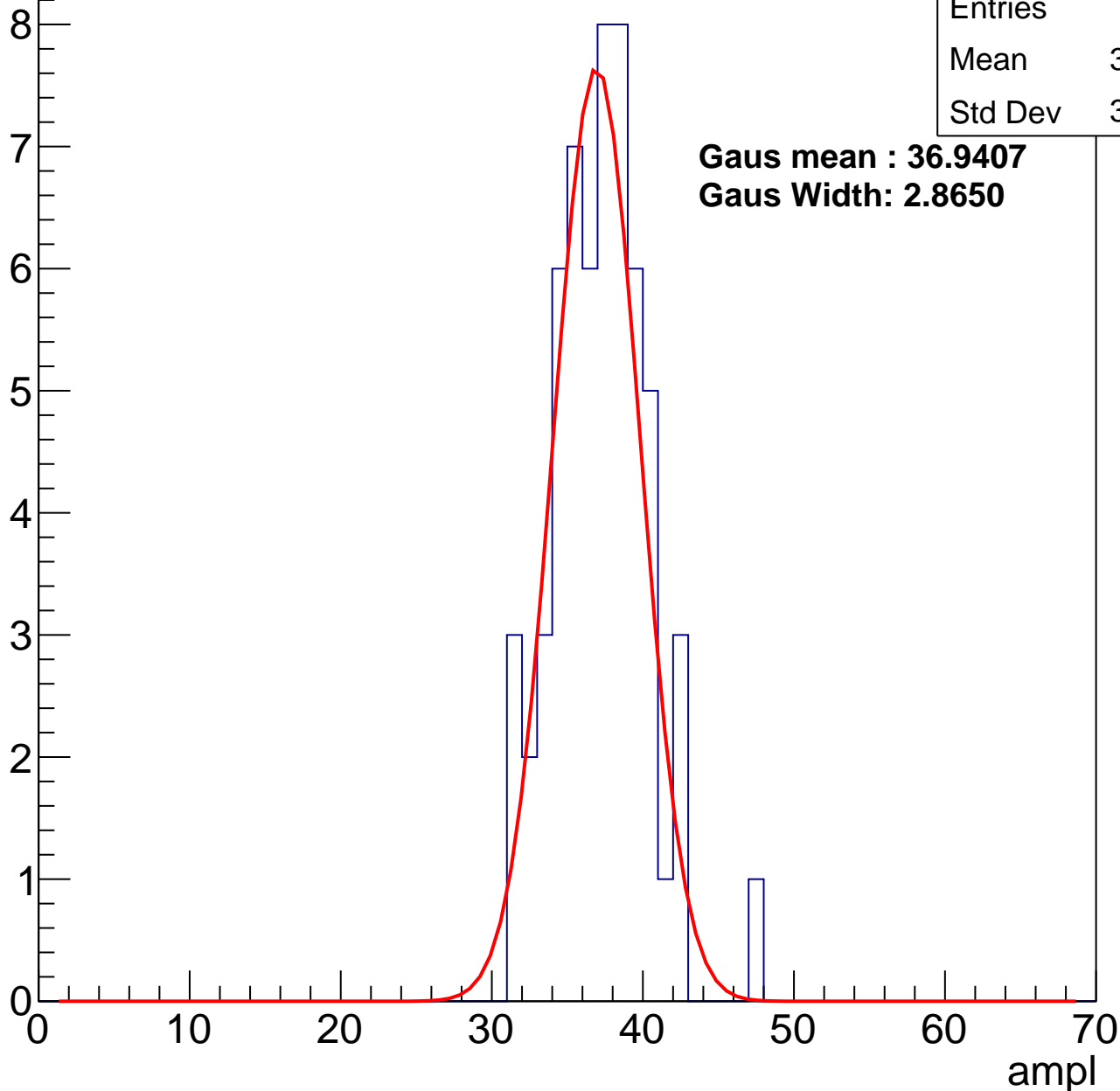
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 36.76 |
| Std Dev | 3.077 |

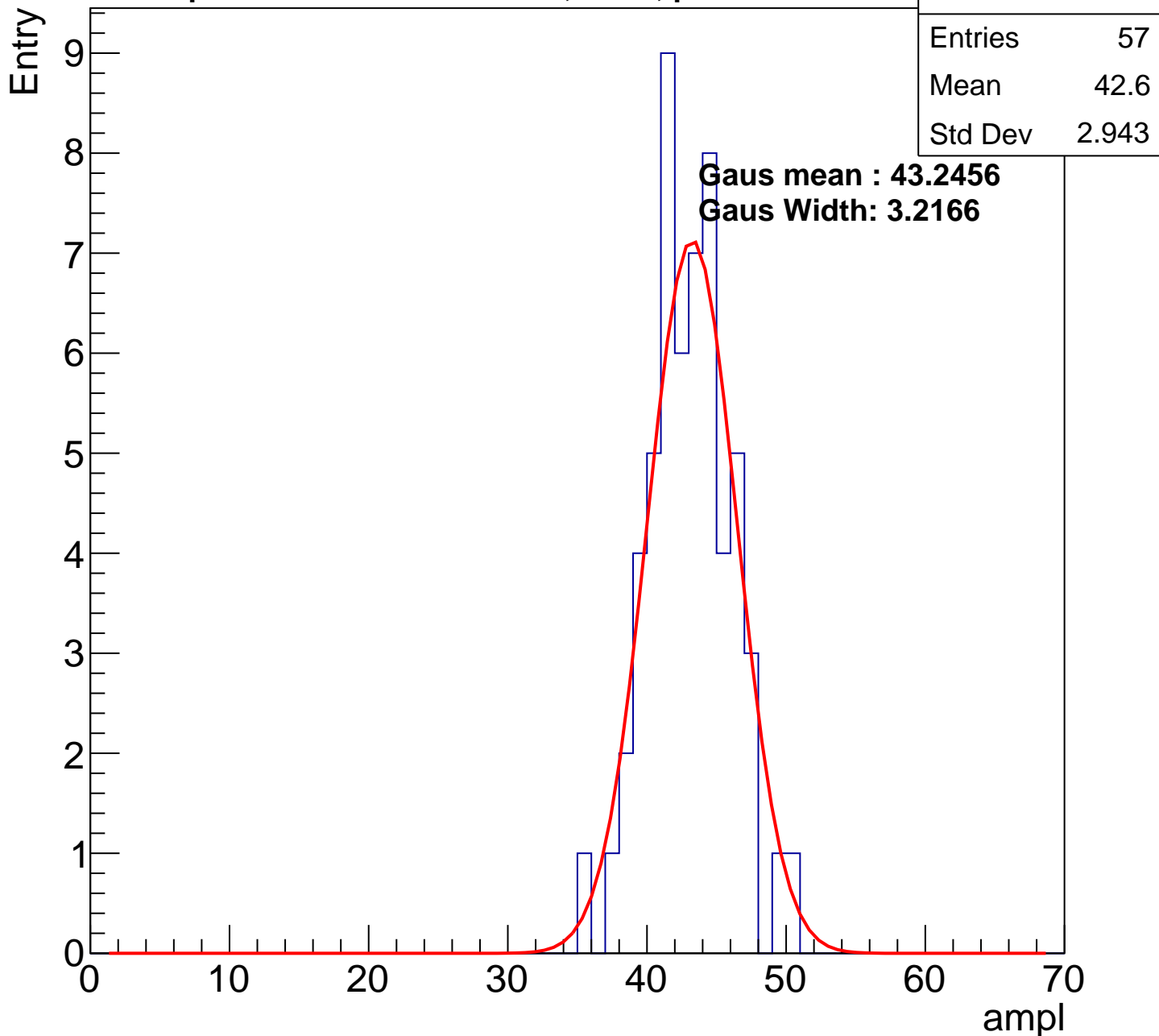
Gaus mean : 36.9407

Gaus Width: 2.8650



B1L100S, U7-ch51, adc2

calib_packv5_042523_0143.root, FC#4, port A2

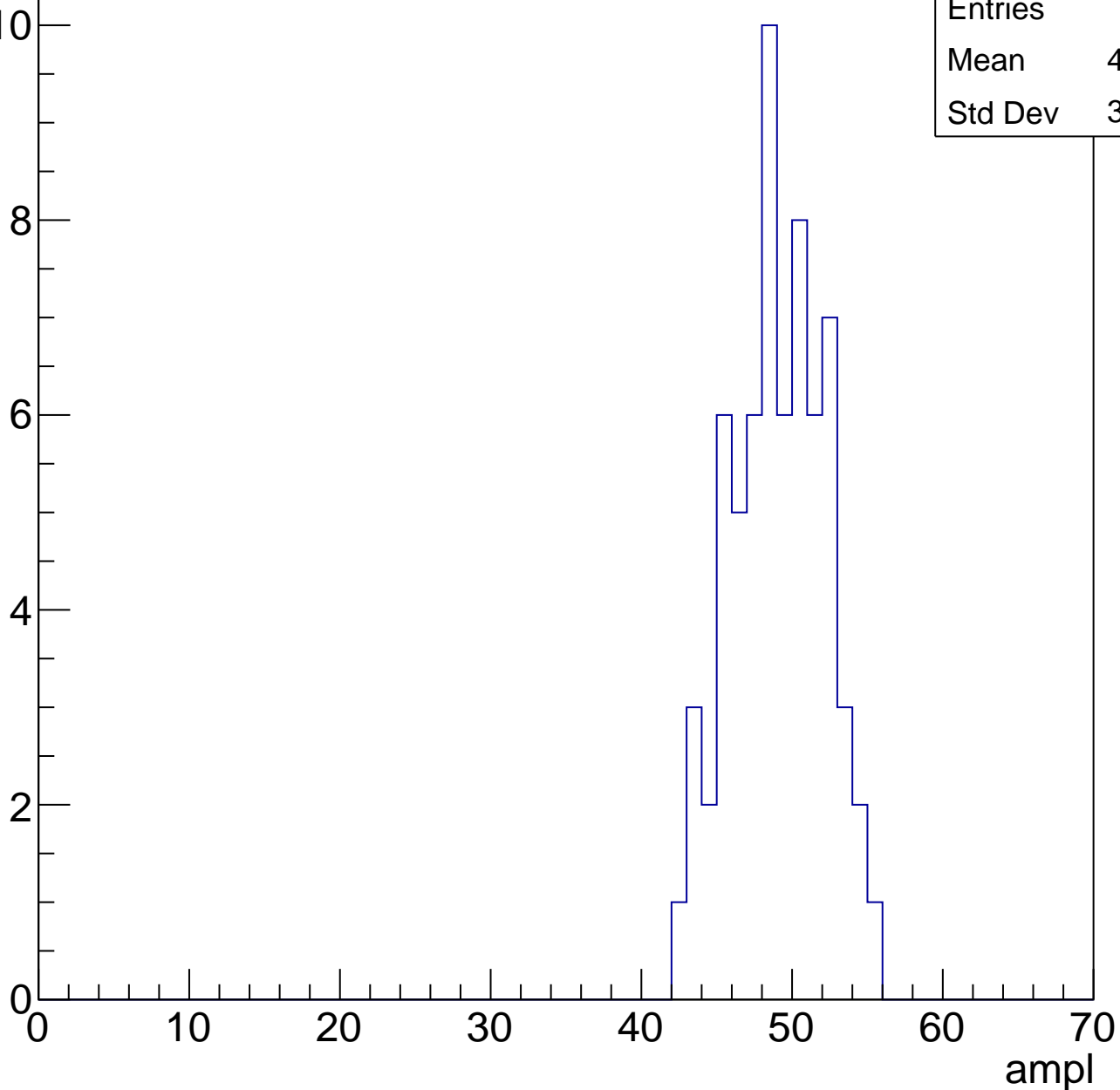


B1L100S, U7-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

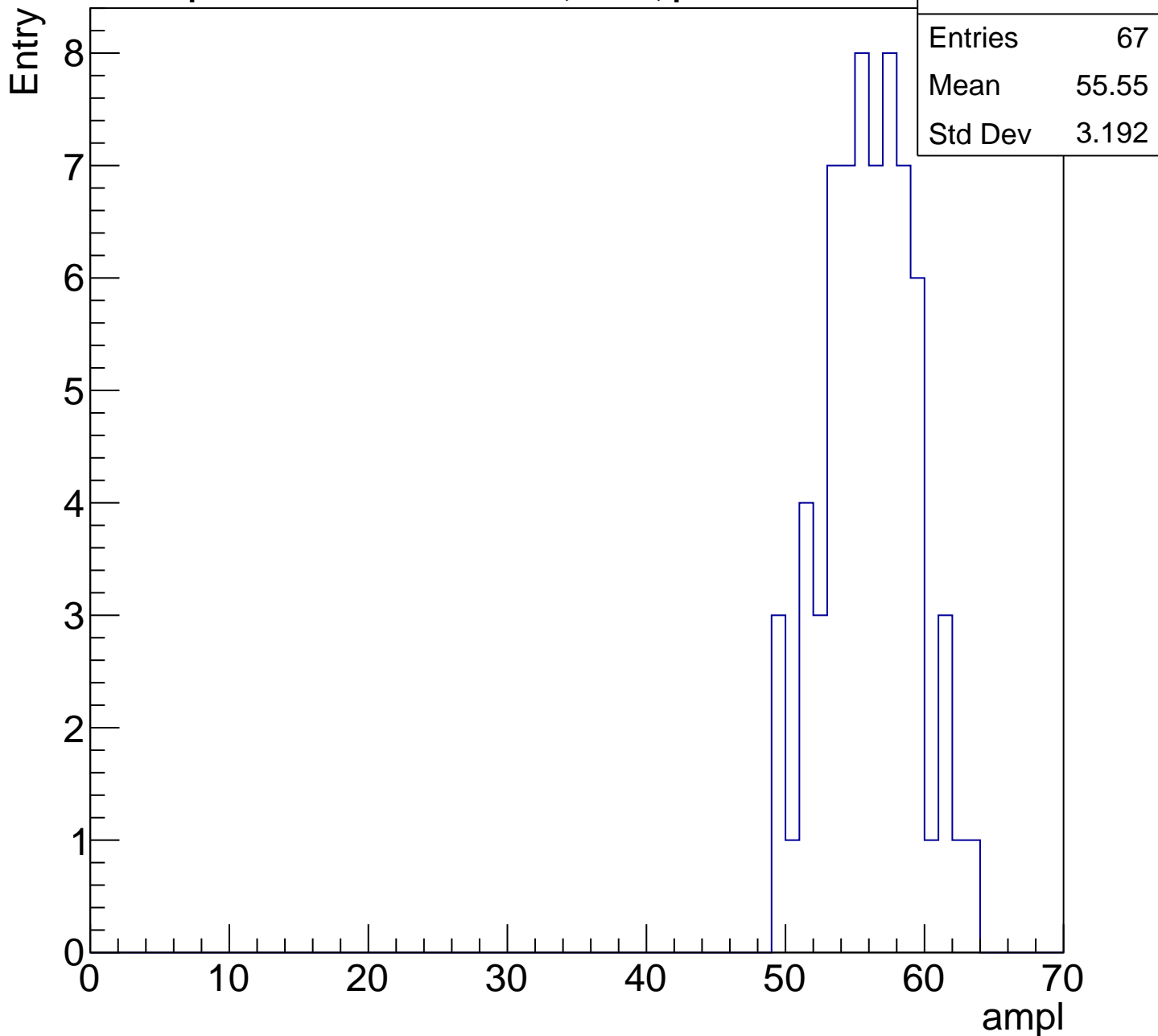
Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.59 |
| Std Dev | 3.015 |



B1L100S, U7-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch51, adc5

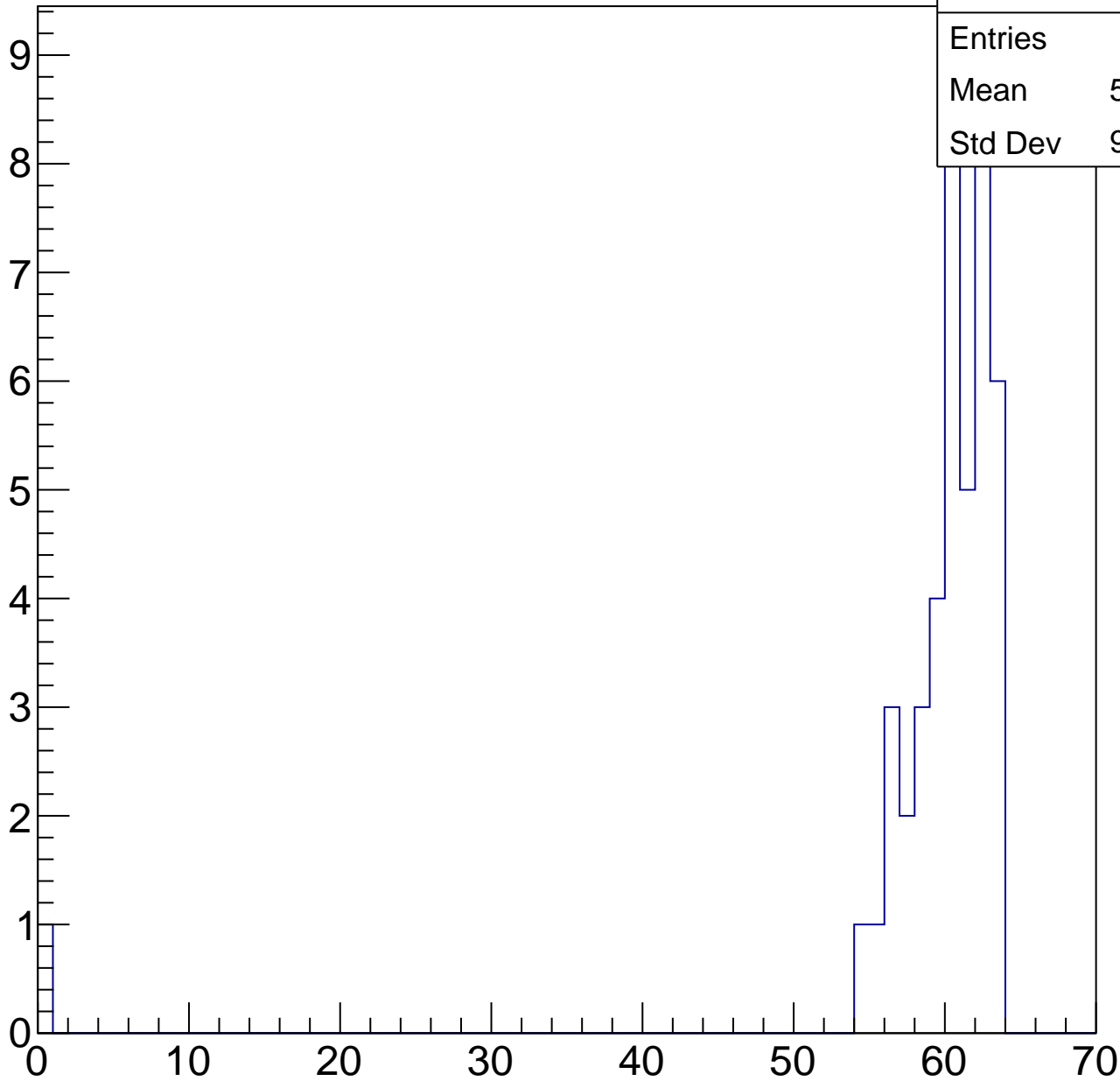
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.68 |
| Std Dev | 9.246 |

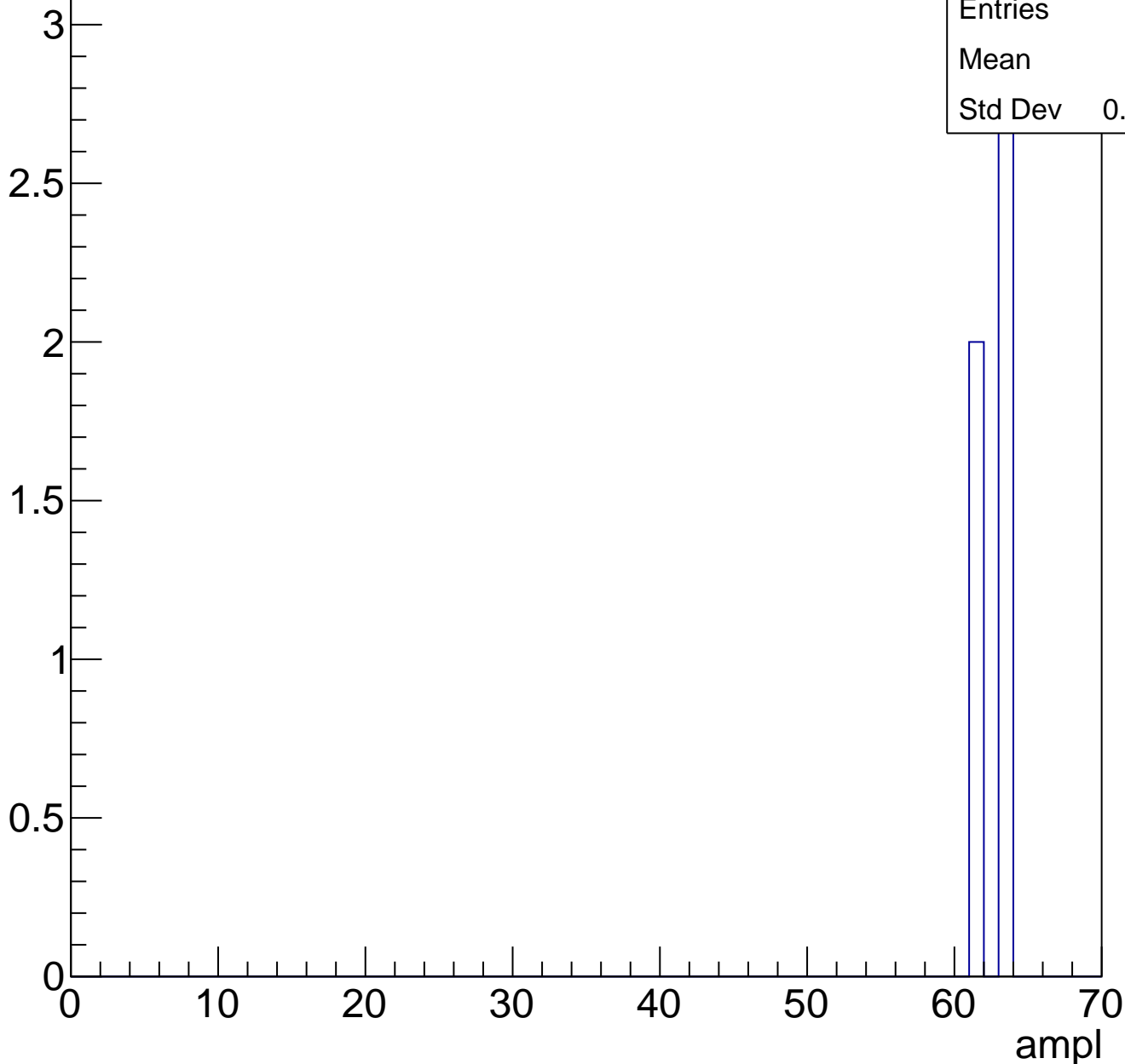
ampl



B1L100S, U7-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

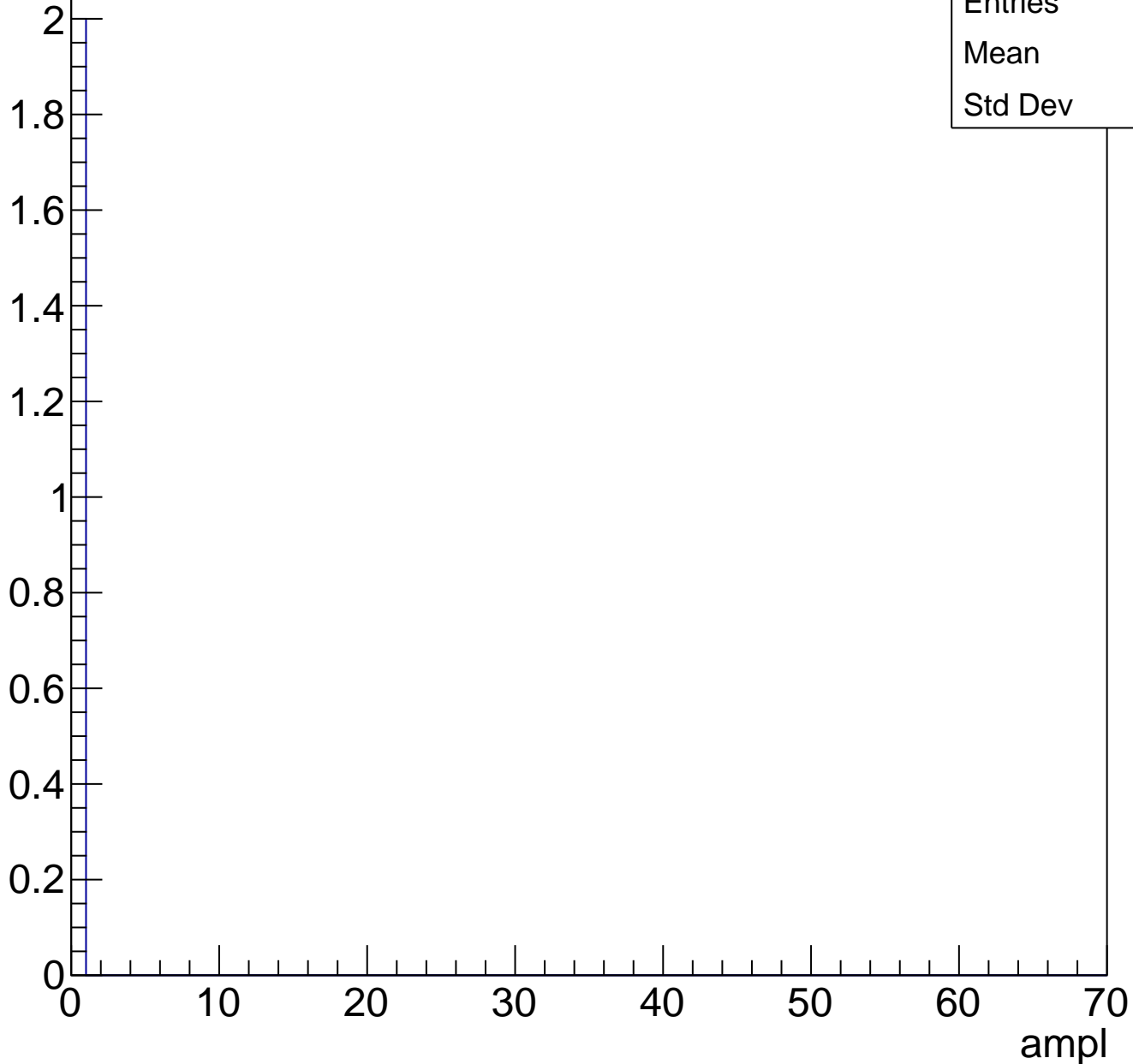
Entry



B1L100S, U7-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch52, adc0

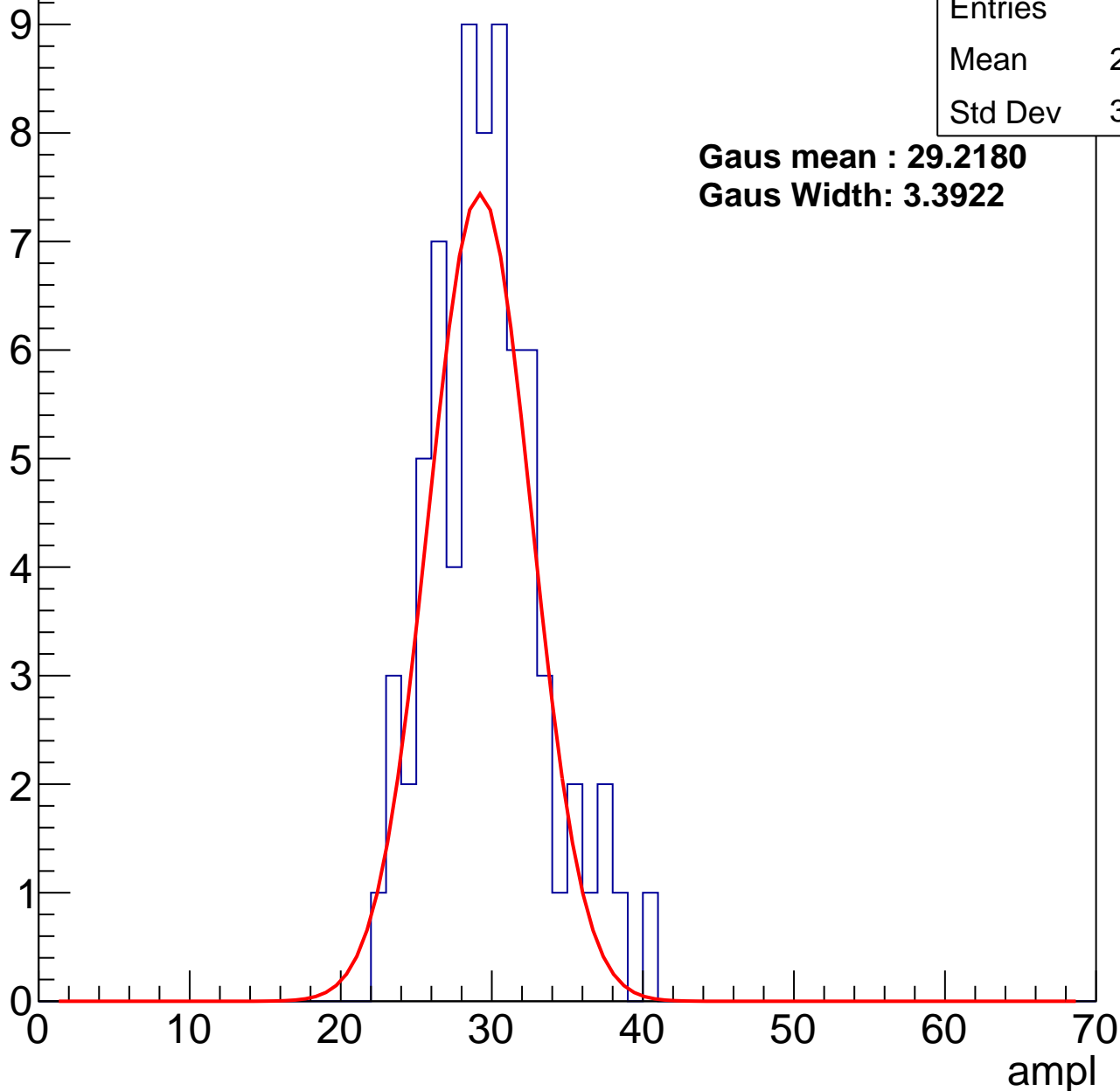
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.25 |
| Std Dev | 3.718 |

Gaus mean : 29.2180

Gaus Width: 3.3922



B1L100S, U7-ch52, adc1

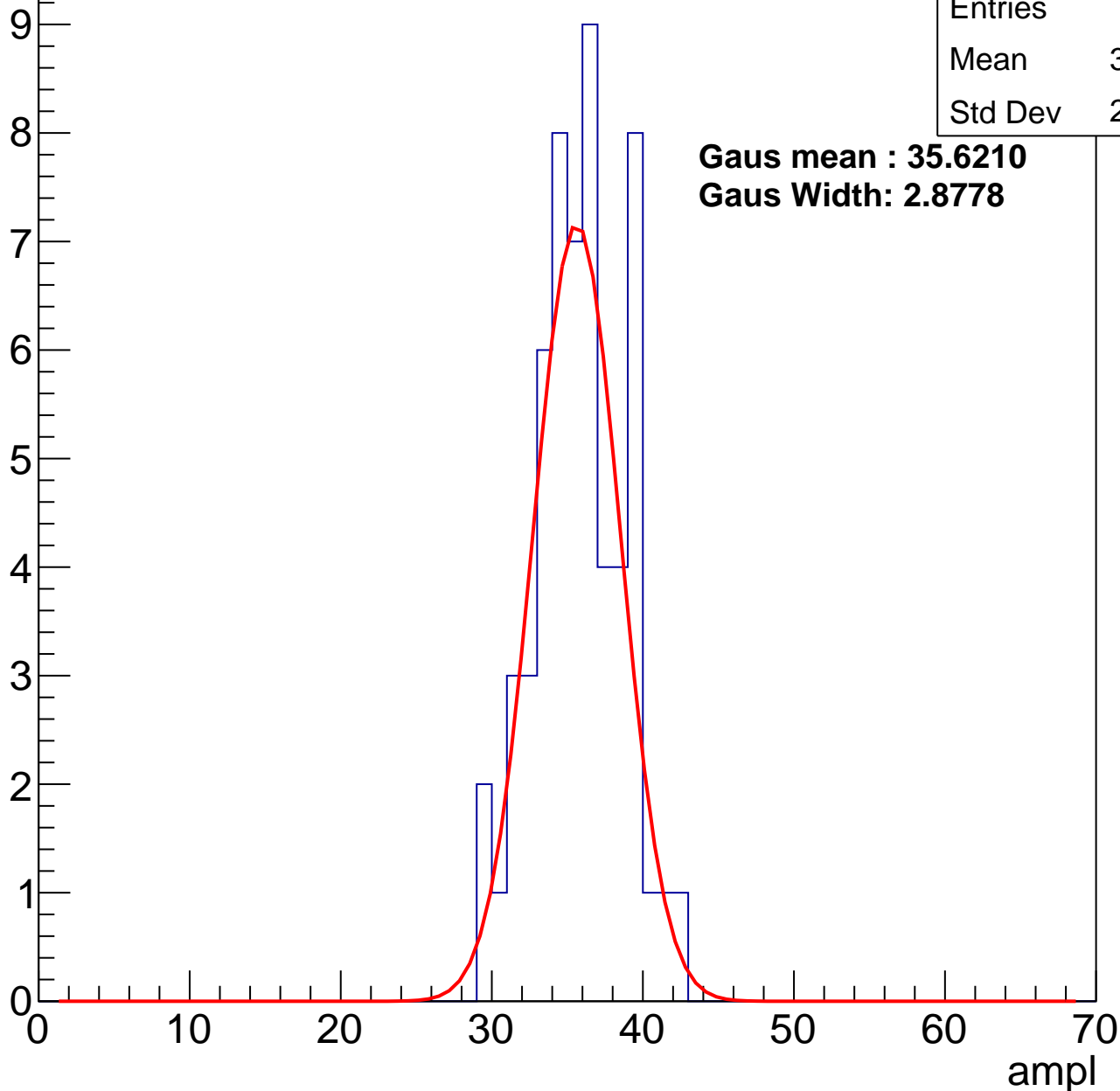
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 35.36 |
| Std Dev | 2.923 |

Gaus mean : 35.6210

Gaus Width: 2.8778



B1L100S, U7-ch52, adc2

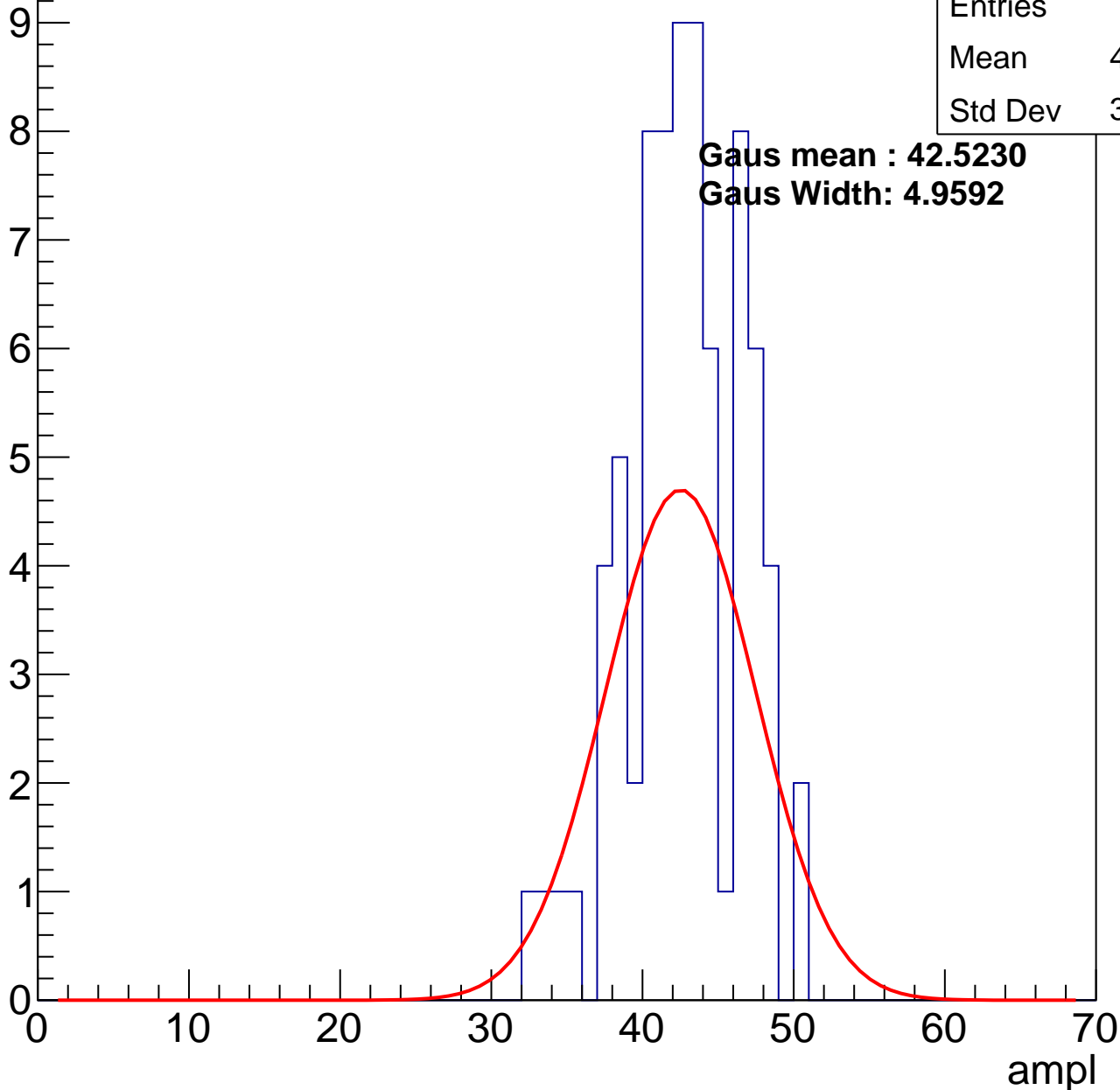
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 42.29 |
| Std Dev | 3.835 |

Gaus mean : 42.5230

Gaus Width: 4.9592

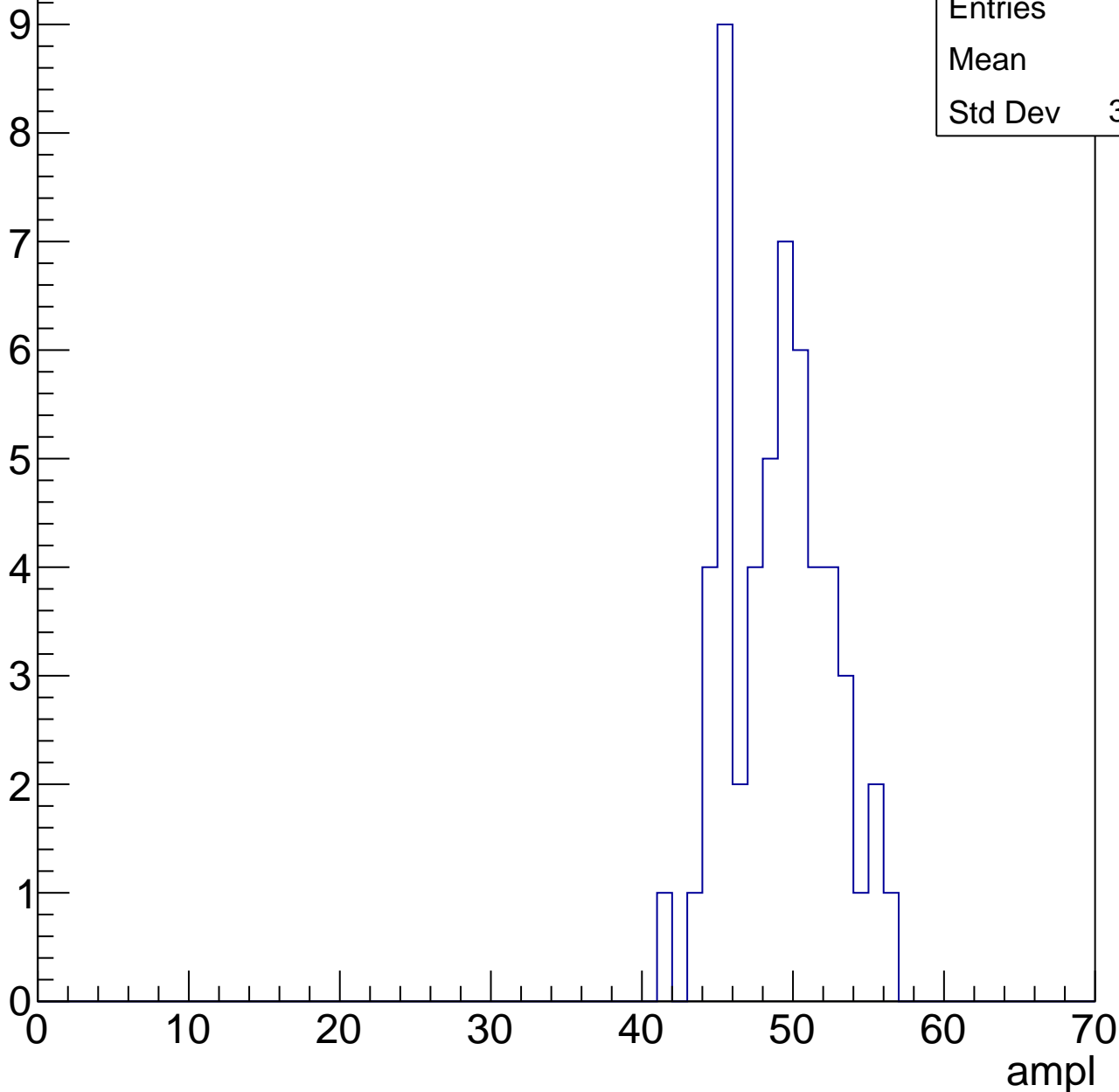


B1L100S, U7-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

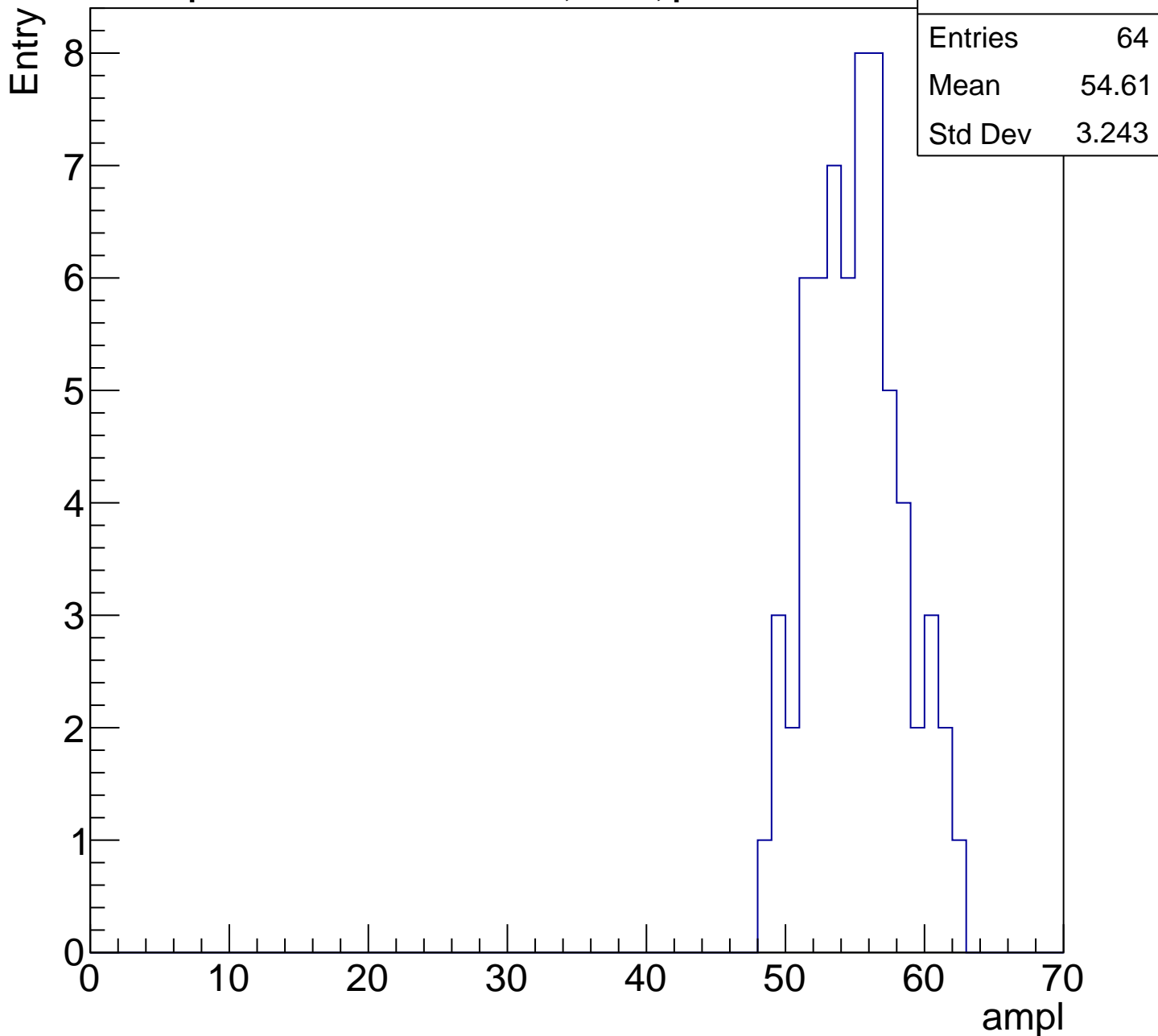
Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 48.5 |
| Std Dev | 3.393 |



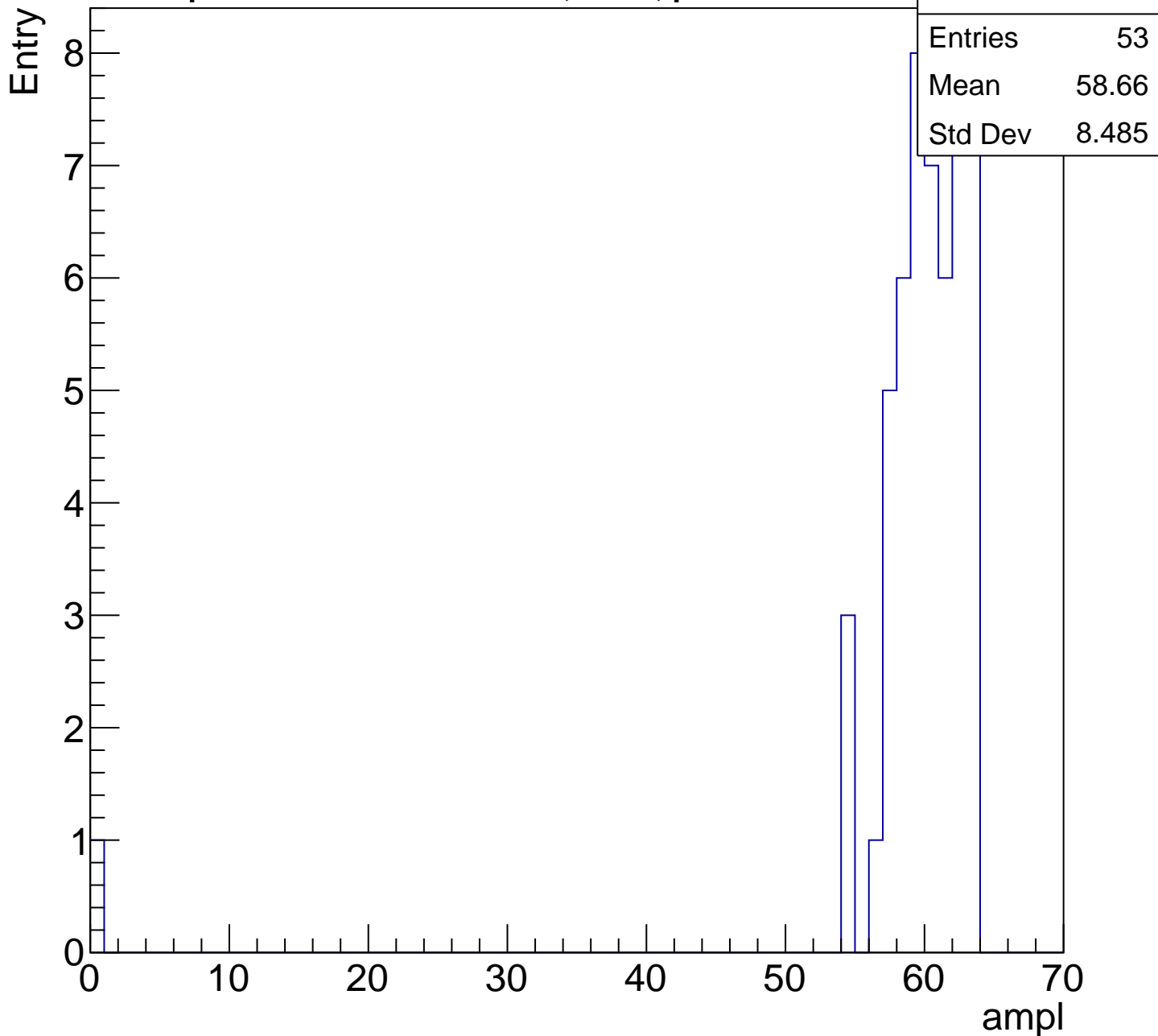
B1L100S, U7-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch52, adc5

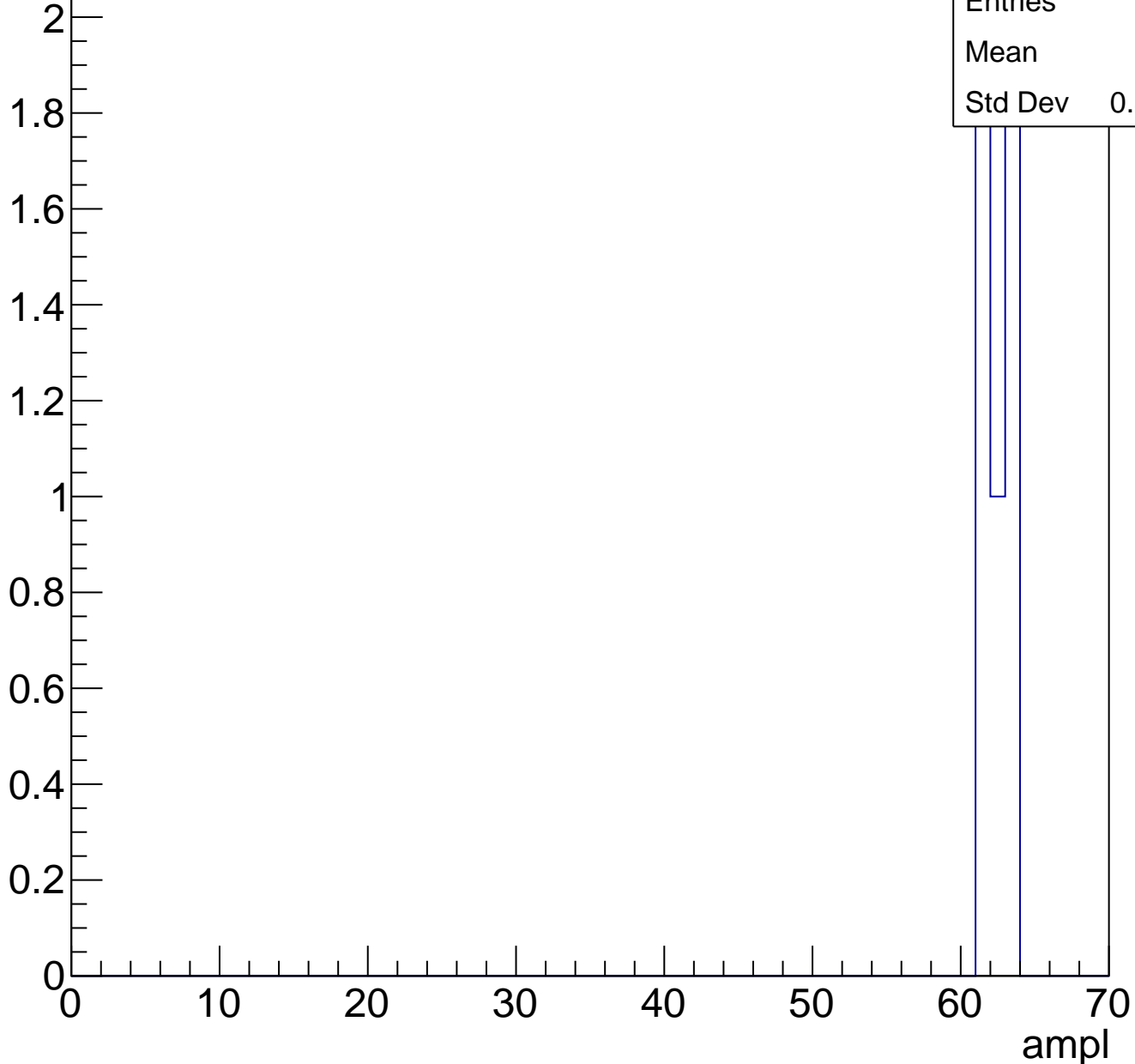
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch53, adc0

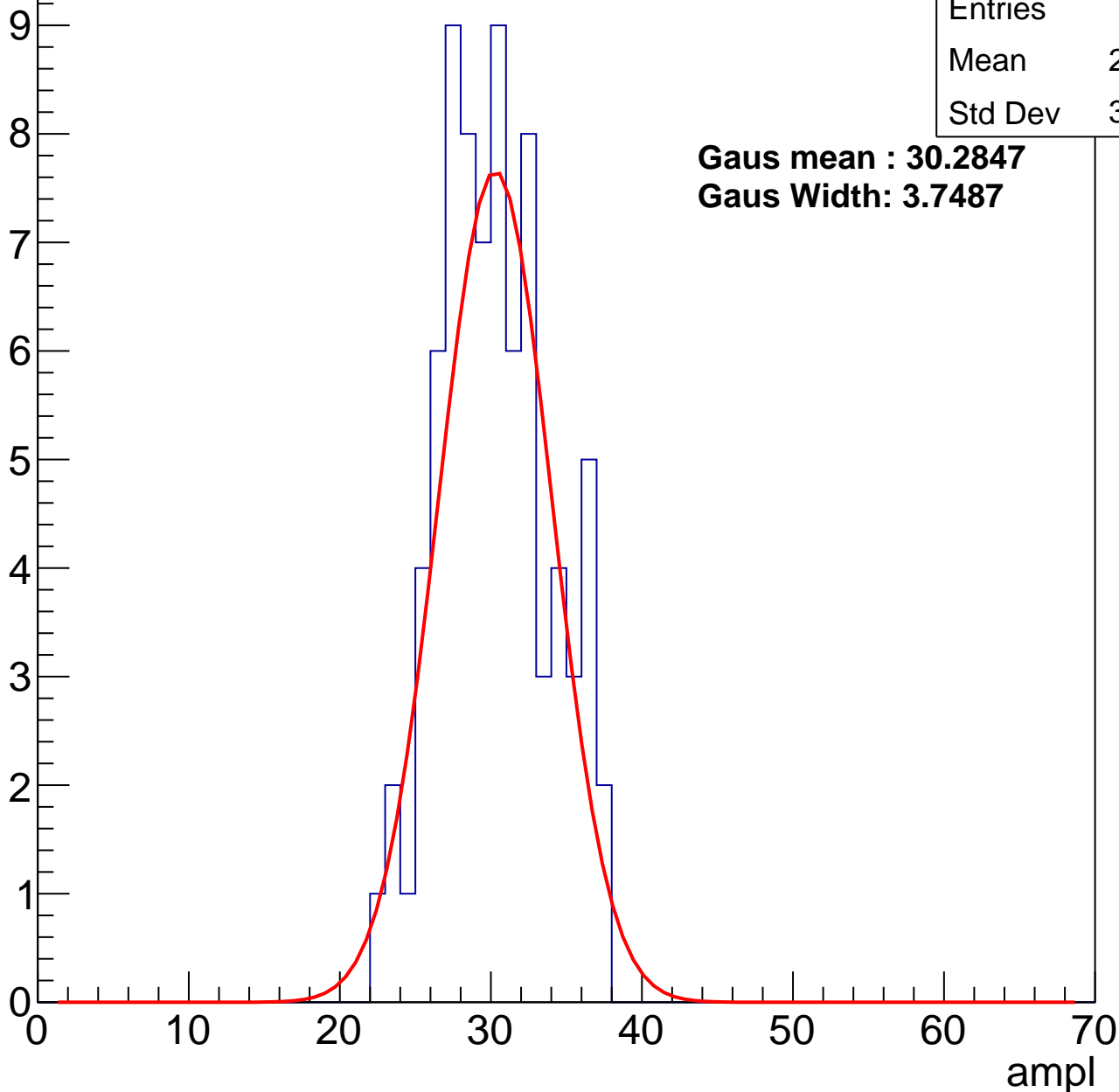
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 29.79 |
| Std Dev | 3.568 |

Gaus mean : 30.2847

Gaus Width: 3.7487



B1L100S, U7-ch53, adc1

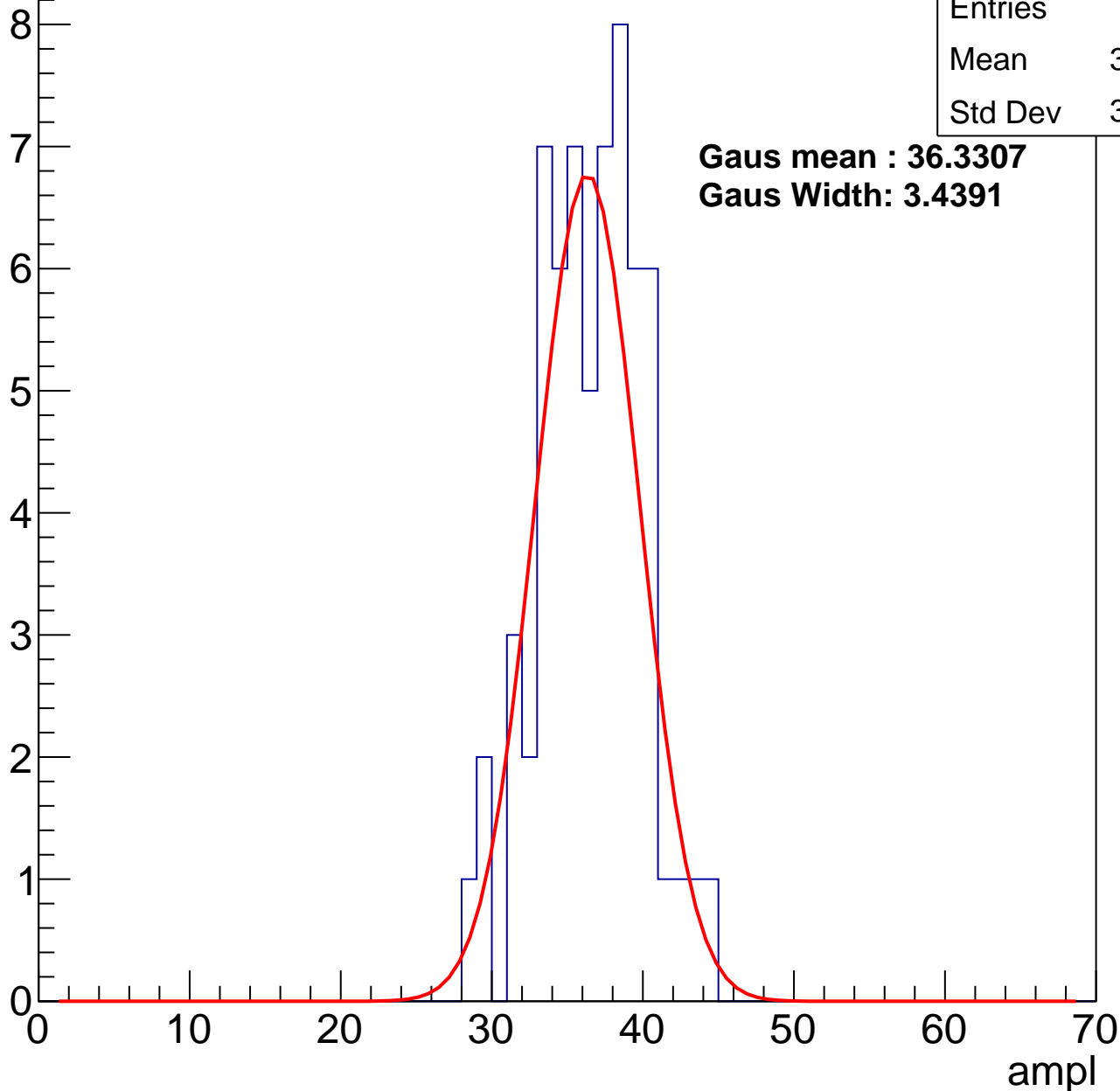
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.09 |
| Std Dev | 3.367 |

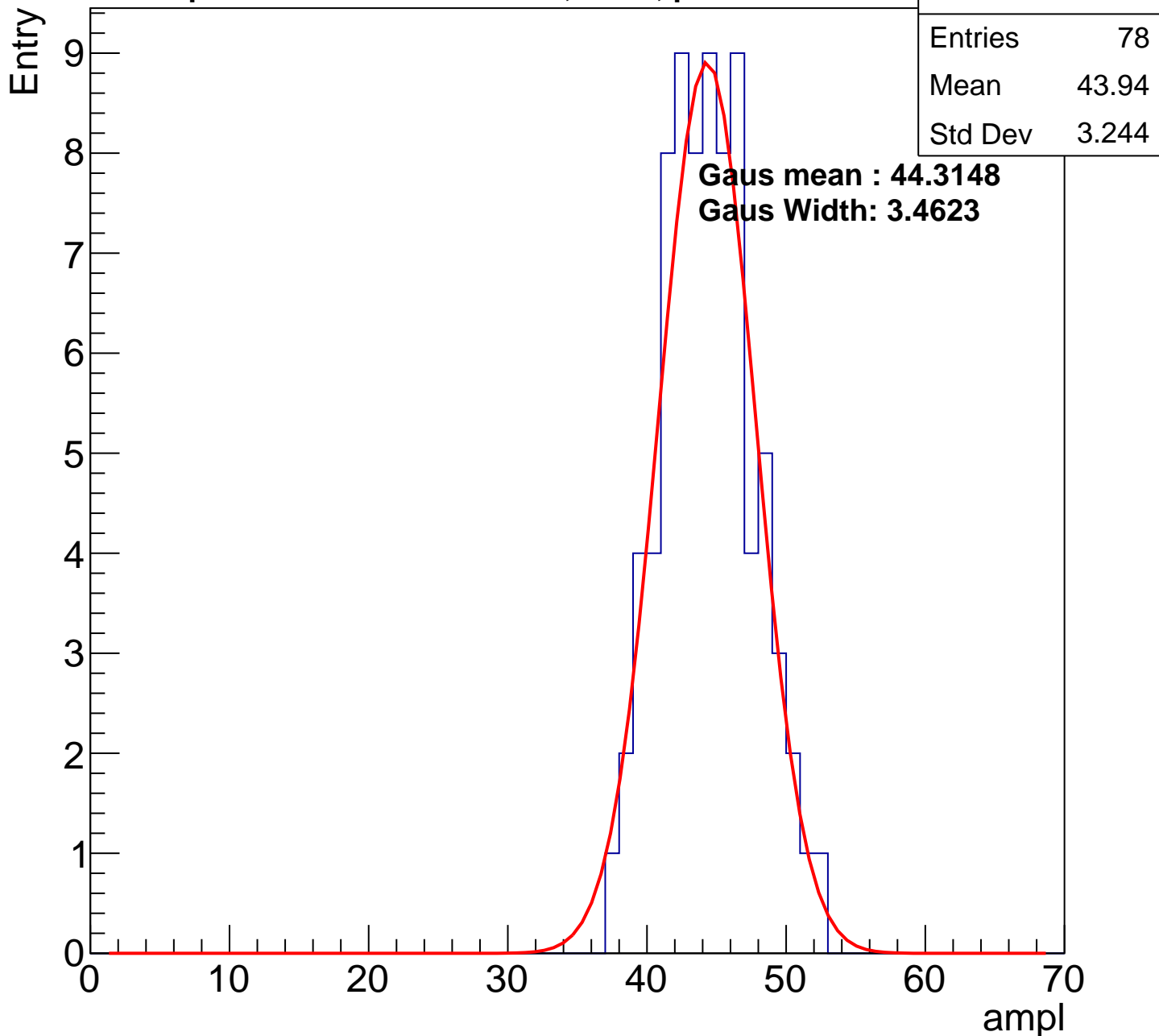
Gaus mean : 36.3307

Gaus Width: 3.4391



B1L100S, U7-ch53, adc2

calib_packv5_042523_0143.root, FC#4, port A2

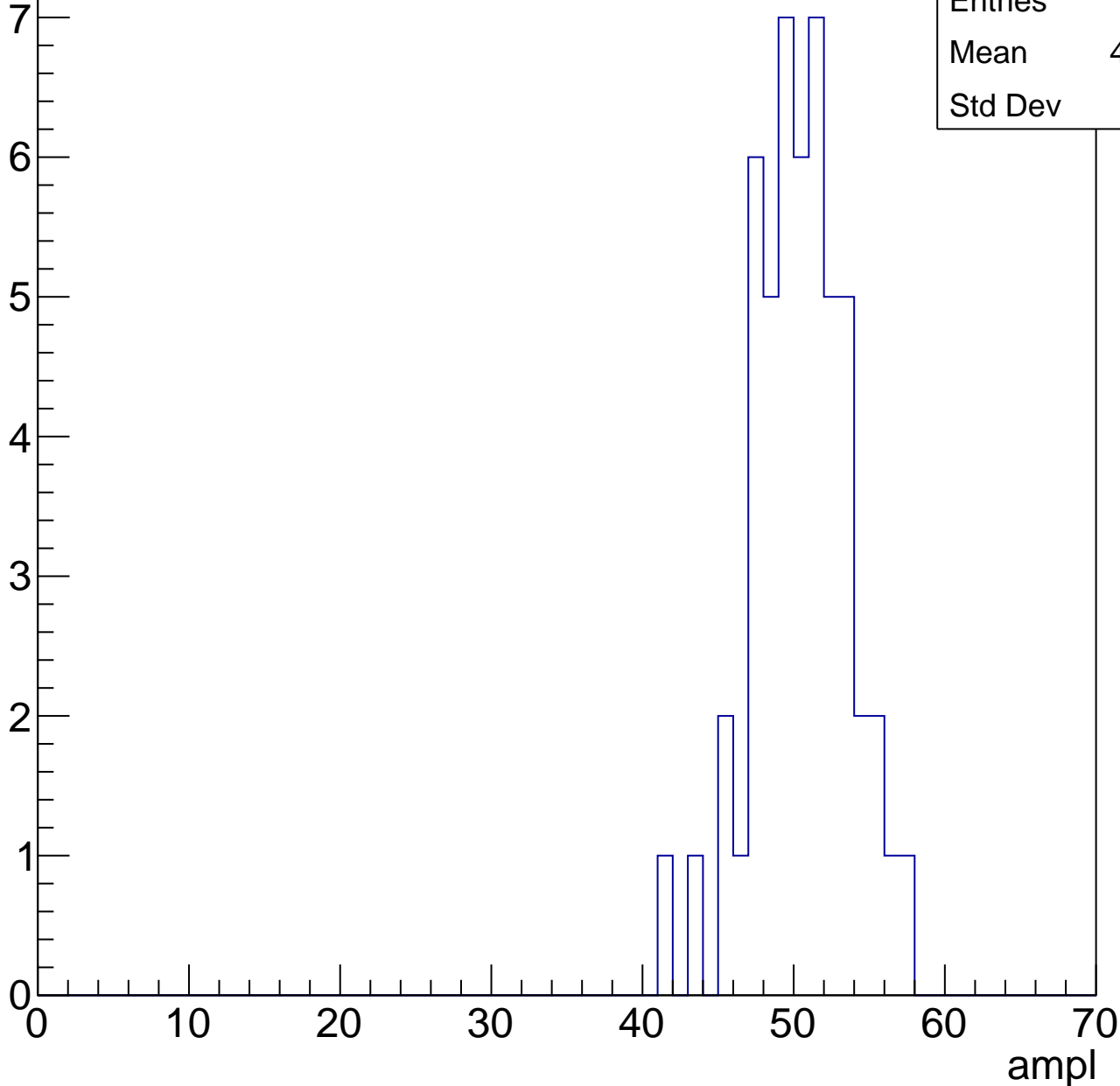


B1L100S, U7-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

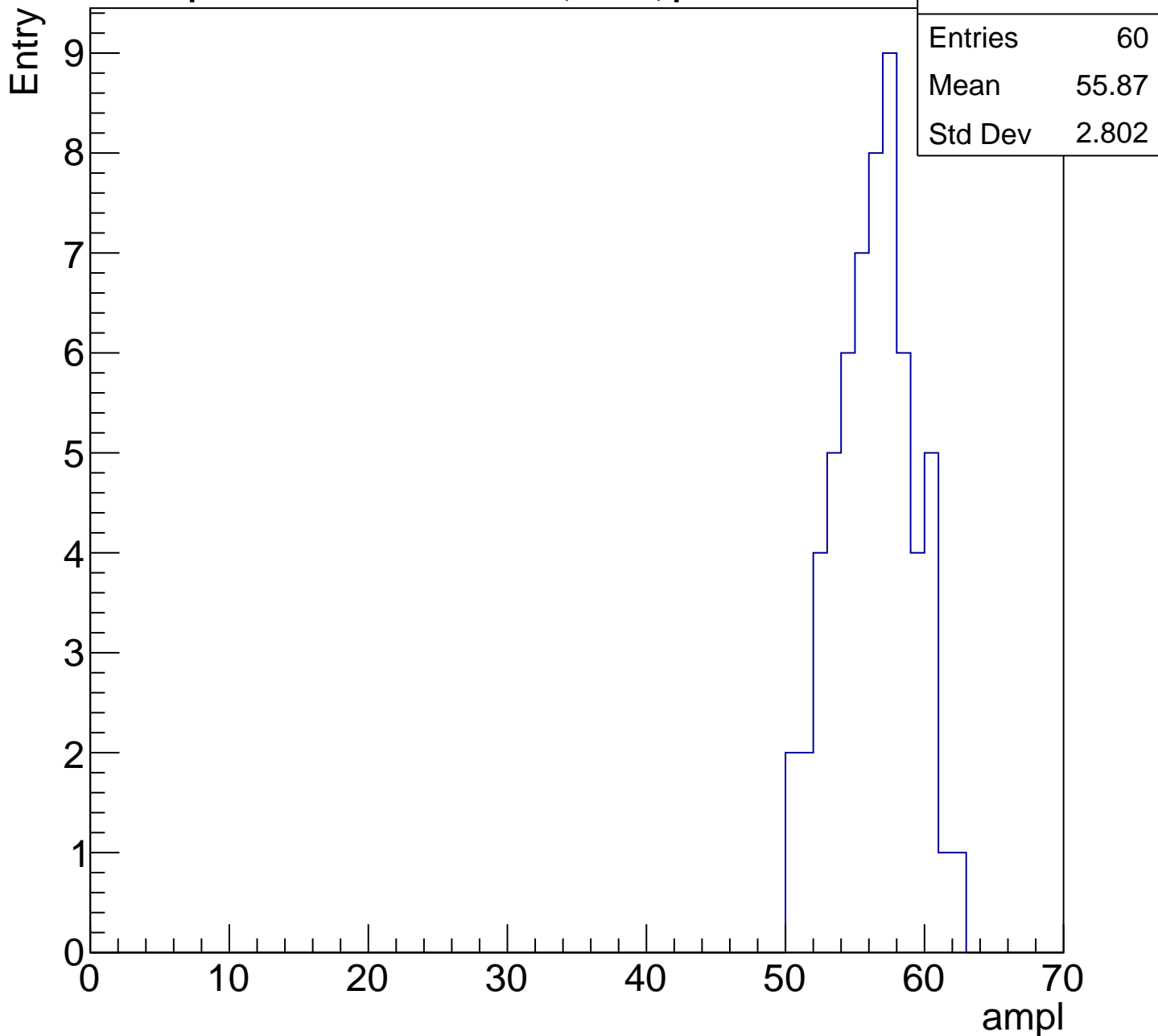
Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 49.96 |
| Std Dev | 3.15 |



B1L100S, U7-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

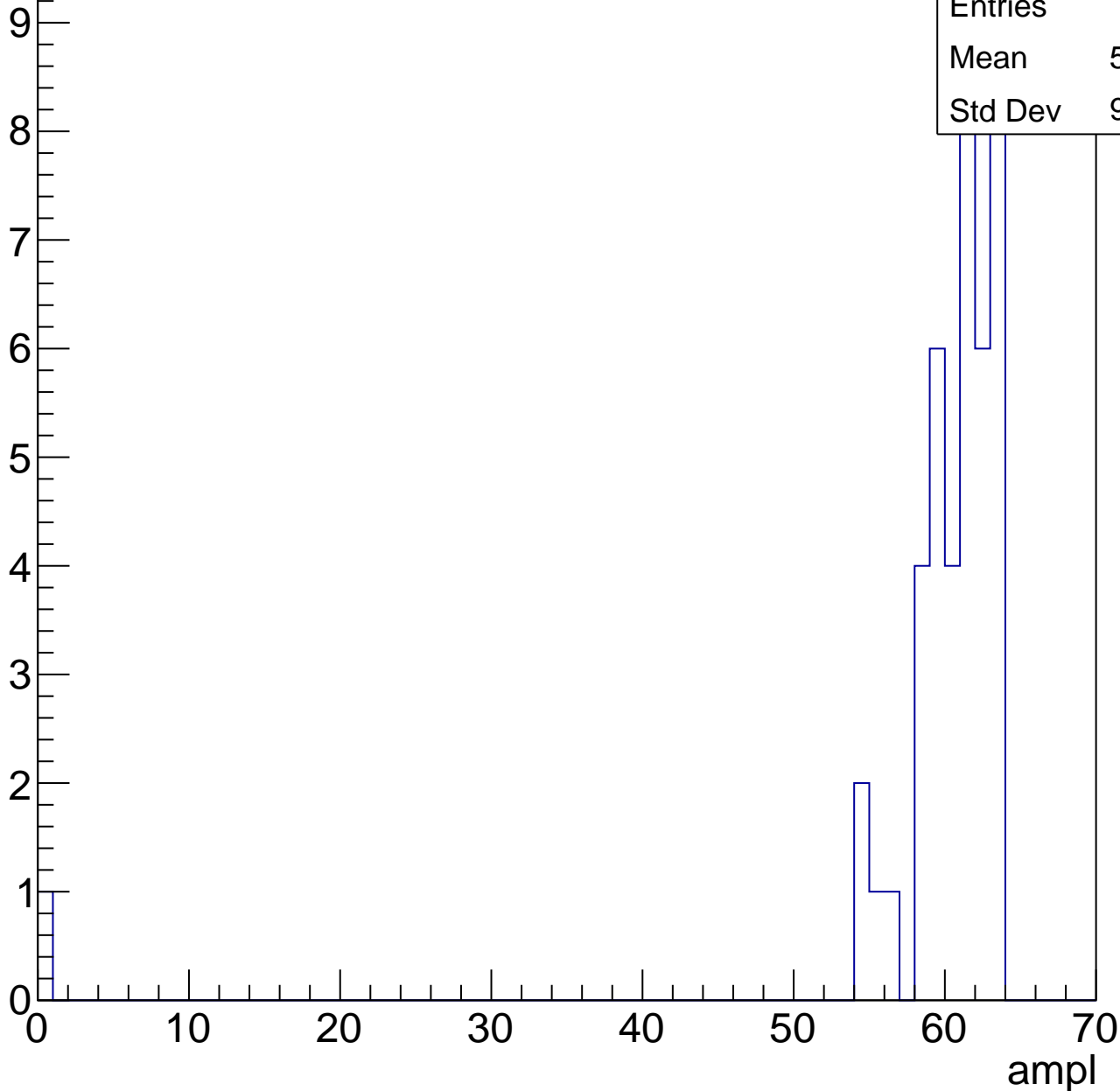


B1L100S, U7-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

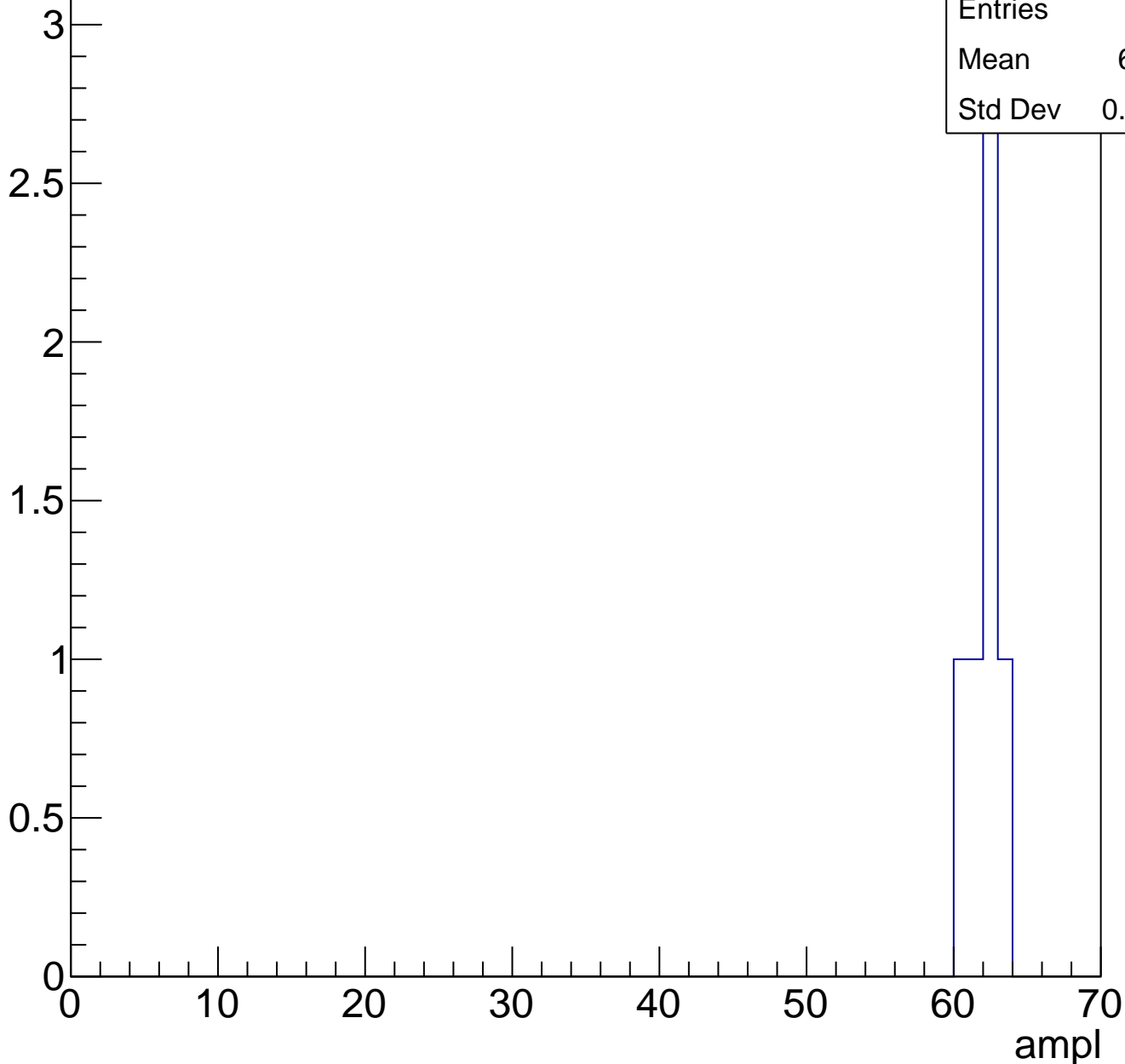
| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.86 |
| Std Dev | 9.503 |



B1L100S, U7-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch54, adc0

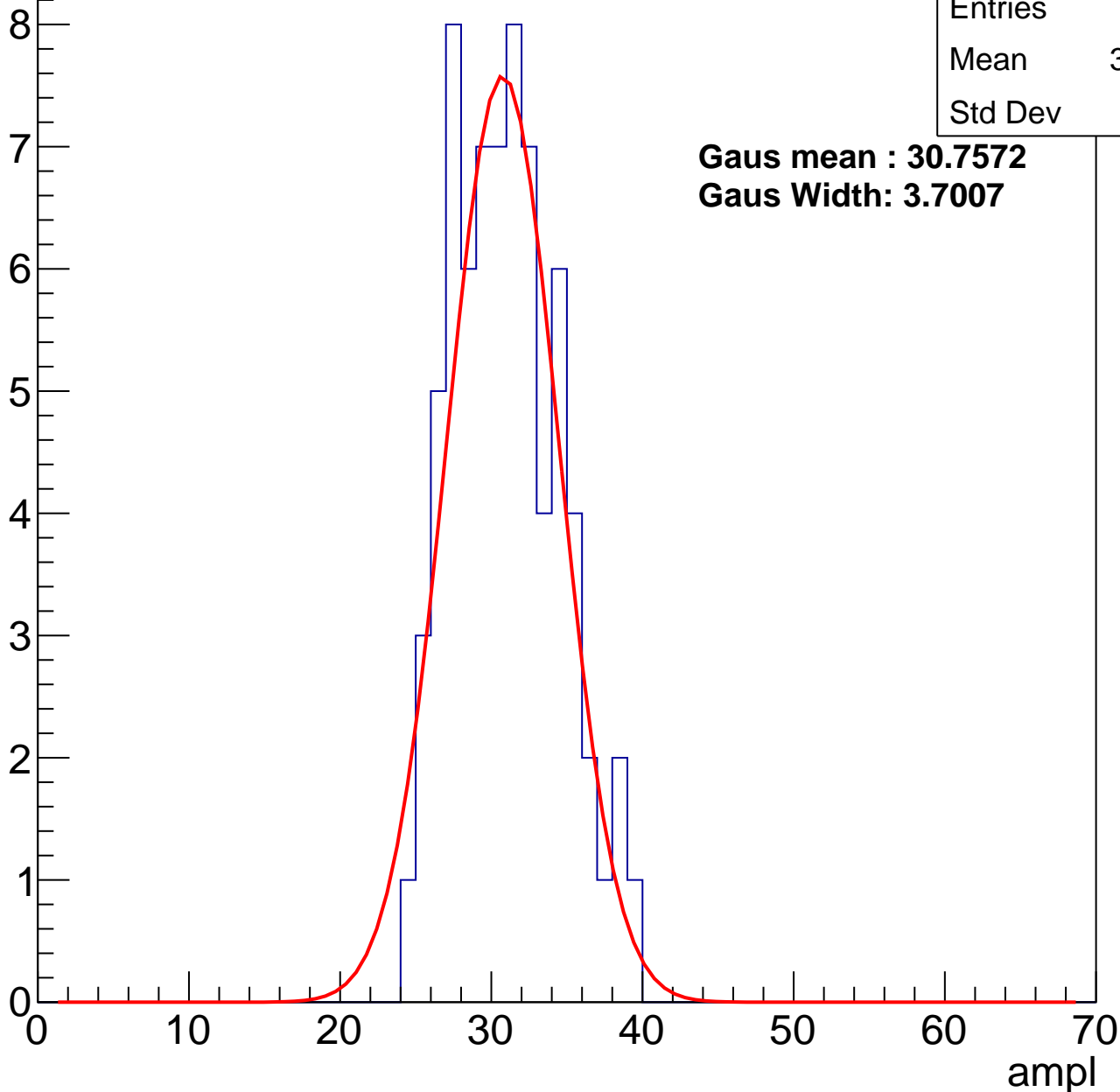
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 30.53 |
| Std Dev | 3.48 |

Gaus mean : 30.7572

Gaus Width: 3.7007



B1L100S, U7-ch54, adc1

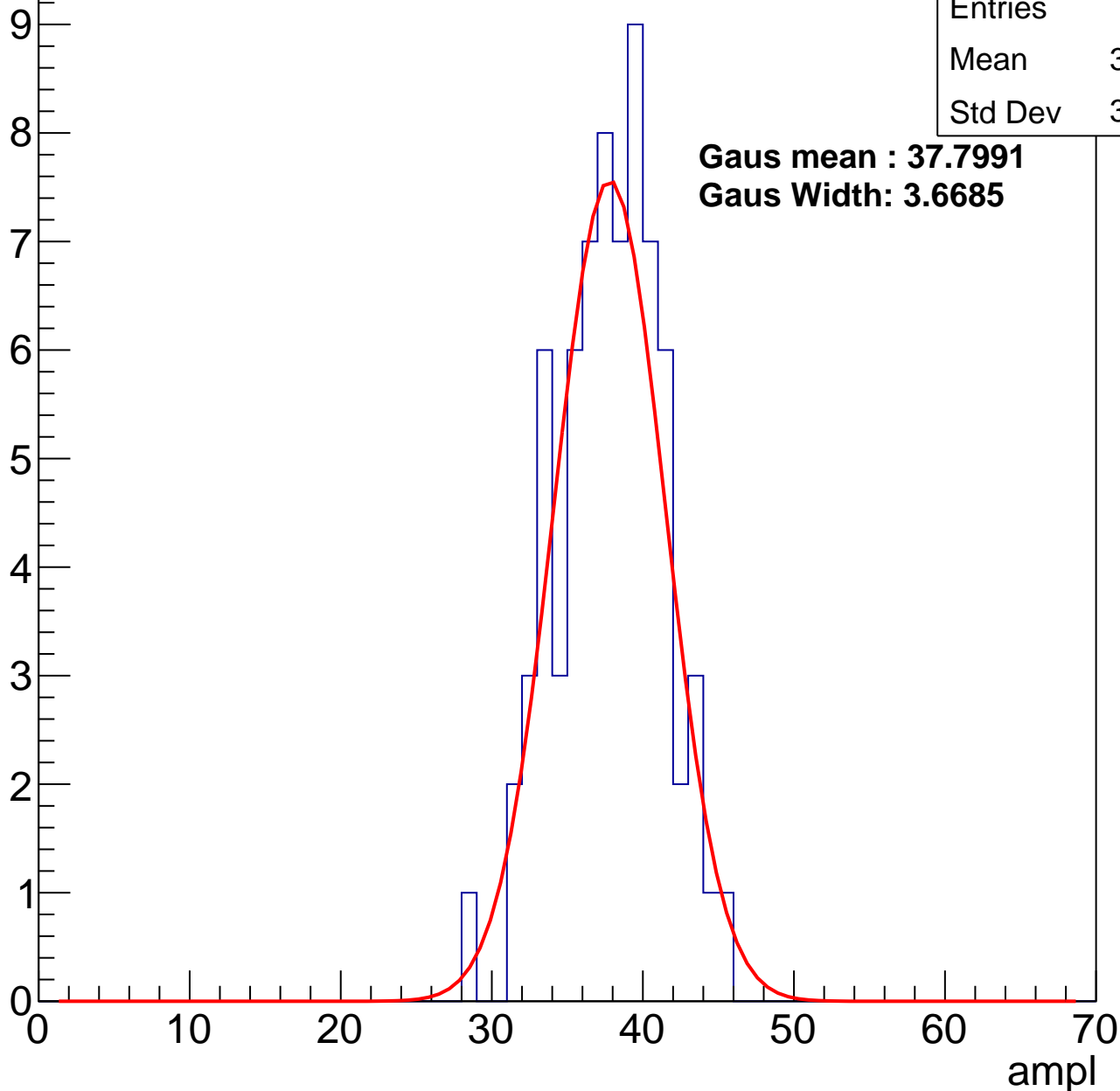
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.35 |
| Std Dev | 3.428 |

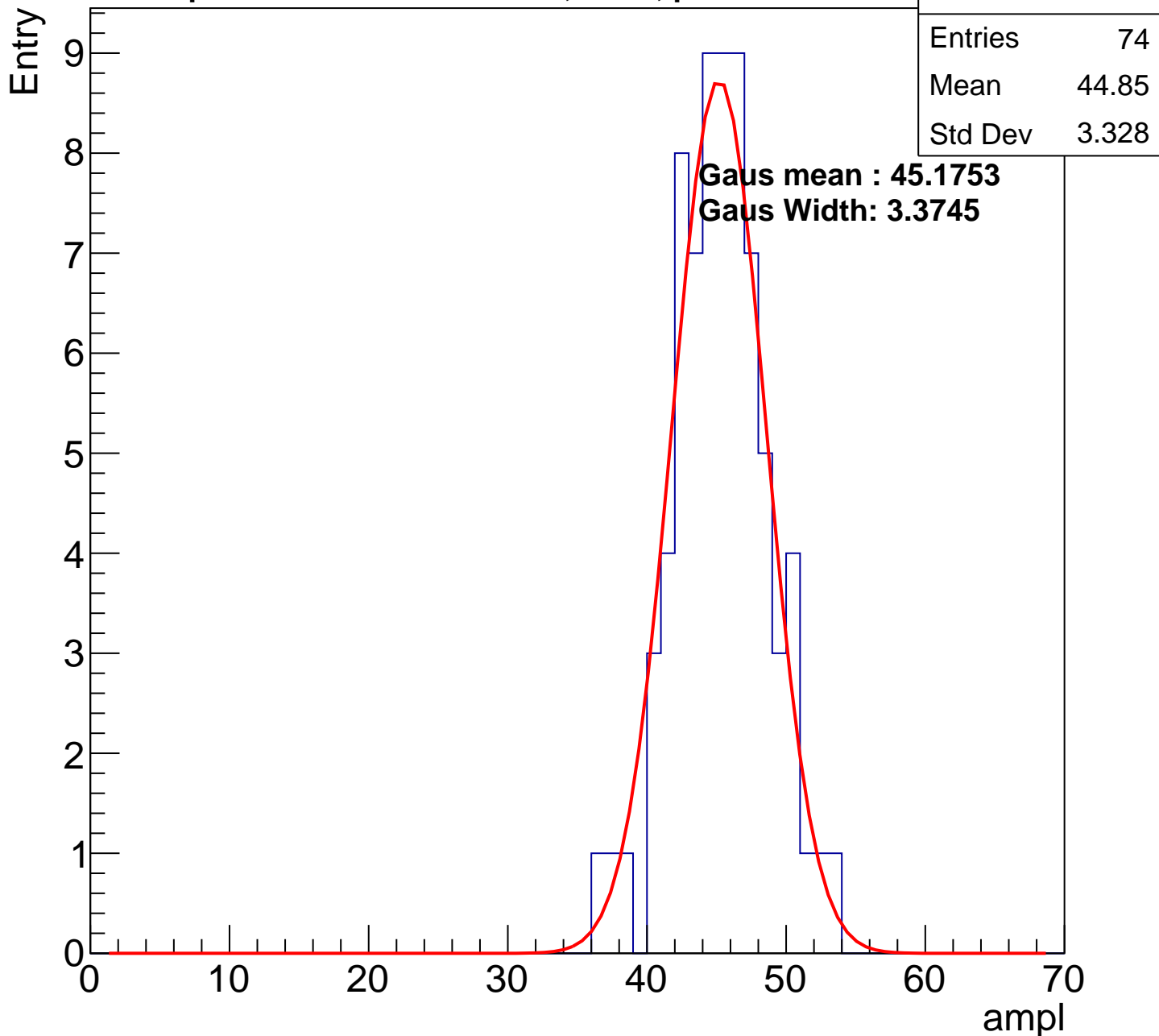
Gaus mean : 37.7991

Gaus Width: 3.6685



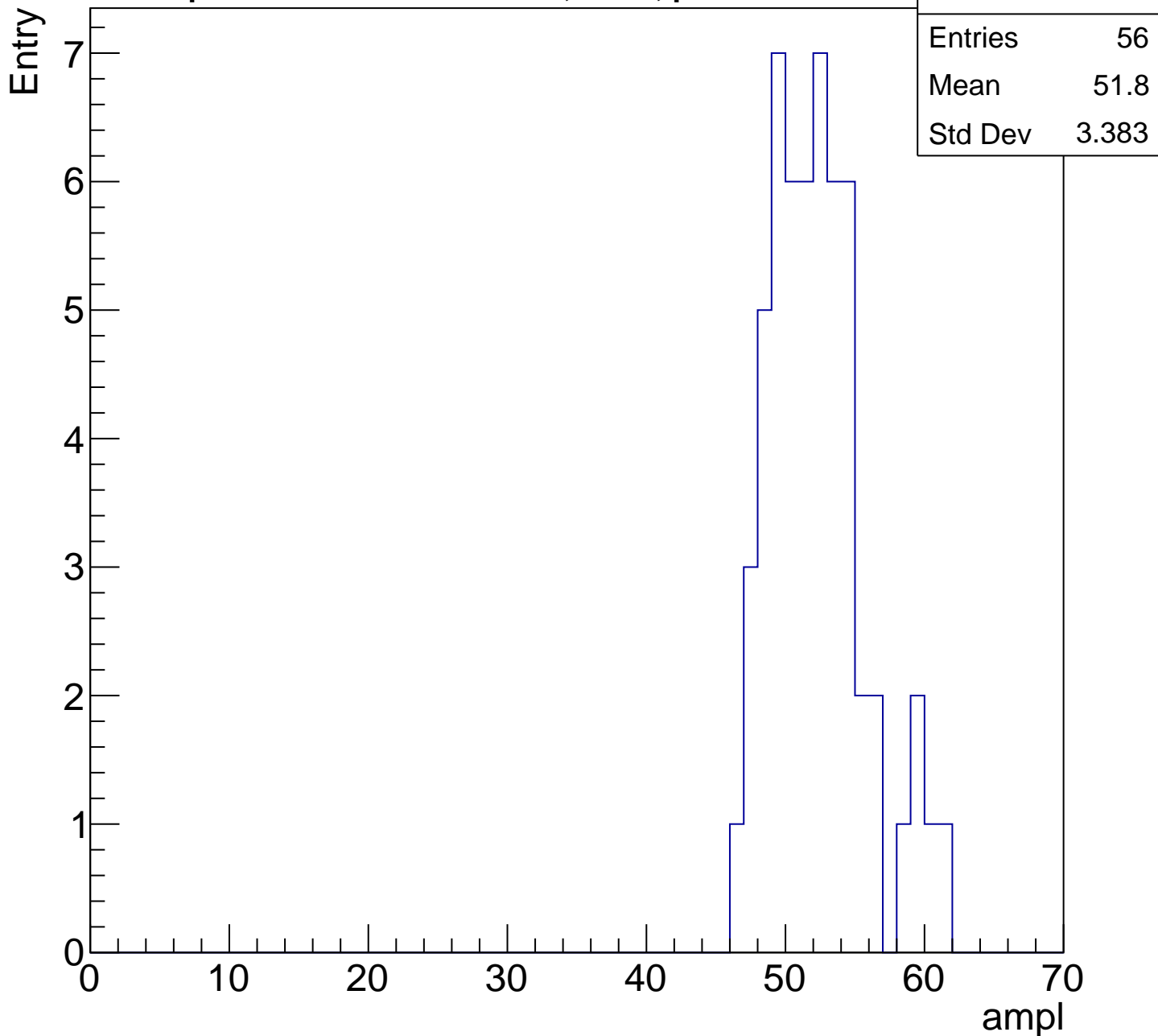
B1L100S, U7-ch54, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

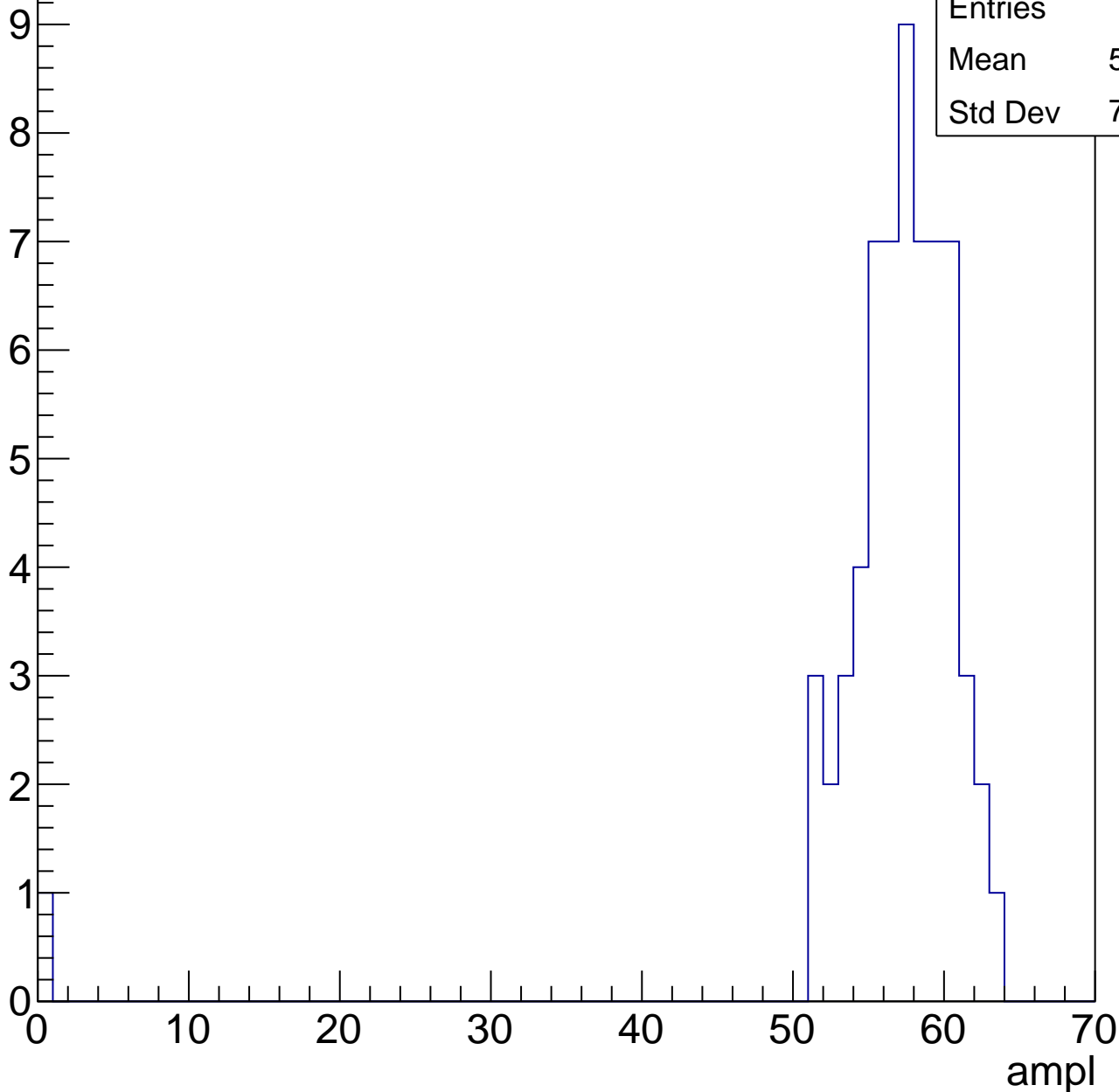


B1L100S, U7-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 56.05 |
| Std Dev | 7.664 |

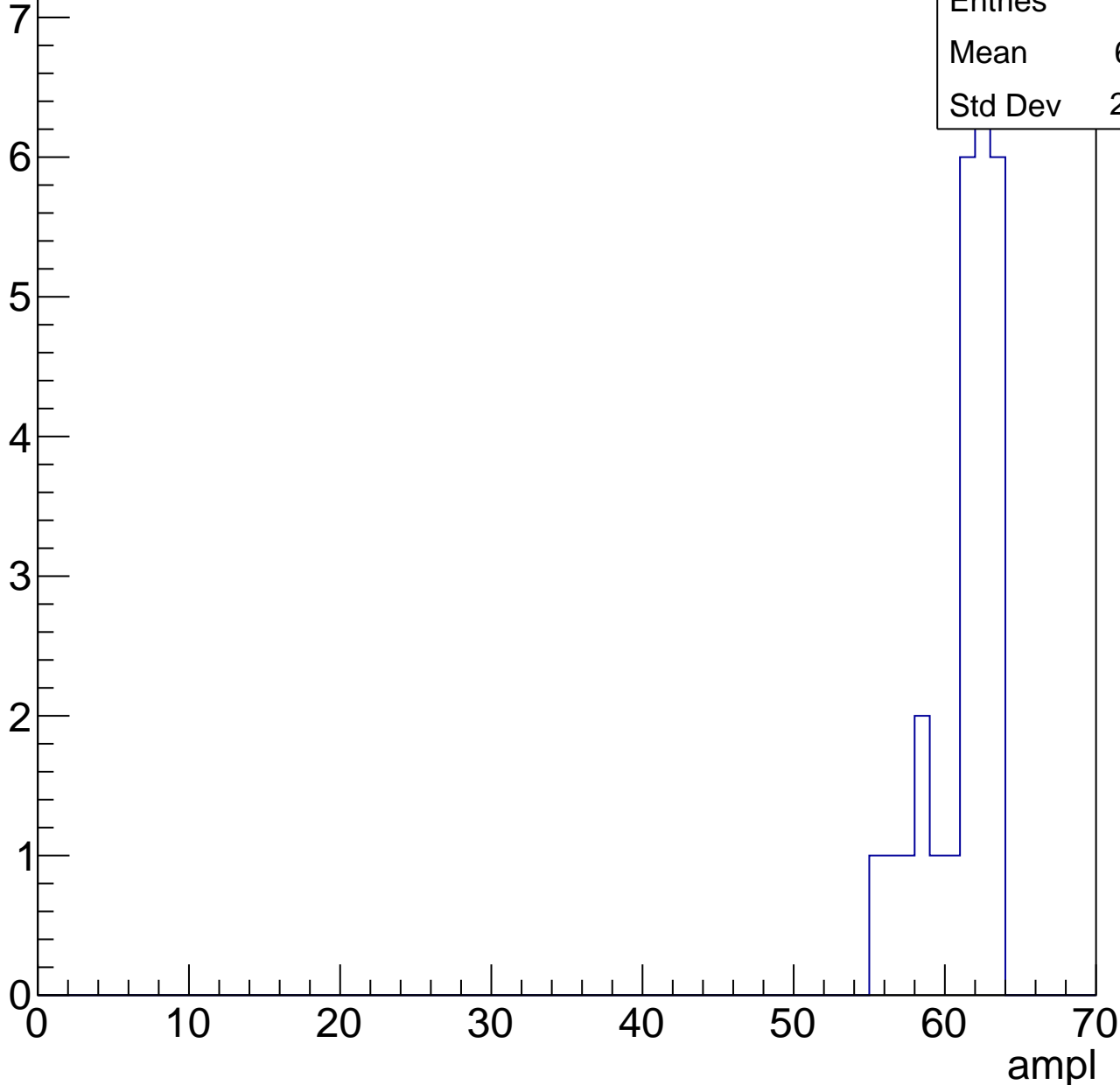


B1L100S, U7-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

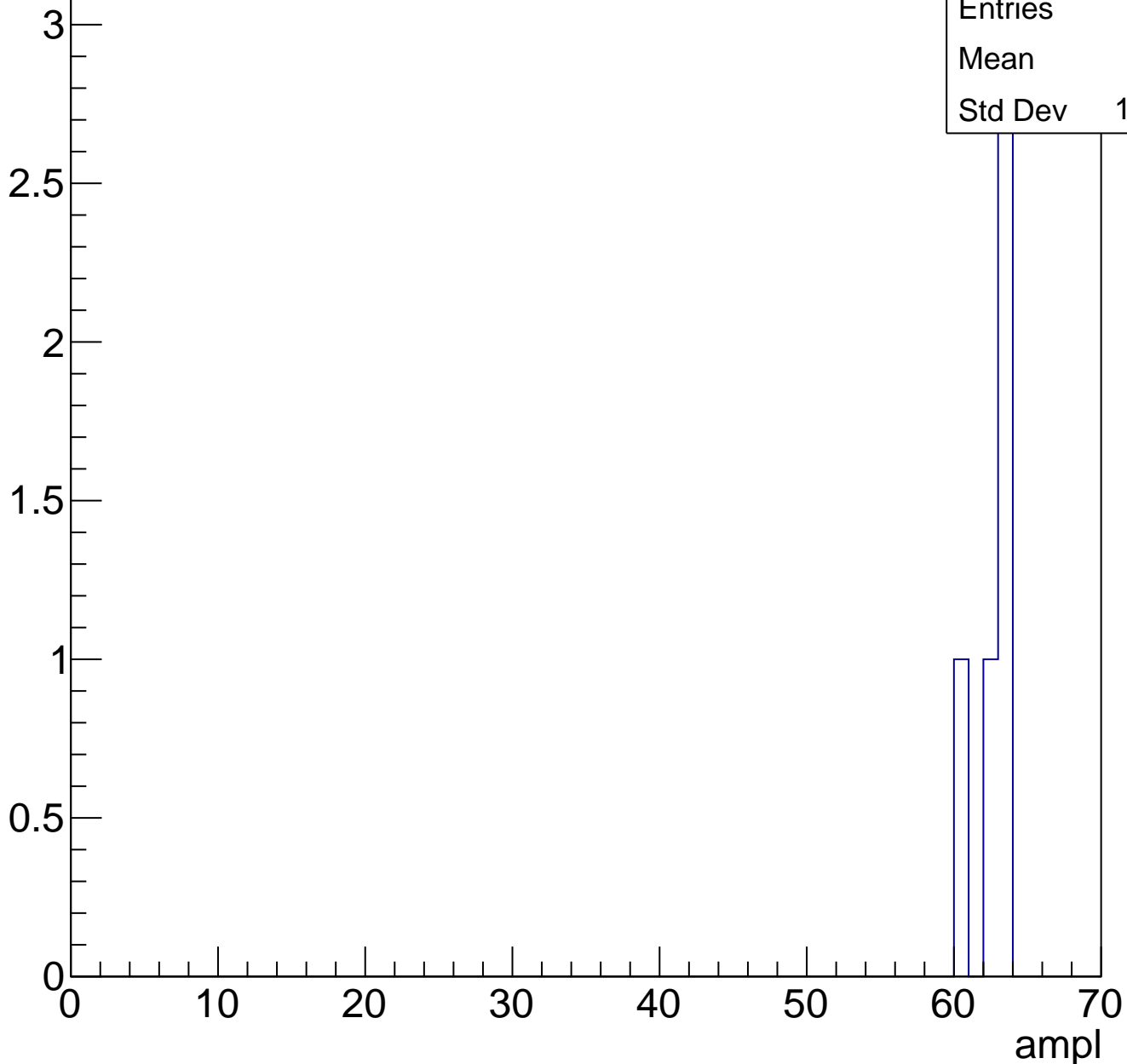
| | |
|---------|-------|
| Entries | 26 |
| Mean | 60.81 |
| Std Dev | 2.236 |



B1L100S, U7-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 5 |
| Mean | 62.2 |
| Std Dev | 1.166 |

B1L100S, U7-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch55, adc0

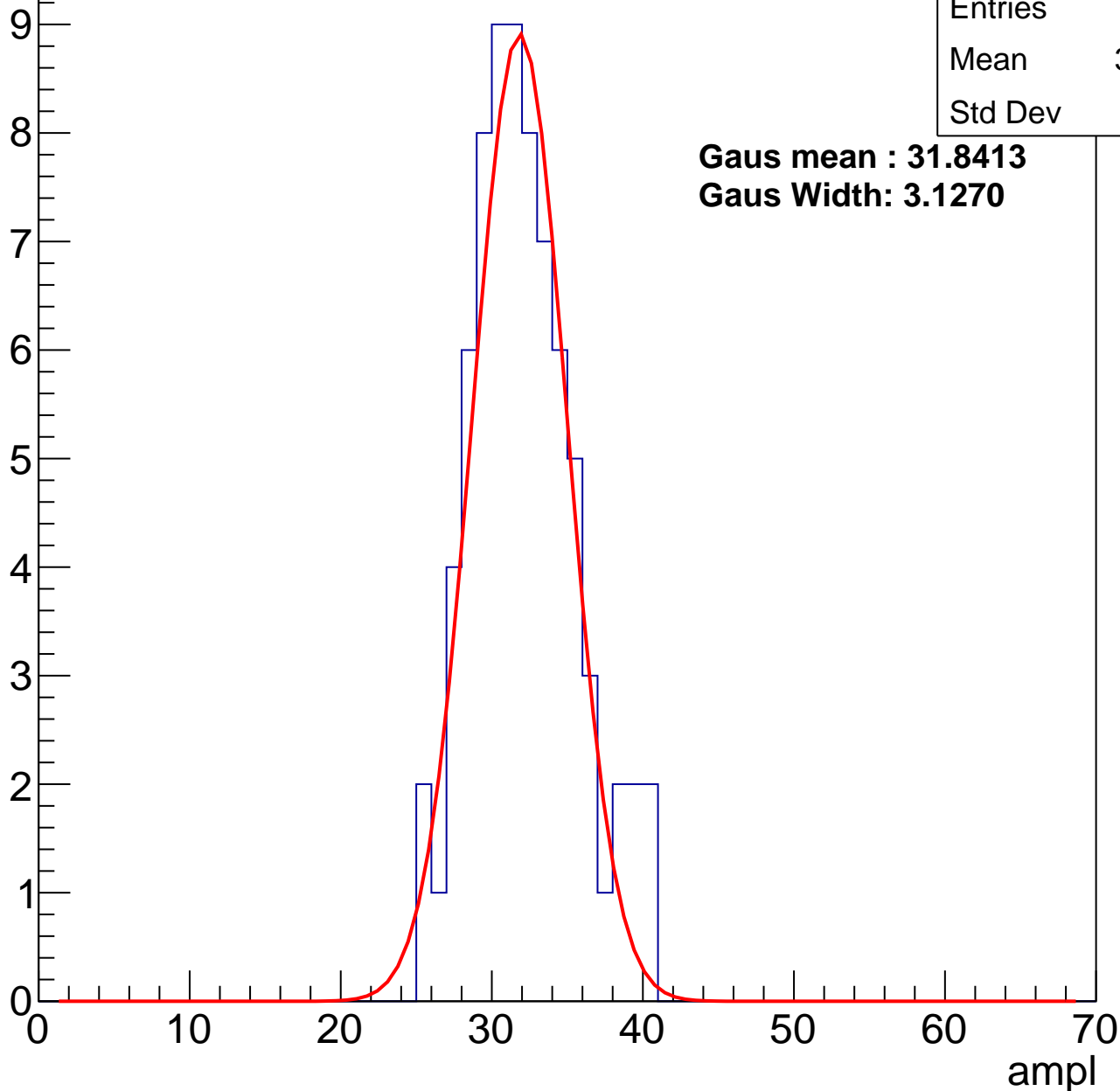
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 31.71 |
| Std Dev | 3.44 |

Gaus mean : 31.8413

Gaus Width: 3.1270



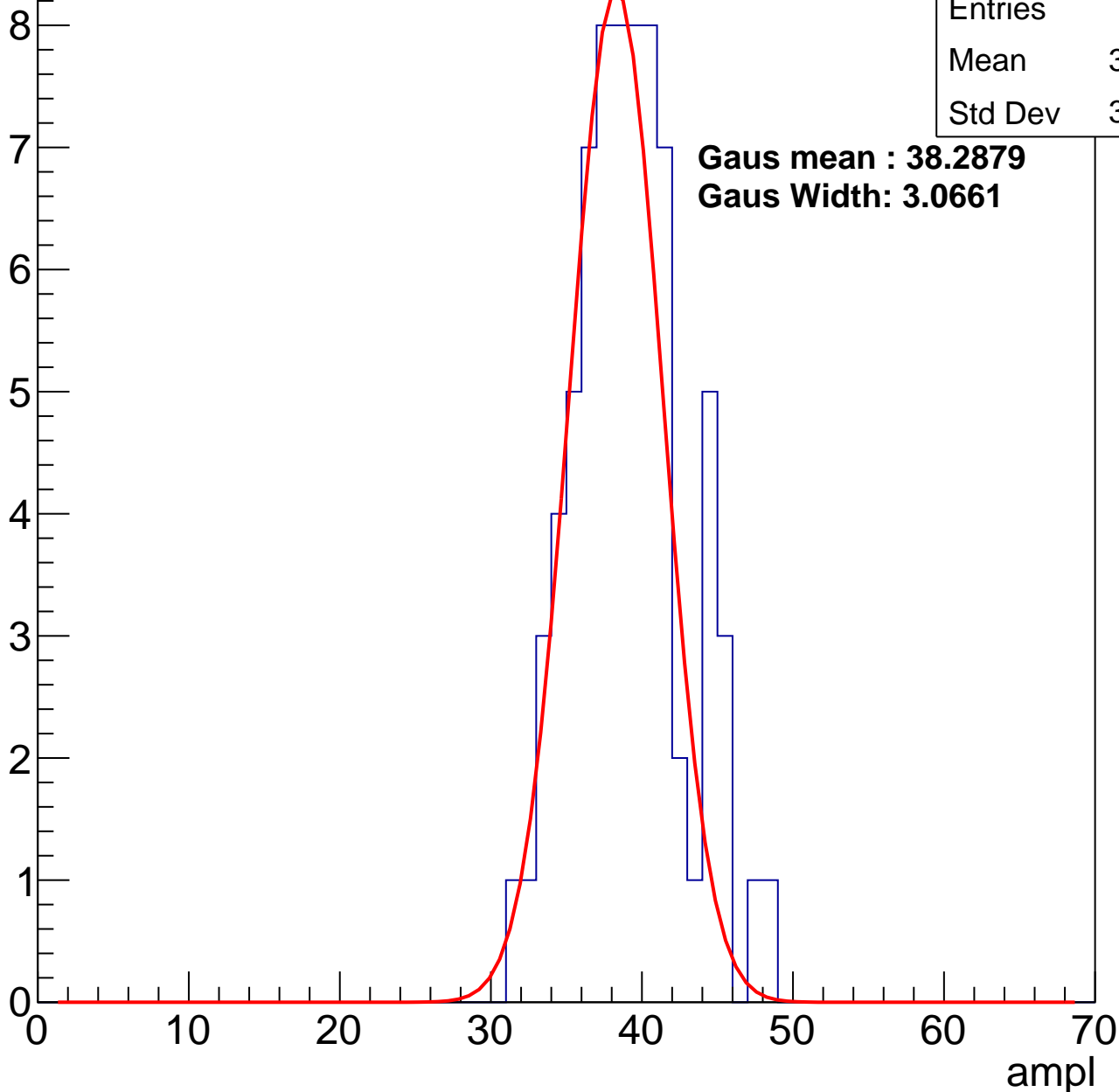
B1L100S, U7-ch55, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 38.64 |
| Std Dev | 3.594 |

Gaus mean : 38.2879
Gaus Width: 3.0661



B1L100S, U7-ch55, adc2

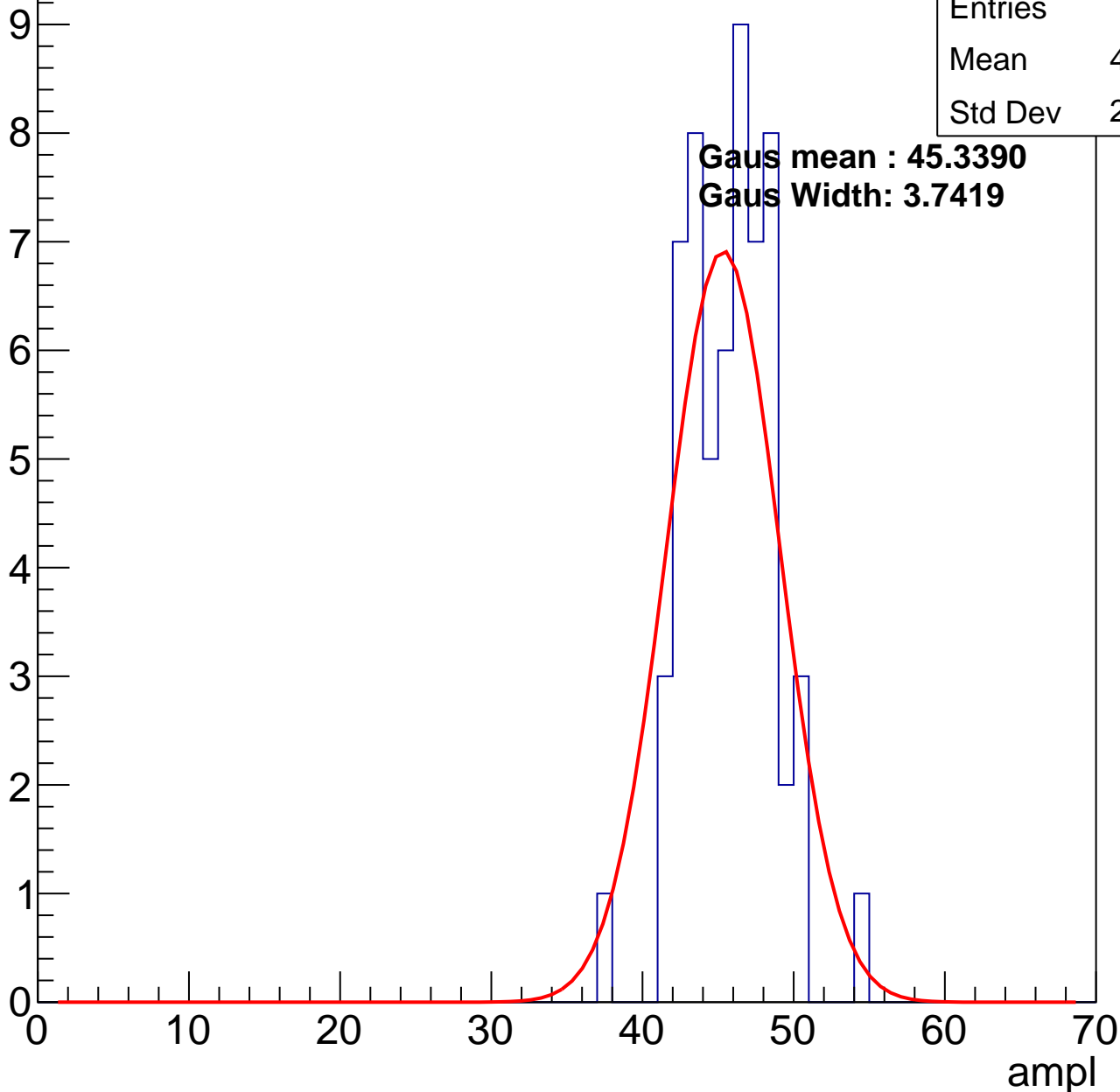
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 45.28 |
| Std Dev | 2.899 |

Gaus mean : 45.3390

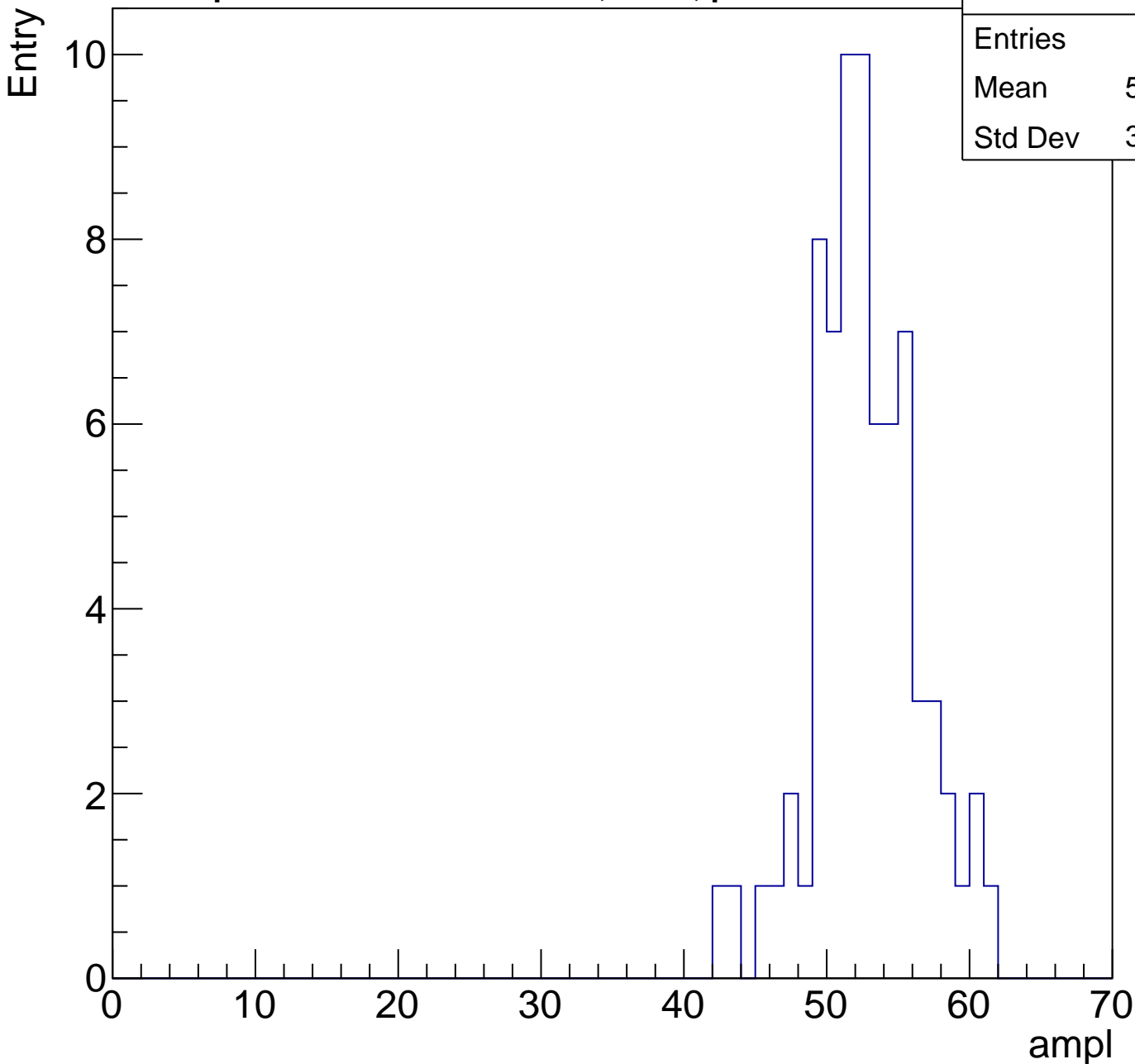
Gaus Width: 3.7419



B1L100S, U7-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 52.22 |
| Std Dev | 3.672 |

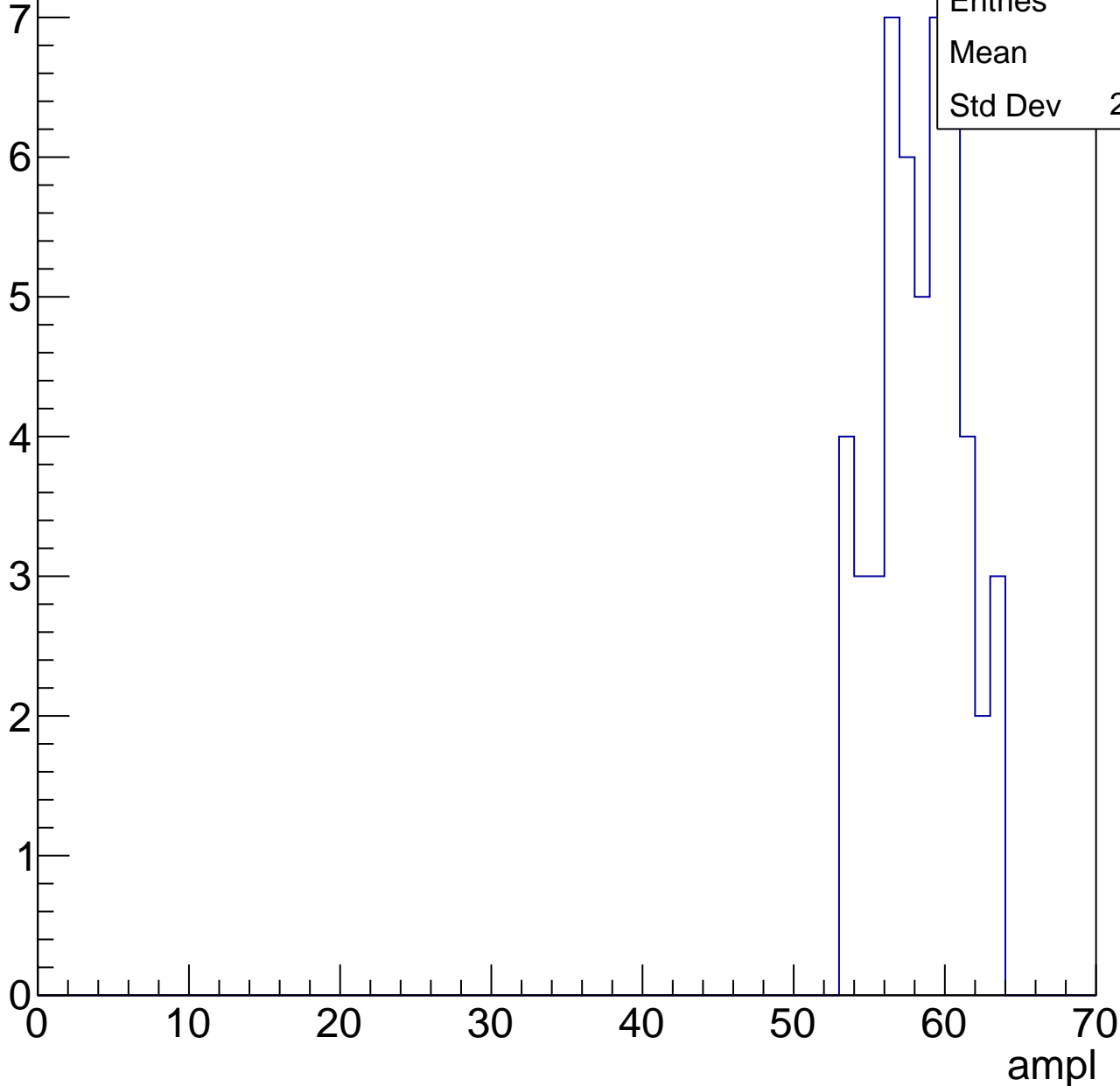


B1L100S, U7-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.9 |
| Std Dev | 2.753 |

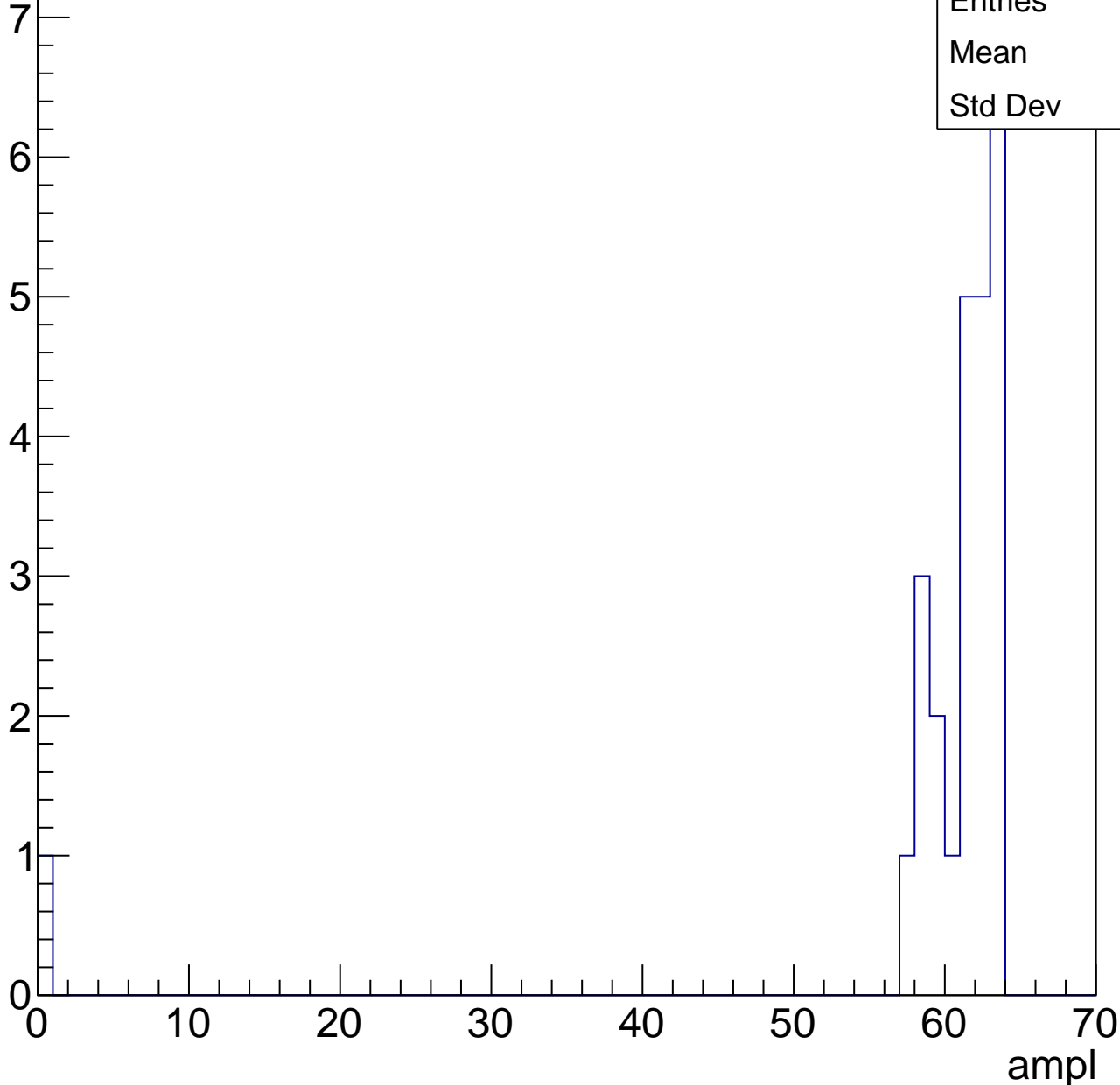


B1L100S, U7-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

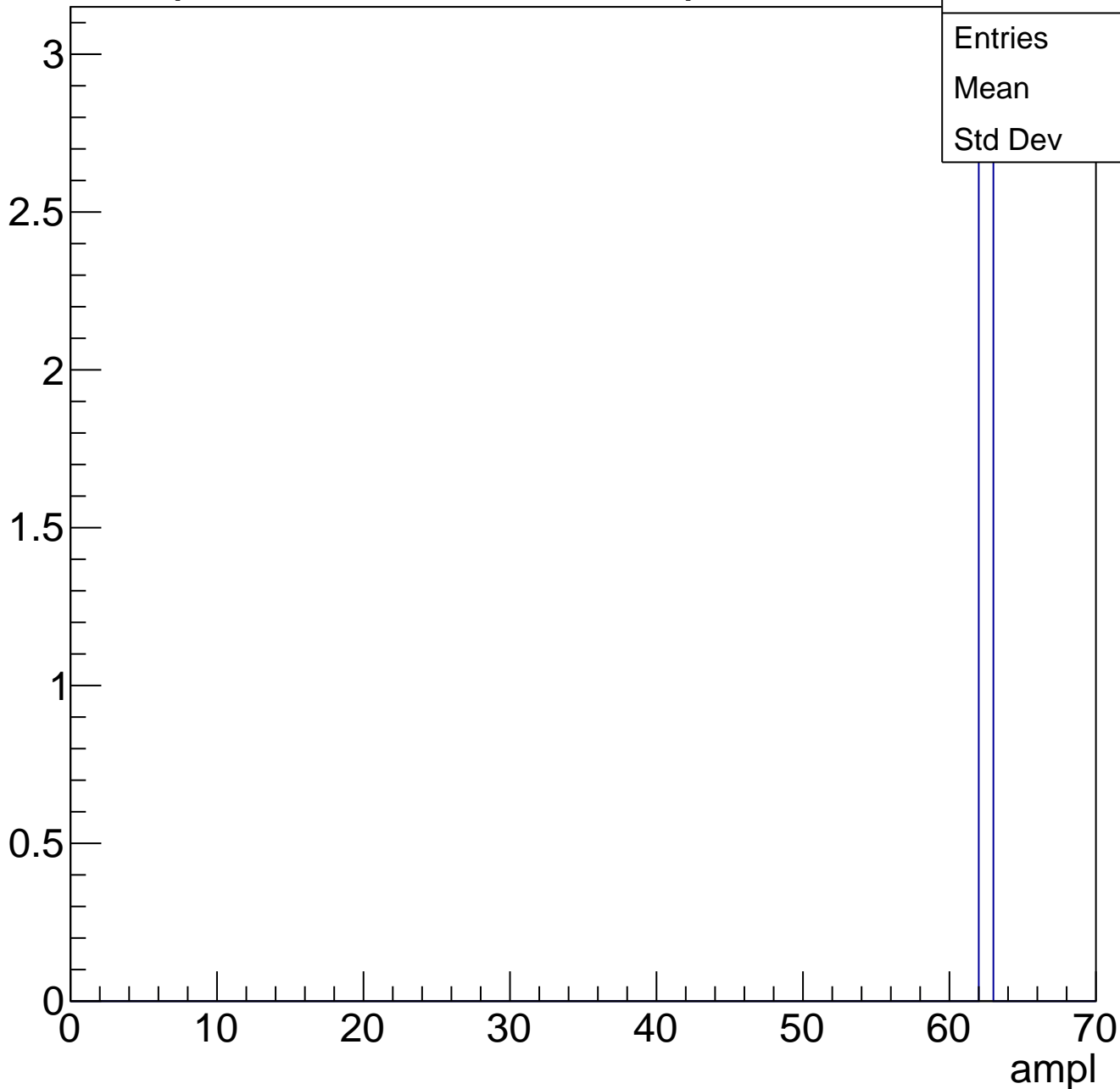
| | |
|---------|------|
| Entries | 25 |
| Mean | 58.6 |
| Std Dev | 12.1 |



B1L100S, U7-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch56, adc0

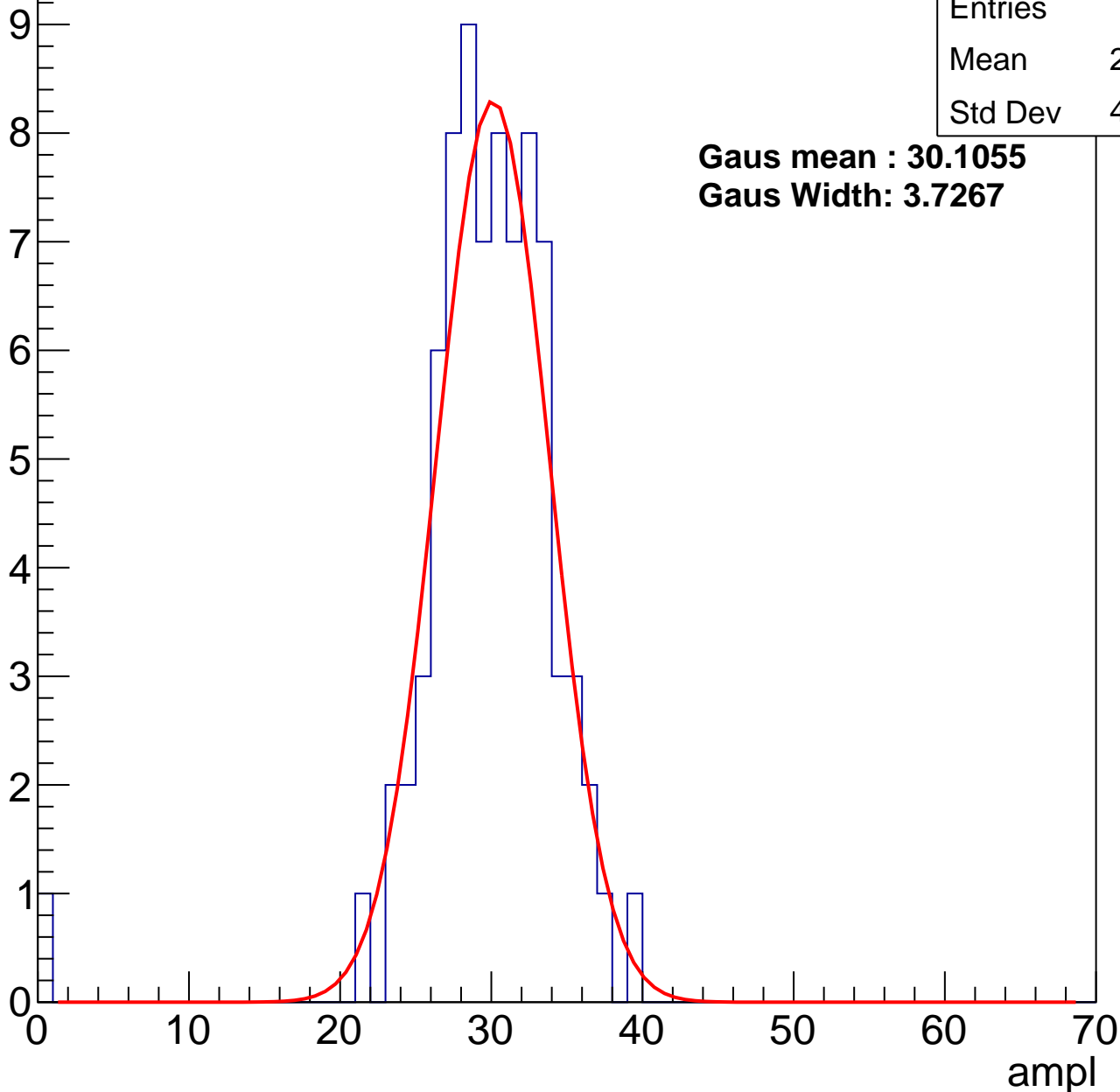
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.32 |
| Std Dev | 4.809 |

Gaus mean : 30.1055

Gaus Width: 3.7267



B1L100S, U7-ch56, adc1

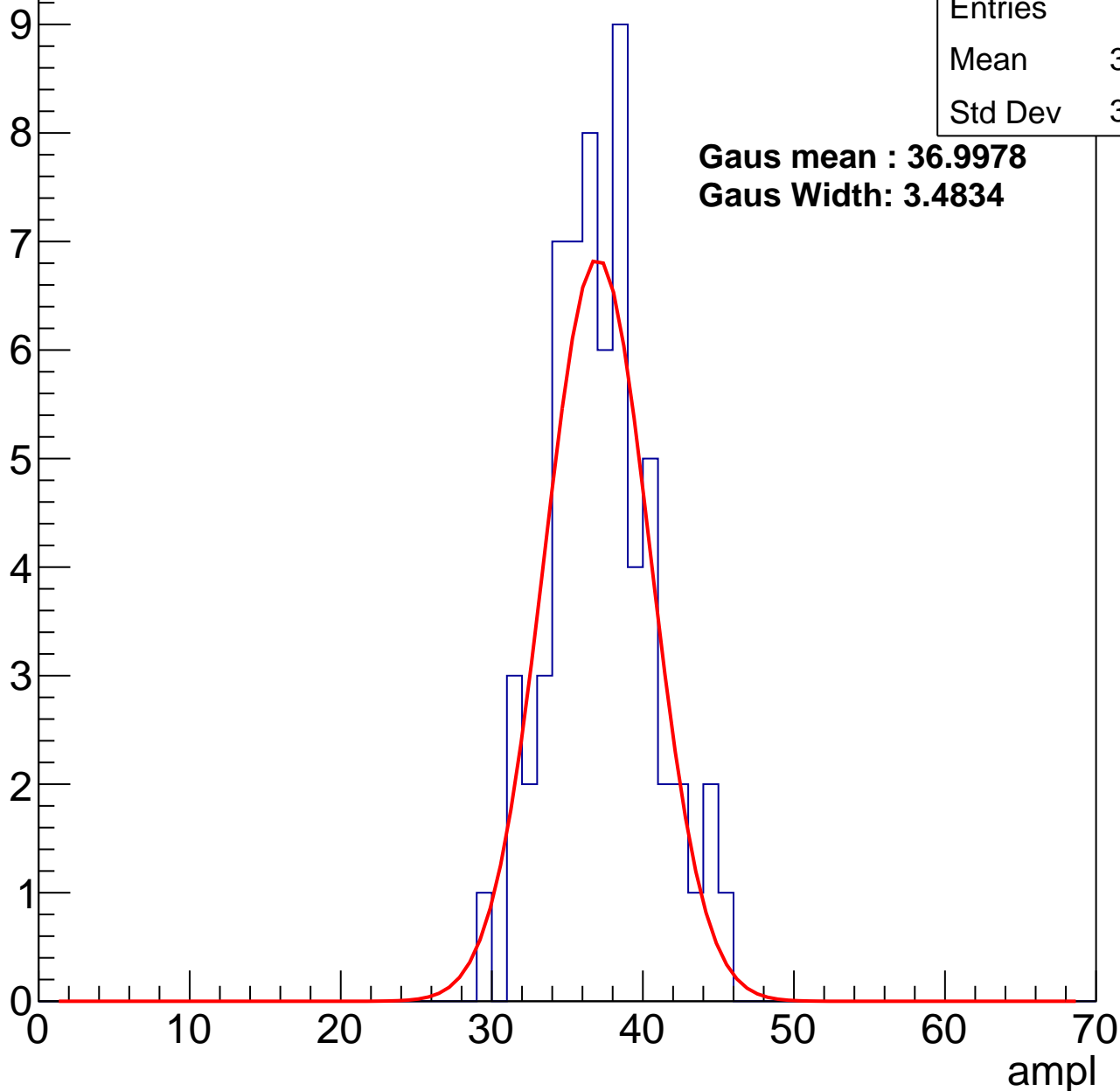
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.79 |
| Std Dev | 3.372 |

Gaus mean : 36.9978

Gaus Width: 3.4834



B1L100S, U7-ch56, adc2

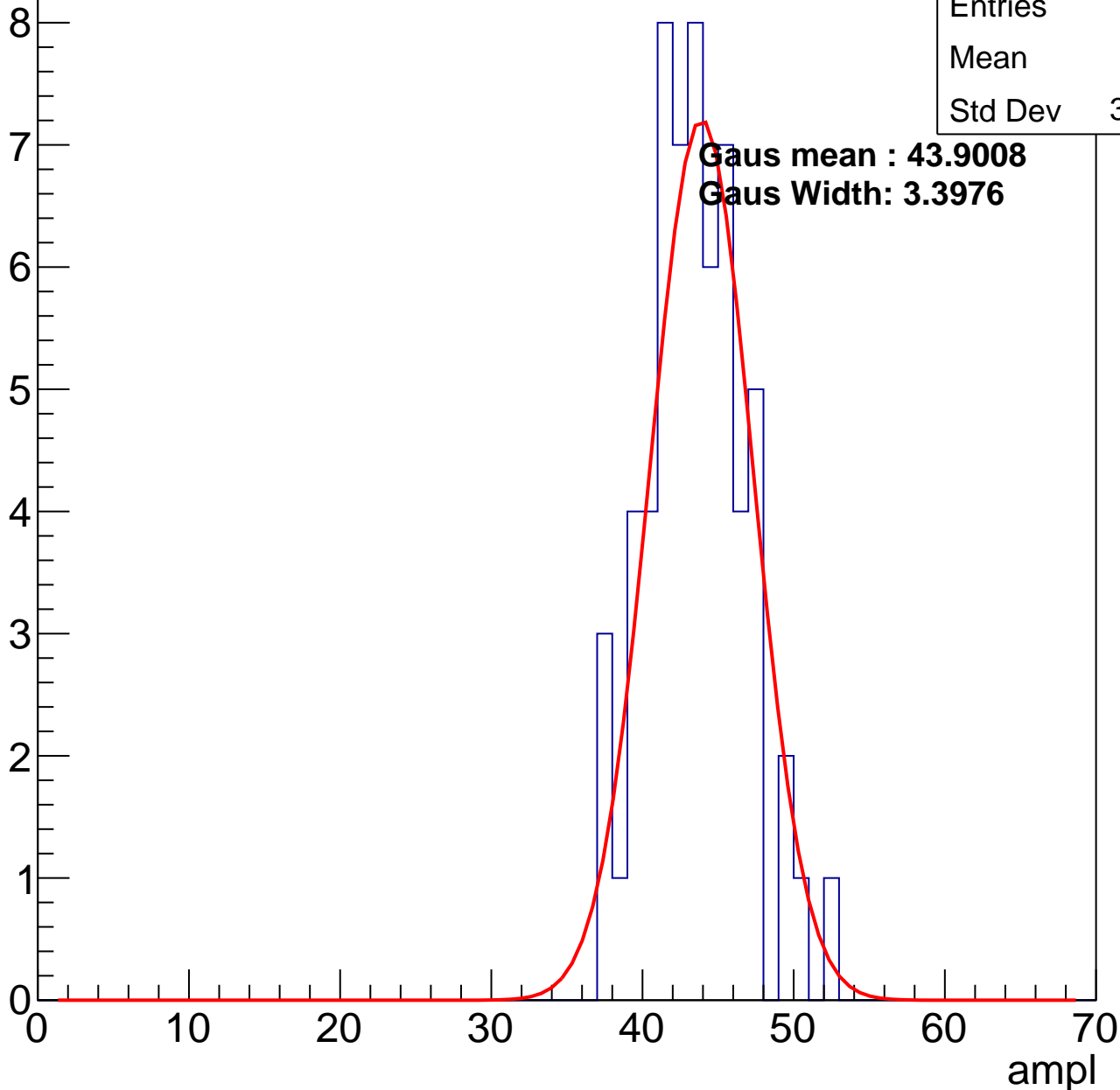
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 43.1 |
| Std Dev | 3.197 |

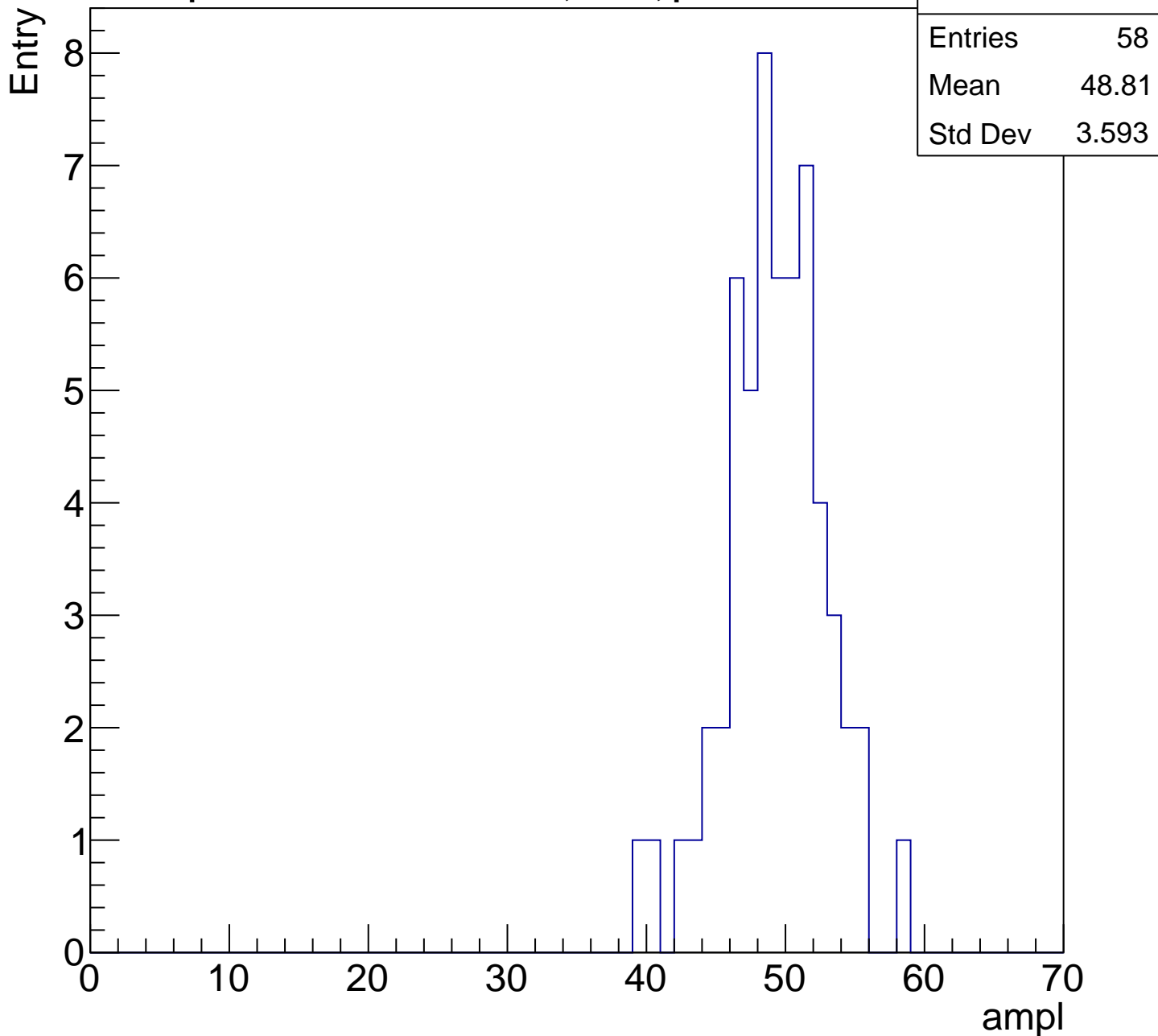
Gaus mean : 43.9008

Gaus Width: 3.3976



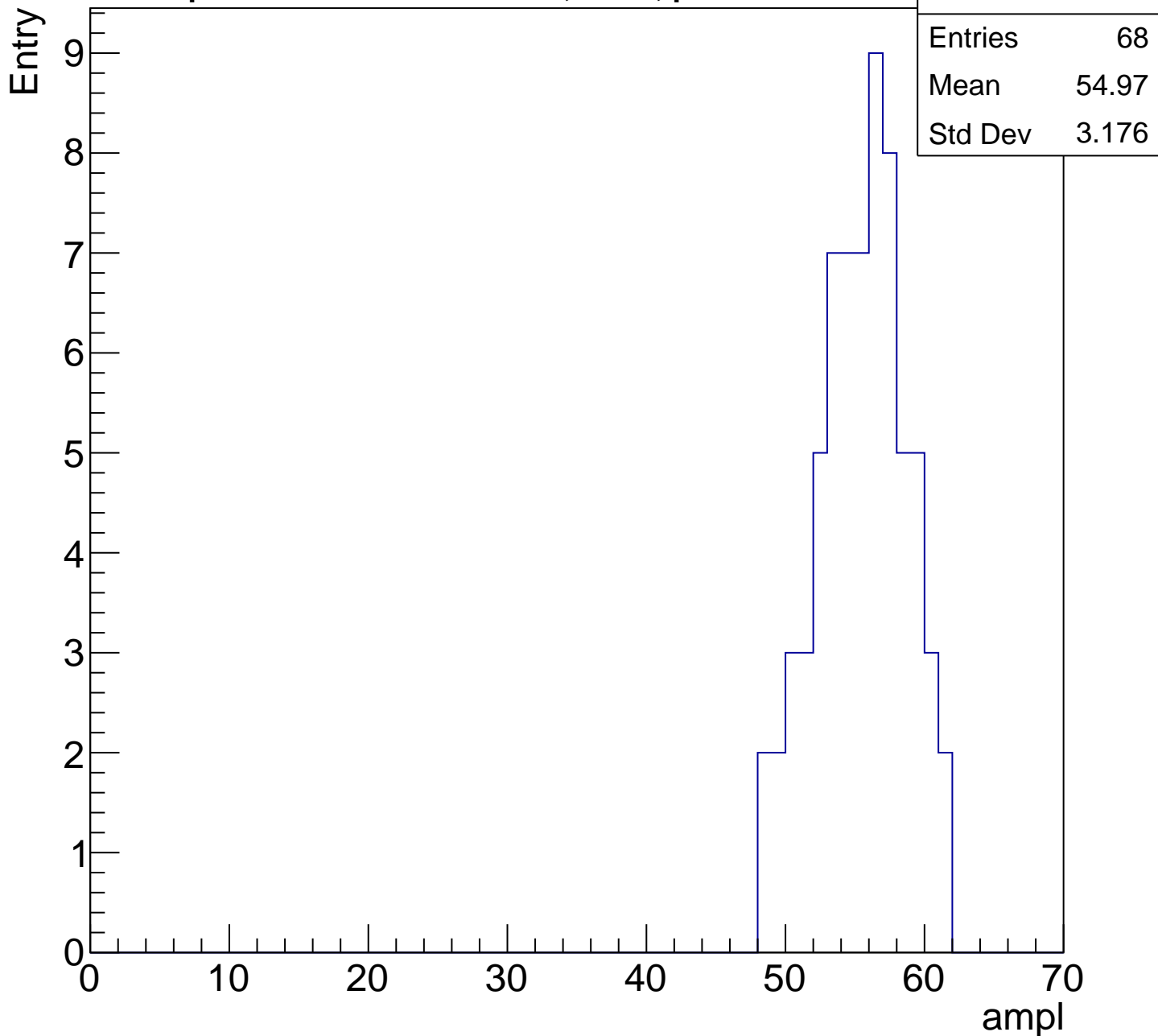
B1L100S, U7-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

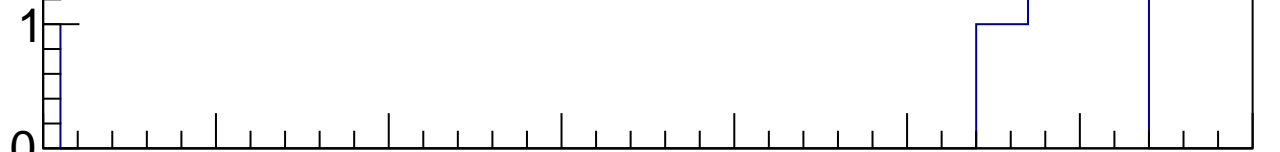
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.15 |
| Std Dev | 8.905 |

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch57, adc0

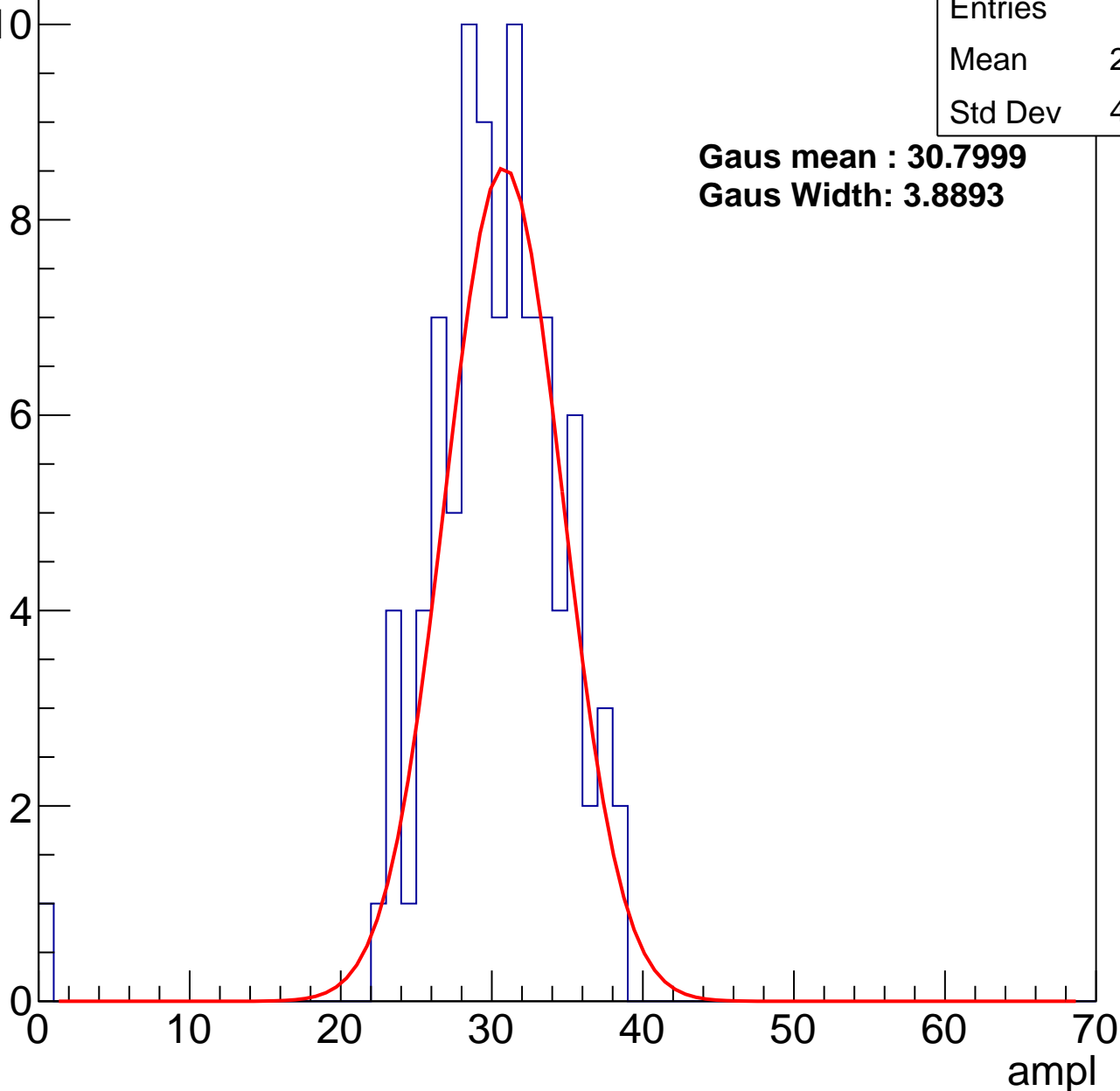
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 90 |
| Mean | 29.73 |
| Std Dev | 4.896 |

Gaus mean : 30.7999

Gaus Width: 3.8893



B1L100S, U7-ch57, adc1

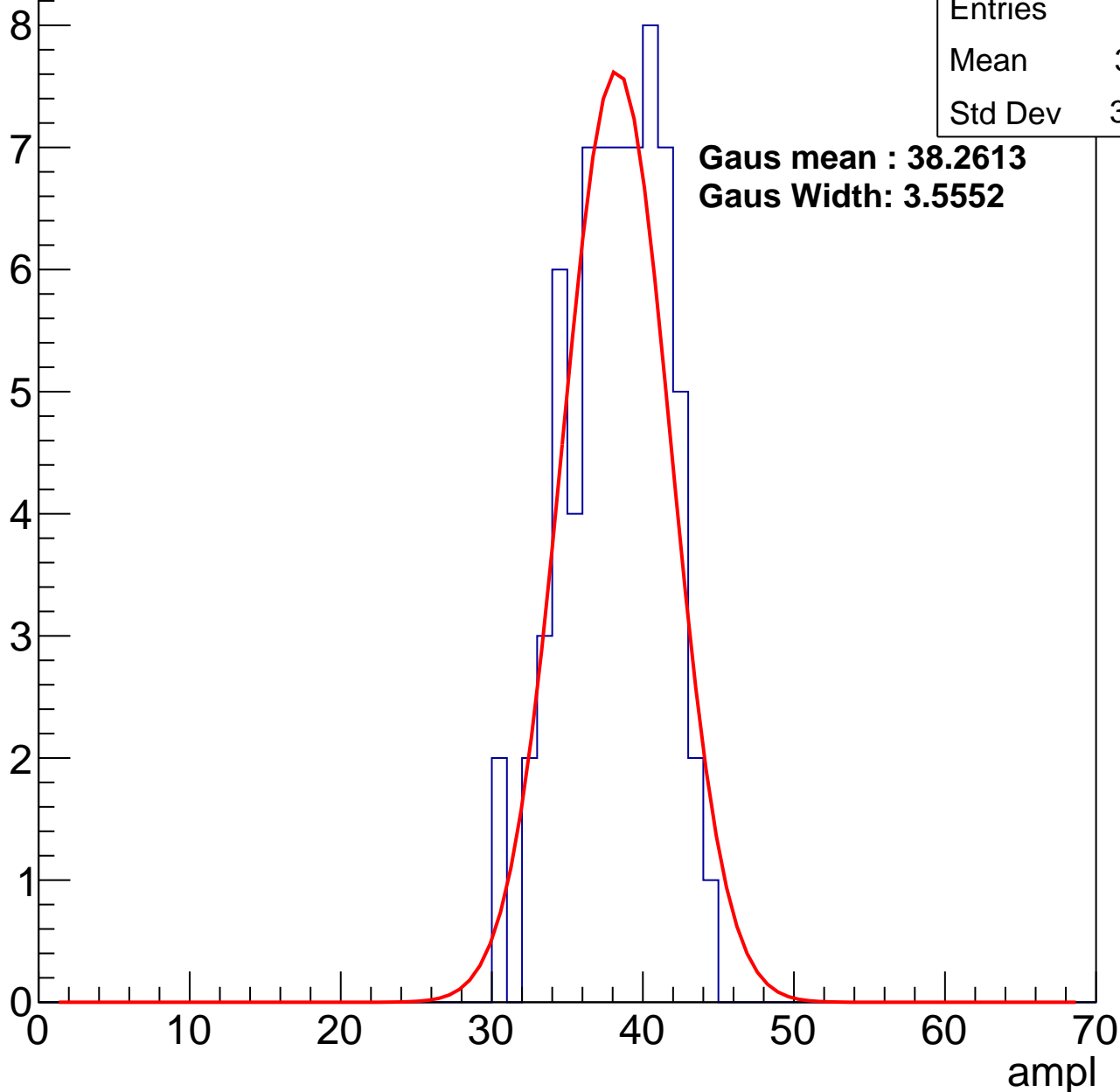
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 37.71 |
| Std Dev | 3.218 |

Gaus mean : 38.2613

Gaus Width: 3.5552



B1L100S, U7-ch57, adc2

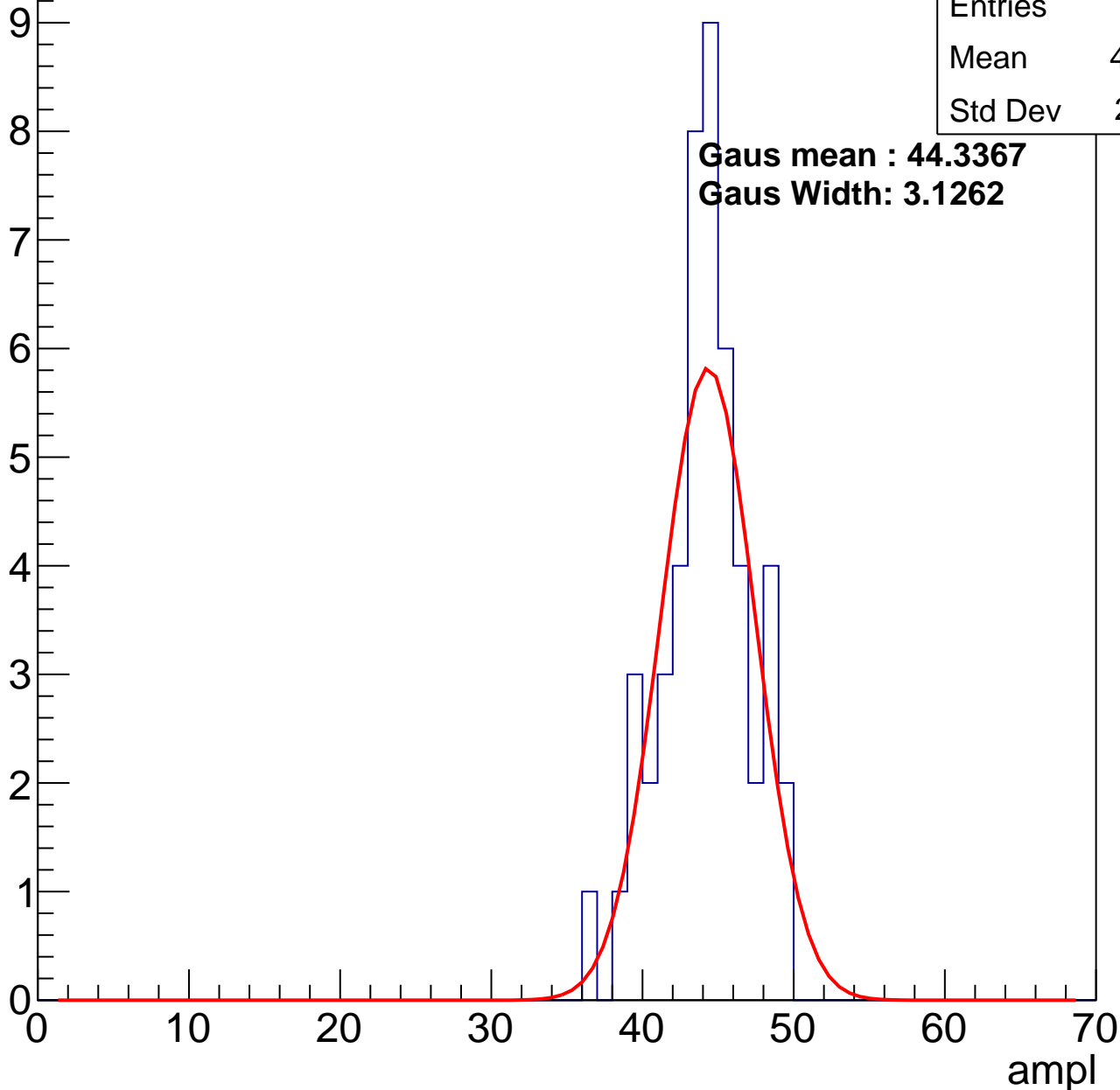
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.67 |
| Std Dev | 2.881 |

Gaus mean : 44.3367

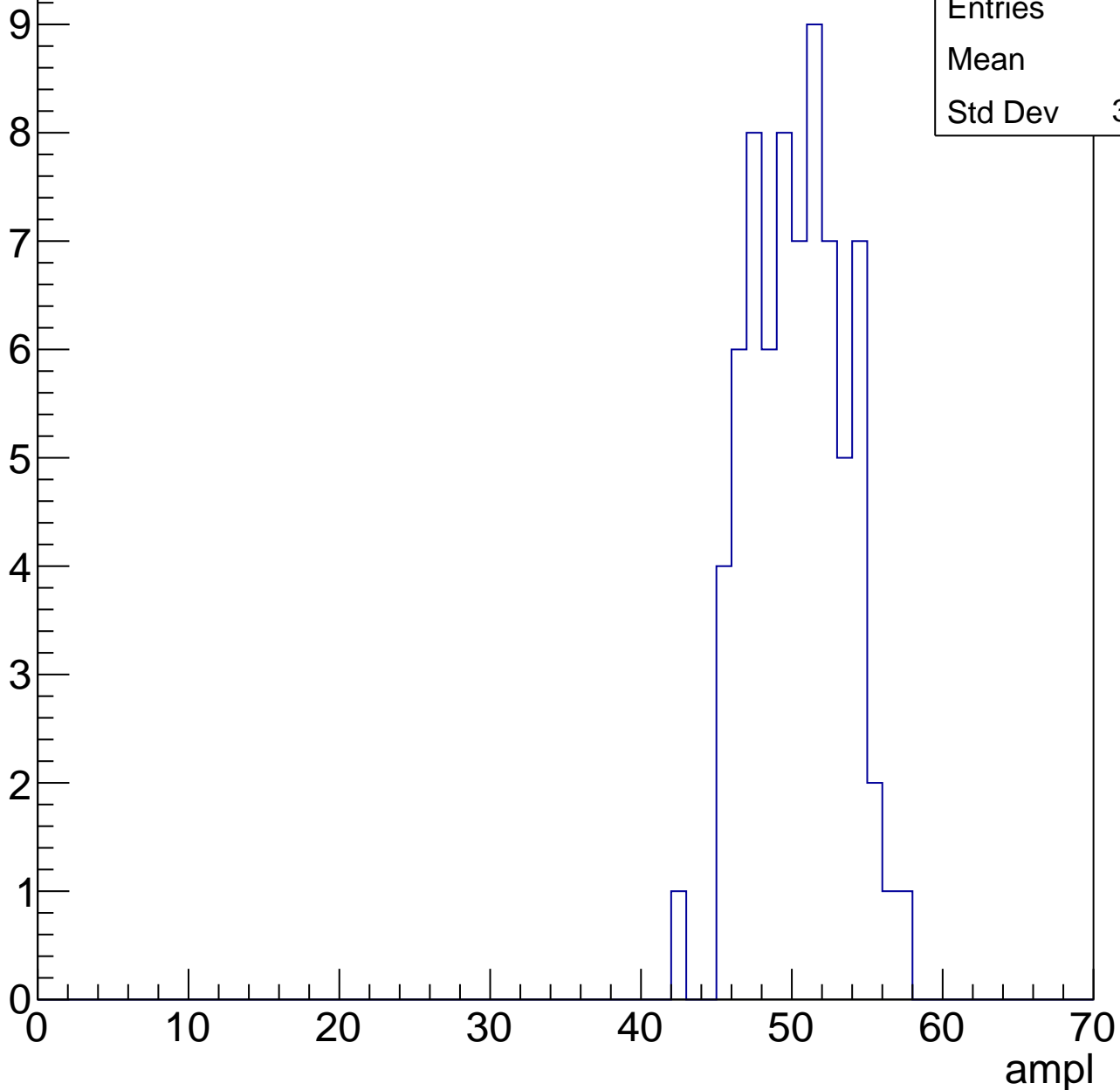
Gaus Width: 3.1262



B1L100S, U7-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

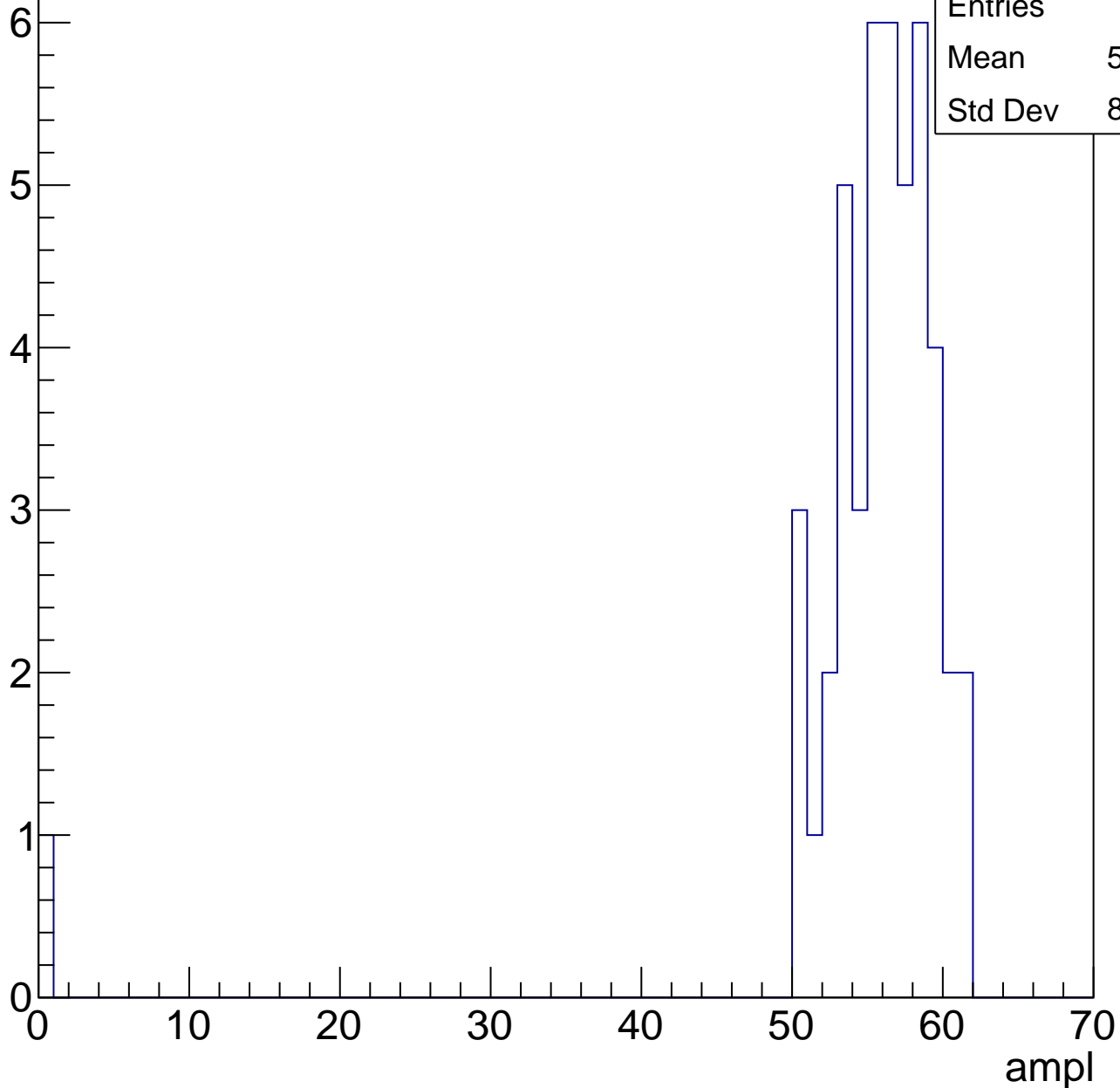
Entry



B1L100S, U7-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

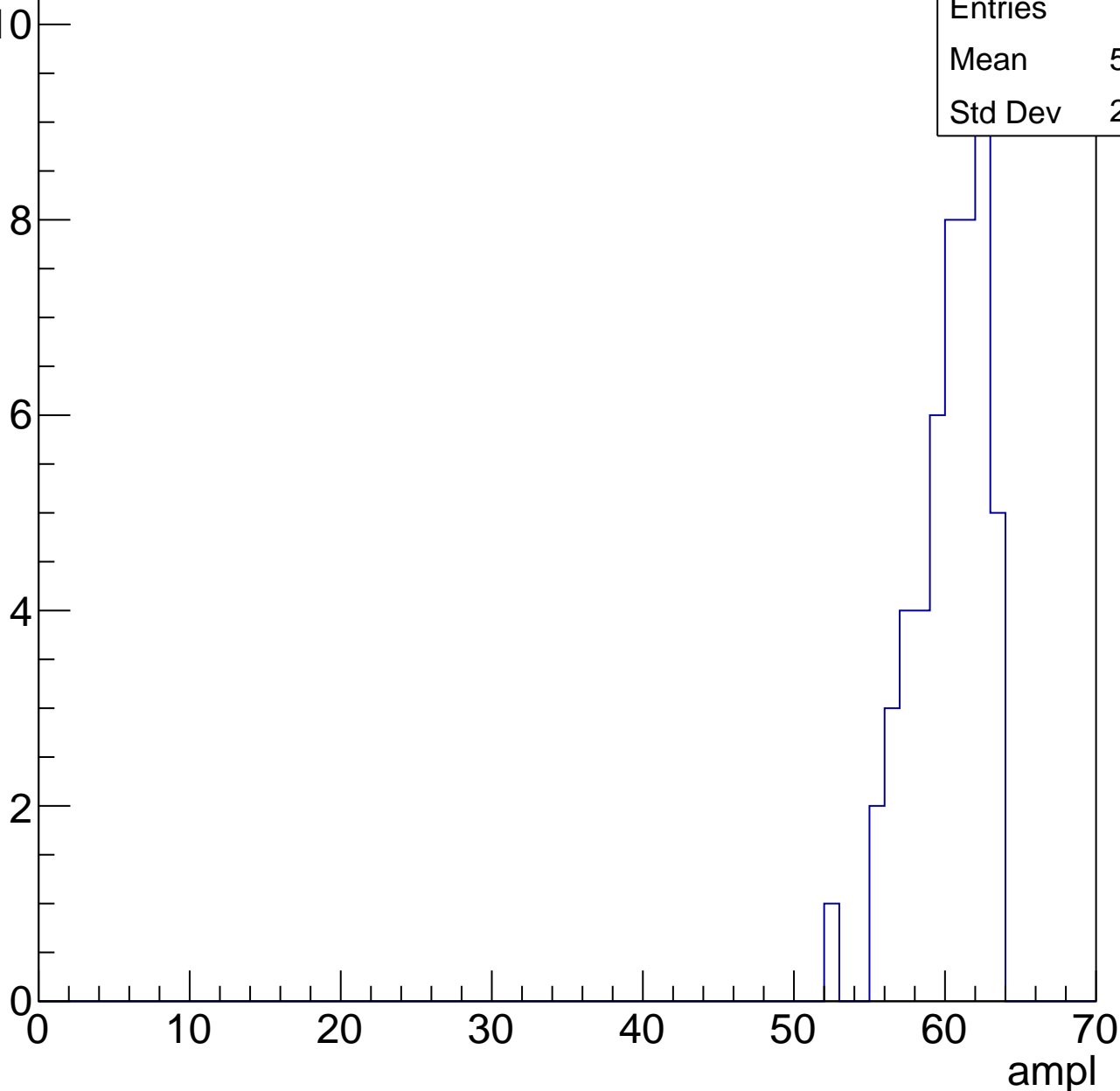


B1L100S, U7-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

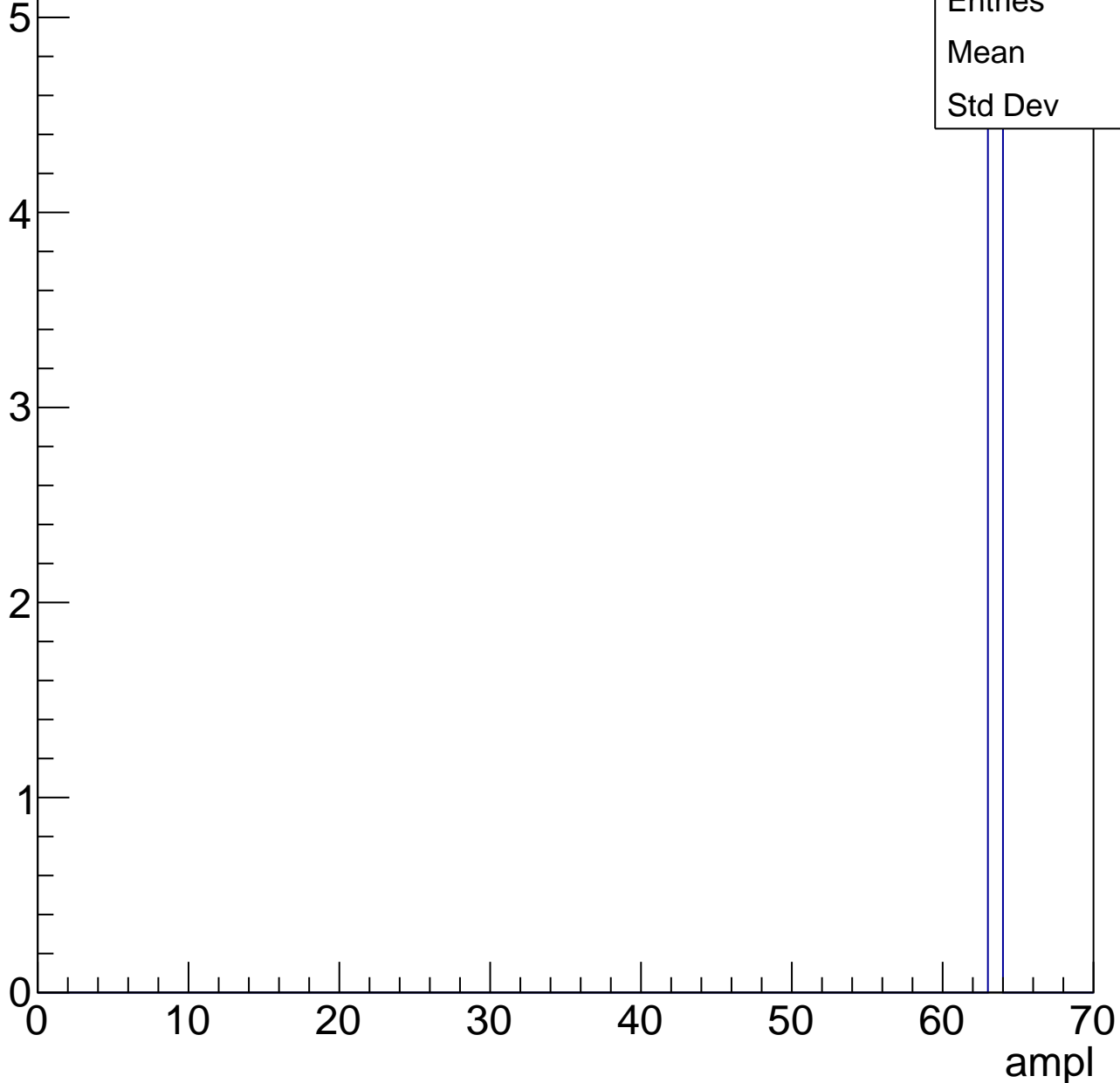
| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.75 |
| Std Dev | 2.464 |



B1L100S, U7-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch58, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 28.27 |
| Std Dev | 3.157 |

Gaus mean : 28.8822

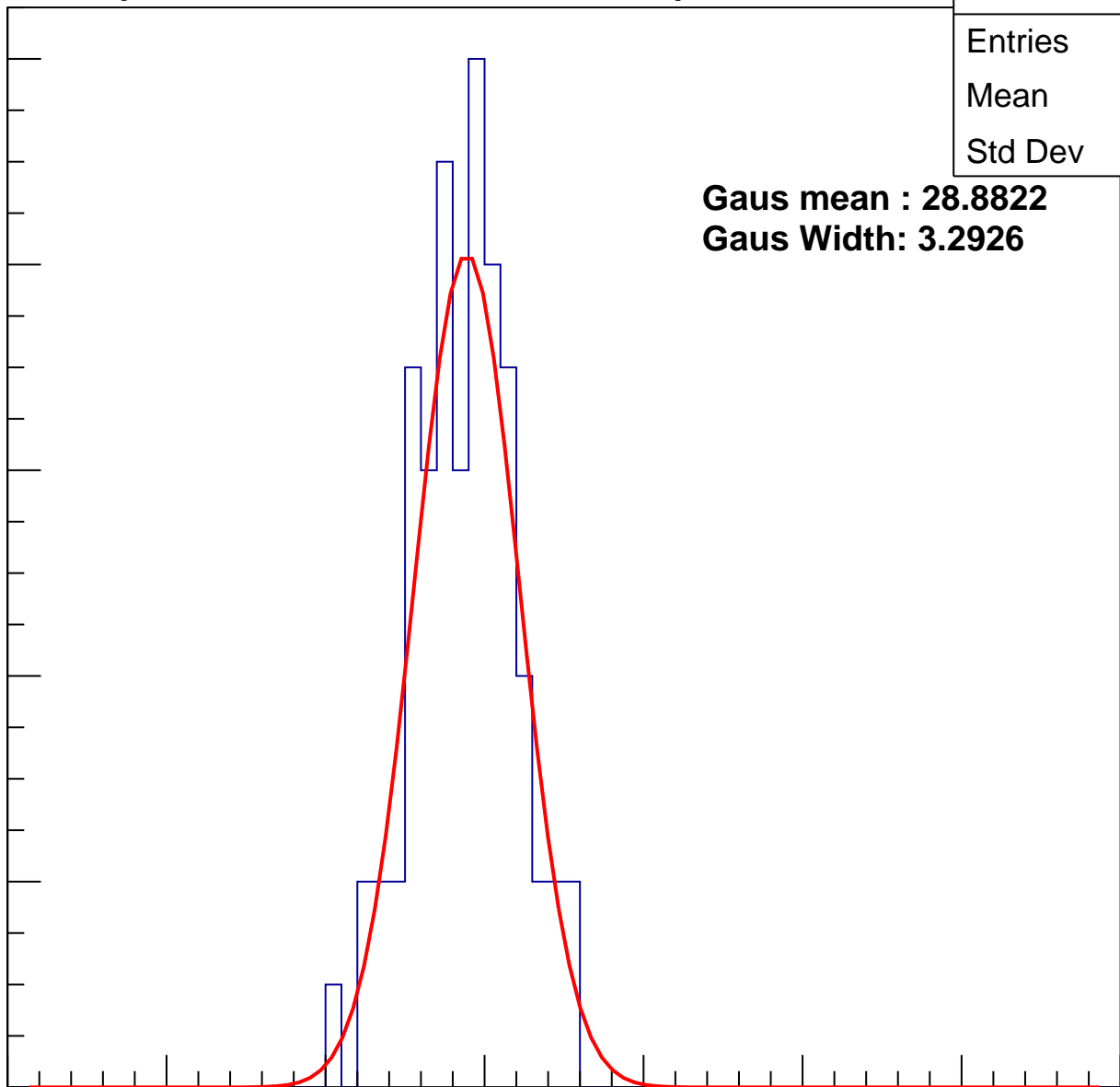
Gaus Width: 3.2926

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch58, adc1

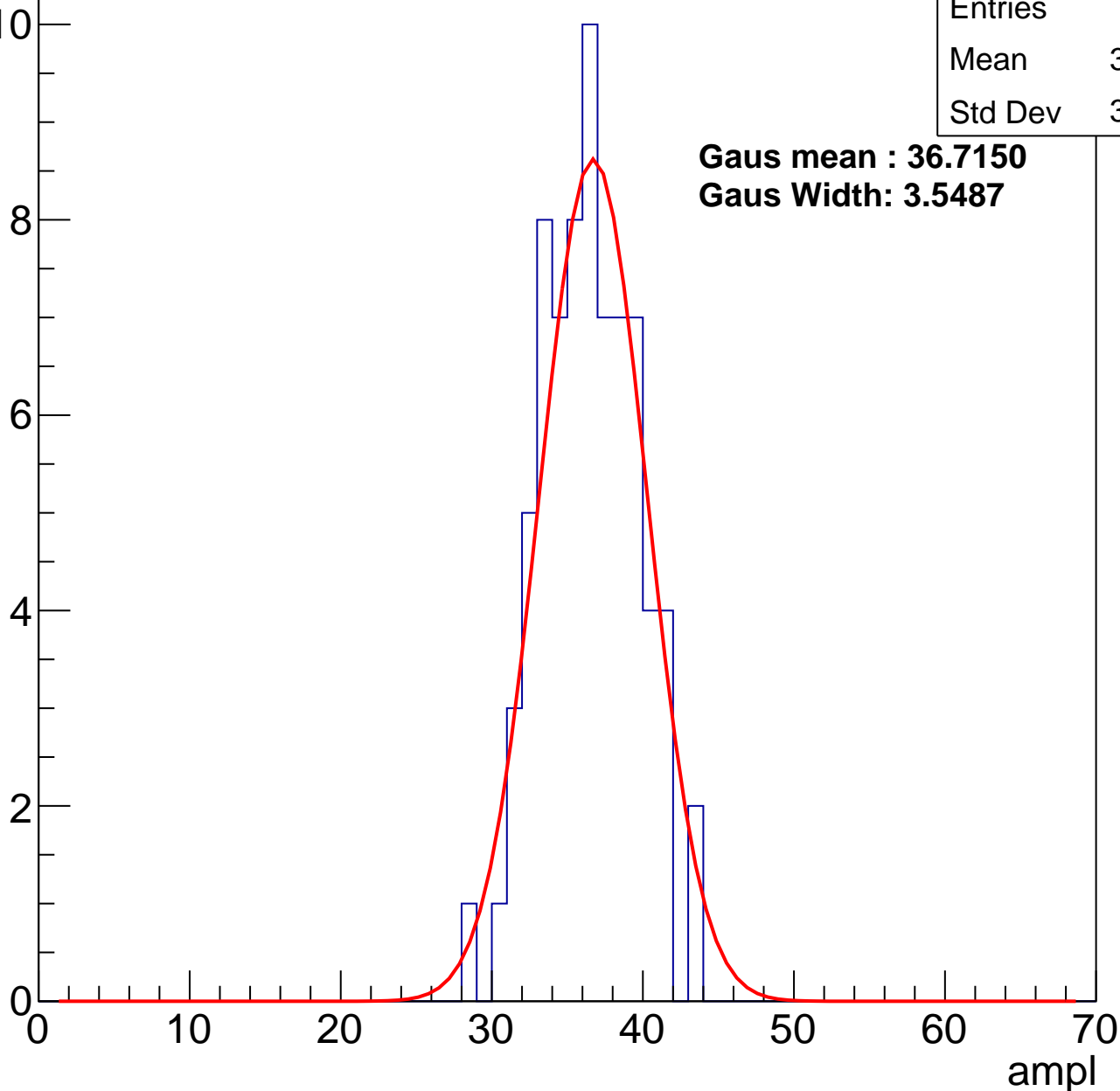
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.96 |
| Std Dev | 3.125 |

Gaus mean : 36.7150

Gaus Width: 3.5487



B1L100S, U7-ch58, adc2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.86 |
| Std Dev | 3.583 |

Gaus mean : 43.3061

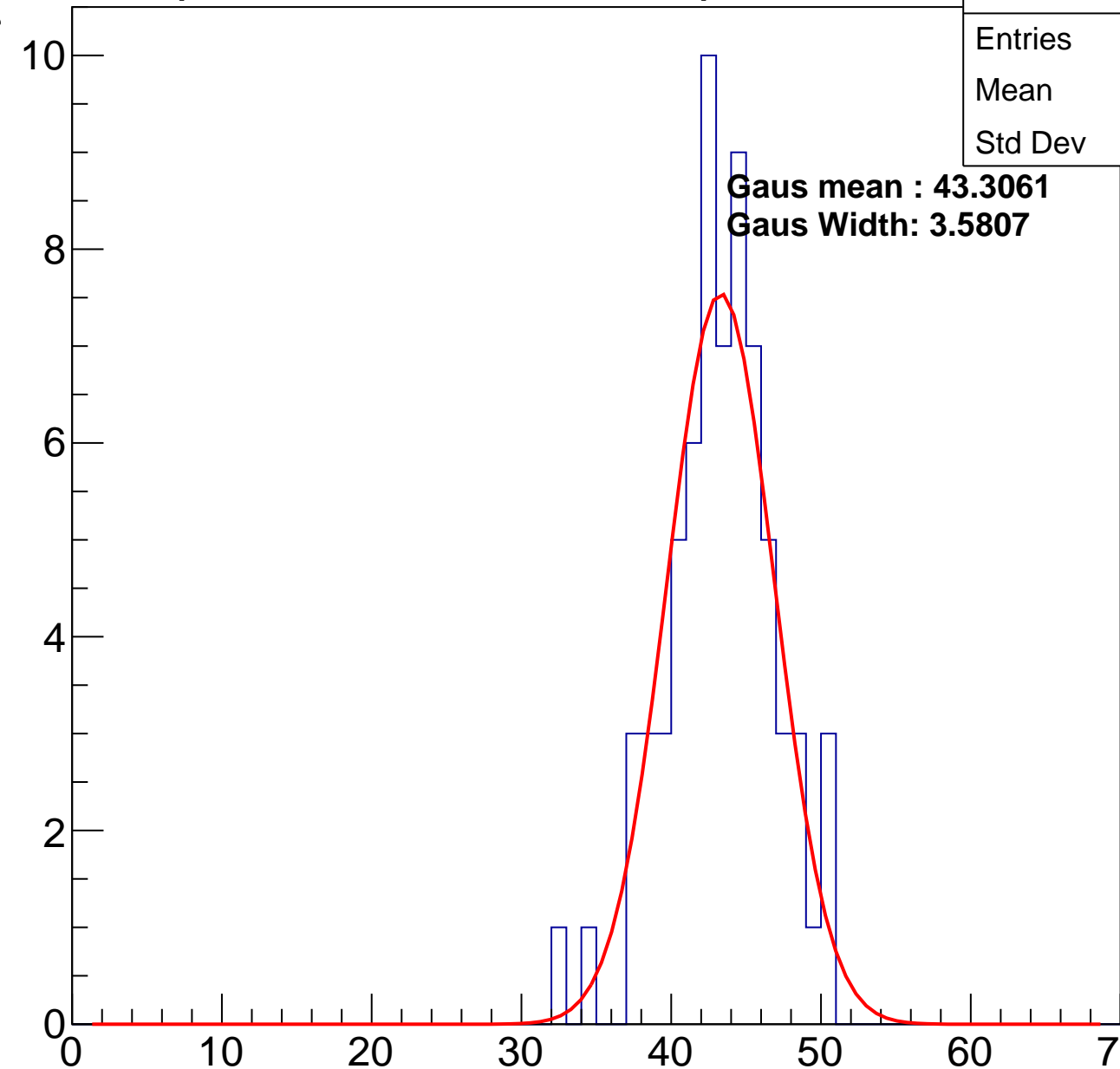
Gaus Width: 3.5807

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

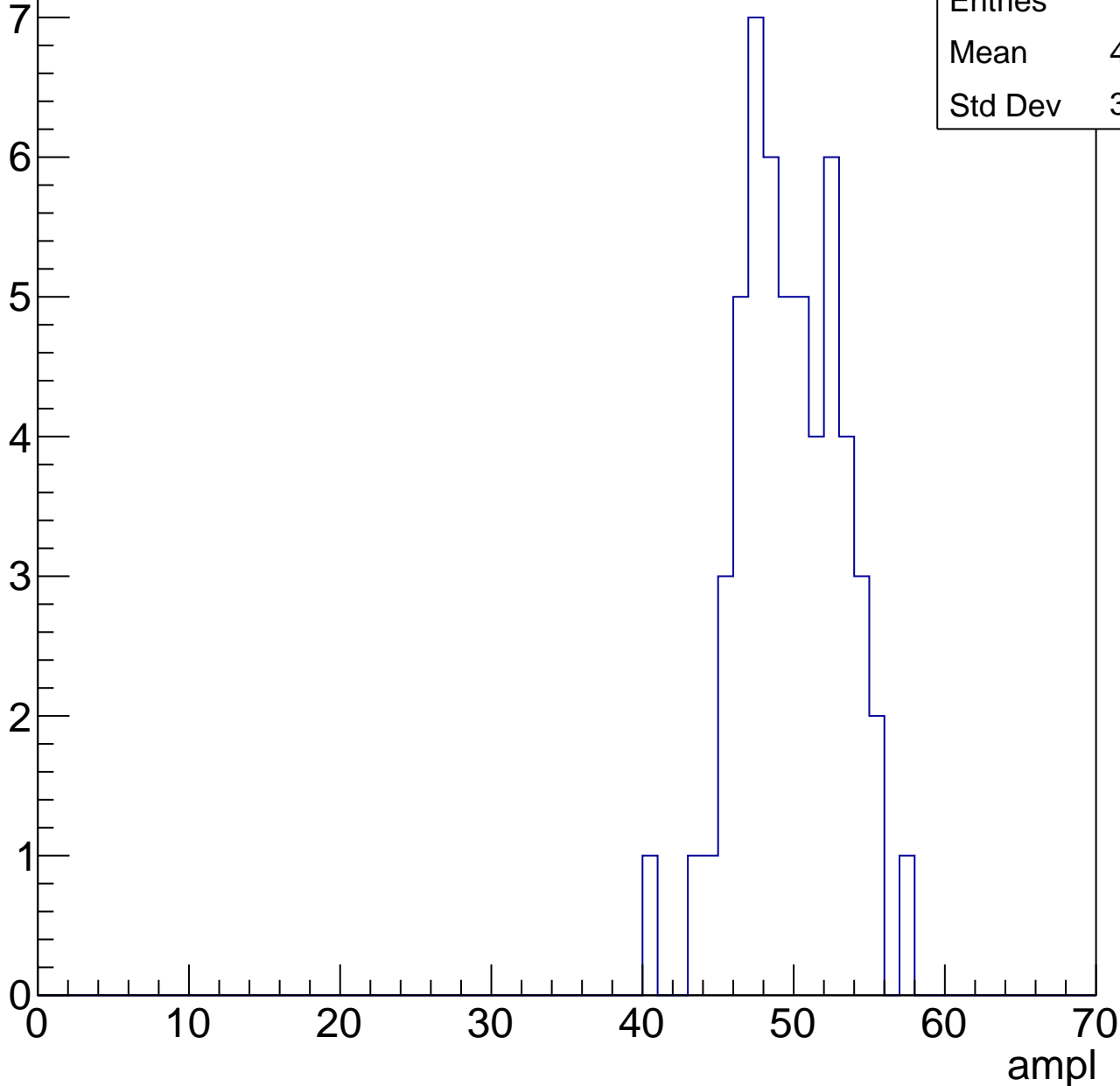


B1L100S, U7-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

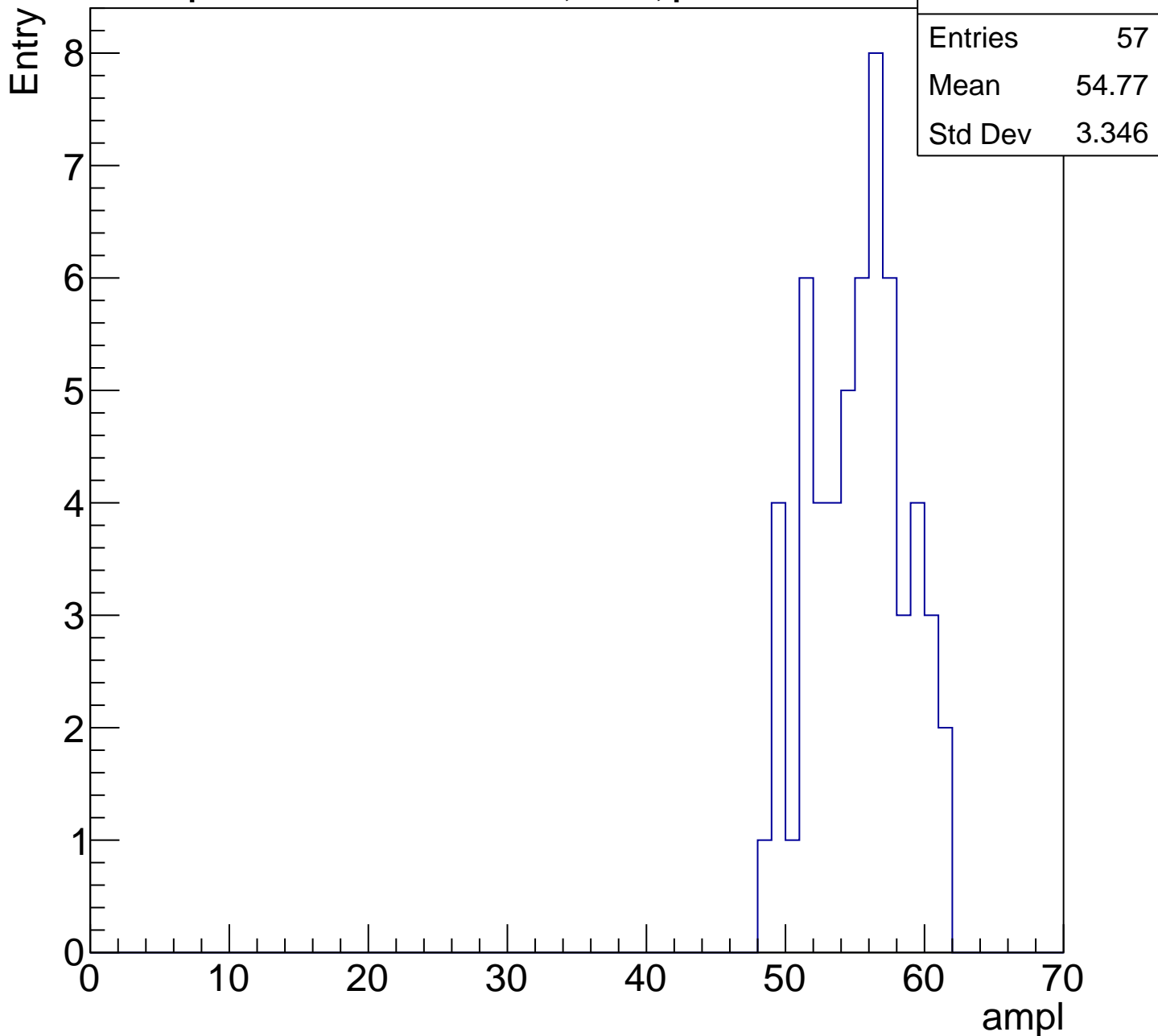
Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 49.28 |
| Std Dev | 3.374 |



B1L100S, U7-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

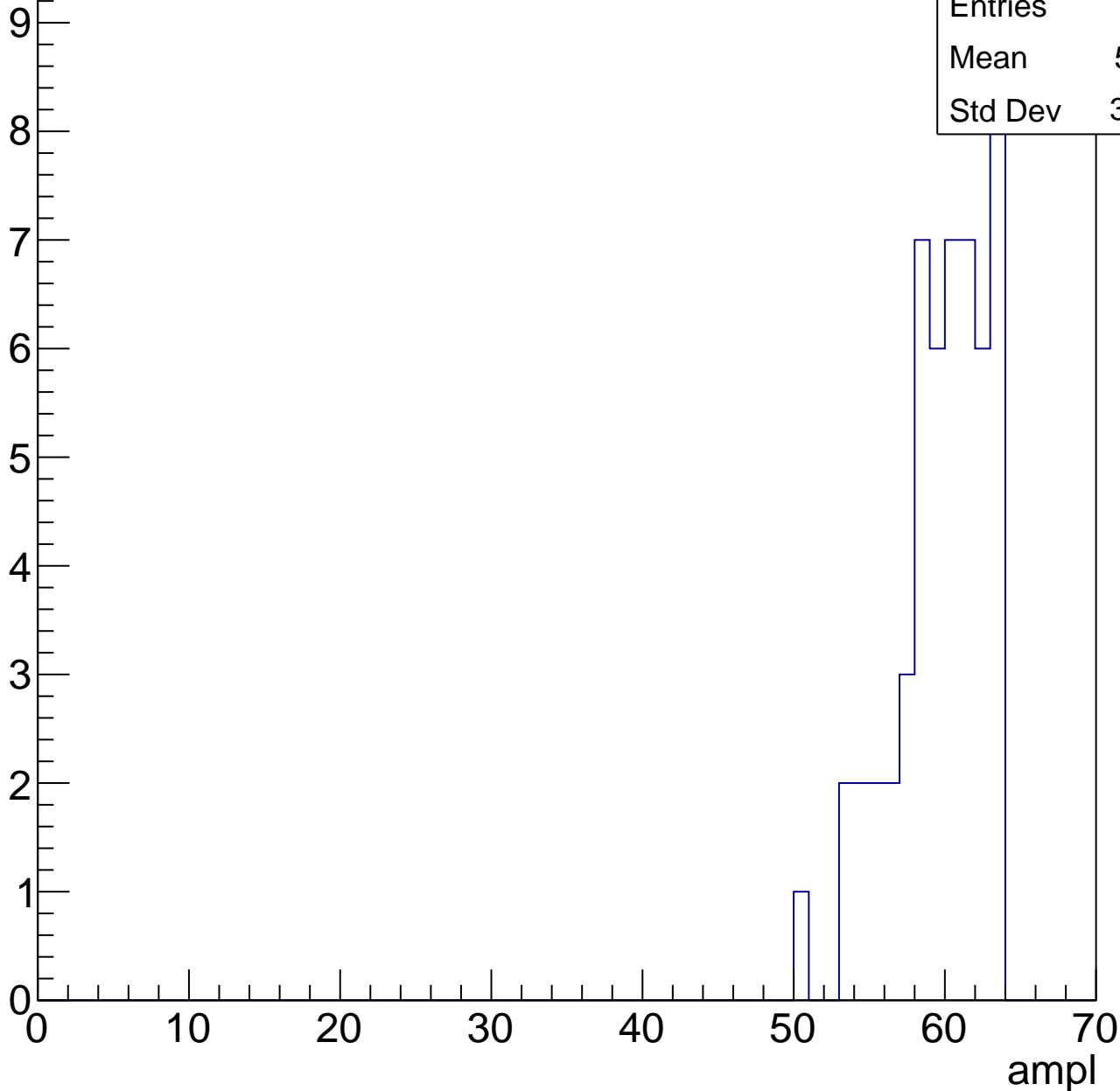


B1L100S, U7-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

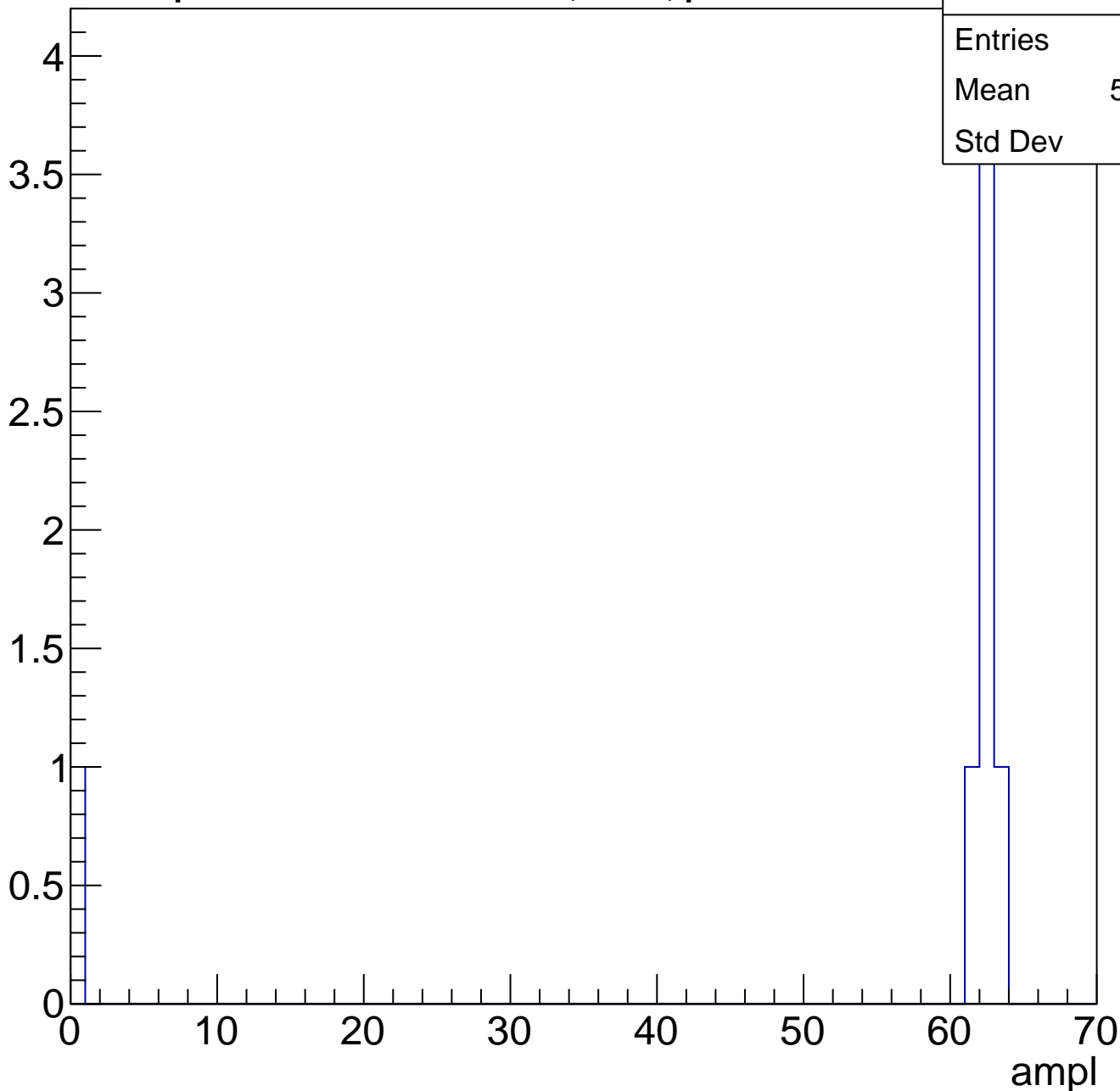
| | |
|---------|-------|
| Entries | 54 |
| Mean | 59.31 |
| Std Dev | 3.042 |



B1L100S, U7-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

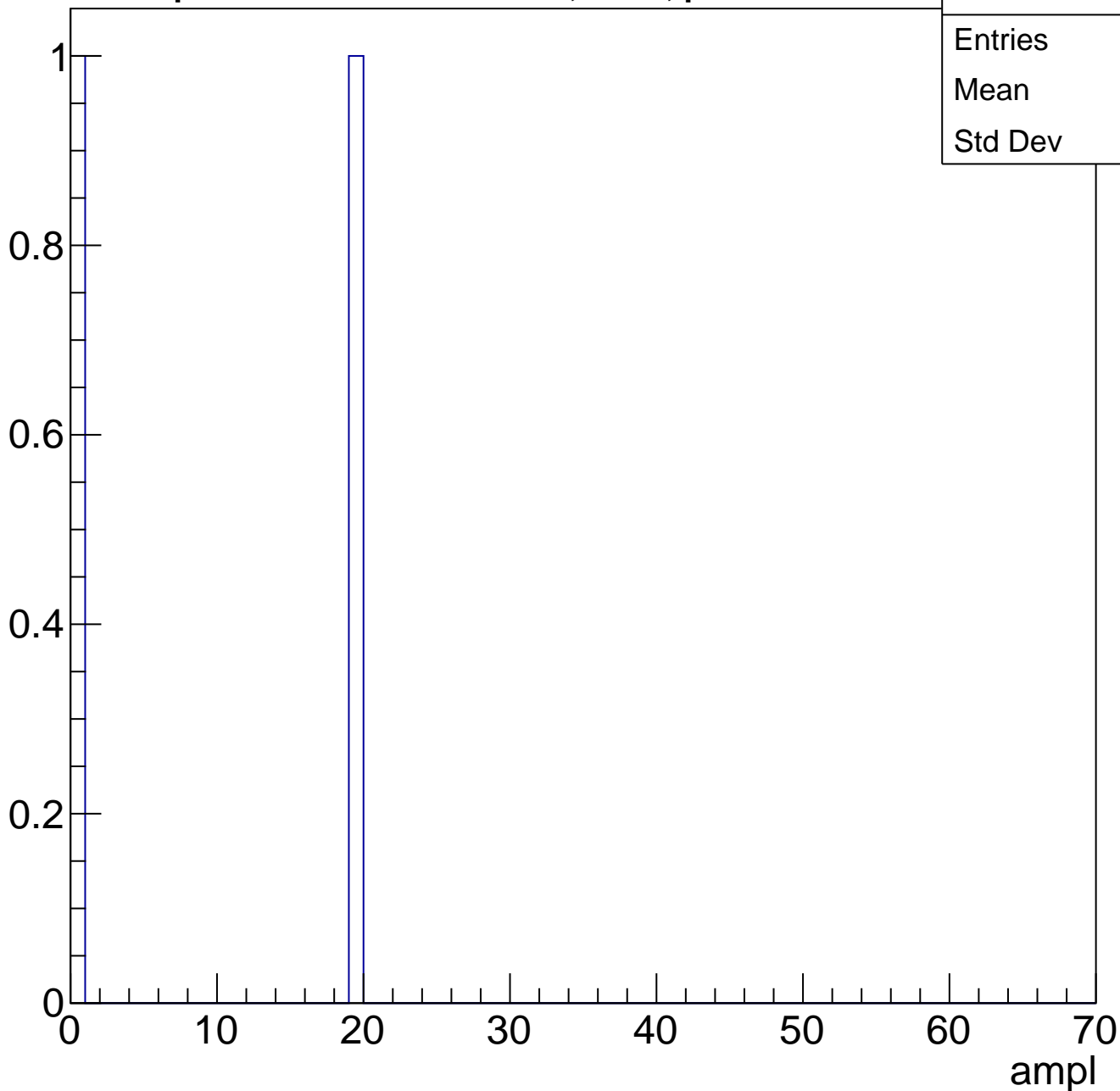
Entry



B1L100S, U7-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-----|
| Entries | 2 |
| Mean | 9.5 |
| Std Dev | 9.5 |

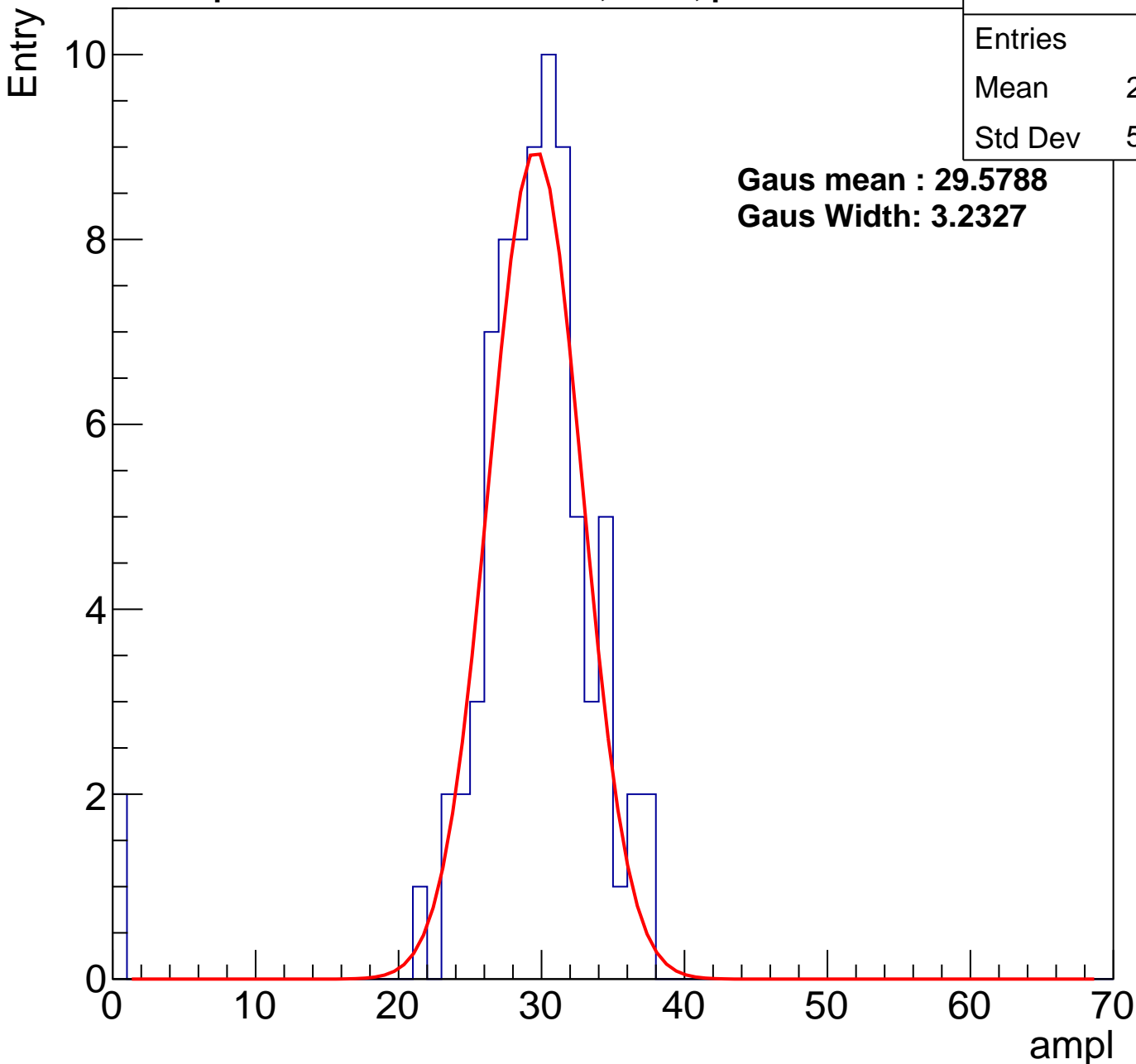
B1L100S, U7-ch59, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.63 |
| Std Dev | 5.664 |

Gaus mean : 29.5788

Gaus Width: 3.2327



B1L100S, U7-ch59, adc1

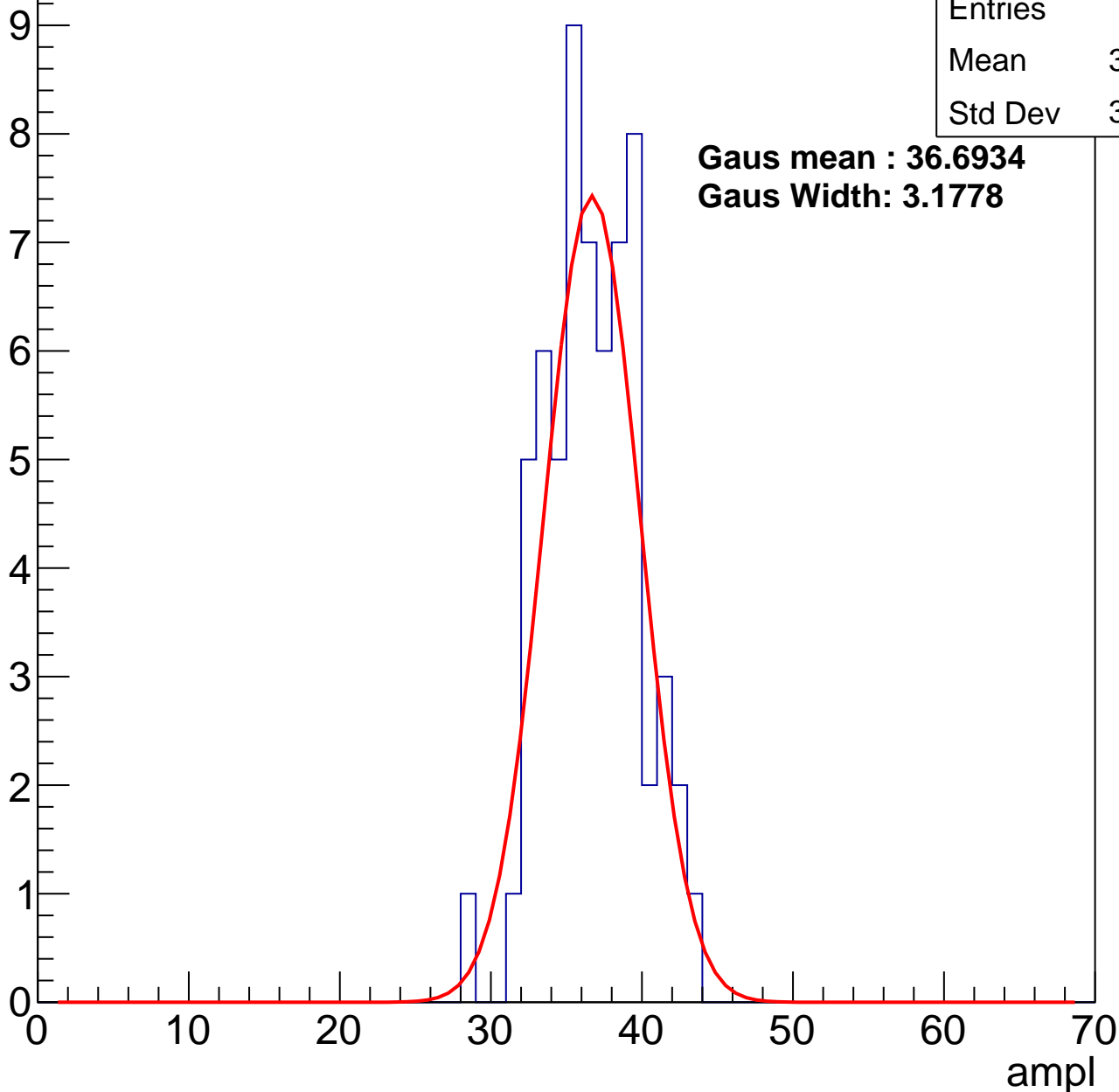
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.25 |
| Std Dev | 3.039 |

Gaus mean : 36.6934

Gaus Width: 3.1778



B1L100S, U7-ch59, adc2

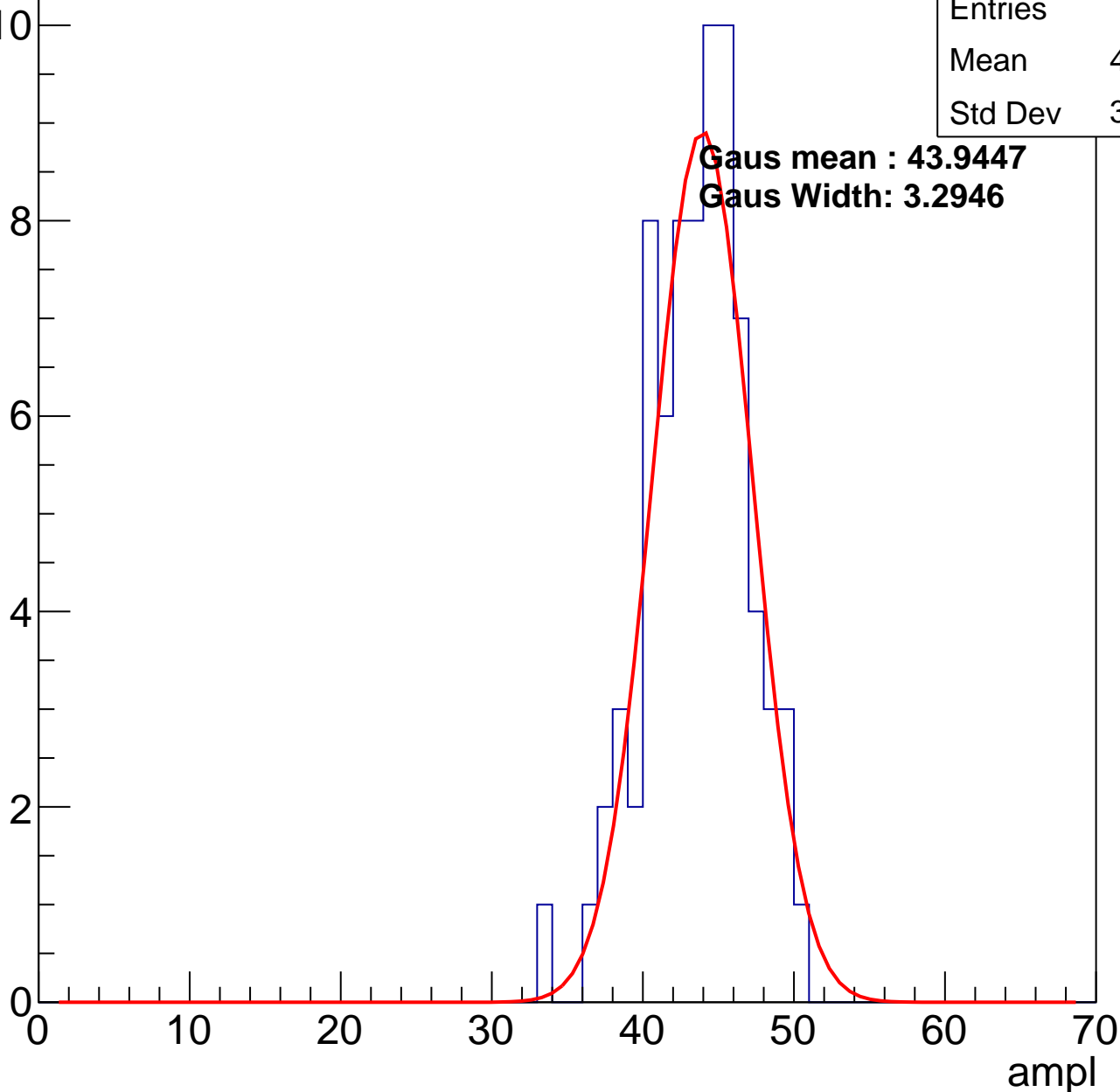
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 43.14 |
| Std Dev | 3.302 |

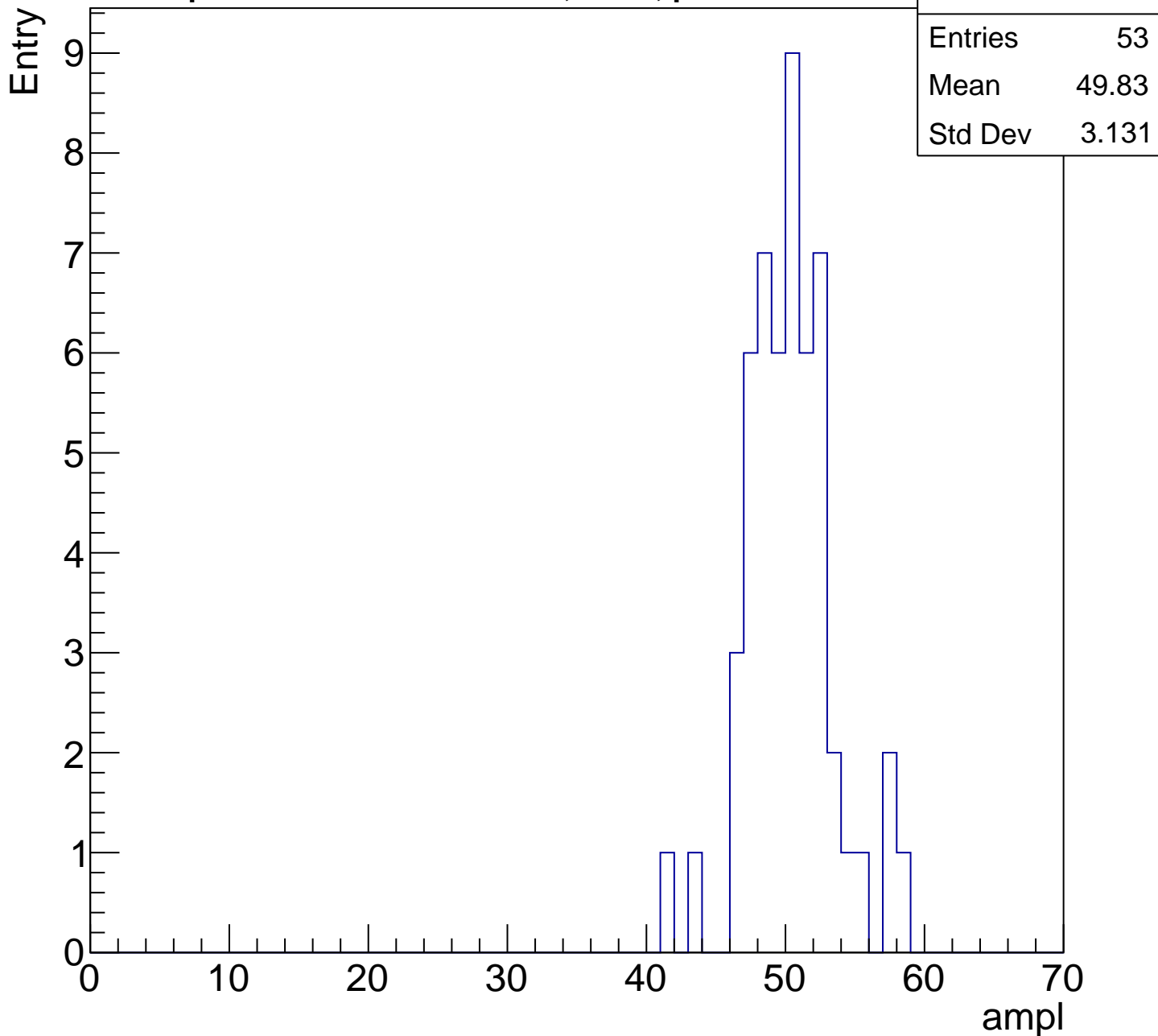
Gaus mean : 43.9447

Gaus Width: 3.2946



B1L100S, U7-ch59, adc3

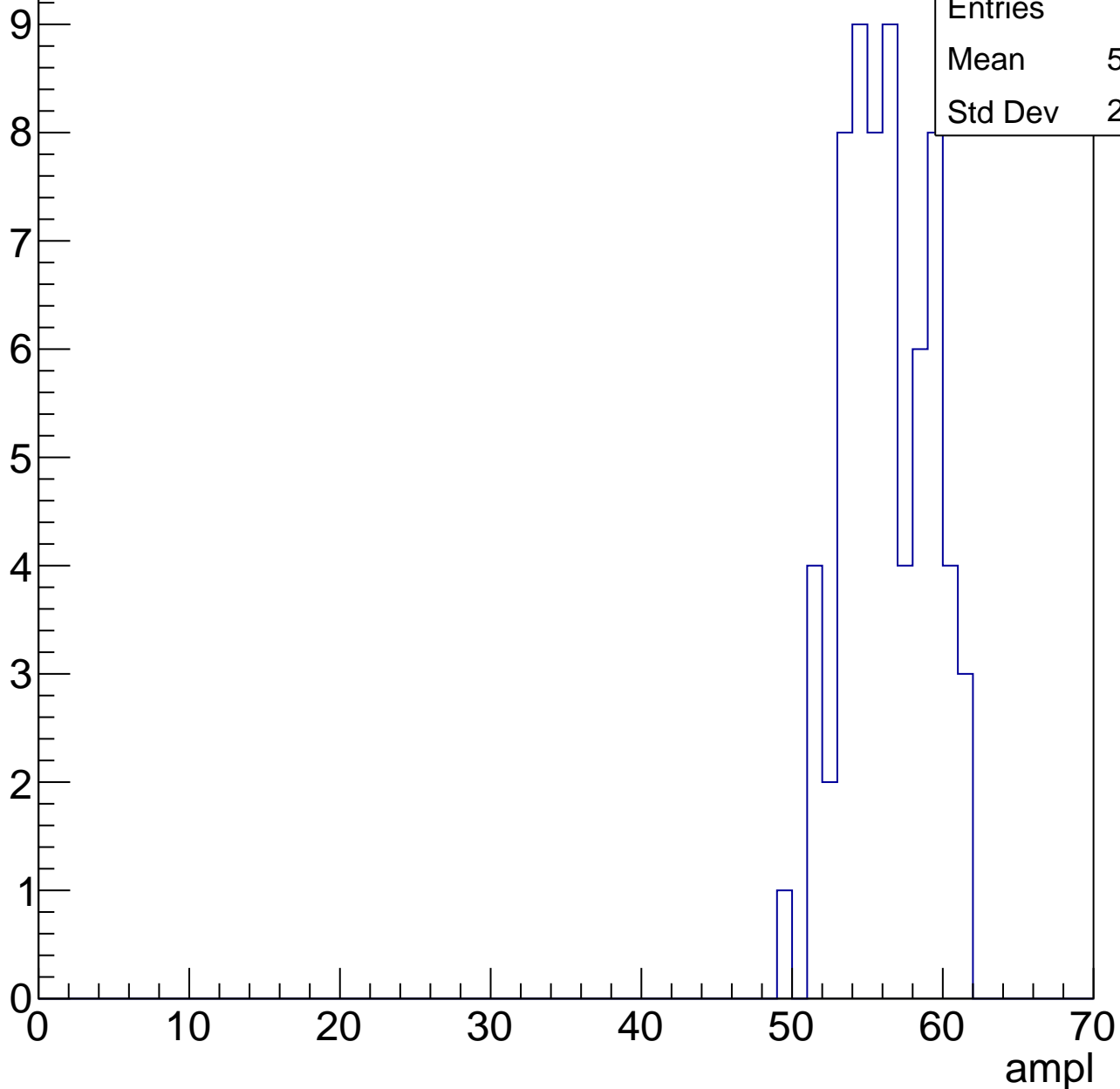
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



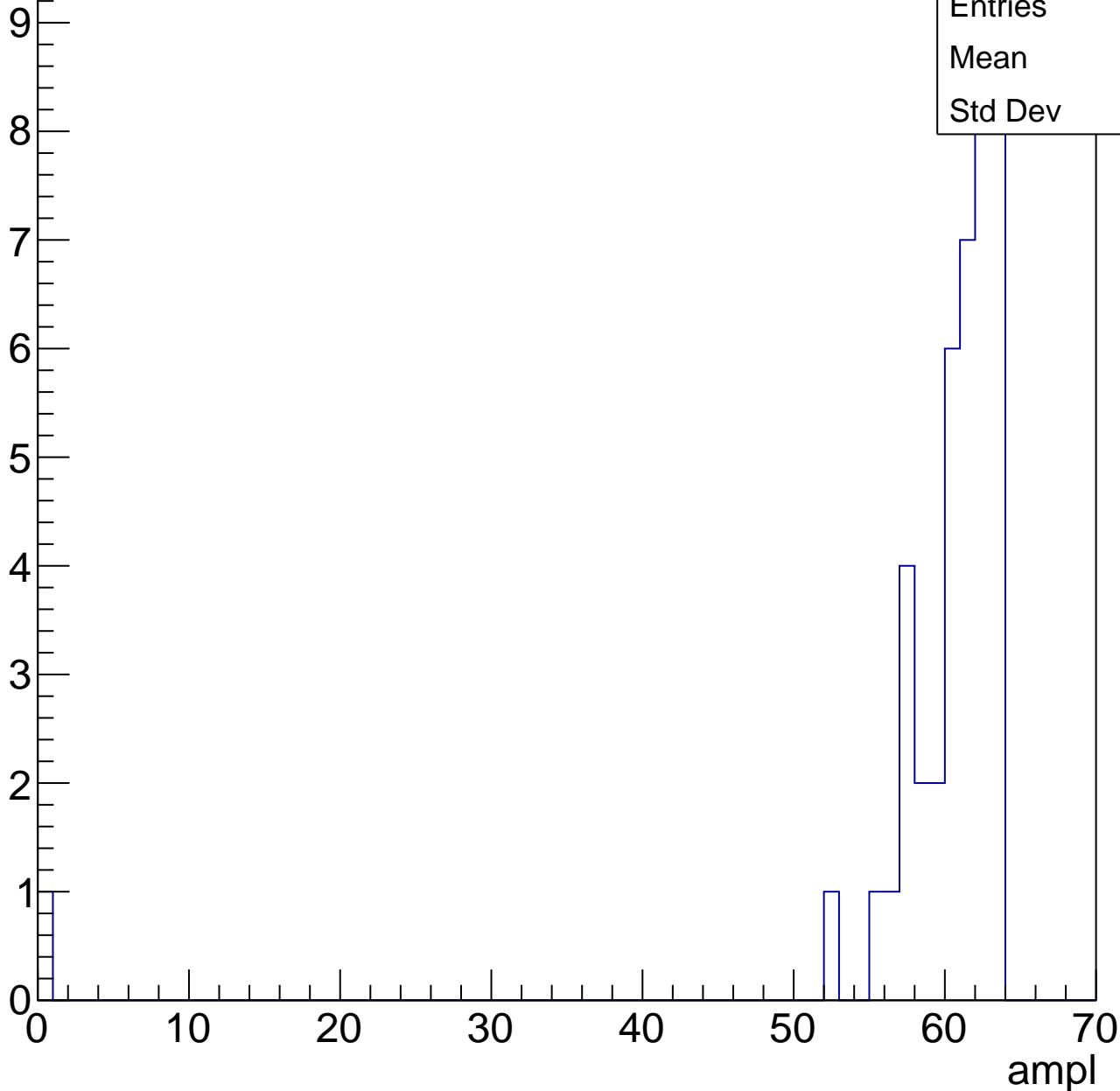
| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.79 |
| Std Dev | 2.842 |

B1L100S, U7-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

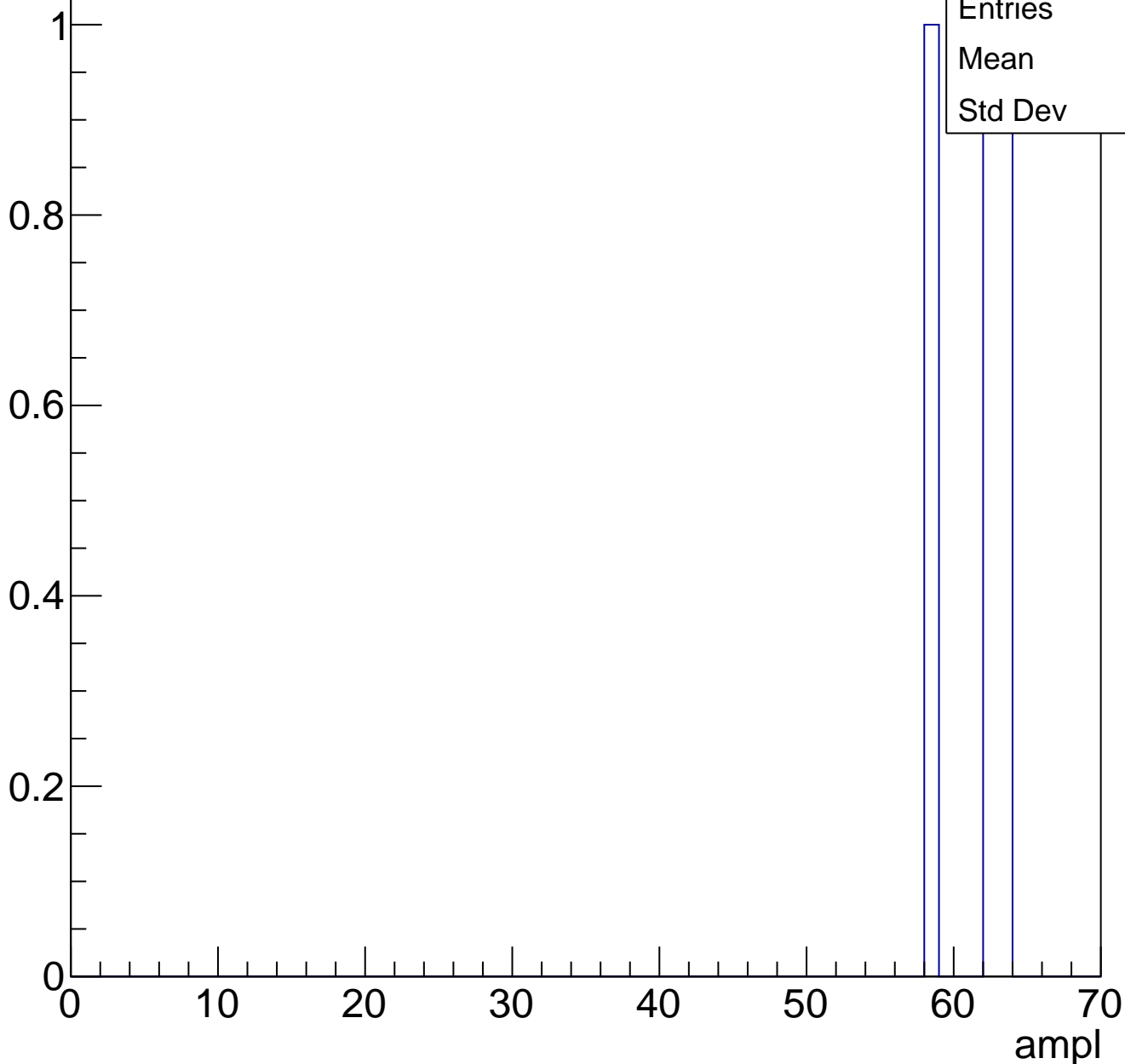
| | |
|---------|------|
| Entries | 43 |
| Mean | 59 |
| Std Dev | 9.44 |



B1L100S, U7-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch60, adc0

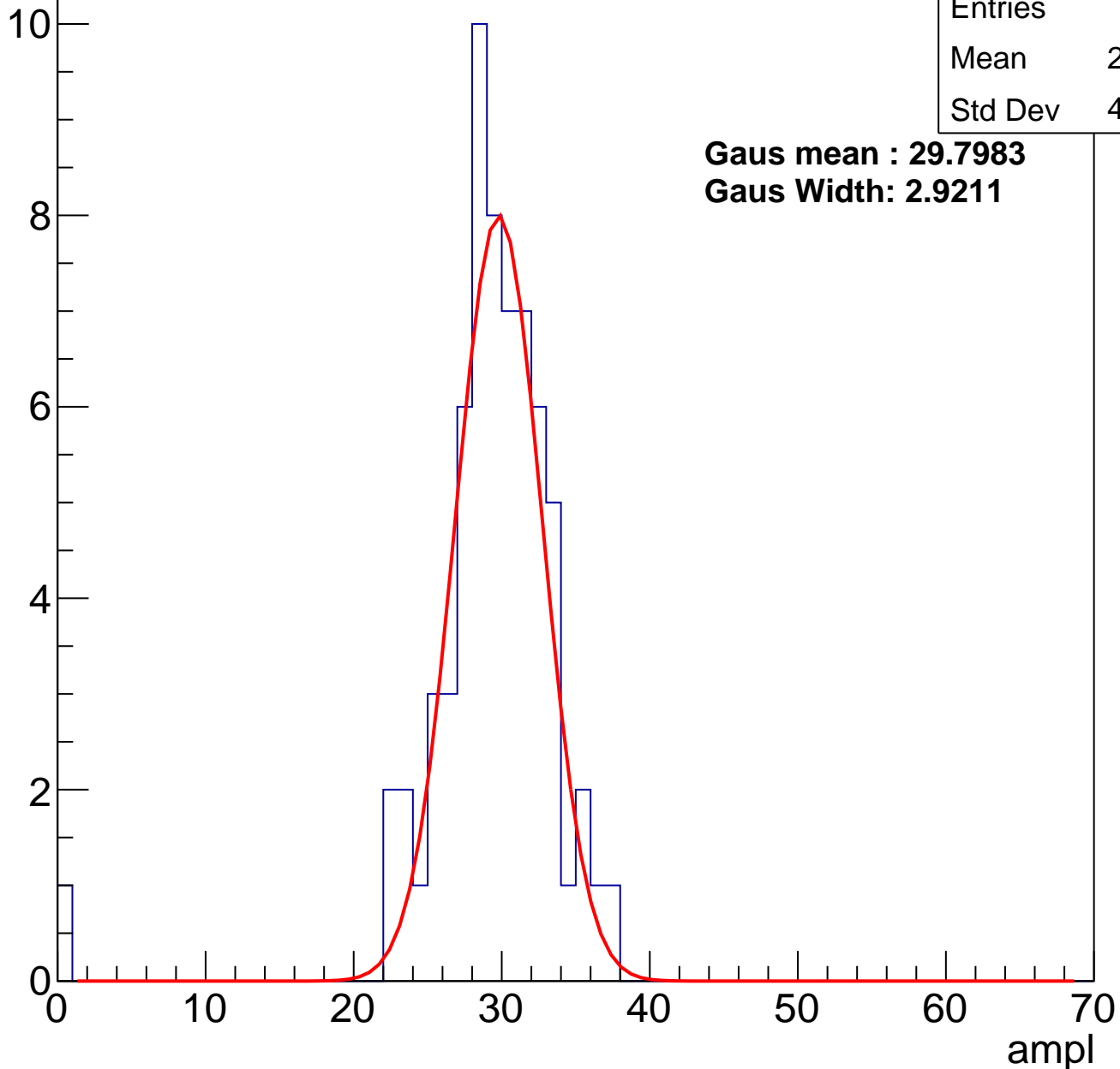
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.82 |
| Std Dev | 4.802 |

Gaus mean : 29.7983

Gaus Width: 2.9211

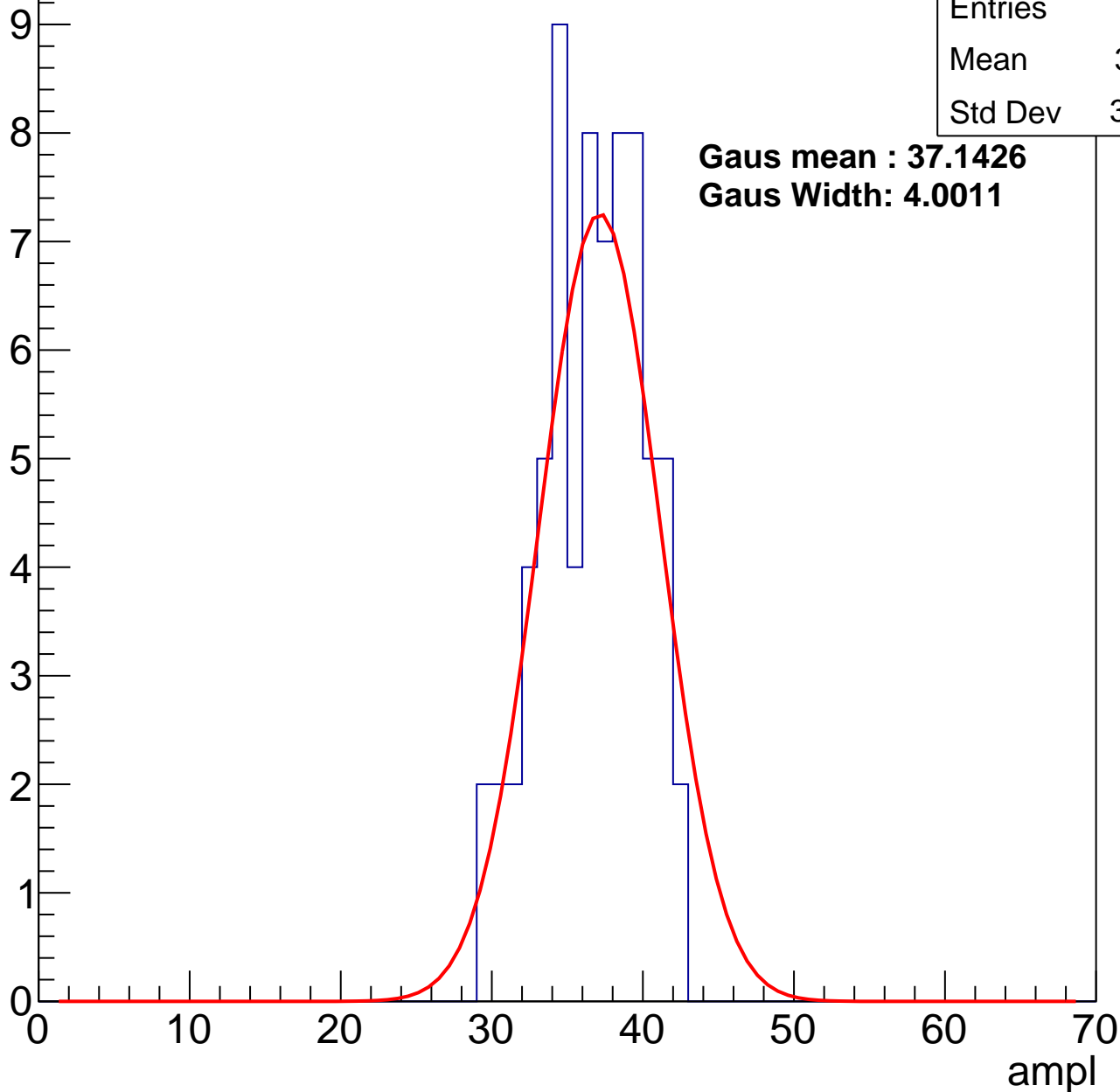
Entry



B1L100S, U7-ch60, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch60, adc2

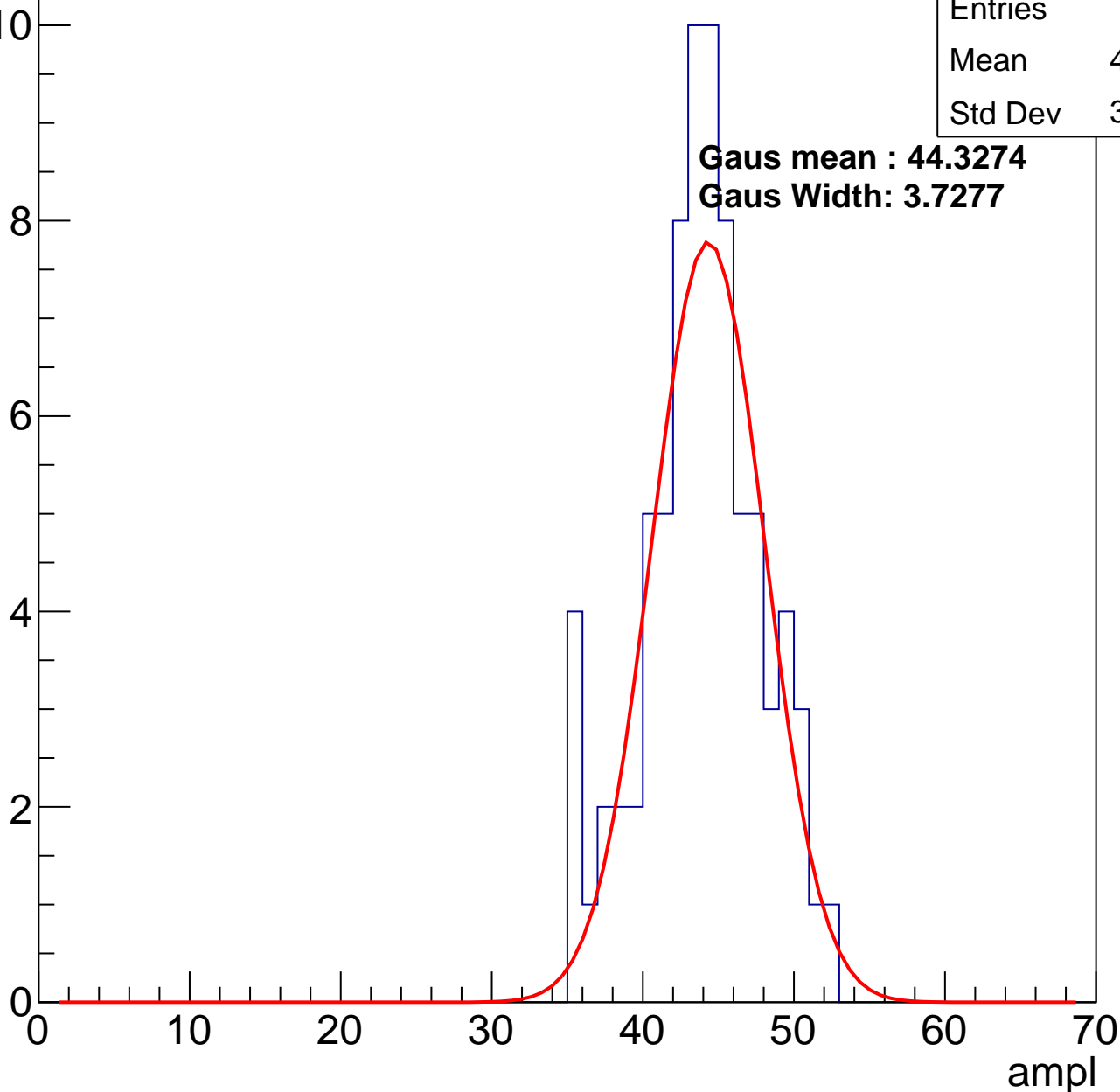
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 43.46 |
| Std Dev | 3.887 |

Gaus mean : 44.3274

Gaus Width: 3.7277

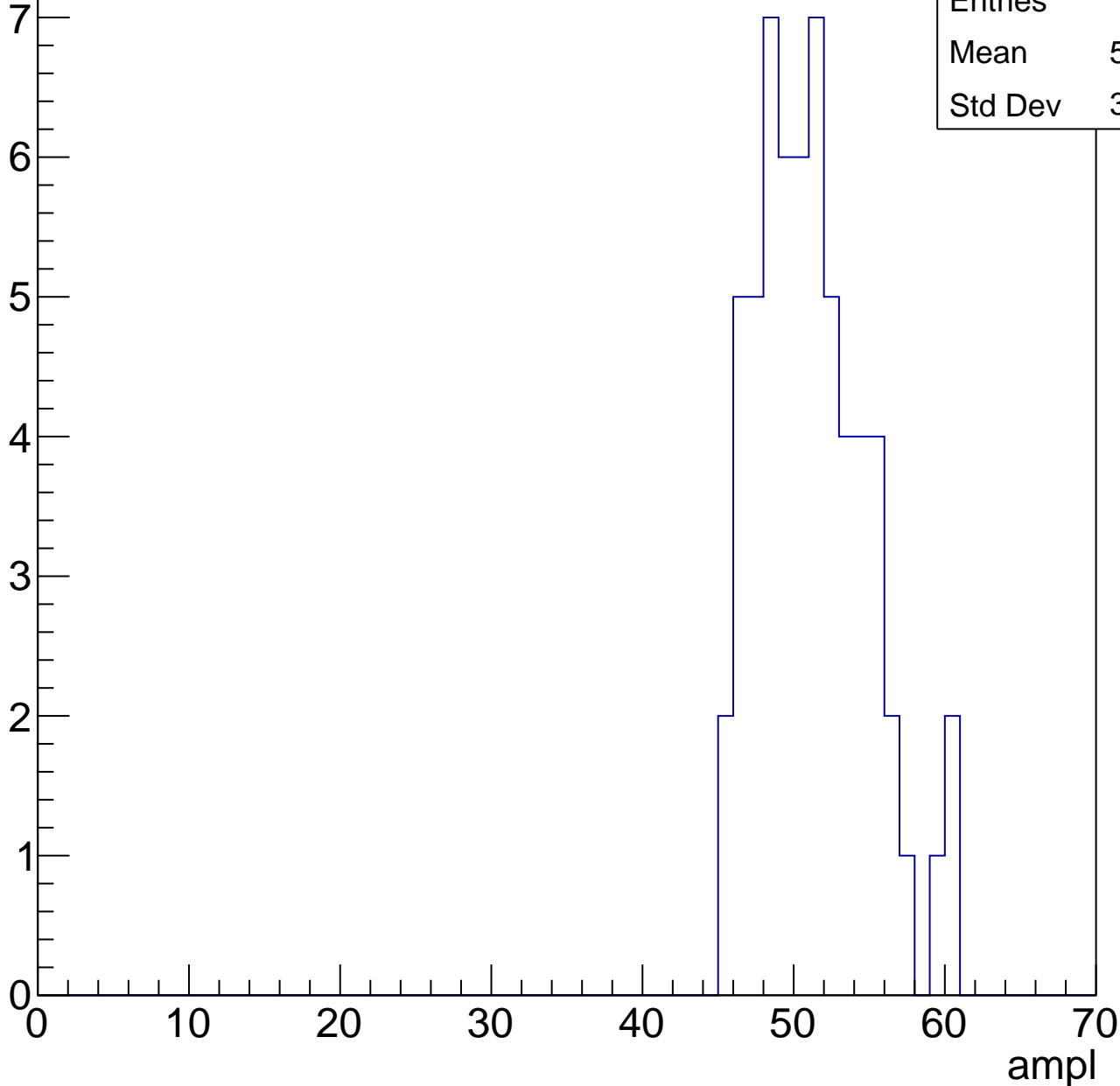


B1L100S, U7-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

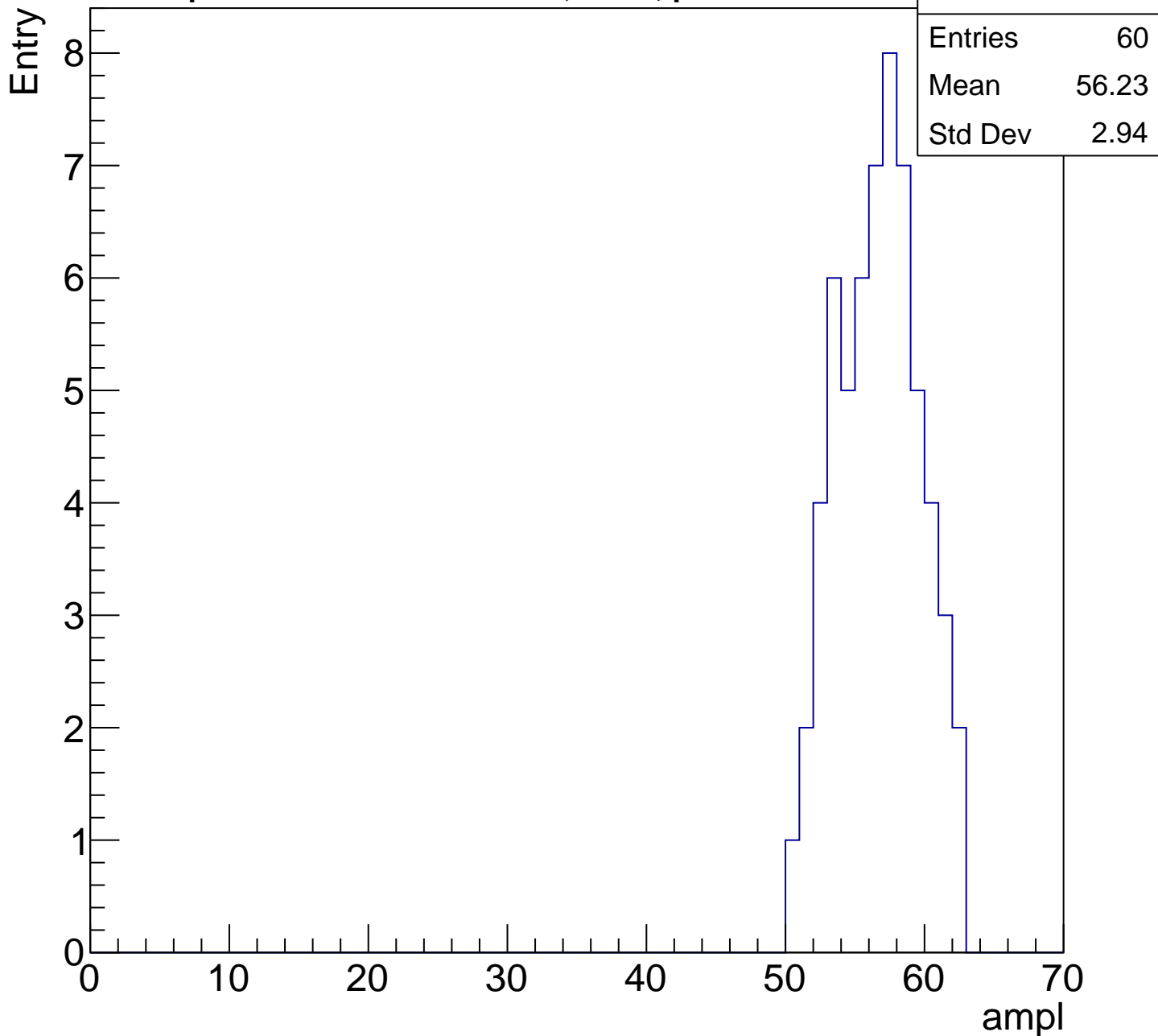
Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 50.79 |
| Std Dev | 3.622 |



B1L100S, U7-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

| | |
|---------|-------|
| Entries | 37 |
| Mean | 60.73 |
| Std Dev | 2.214 |

0

2

4

6

8

10

B1L100S, U7-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch61, adc0

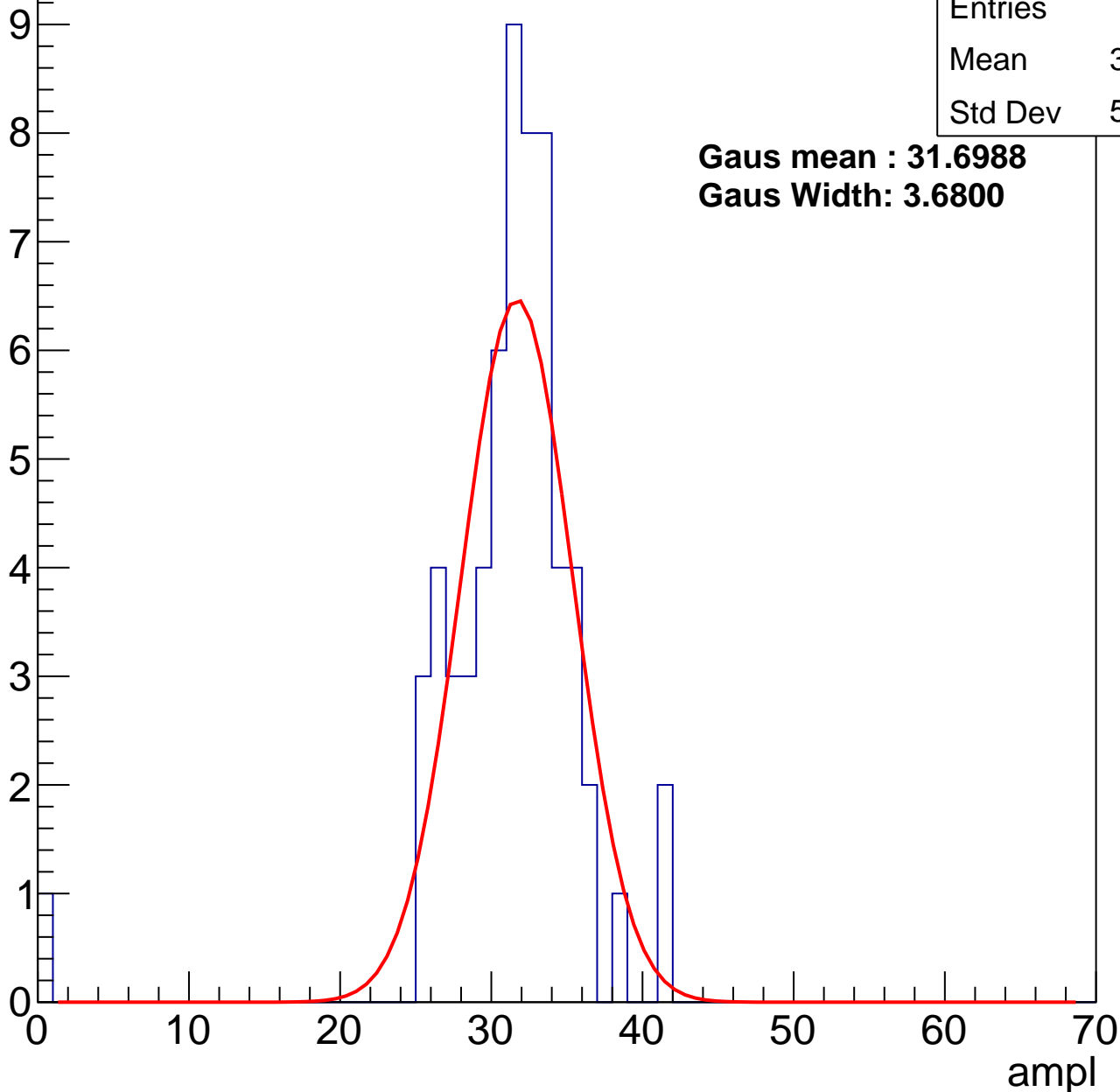
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.76 |
| Std Dev | 5.238 |

Gaus mean : 31.6988

Gaus Width: 3.6800



B1L100S, U7-ch61, adc1

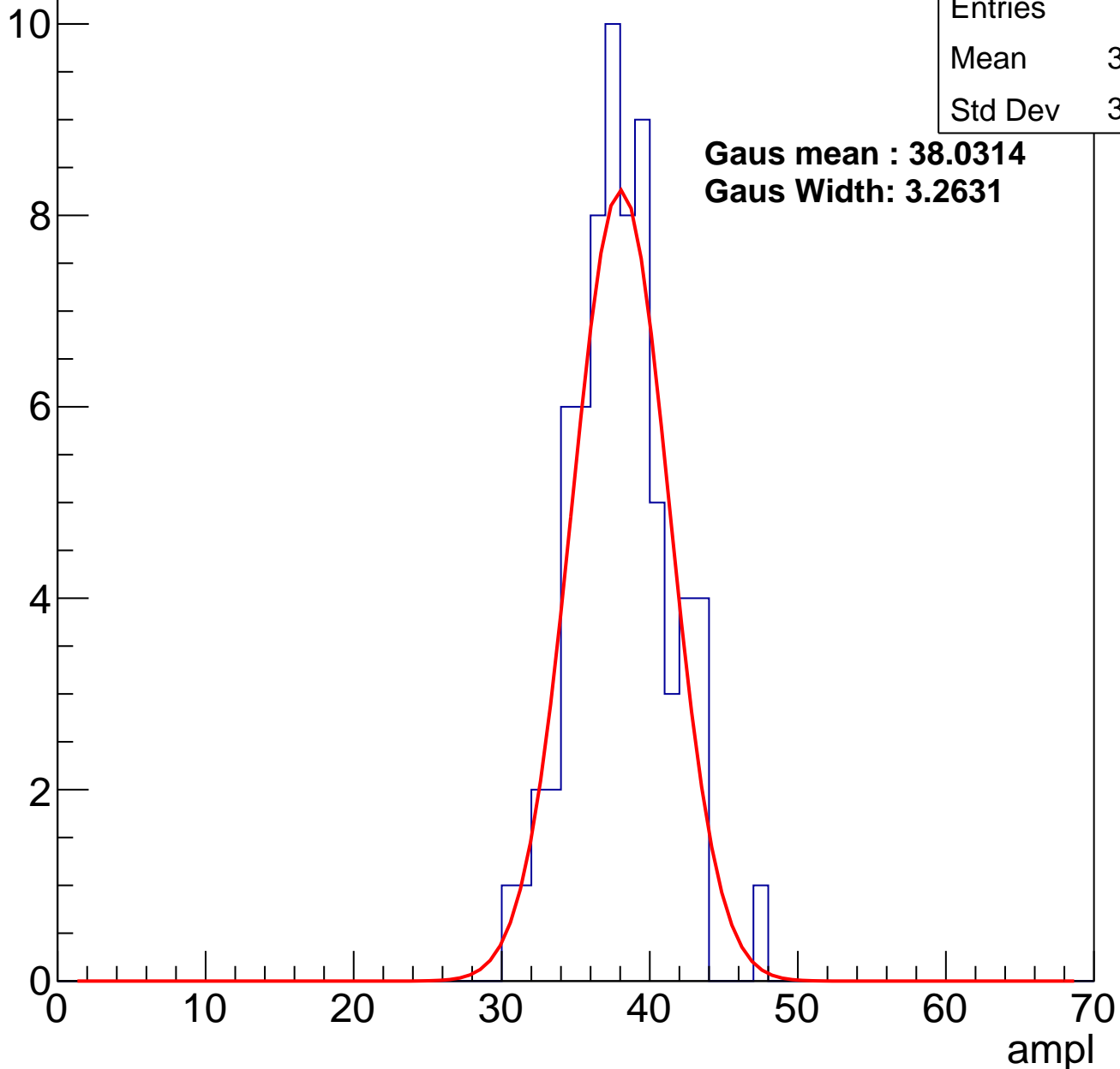
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.54 |
| Std Dev | 3.192 |

Gaus mean : 38.0314

Gaus Width: 3.2631

Entry



B1L100S, U7-ch61, adc2

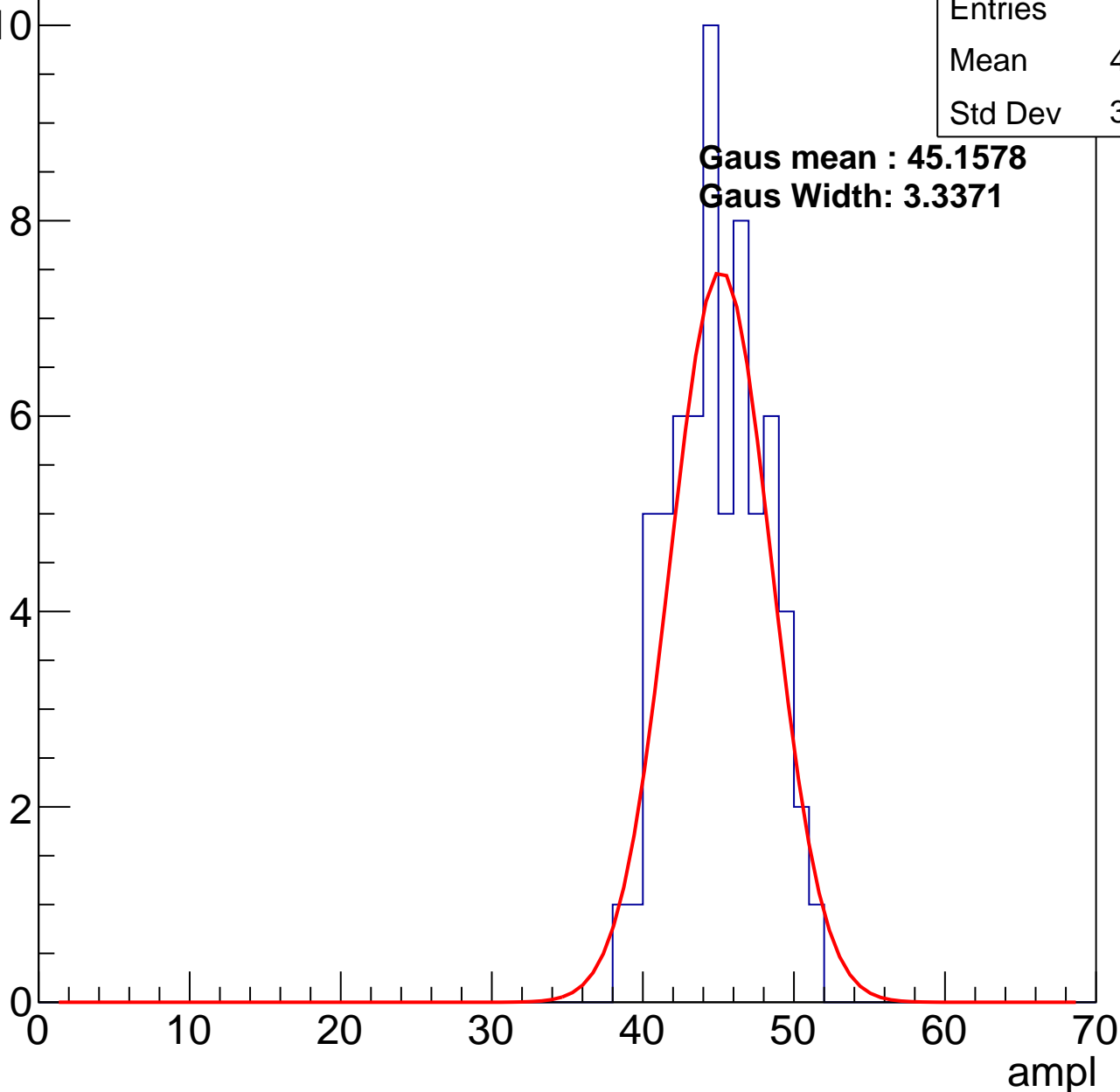
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 44.54 |
| Std Dev | 3.028 |

Gaus mean : 45.1578

Gaus Width: 3.3371

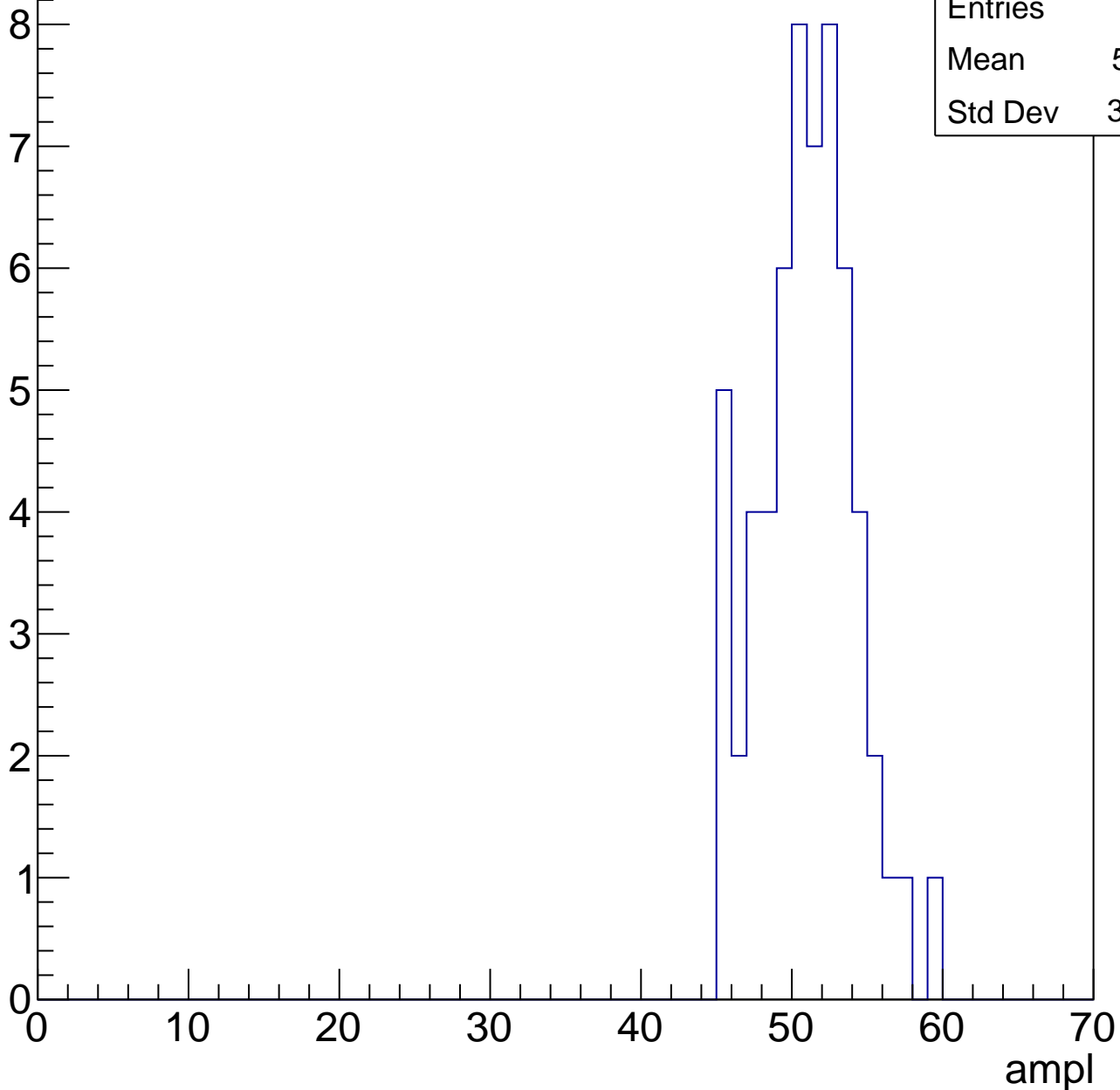


B1L100S, U7-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

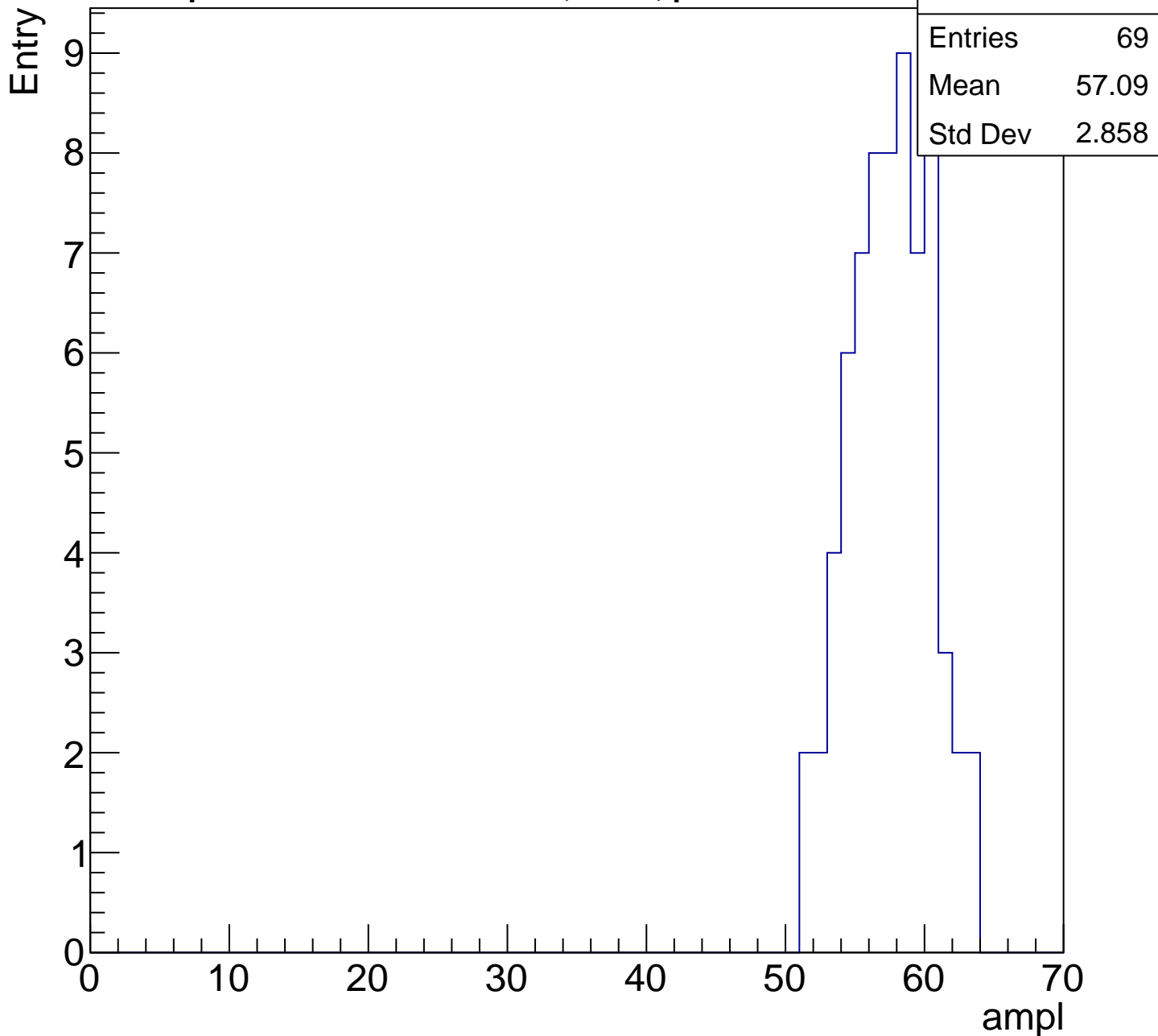
Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 50.51 |
| Std Dev | 3.116 |



B1L100S, U7-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

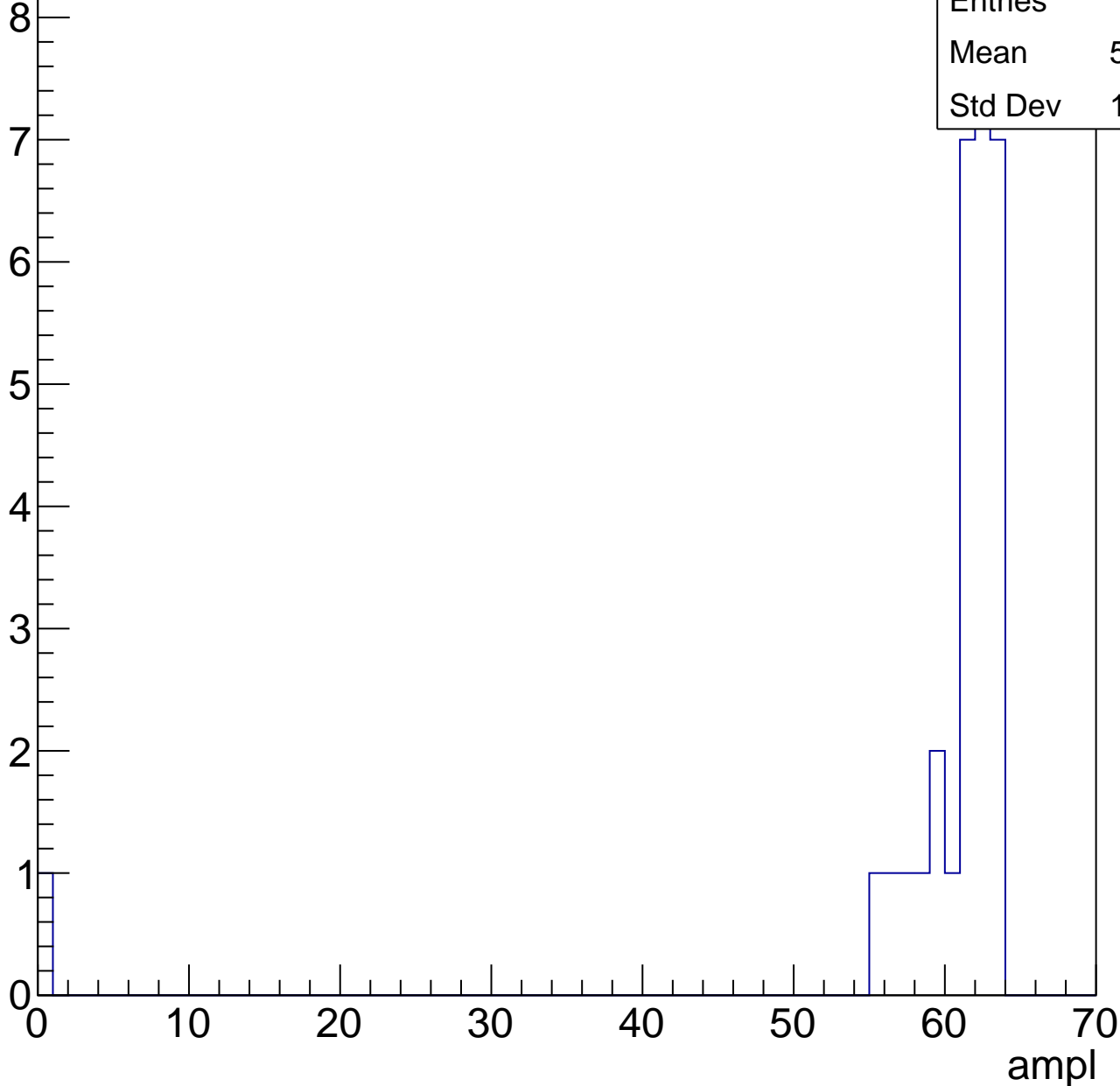


B1L100S, U7-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 30 |
| Mean | 58.93 |
| Std Dev | 11.14 |



B1L100S, U7-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B1L100S, U7-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch62, adc0

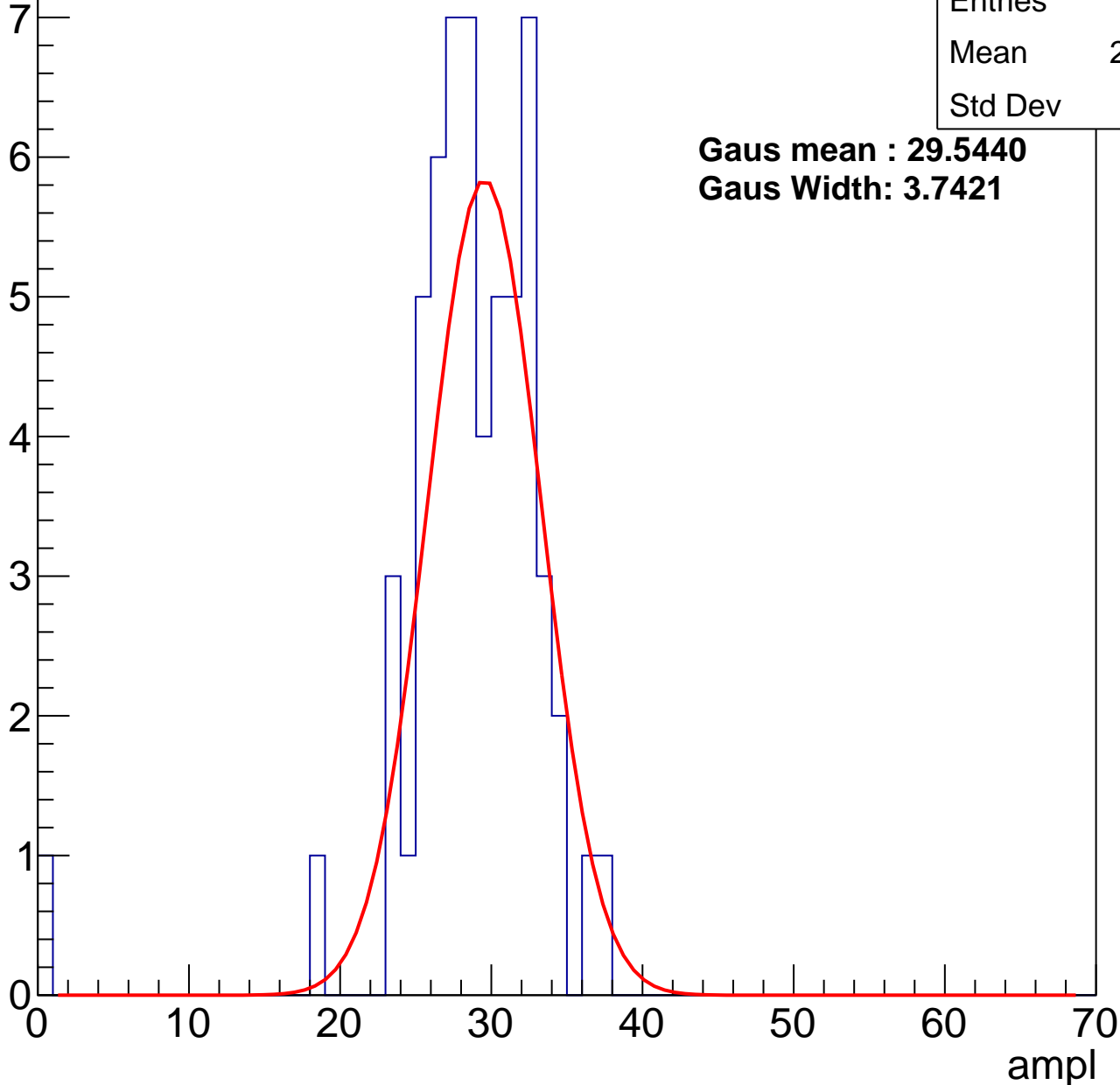
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 28.17 |
| Std Dev | 5.08 |

Gaus mean : 29.5440

Gaus Width: 3.7421



B1L100S, U7-ch62, adc1

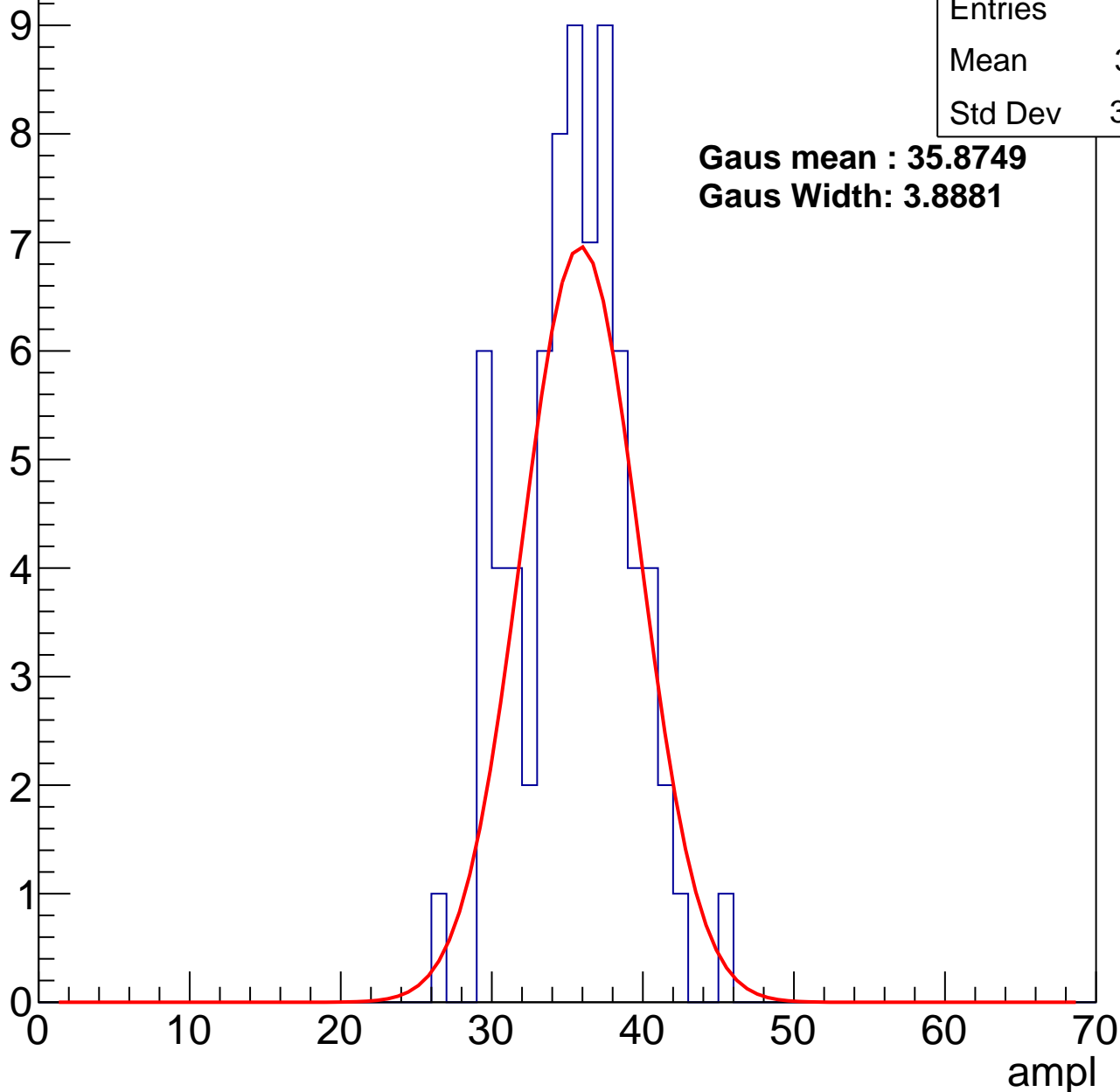
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.01 |
| Std Dev | 3.663 |

Gaus mean : 35.8749

Gaus Width: 3.8881



B1L100S, U7-ch62, adc2

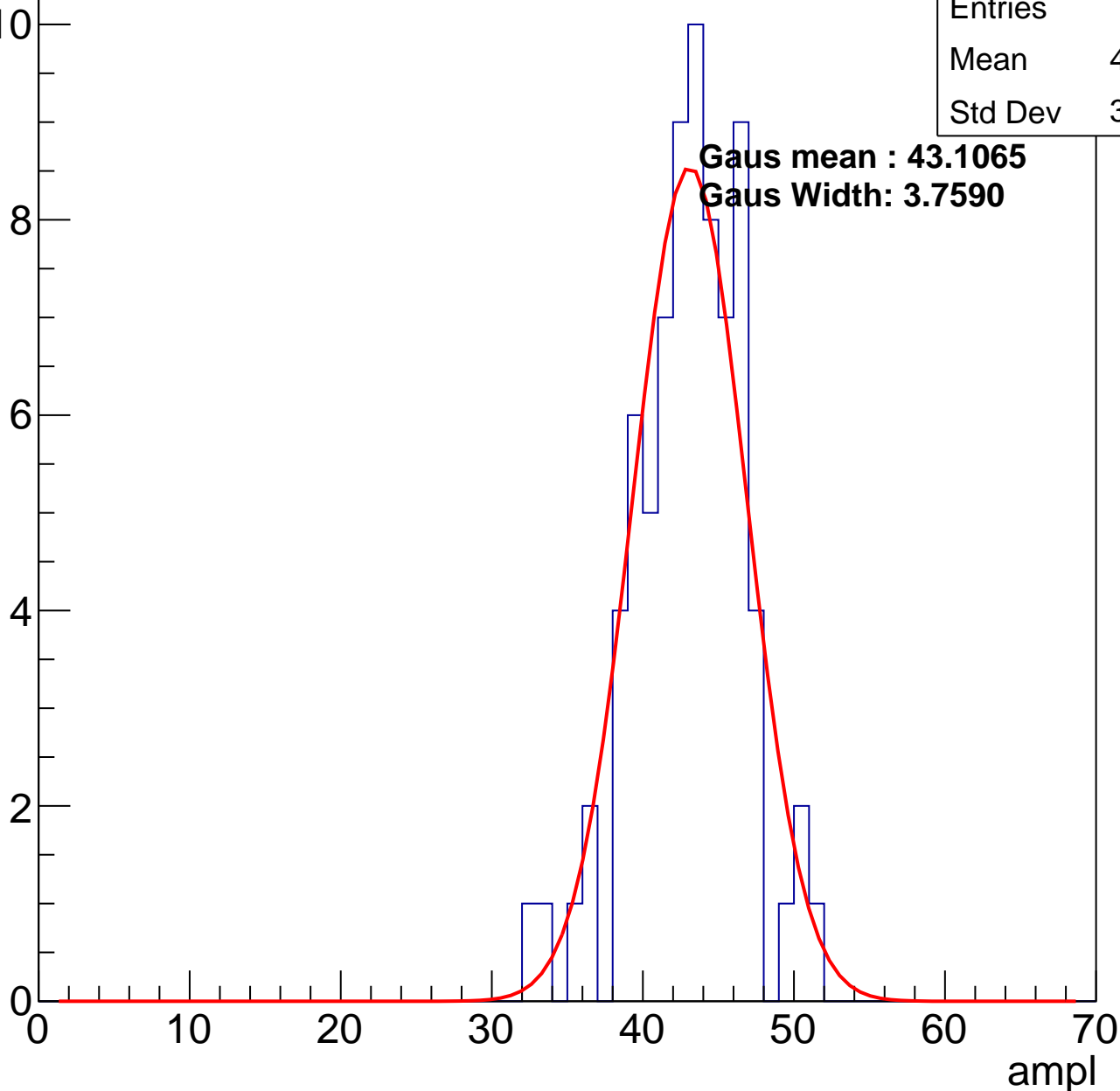
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.59 |
| Std Dev | 3.628 |

Gaus mean : 43.1065

Gaus Width: 3.7590

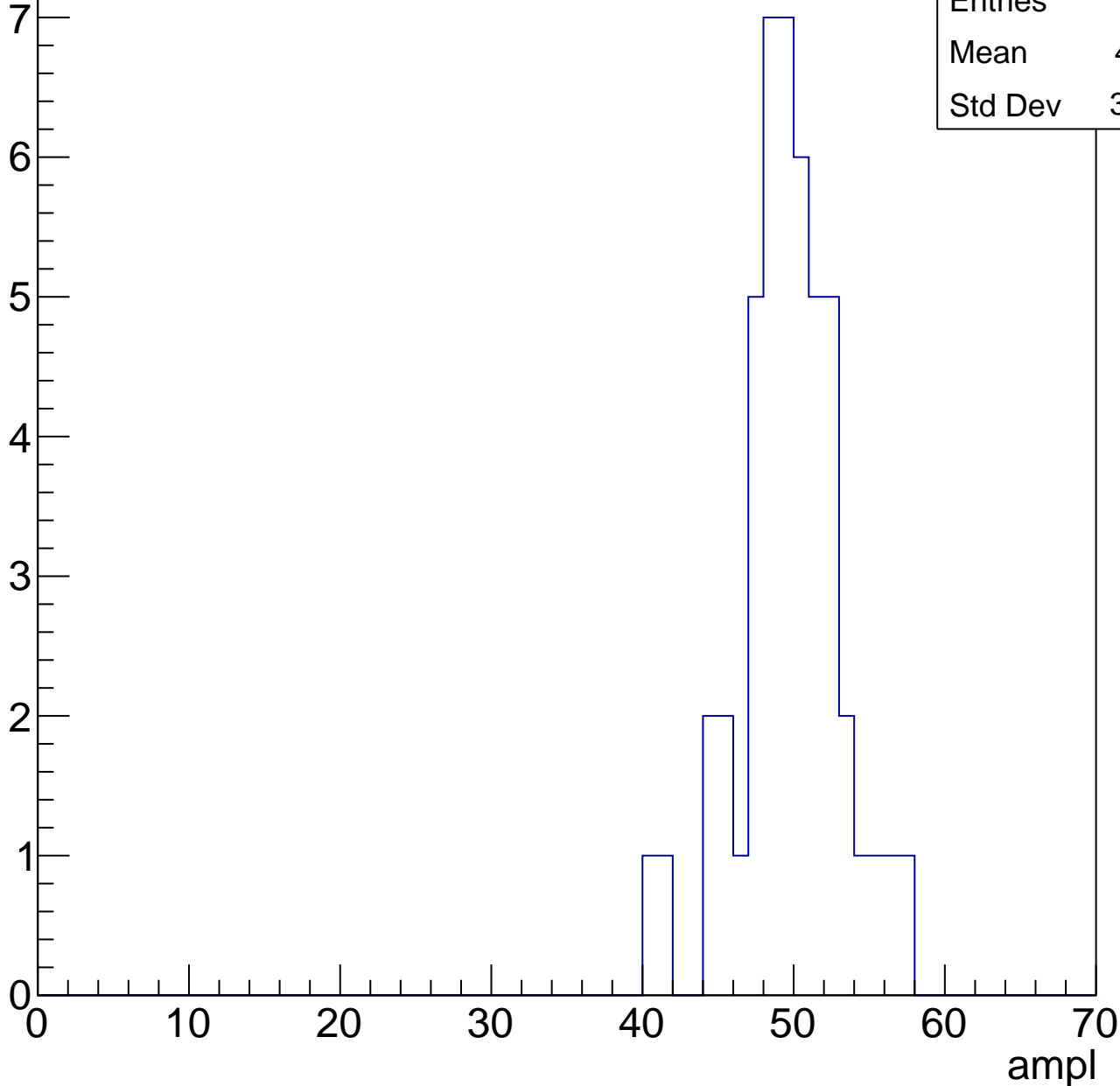


B1L100S, U7-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 49.21 |
| Std Dev | 3.348 |

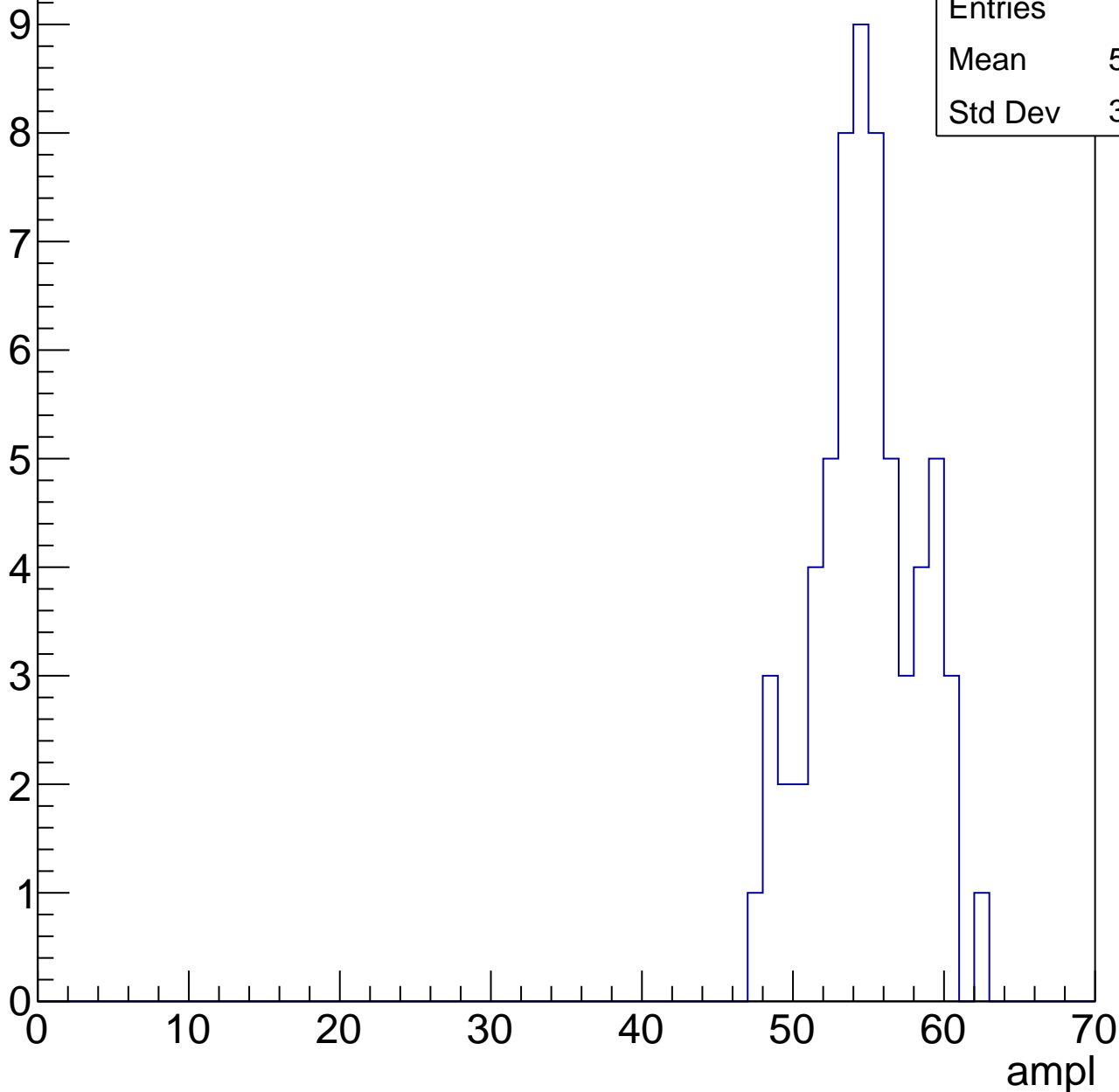


B1L100S, U7-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 54.33 |
| Std Dev | 3.362 |



B1L100S, U7-ch62, adc5

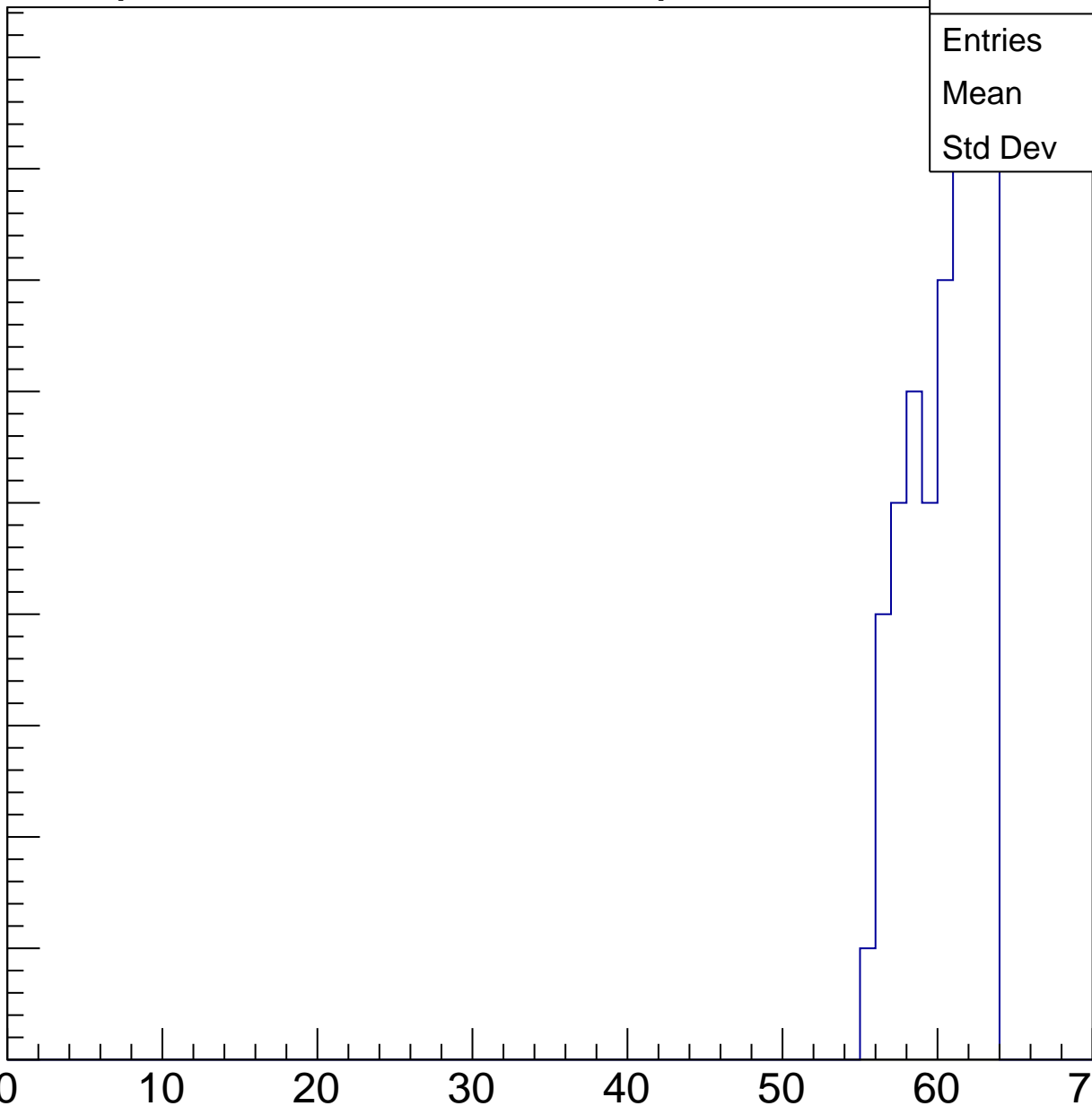
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.92 |
| Std Dev | 2.281 |

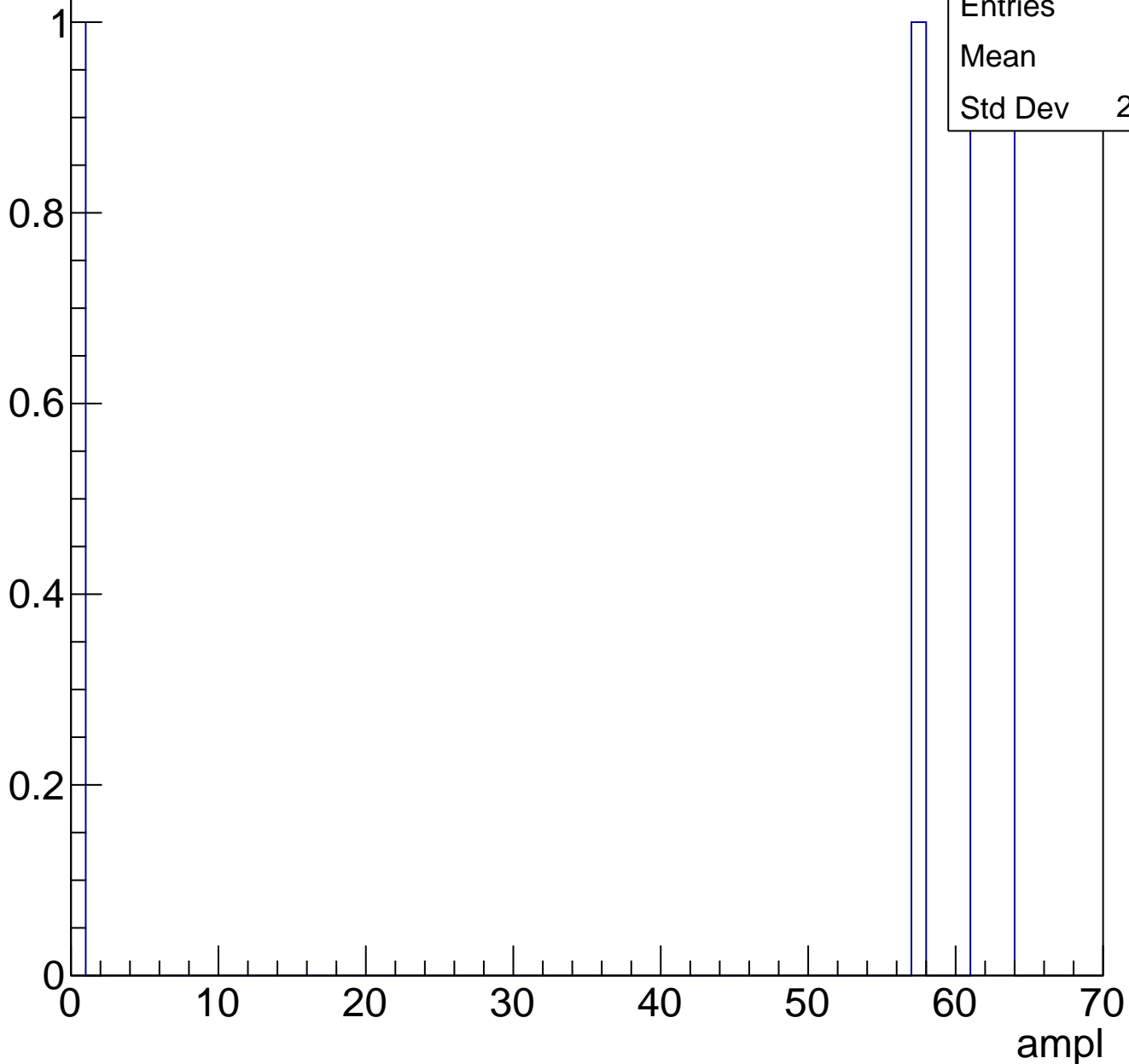
ampl



B1L100S, U7-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B1L100S, U7-ch63, adc0

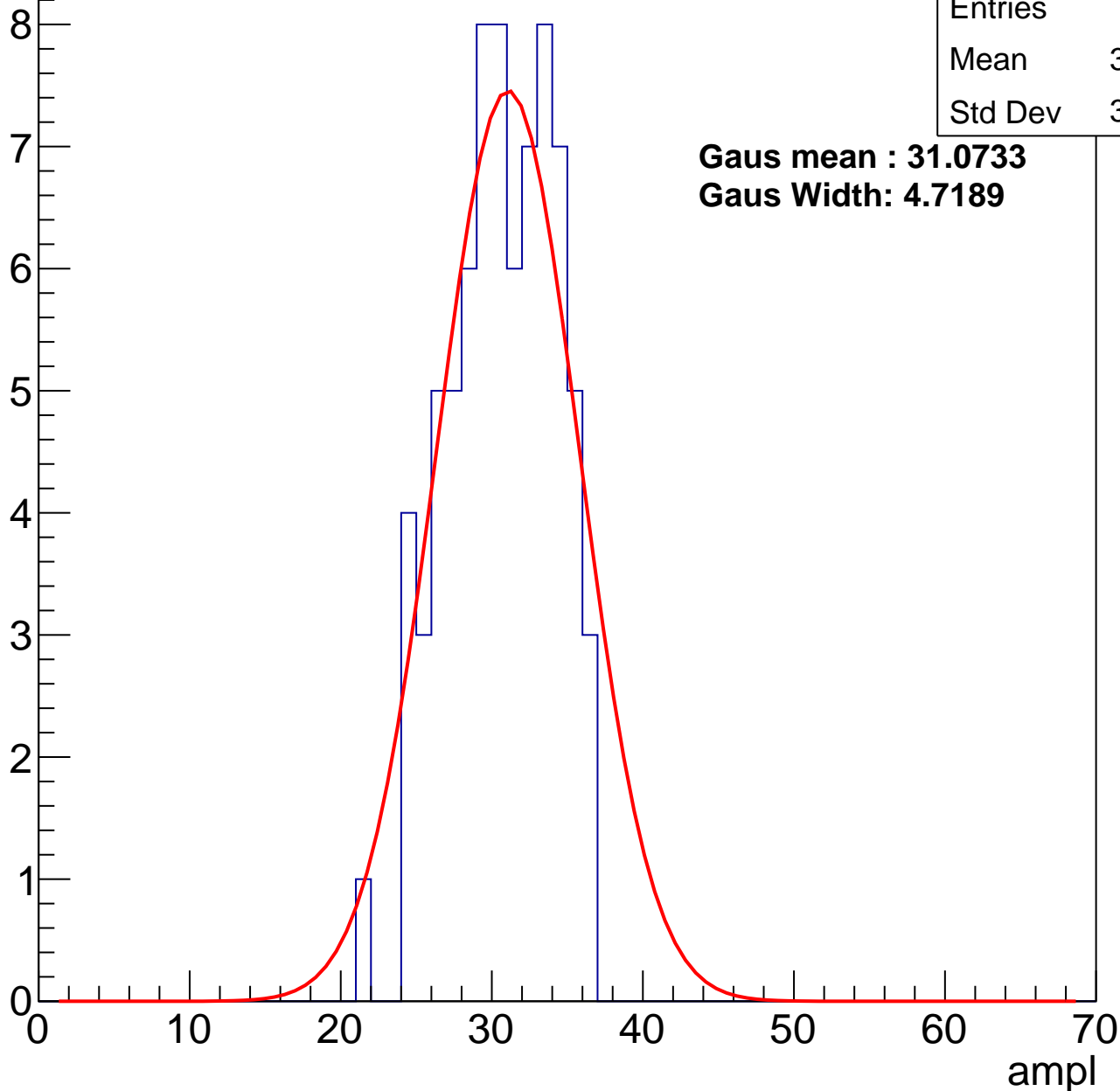
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.16 |
| Std Dev | 3.453 |

Gaus mean : 31.0733

Gaus Width: 4.7189



B1L100S, U7-ch63, adc1

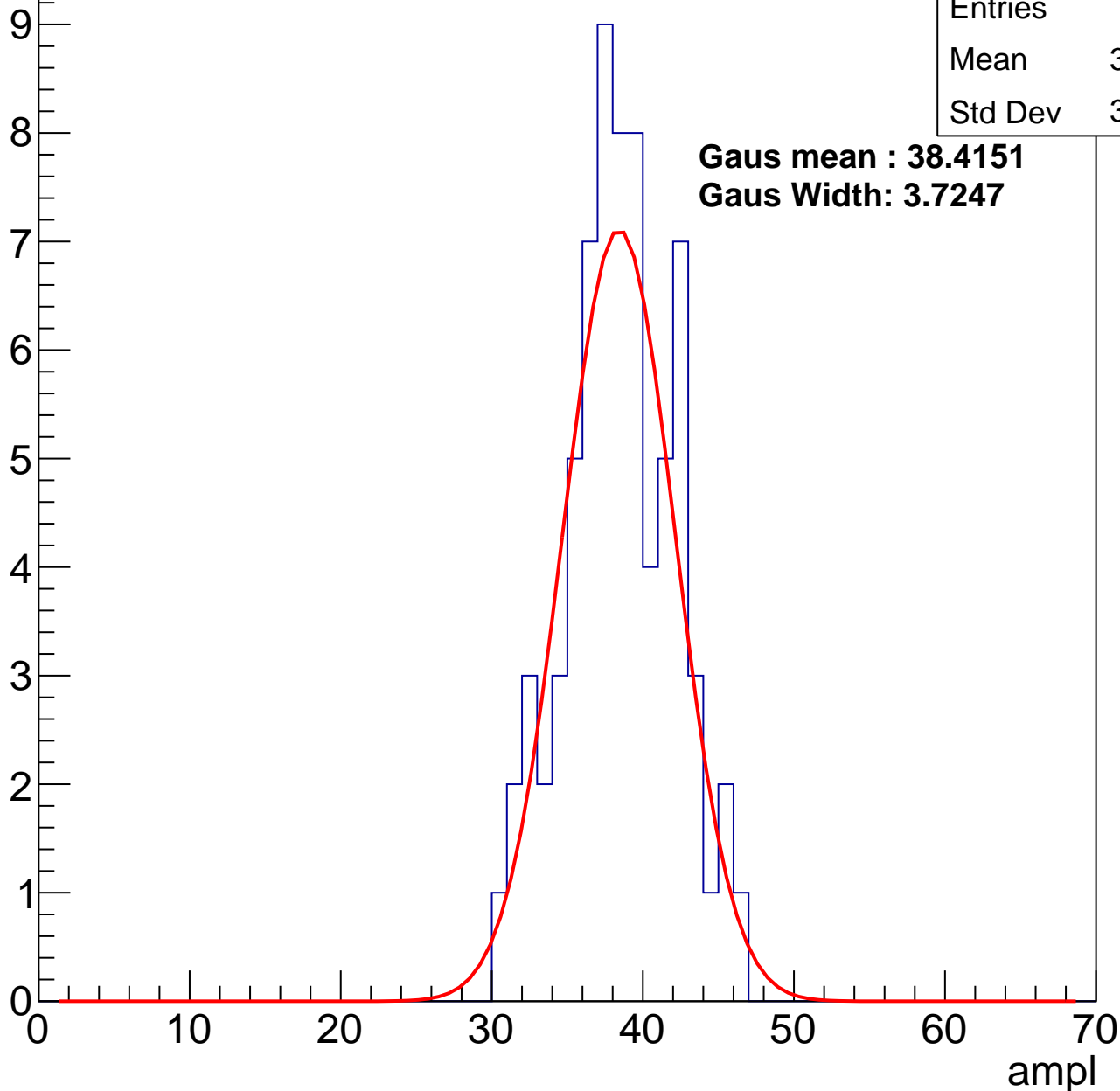
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 38.03 |
| Std Dev | 3.568 |

Gaus mean : 38.4151

Gaus Width: 3.7247



B1L100S, U7-ch63, adc2

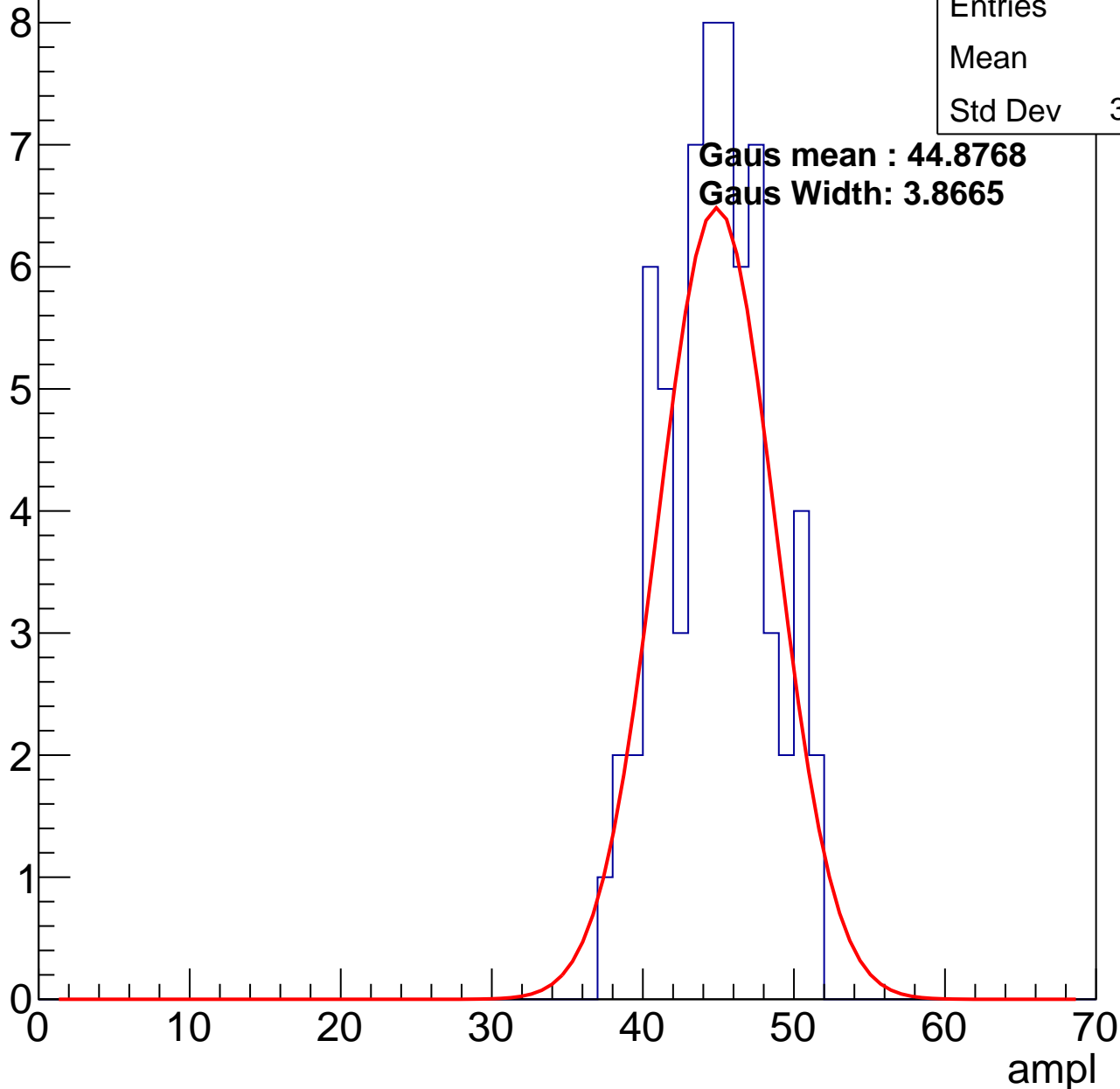
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44.3 |
| Std Dev | 3.393 |

Gaus mean : 44.8768

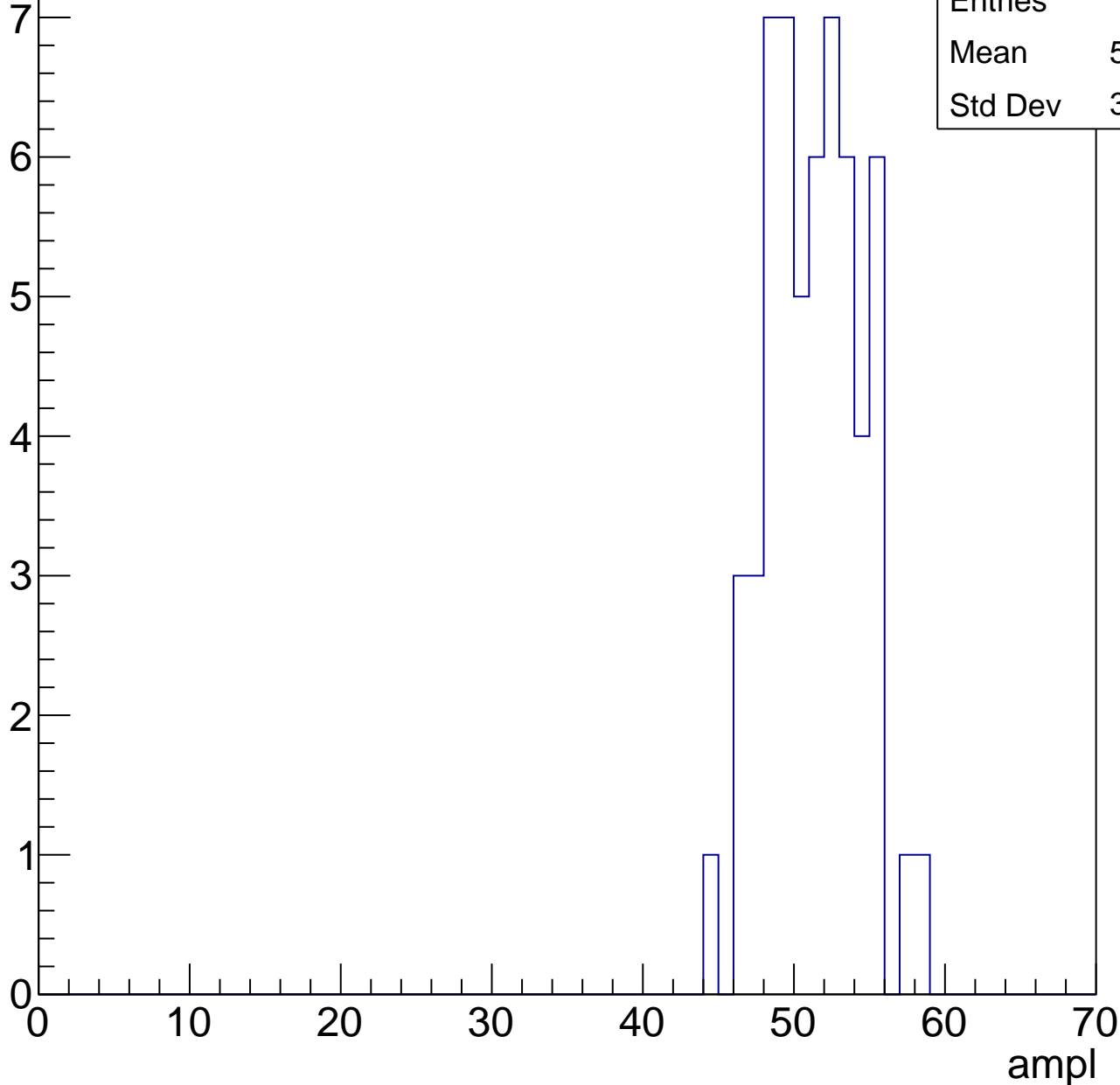
Gaus Width: 3.8665



B1L100S, U7-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

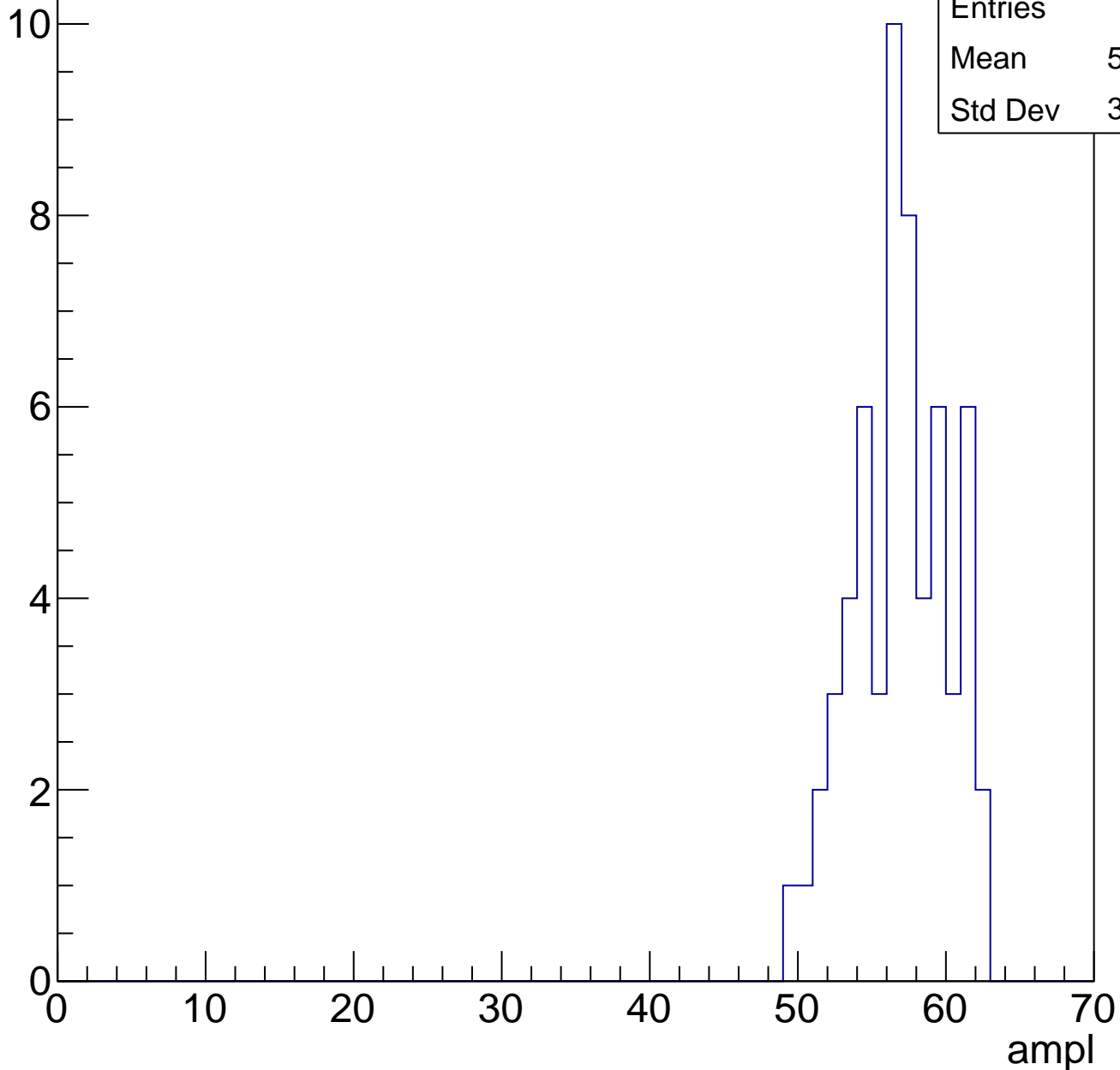


B1L100S, U7-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.44 |
| Std Dev | 3.142 |

Entry



B1L100S, U7-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

6

5

4

3

2

1

0

Entries 33

Mean 58.48

Std Dev 10.53

ampl

0

10

20

30

40

50

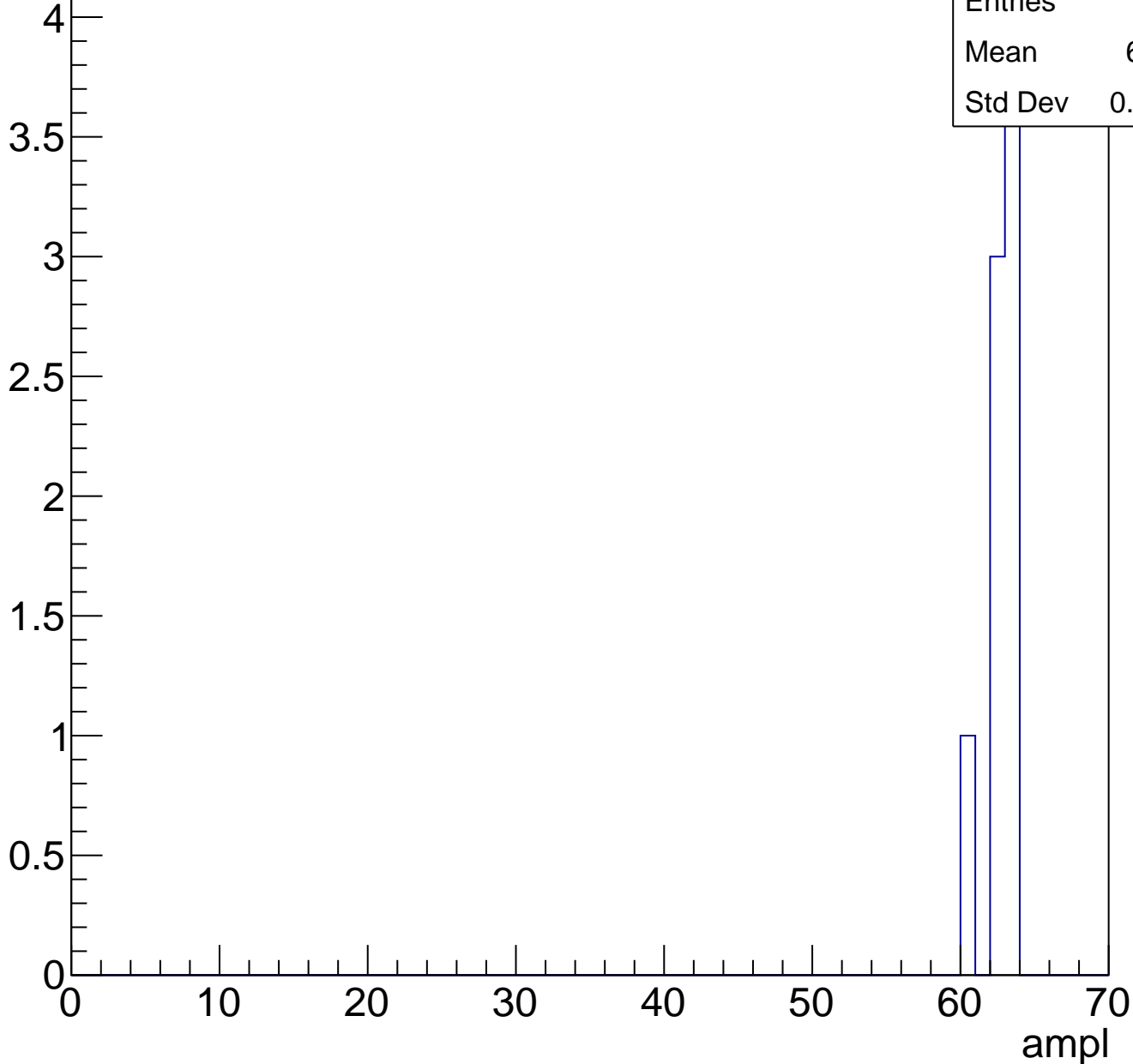
60

70

B1L100S, U7-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch64, adc0

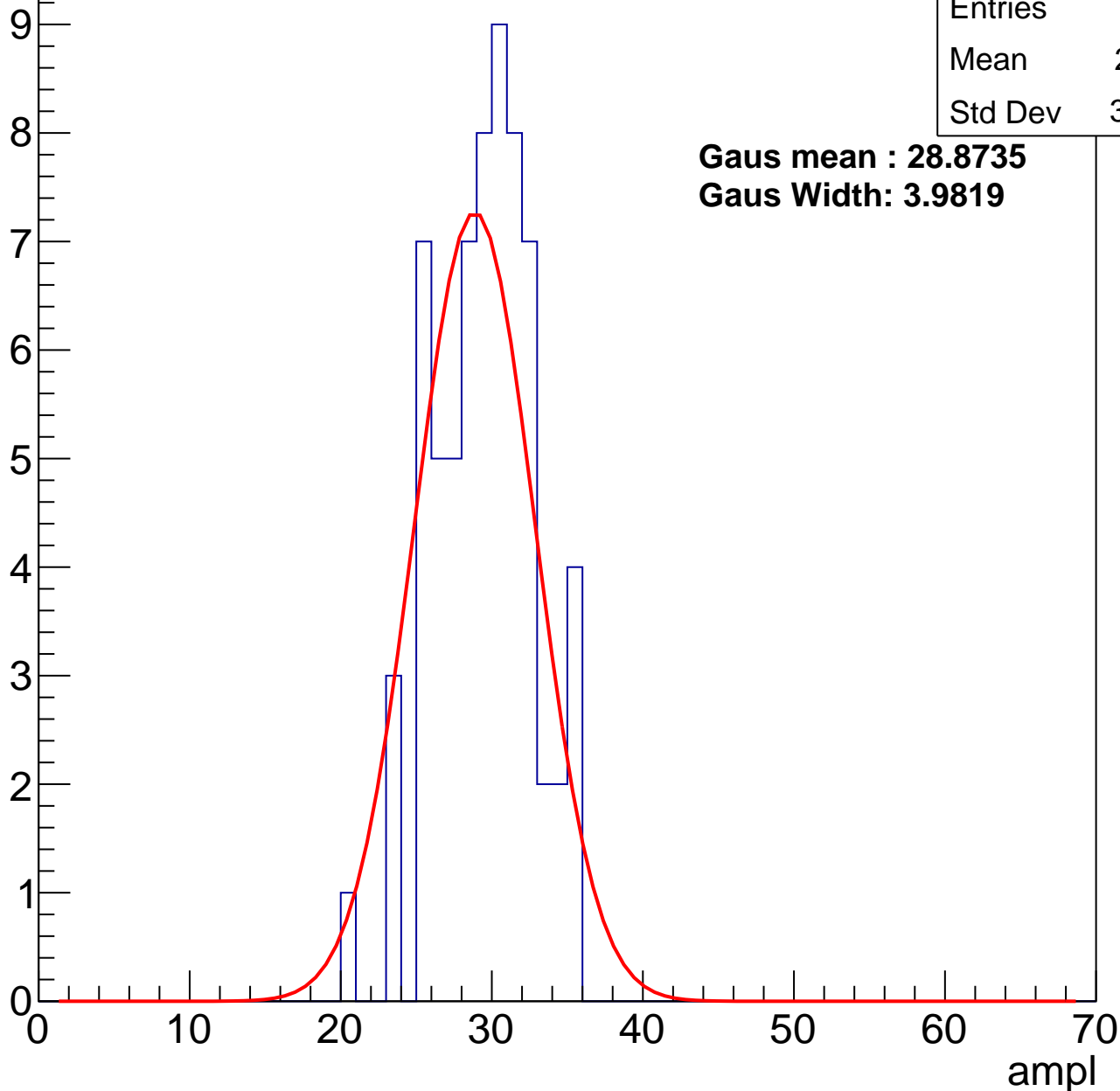
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.01 |
| Std Dev | 3.215 |

Gaus mean : 28.8735

Gaus Width: 3.9819



B1L100S, U7-ch64, adc1

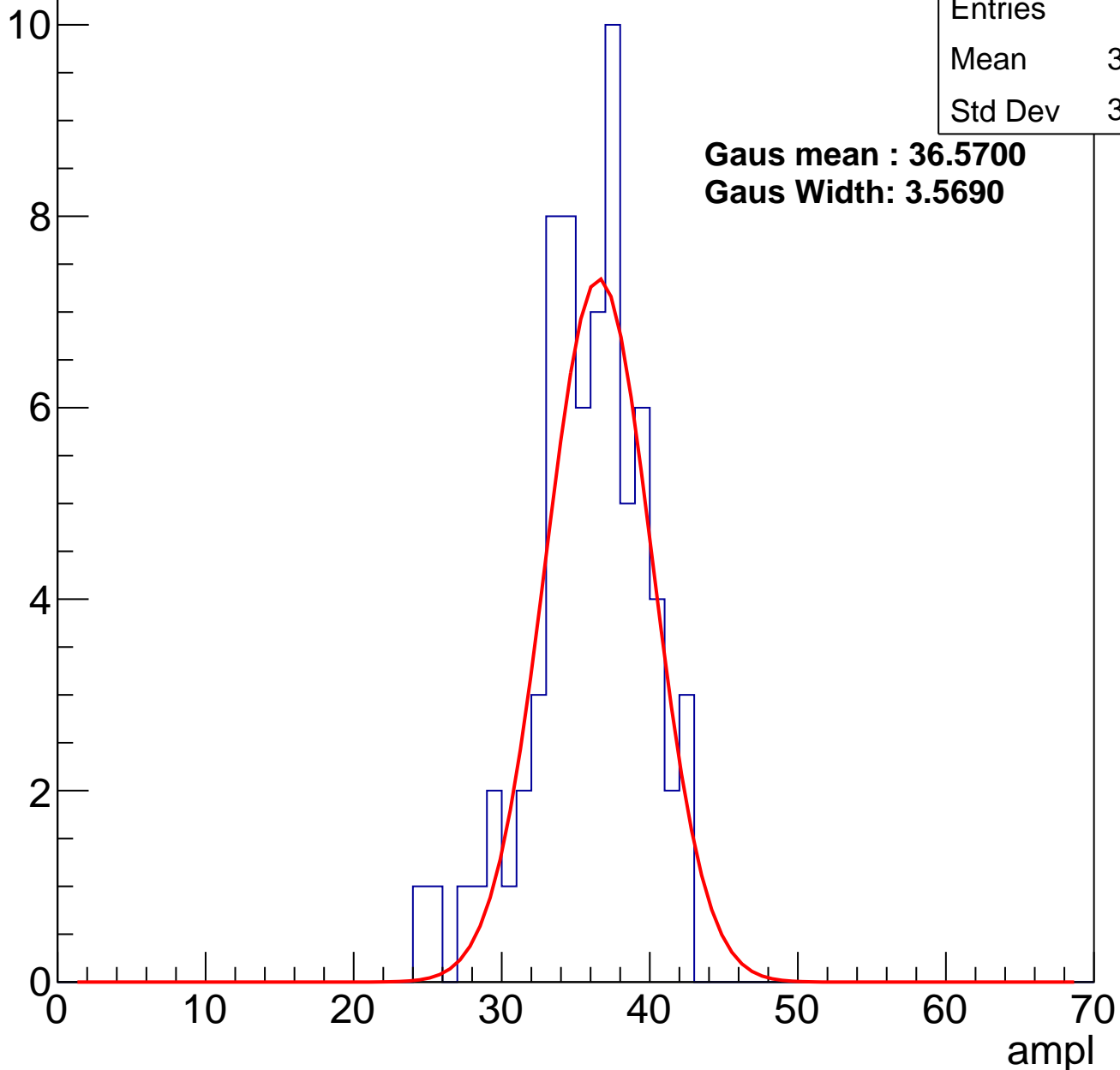
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.35 |
| Std Dev | 3.805 |

Gaus mean : 36.5700

Gaus Width: 3.5690

Entry



B1L100S, U7-ch64, adc2

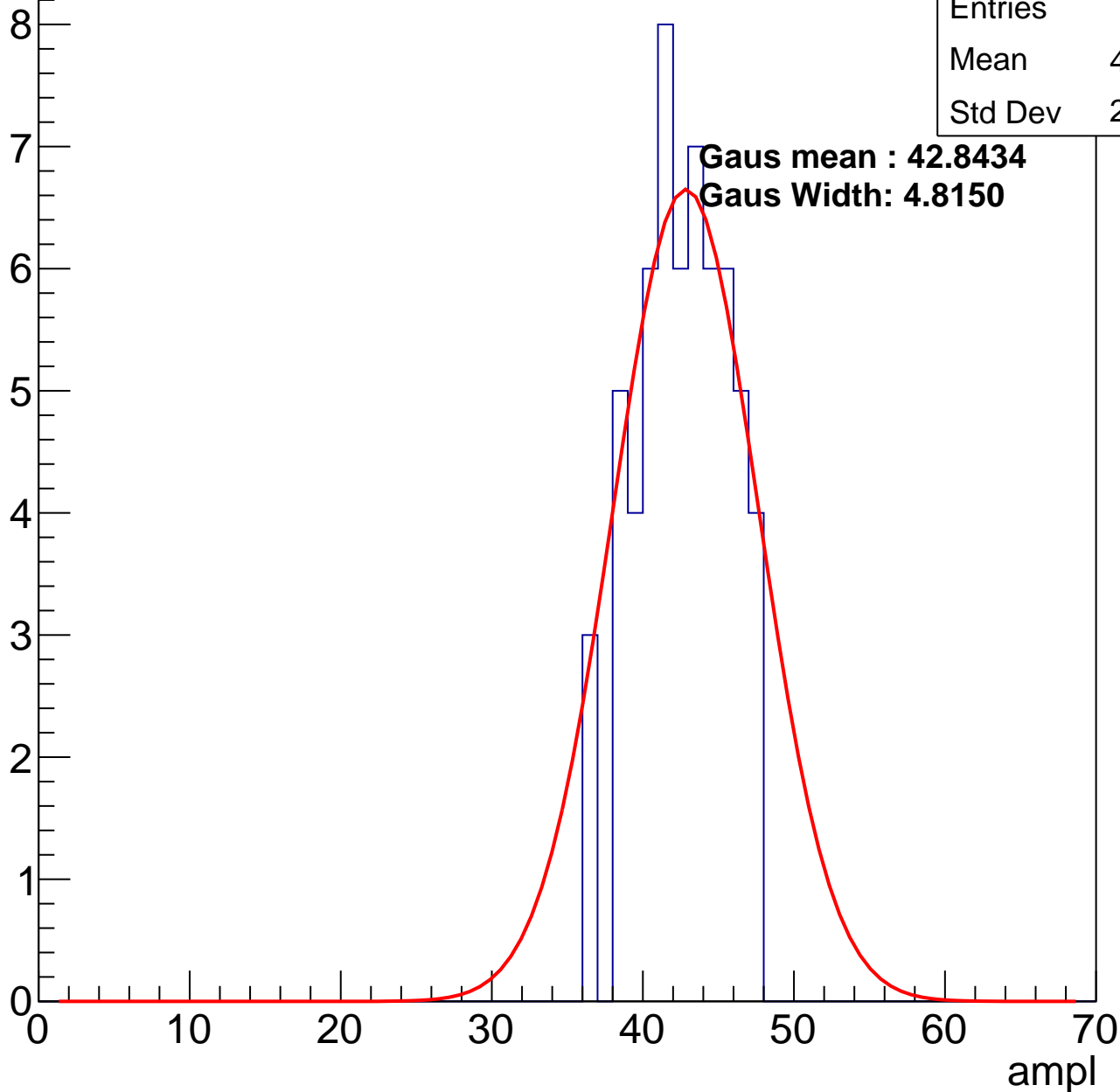
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.12 |
| Std Dev | 2.944 |

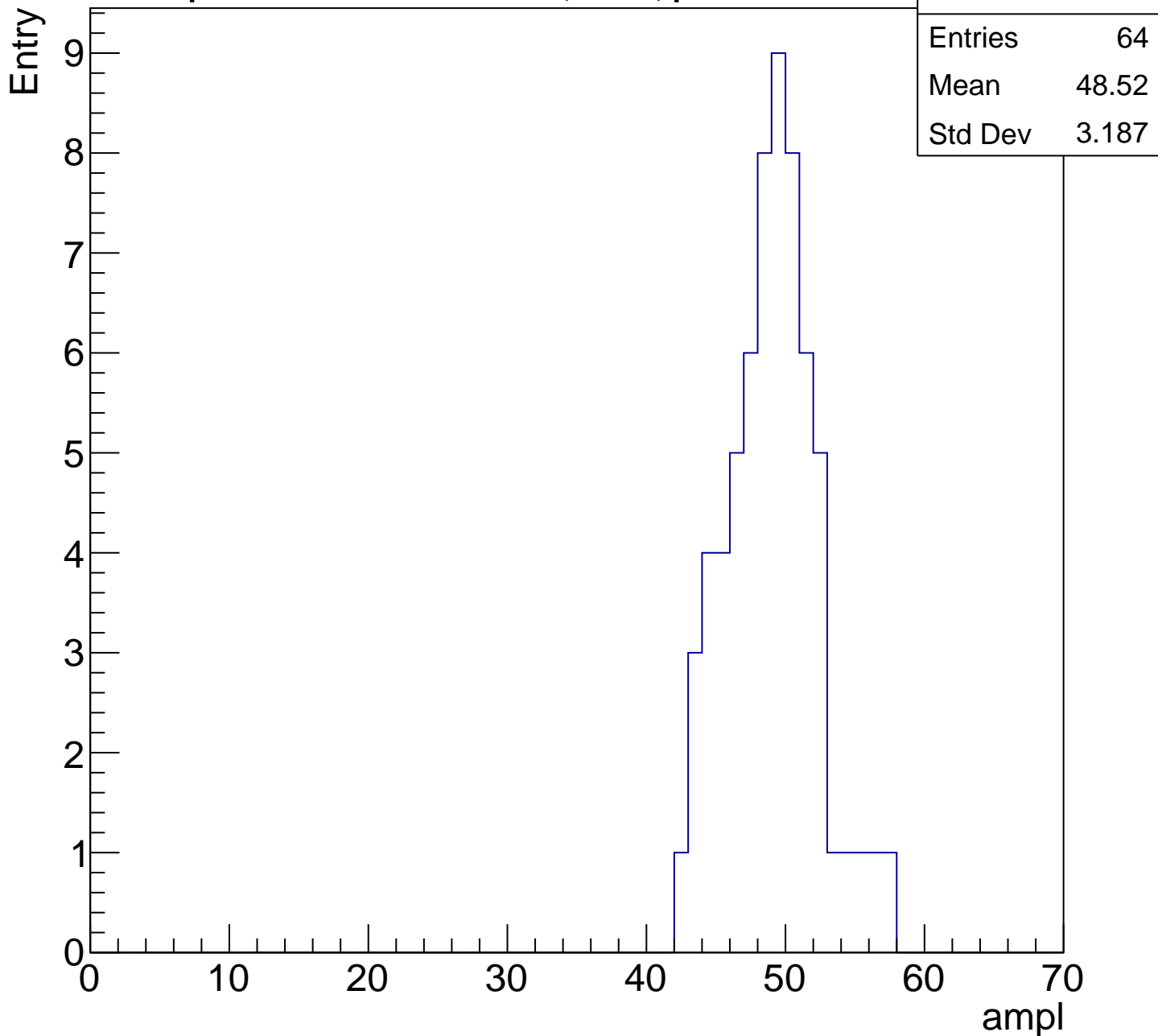
Gaus mean : 42.8434

Gaus Width: 4.8150



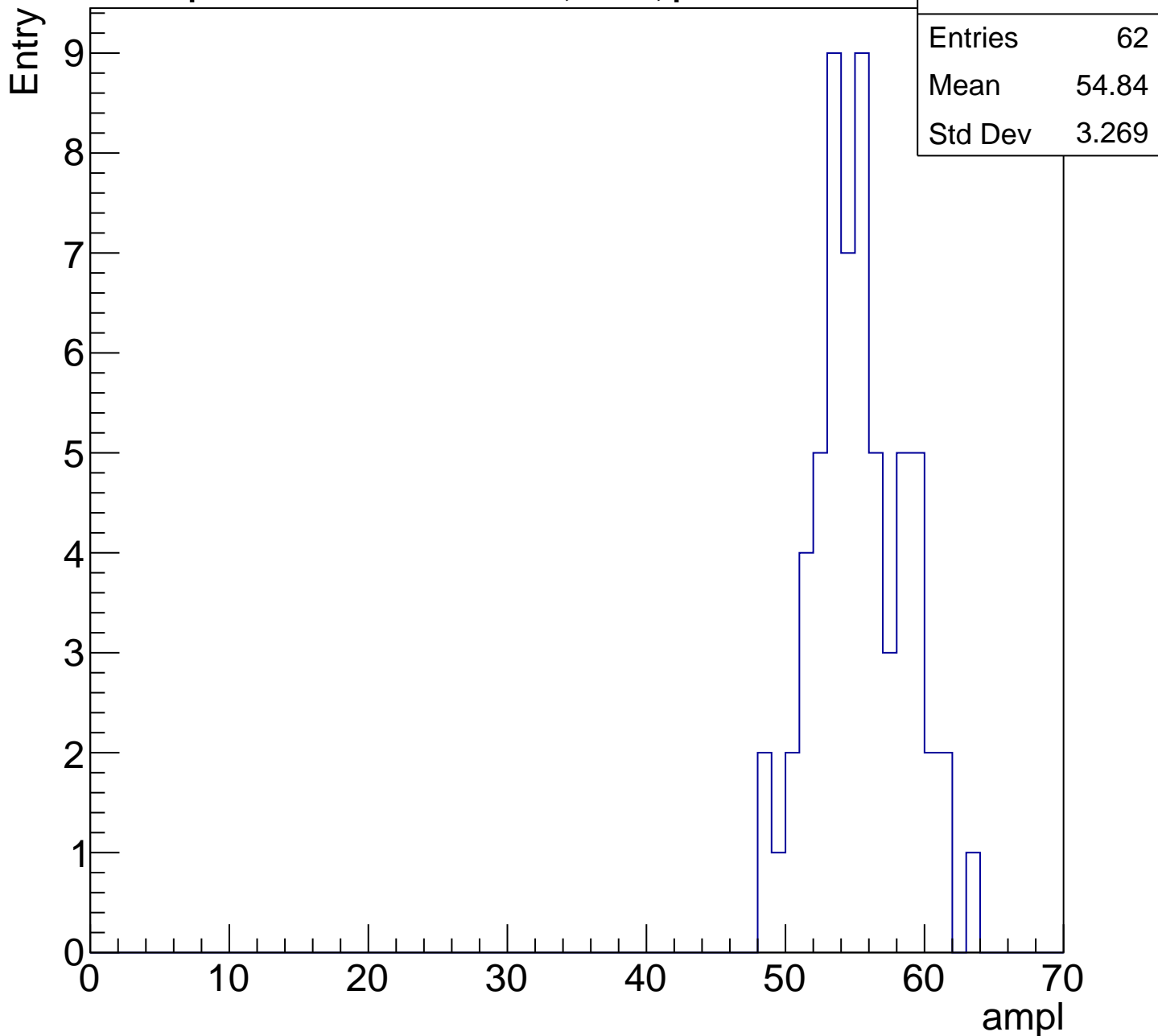
B1L100S, U7-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 45

Mean 58.07

Std Dev 9.091

ampl

0

10

20

30

40

50

60

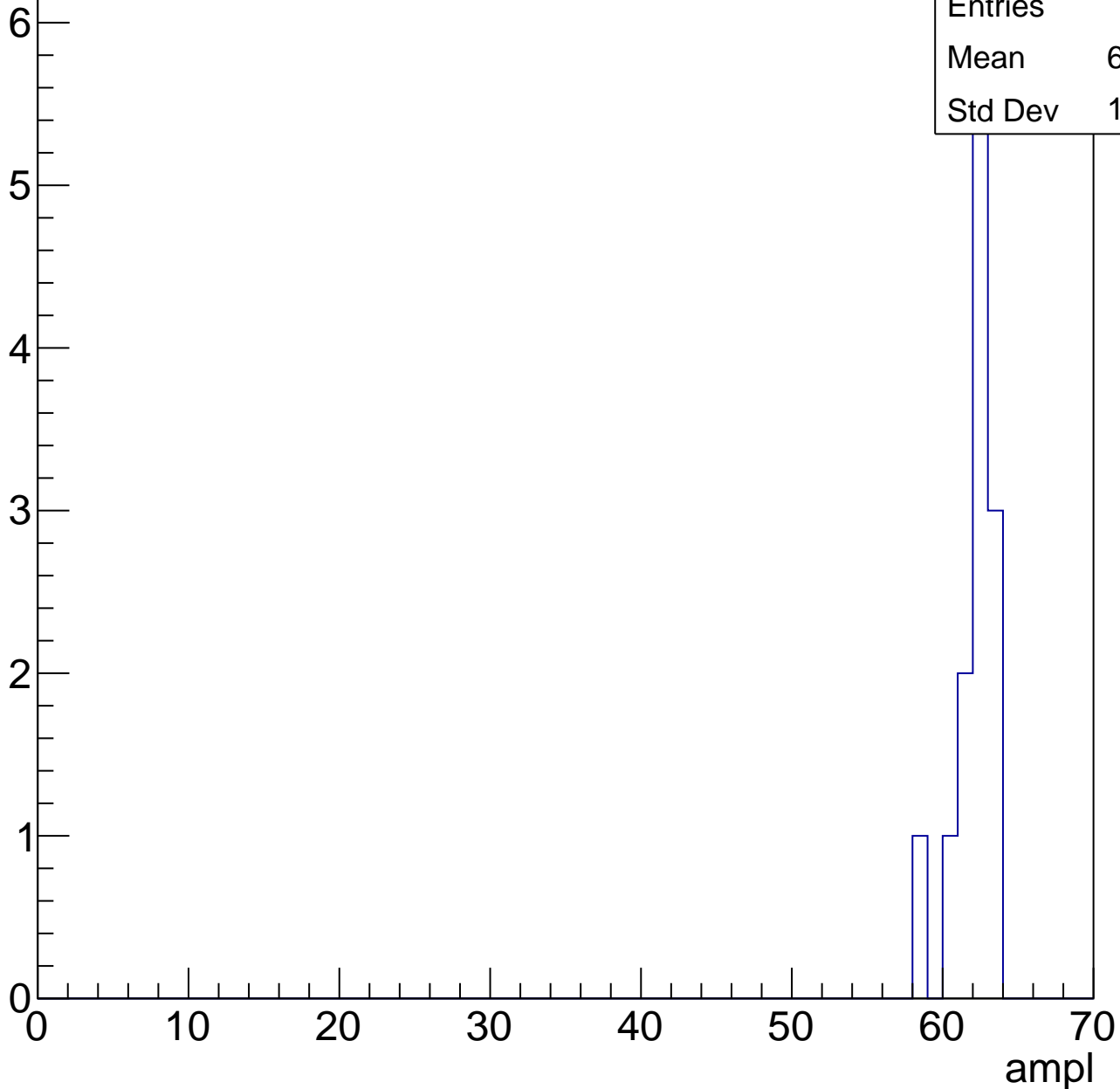
70

B1L100S, U7-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.62 |
| Std Dev | 1.332 |



B1L100S, U7-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch65, adc0

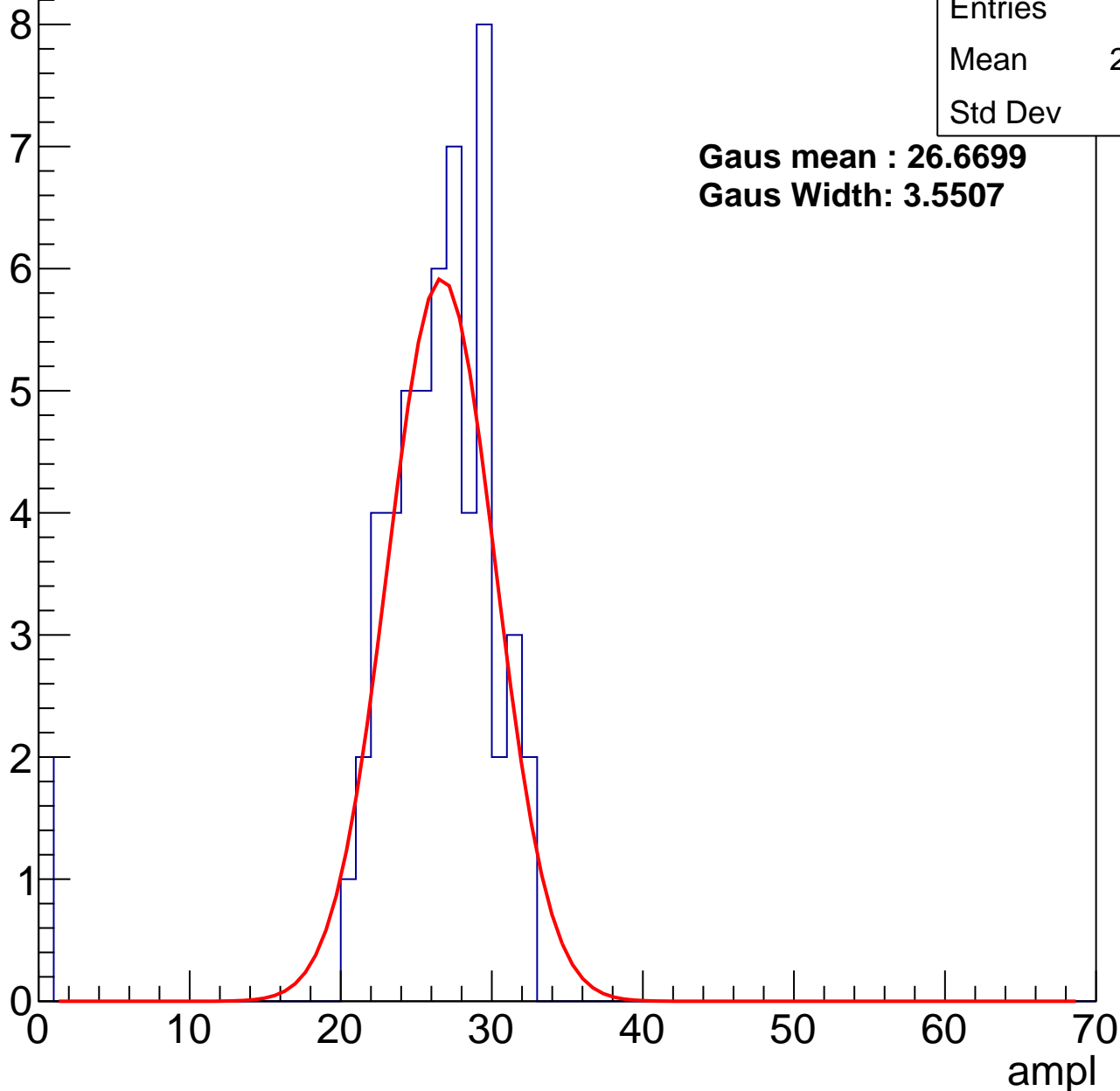
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 25.33 |
| Std Dev | 5.74 |

Gaus mean : 26.6699

Gaus Width: 3.5507



B1L100S, U7-ch65, adc1

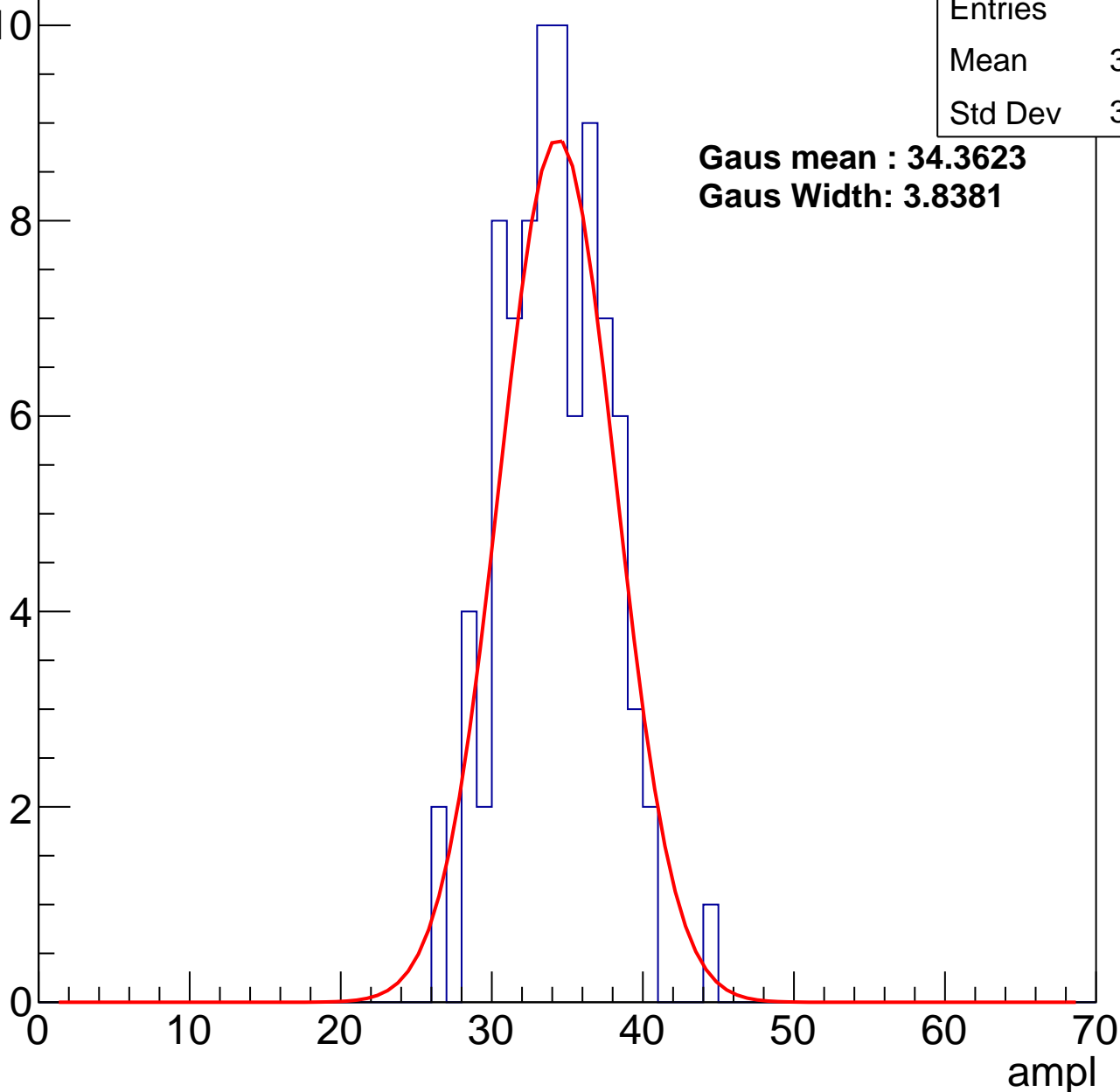
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 33.73 |
| Std Dev | 3.424 |

Gaus mean : 34.3623

Gaus Width: 3.8381



B1L100S, U7-ch65, adc2

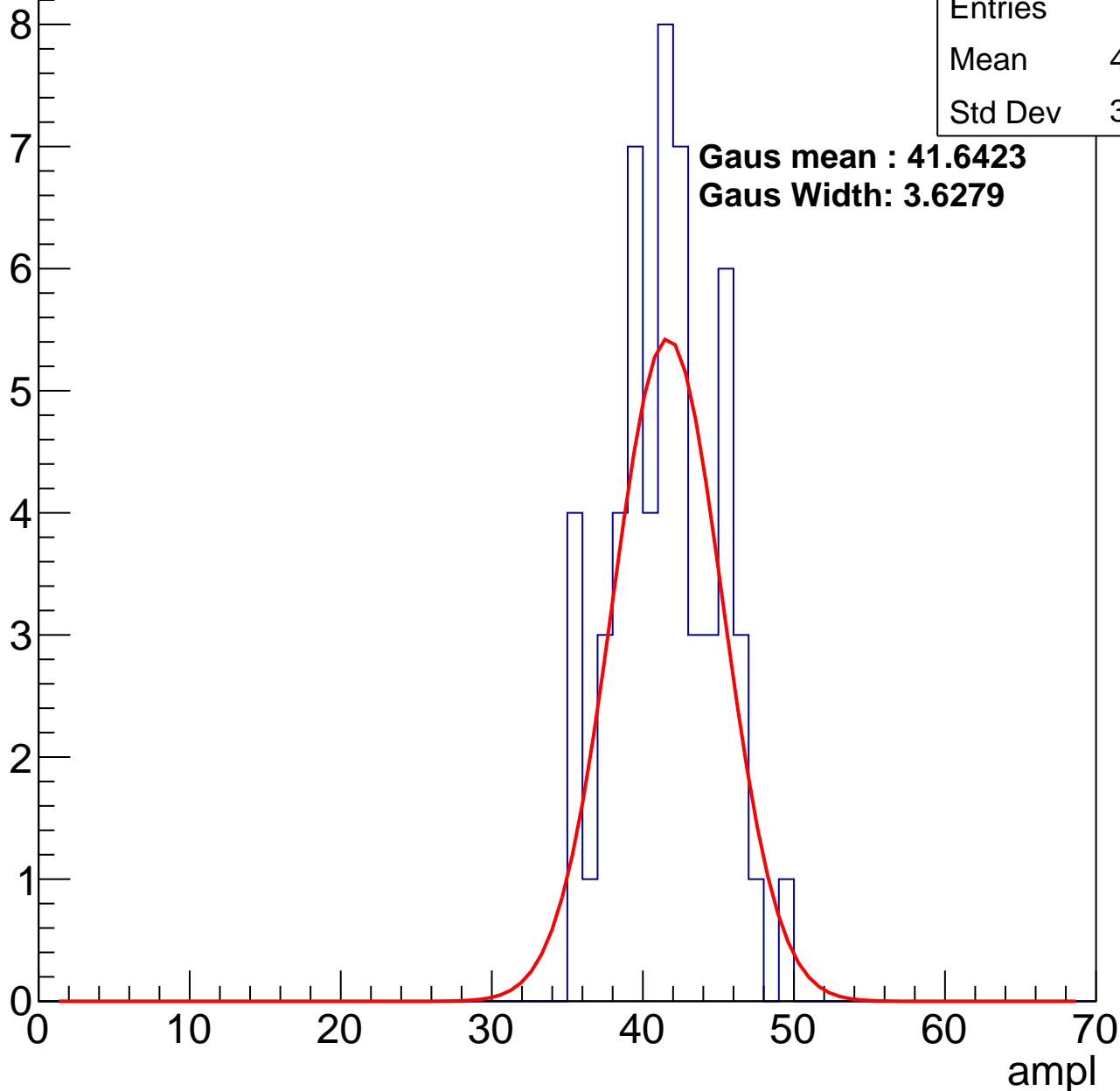
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 41.07 |
| Std Dev | 3.308 |

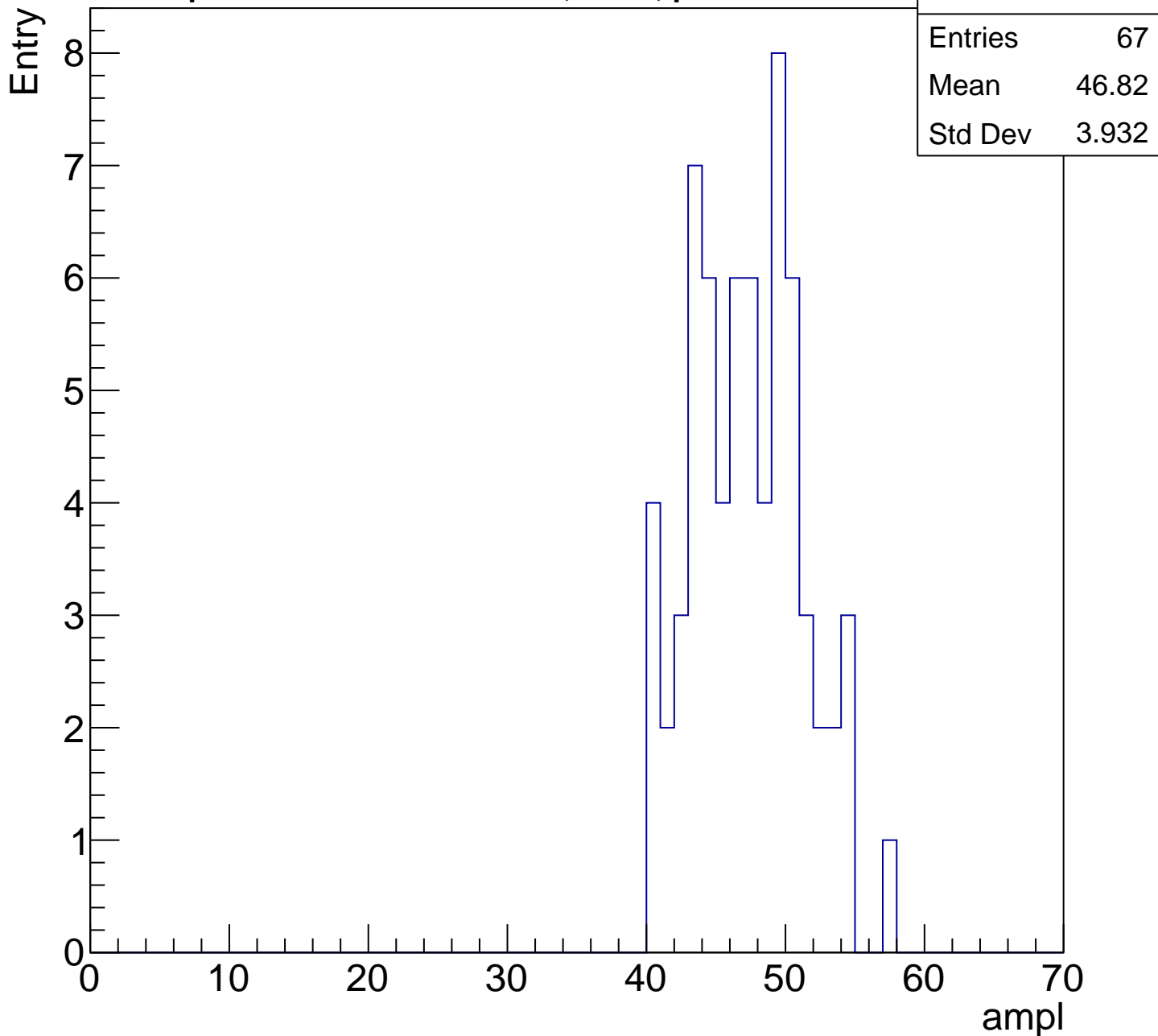
Gaus mean : 41.6423

Gaus Width: 3.6279



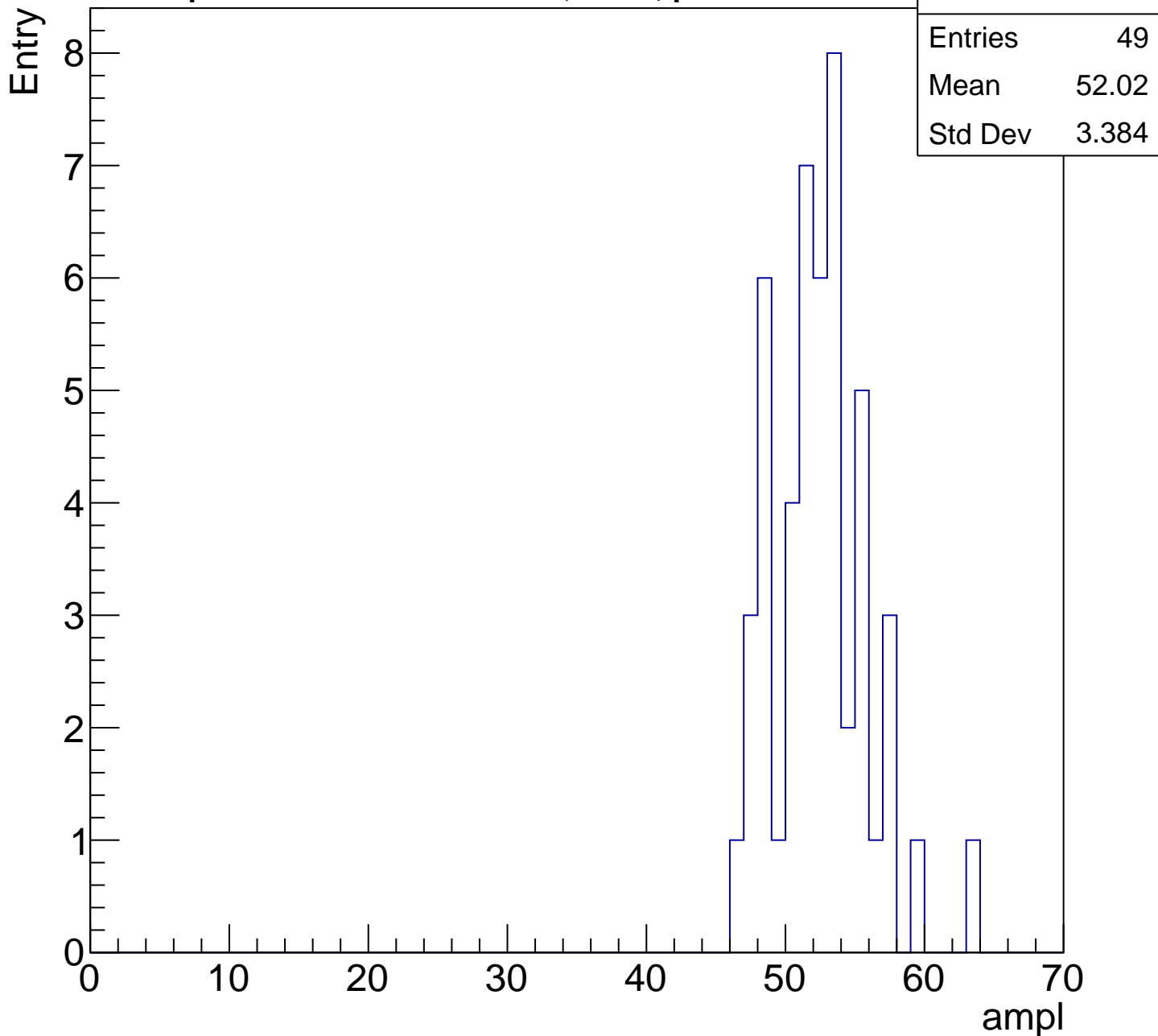
B1L100S, U7-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

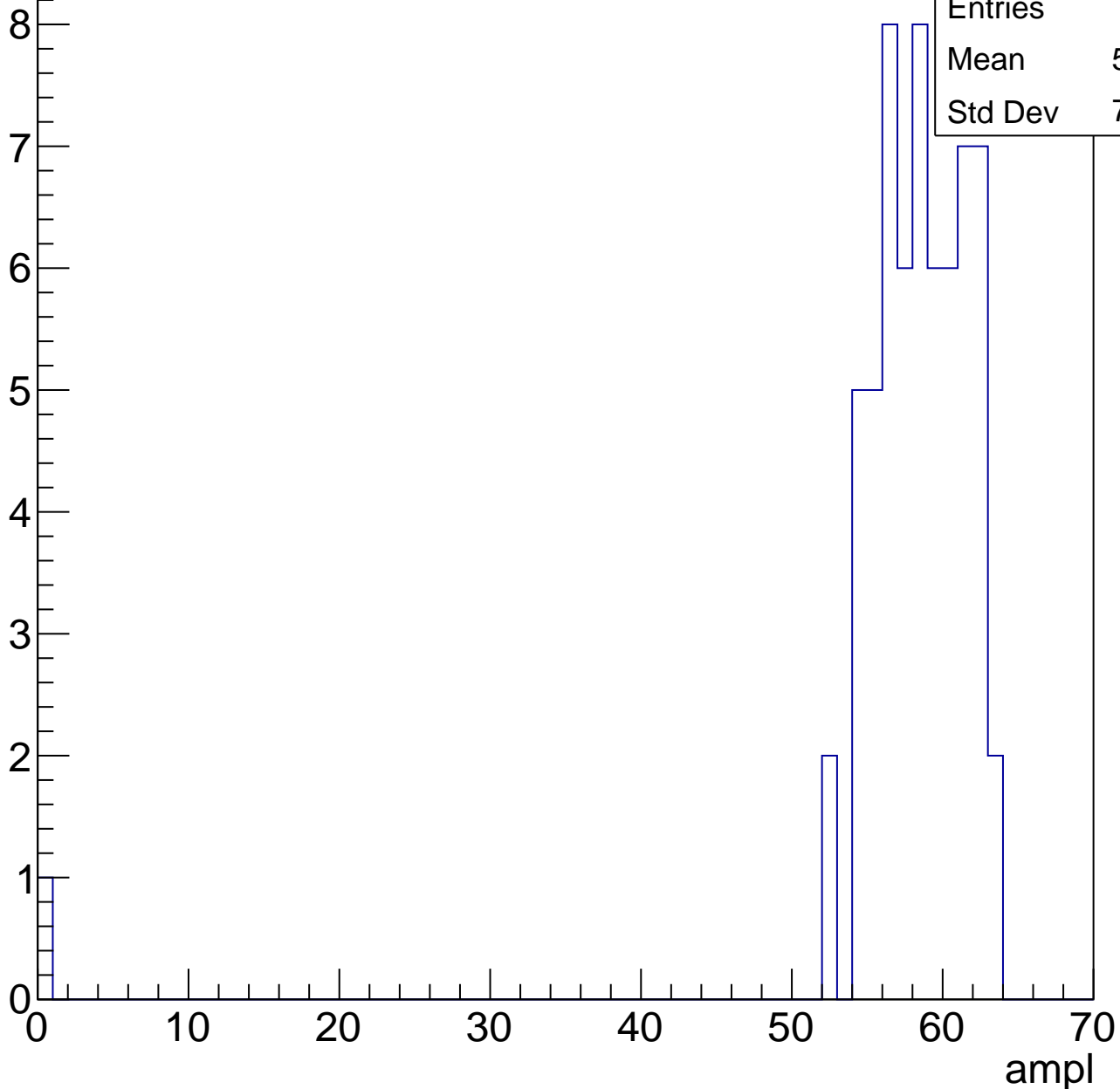


B1L100S, U7-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.21 |
| Std Dev | 7.781 |

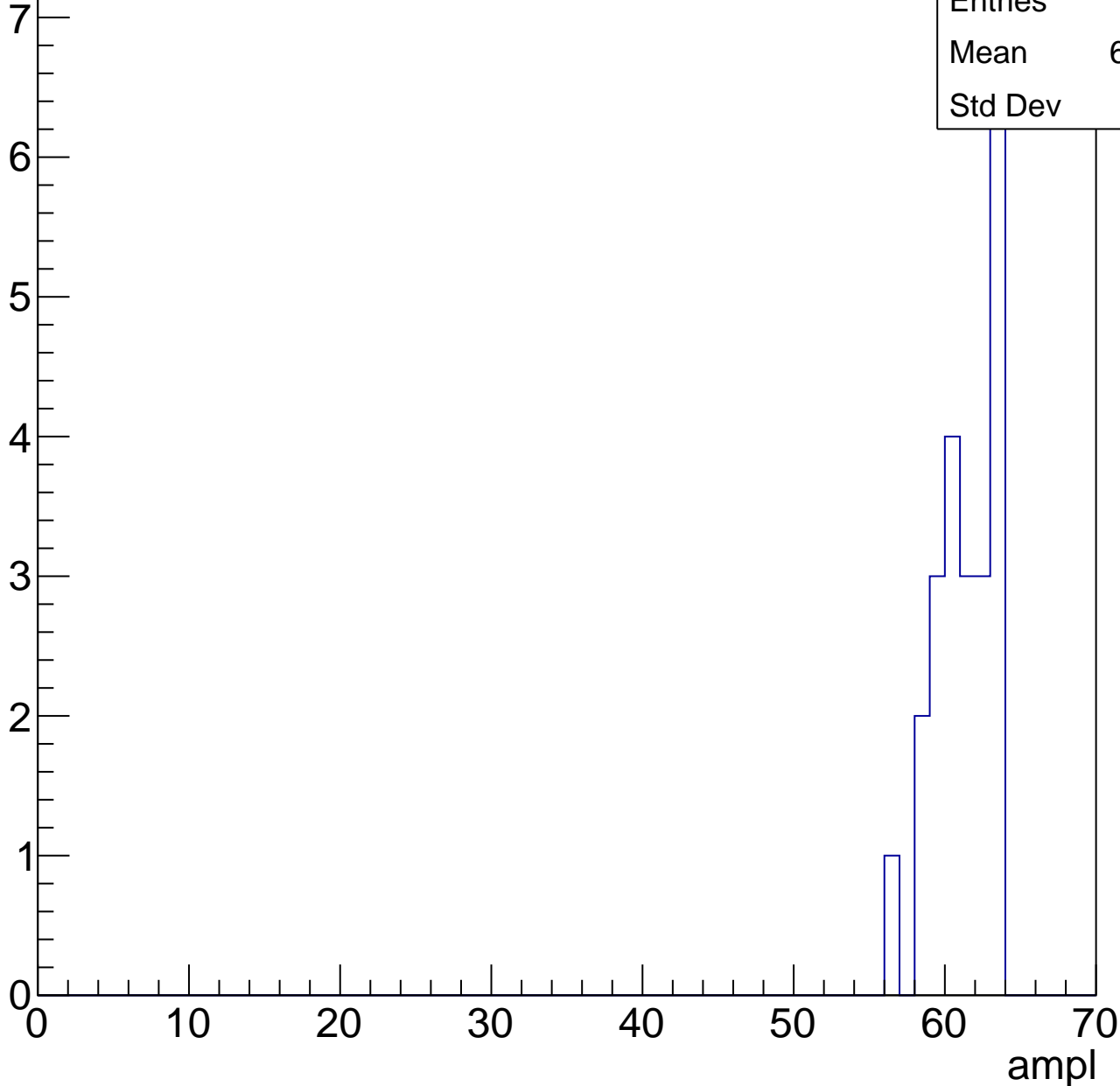


B1L100S, U7-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 23 |
| Mean | 60.83 |
| Std Dev | 1.97 |



B1L100S, U7-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch66, adc0

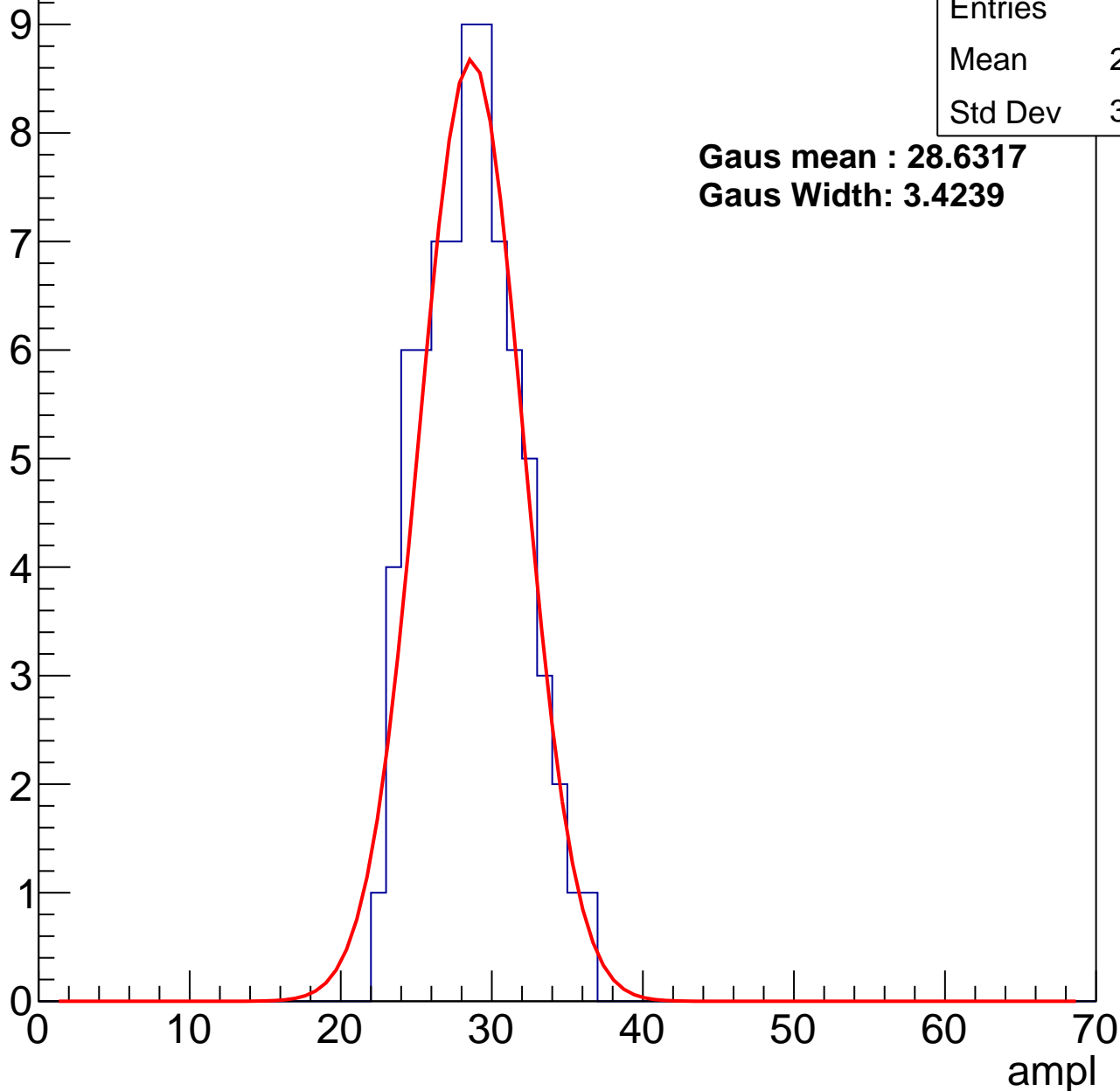
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.19 |
| Std Dev | 3.182 |

Gaus mean : 28.6317

Gaus Width: 3.4239



B1L100S, U7-ch66, adc1

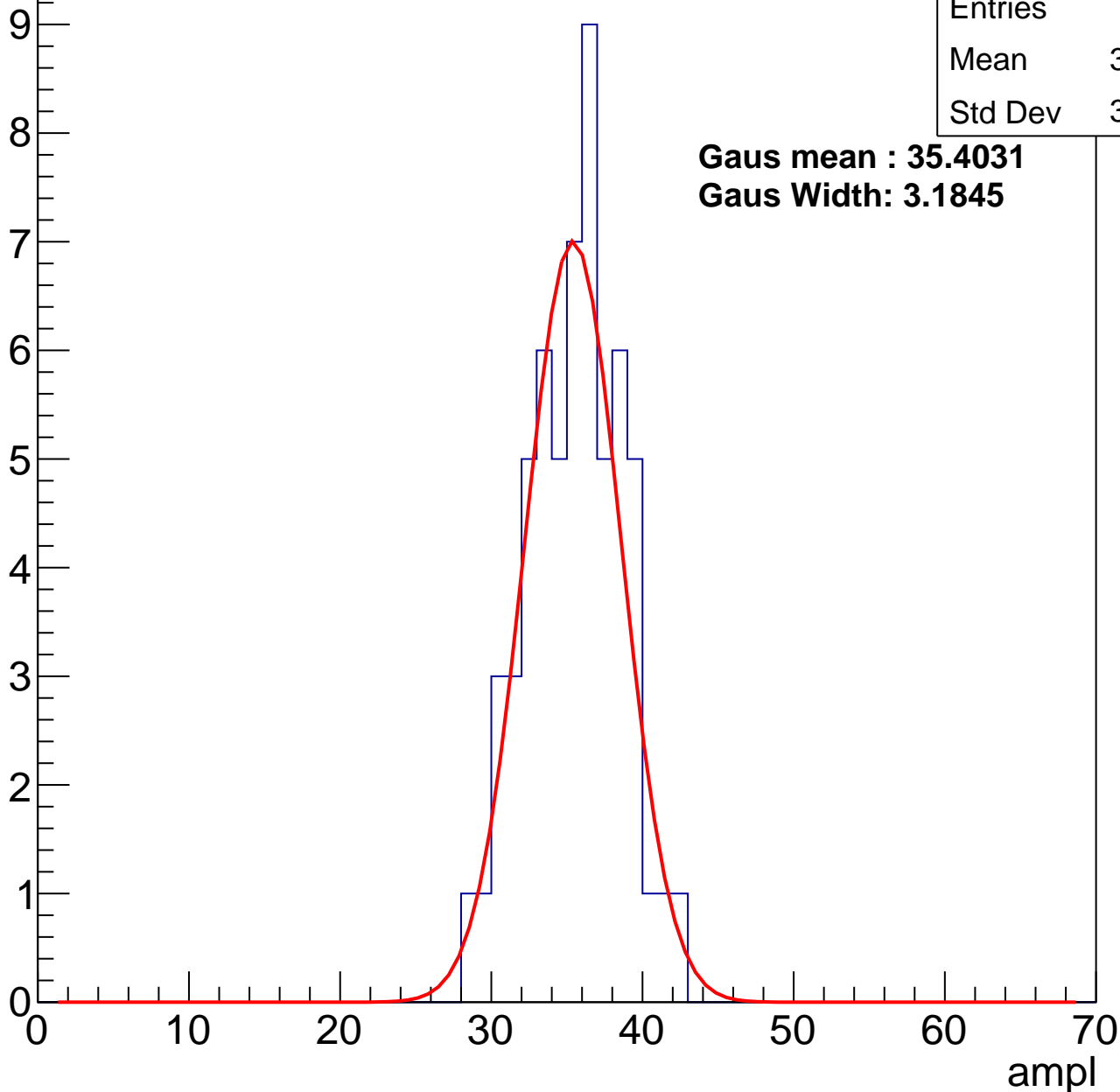
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.05 |
| Std Dev | 3.067 |

Gaus mean : 35.4031

Gaus Width: 3.1845



B1L100S, U7-ch66, adc2

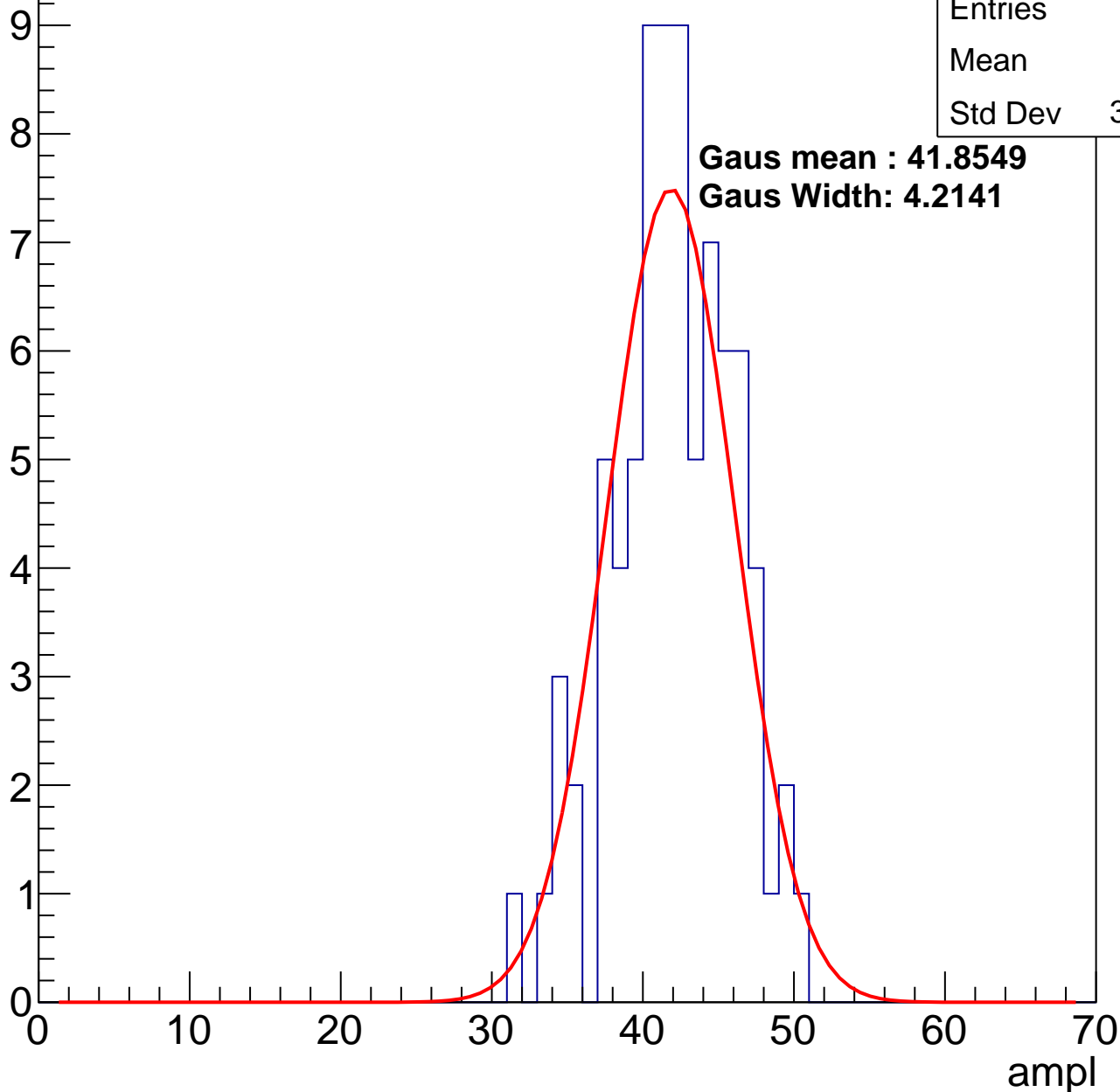
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 41.6 |
| Std Dev | 3.936 |

Gaus mean : 41.8549

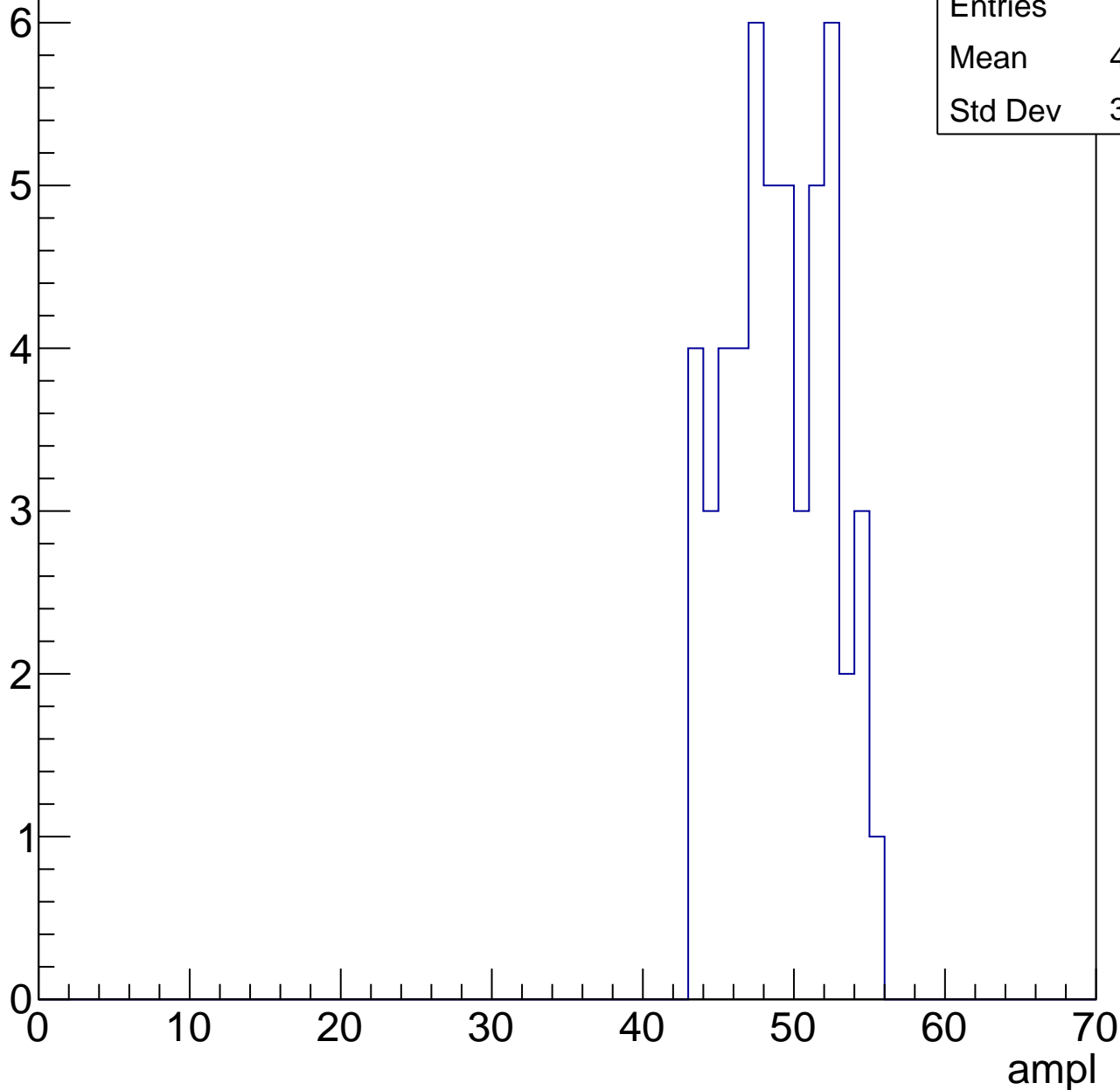
Gaus Width: 4.2141



B1L100S, U7-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

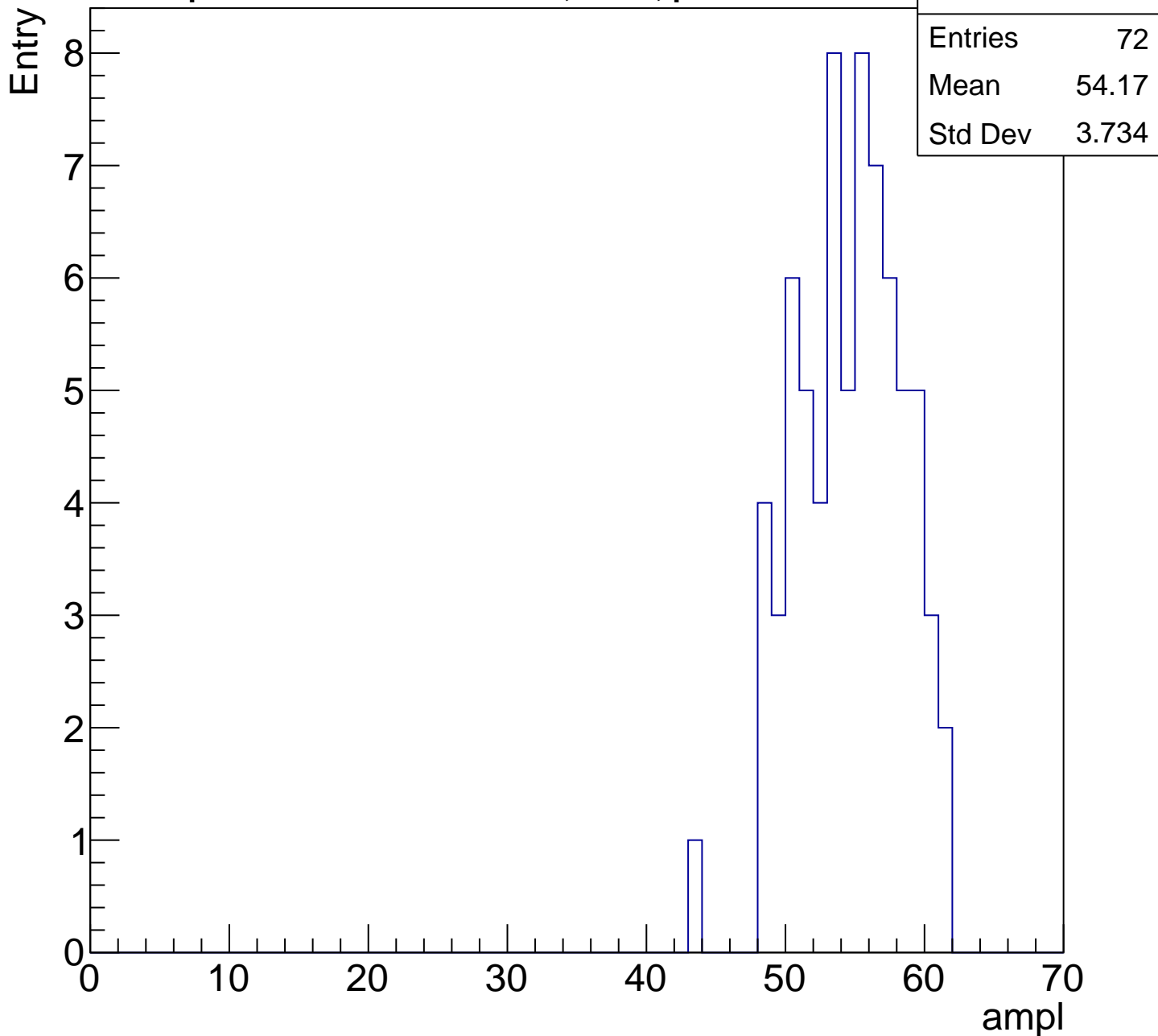
Entry



| | |
|---------|-------|
| Entries | 51 |
| Mean | 48.53 |
| Std Dev | 3.304 |

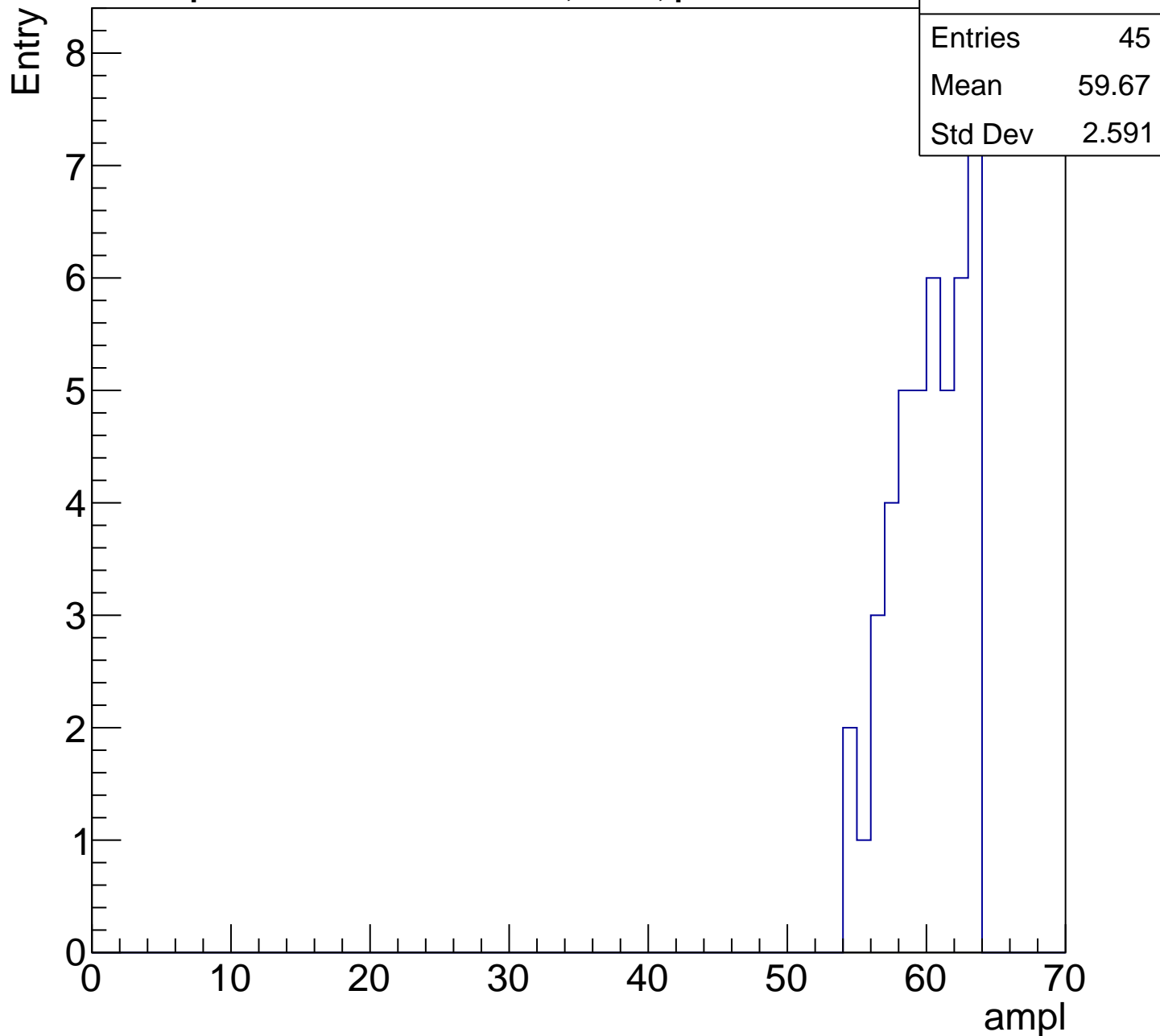
B1L100S, U7-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch66, adc5

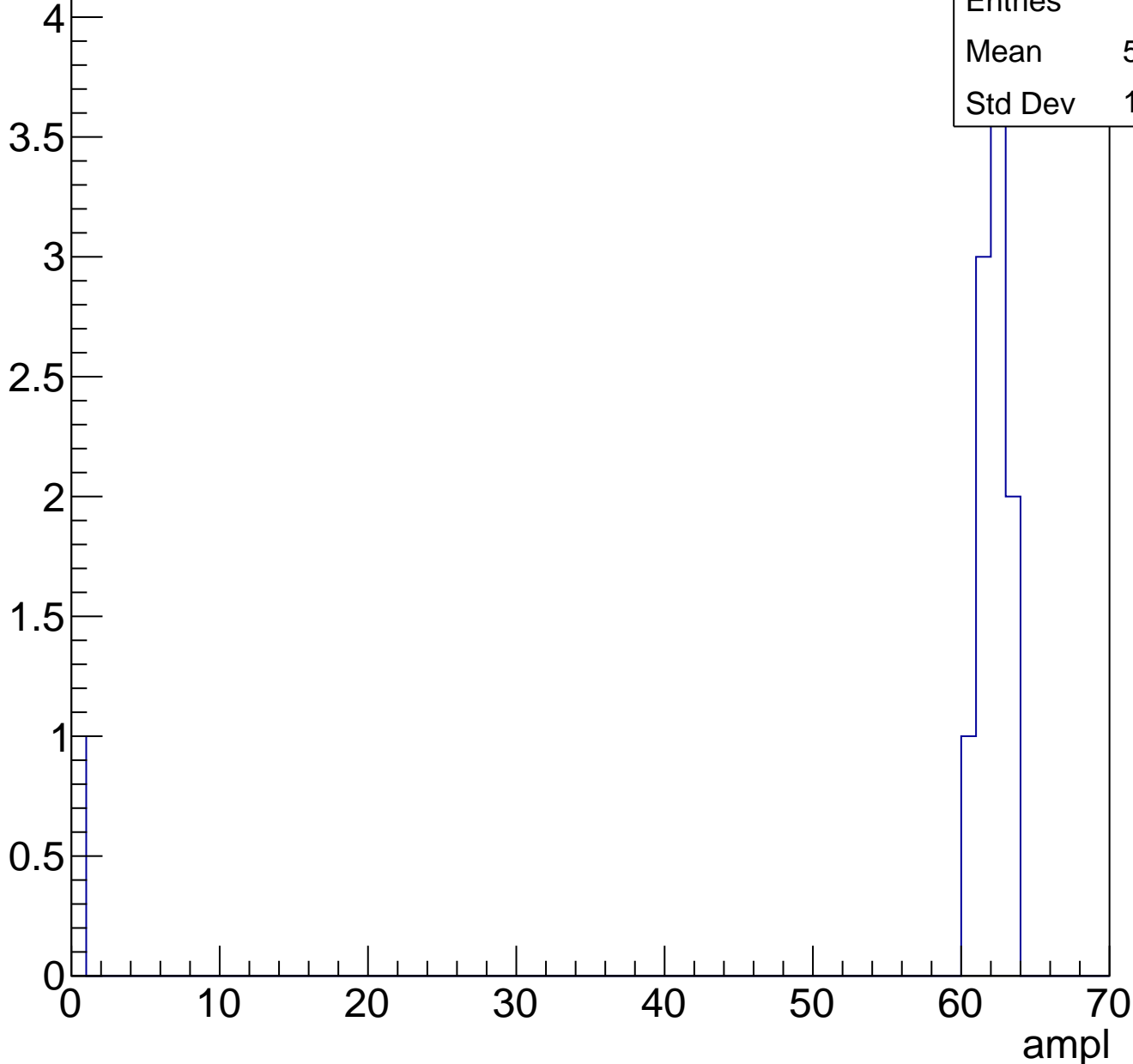
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch67, adc0

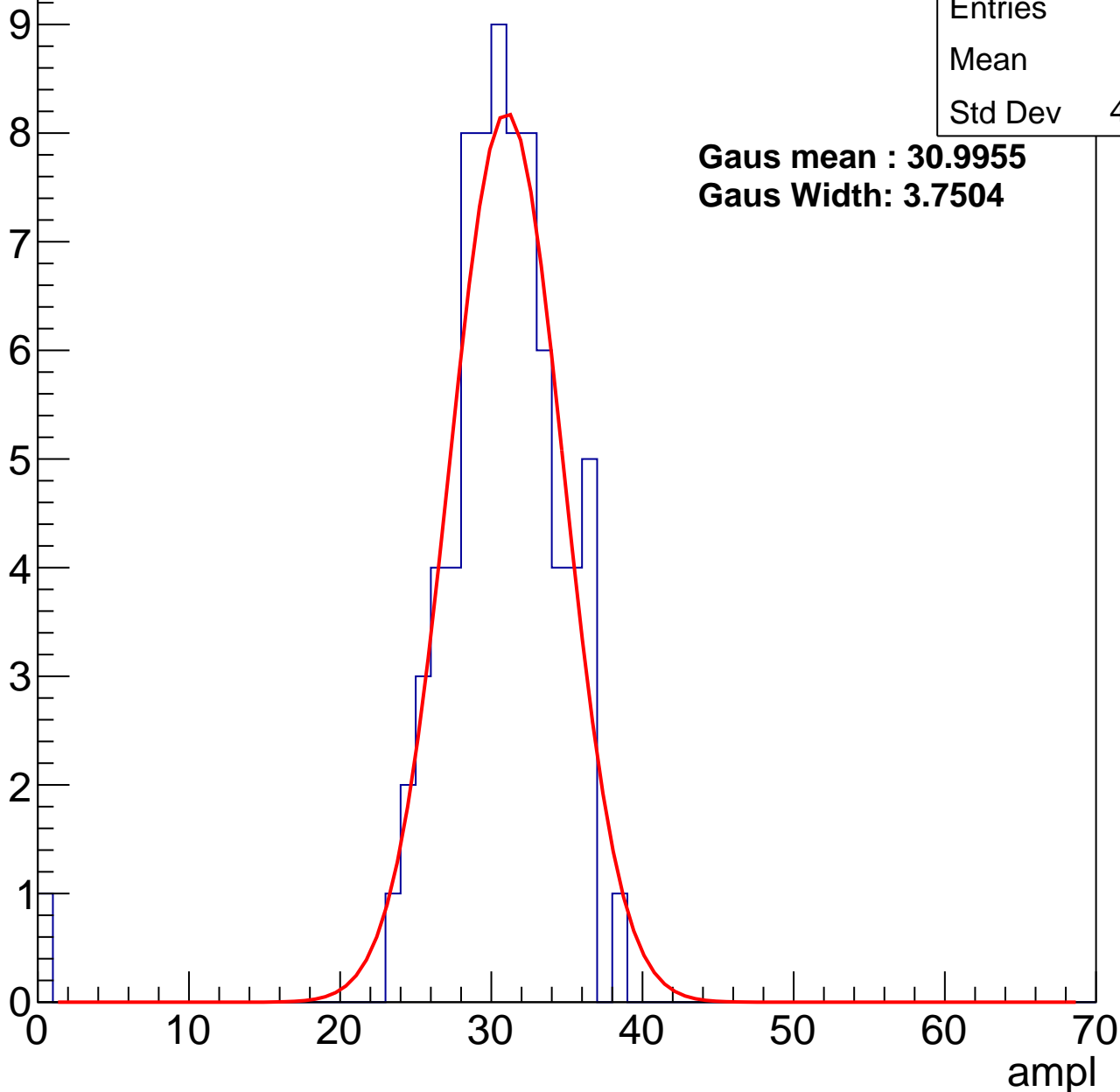
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30 |
| Std Dev | 4.782 |

Gaus mean : 30.9955

Gaus Width: 3.7504



B1L100S, U7-ch67, adc1

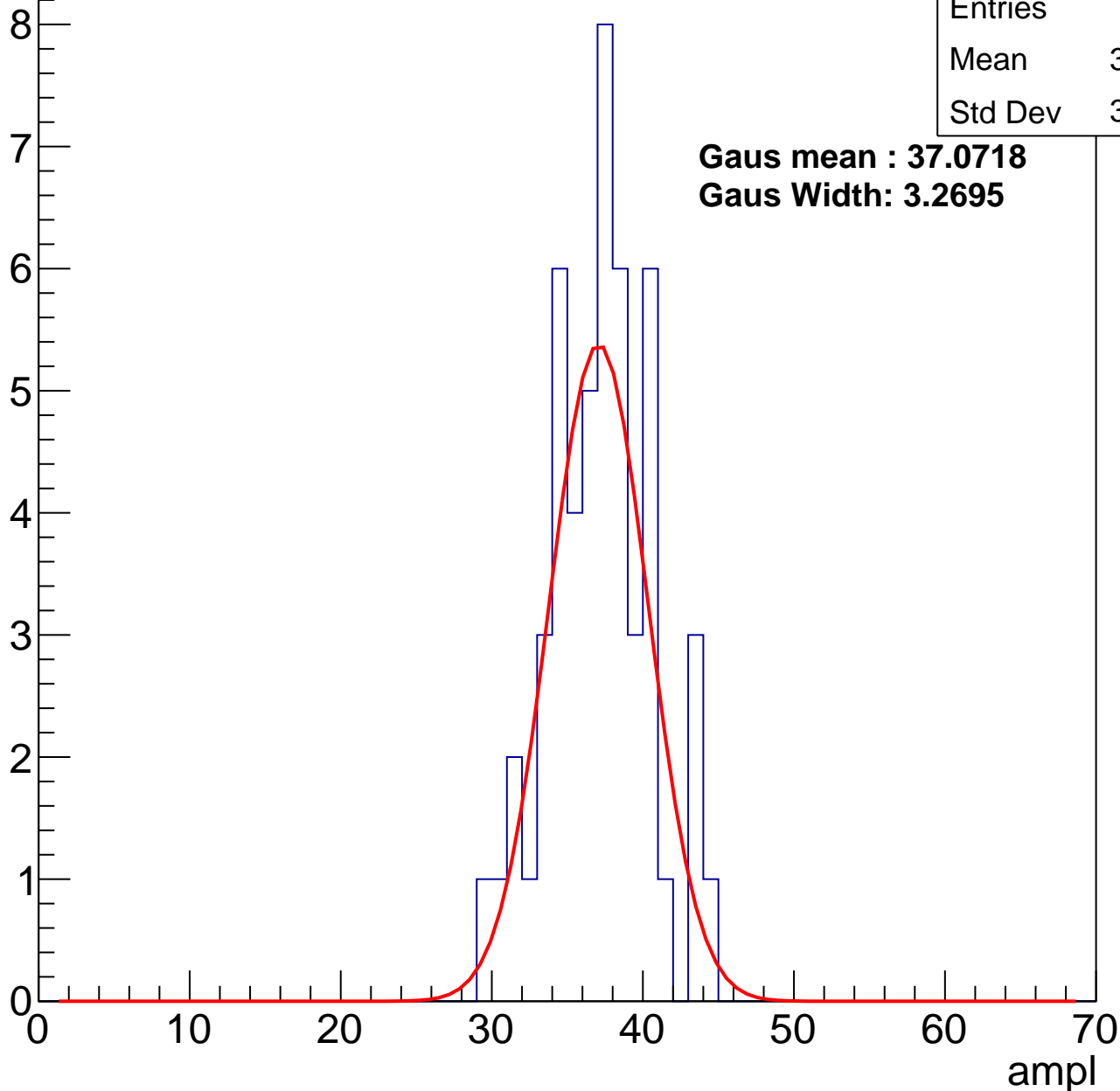
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 36.69 |
| Std Dev | 3.352 |

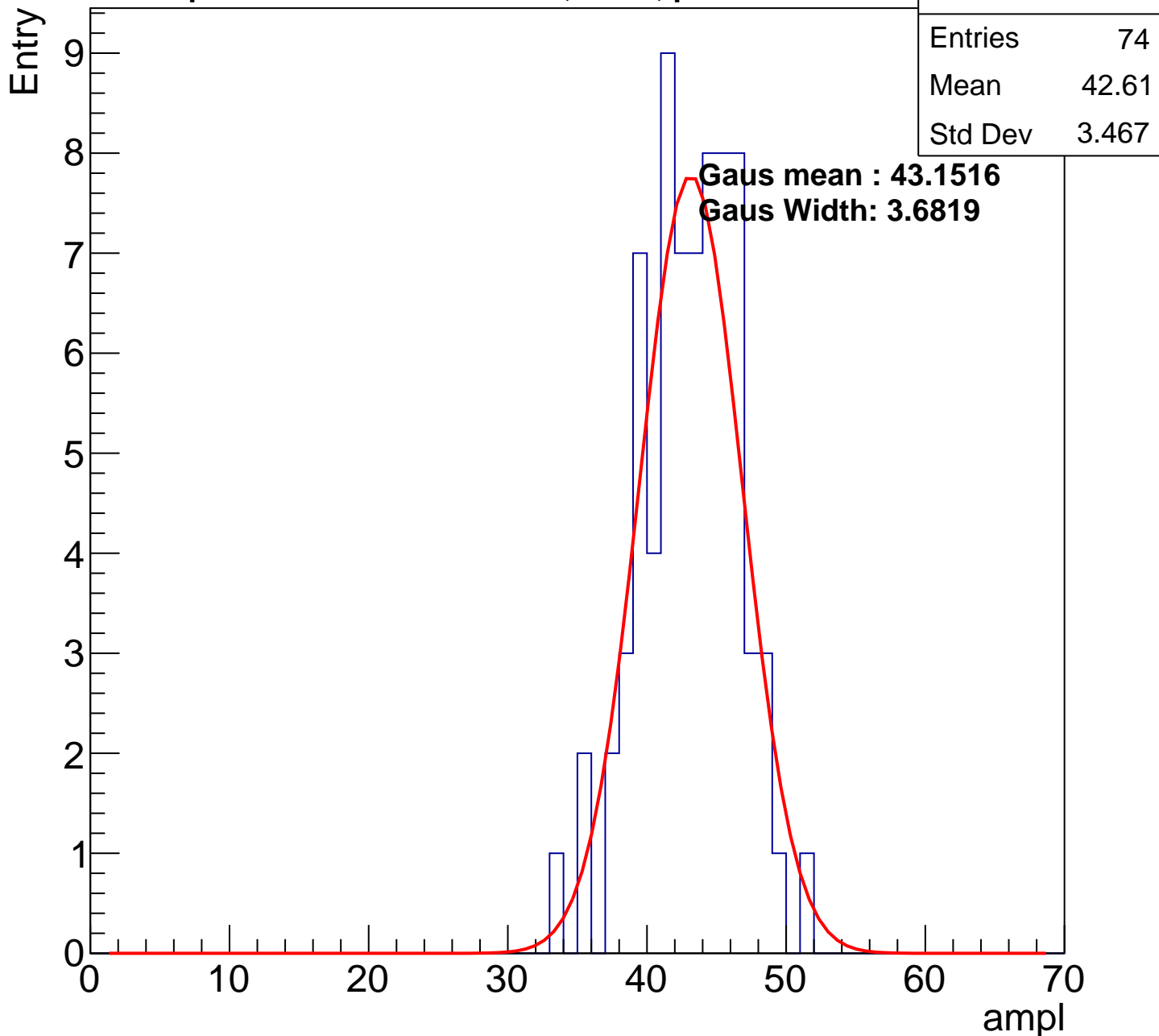
Gaus mean : 37.0718

Gaus Width: 3.2695



B1L100S, U7-ch67, adc2

calib_packv5_042523_0143.root, FC#4, port A2

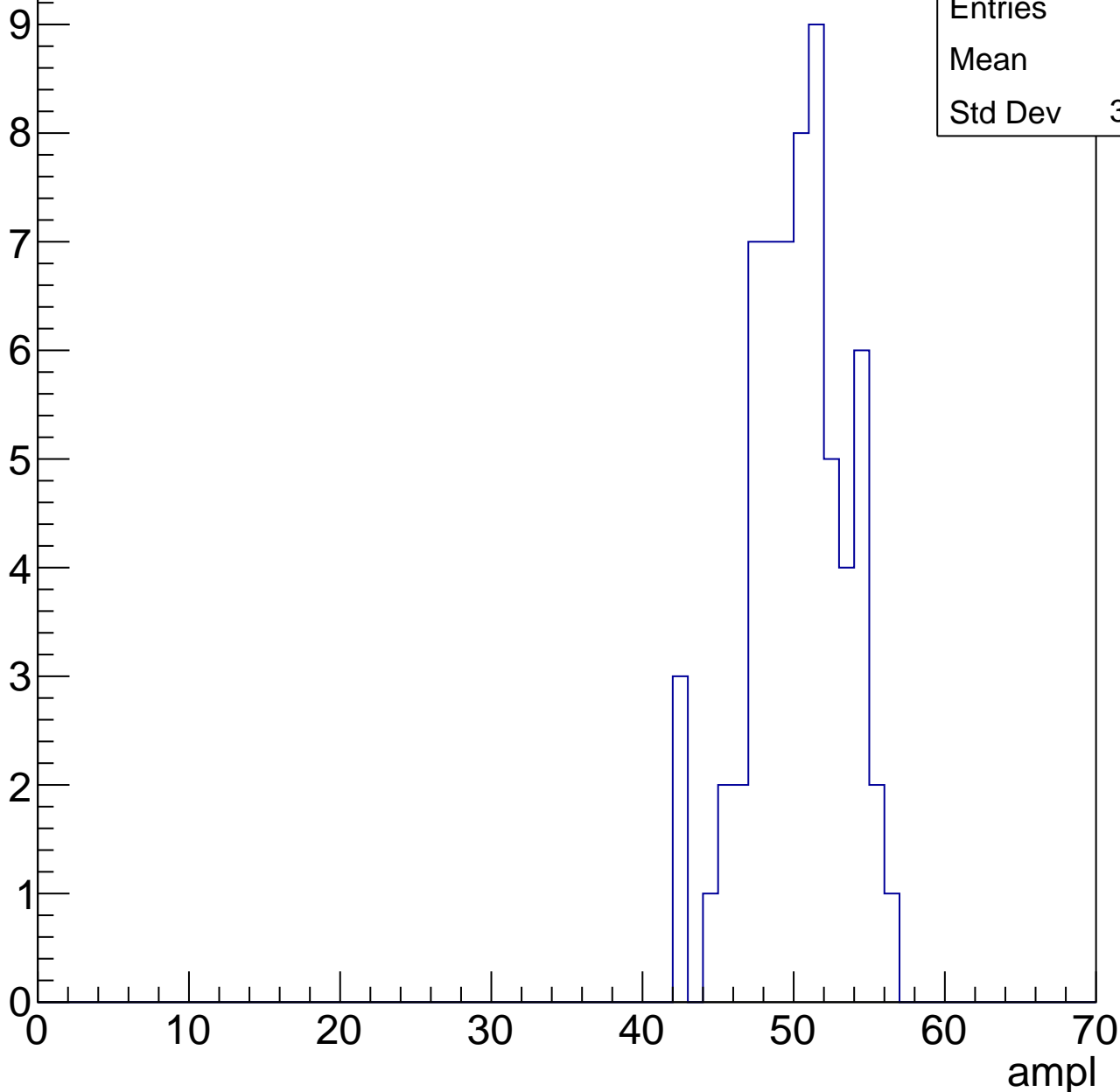


B1L100S, U7-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

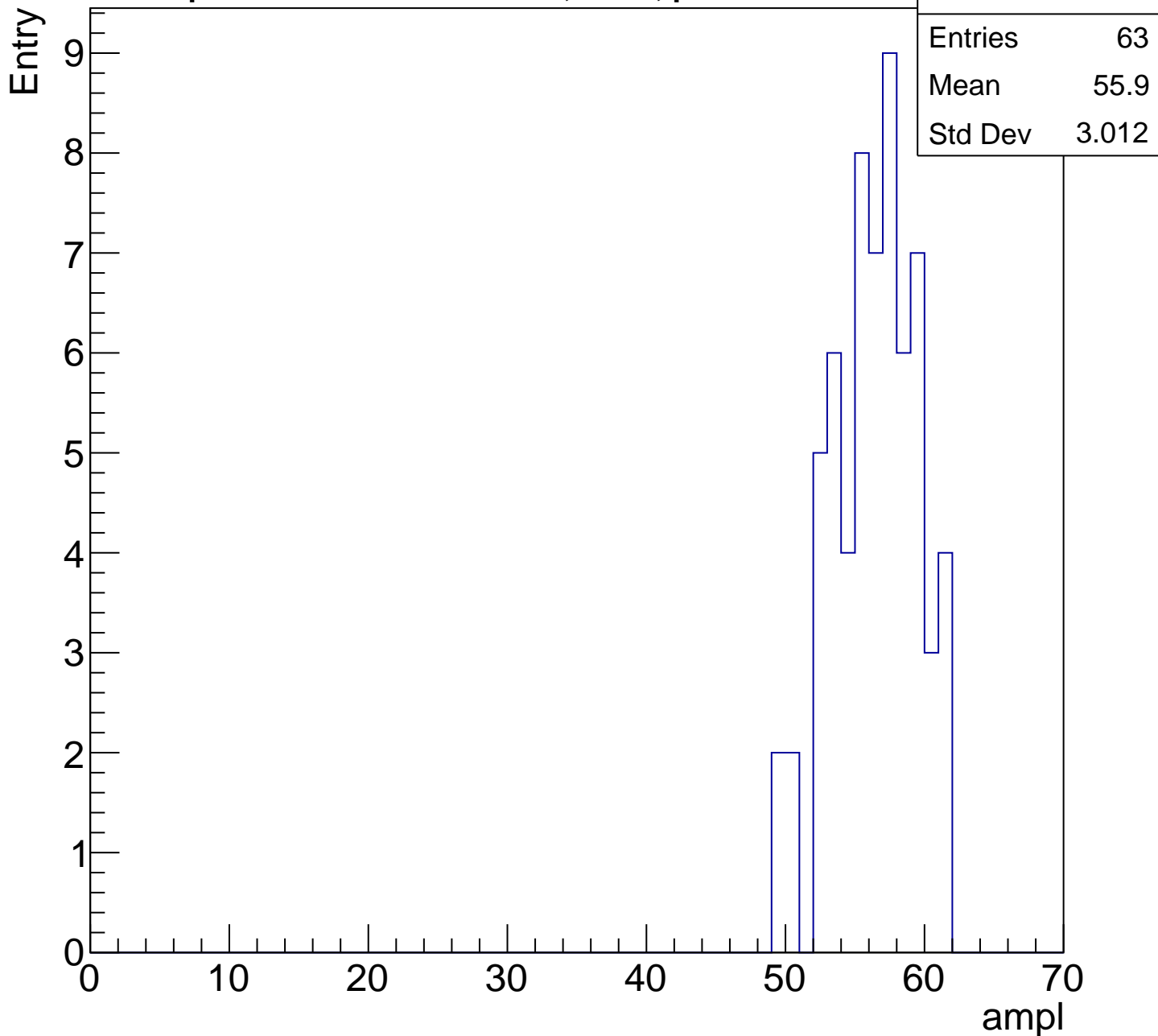
Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.7 |
| Std Dev | 3.185 |



B1L100S, U7-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

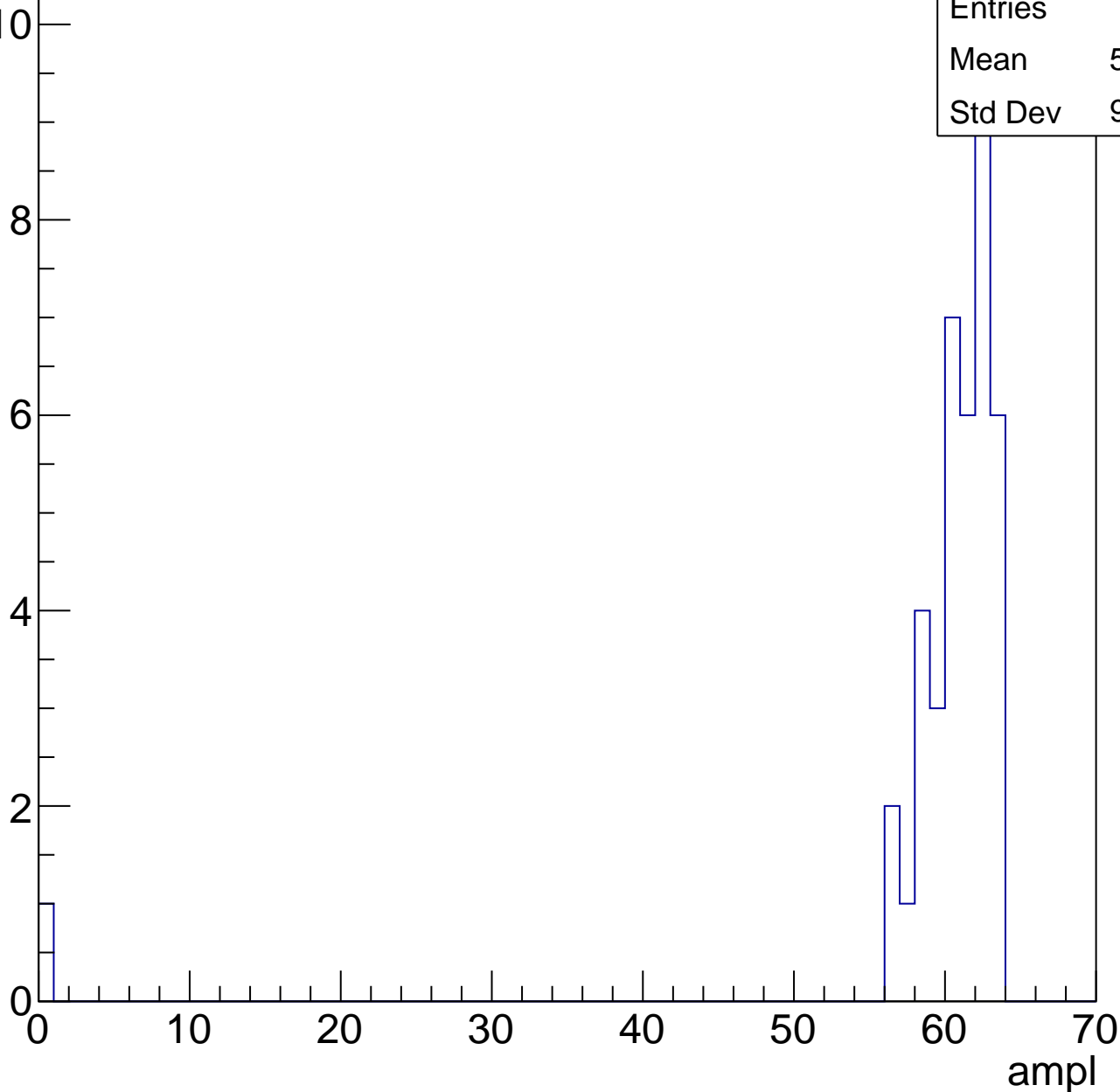


B1L100S, U7-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.05 |
| Std Dev | 9.649 |



B1L100S, U7-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

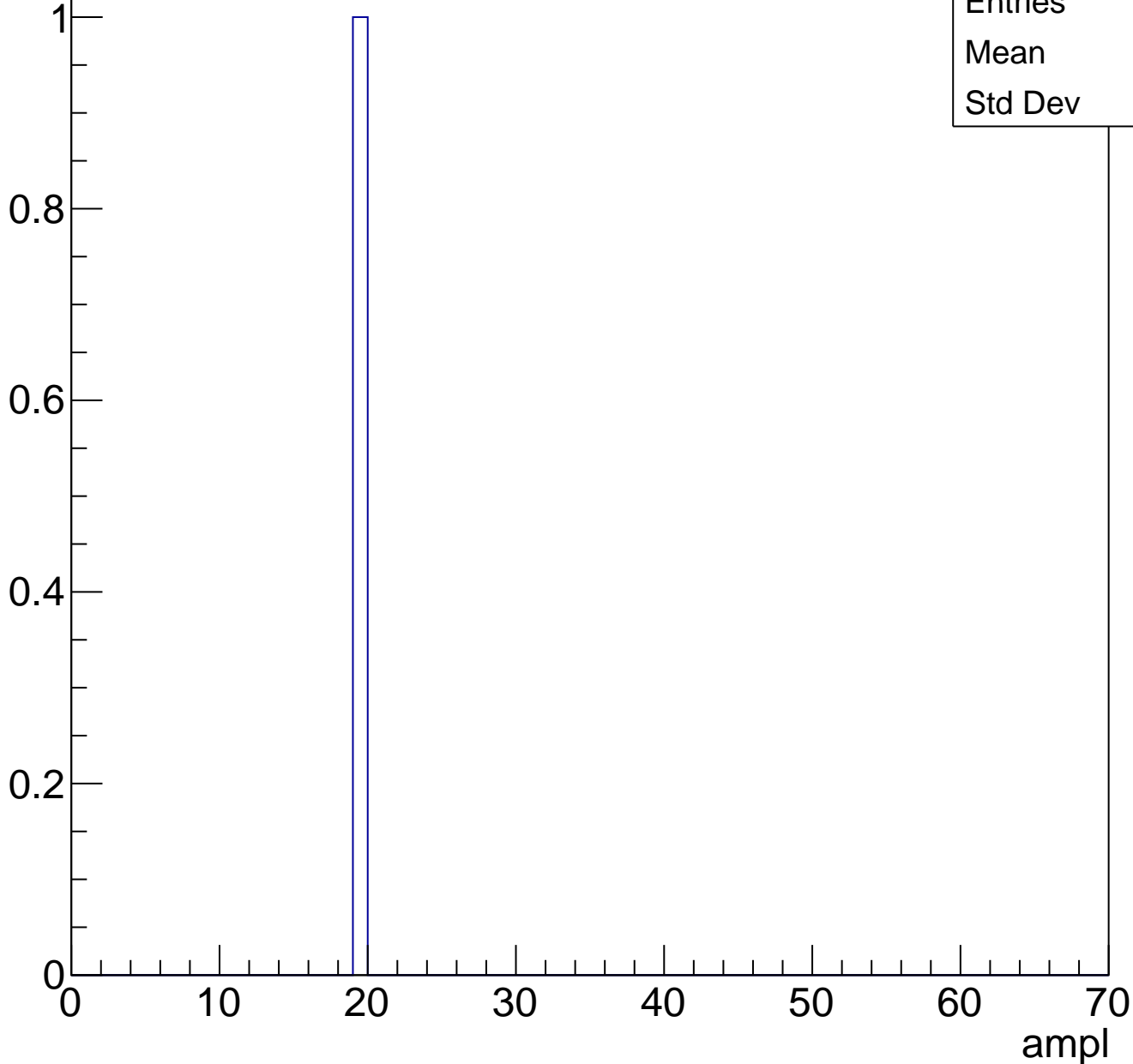
Entry



B1L100S, U7-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch68, adc0

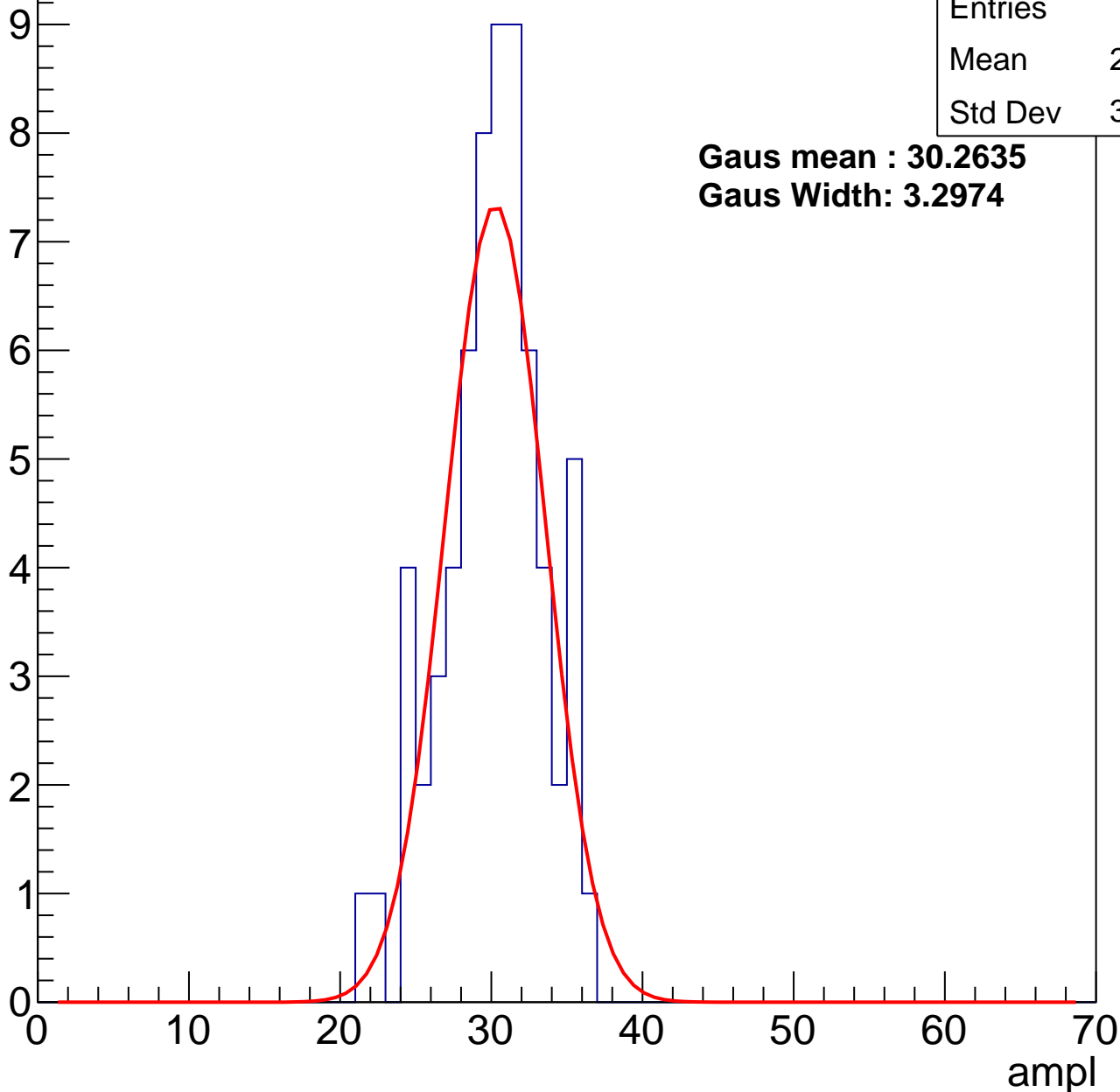
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 29.65 |
| Std Dev | 3.302 |

Gaus mean : 30.2635

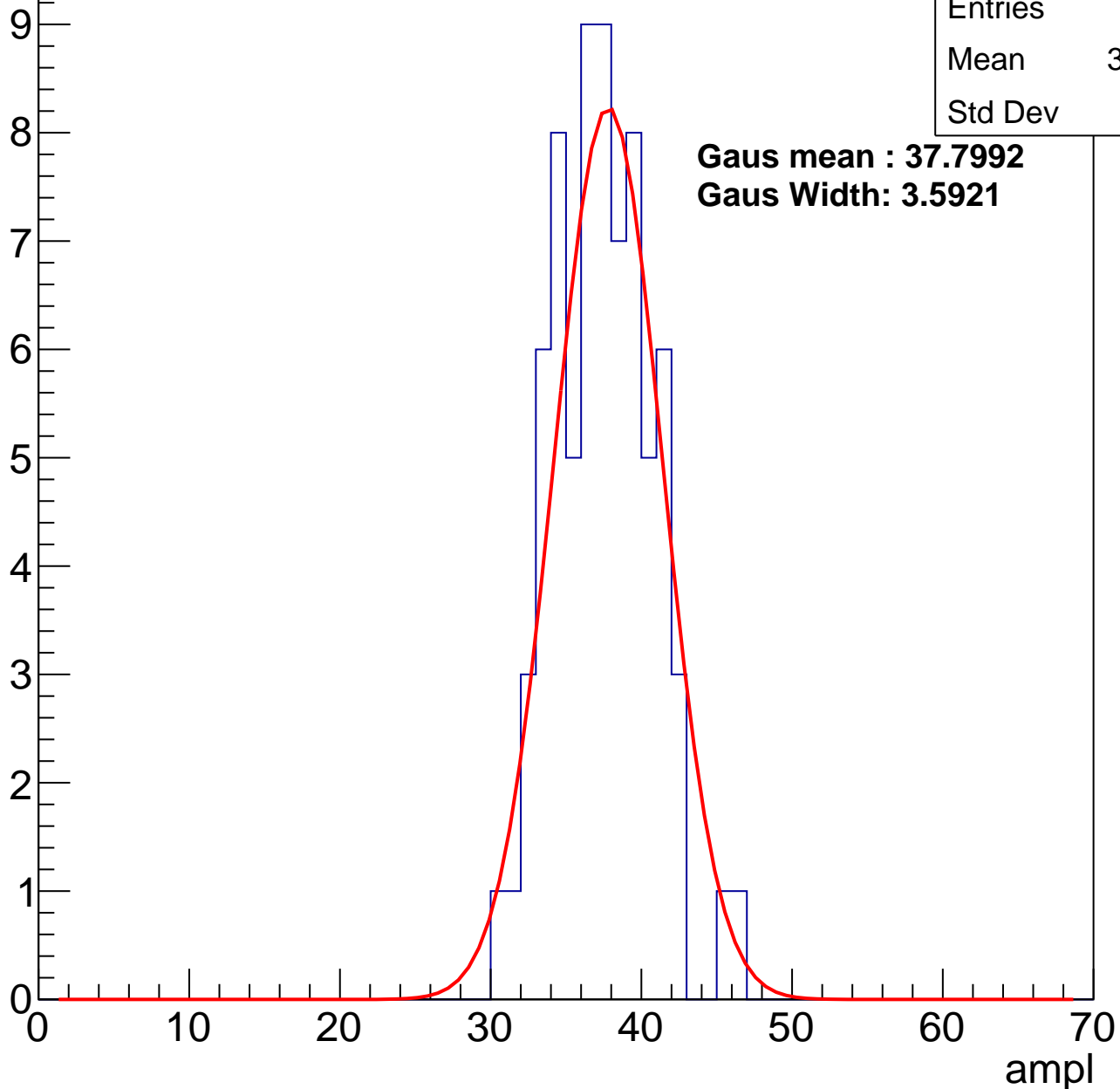
Gaus Width: 3.2974



B1L100S, U7-ch68, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch68, adc2

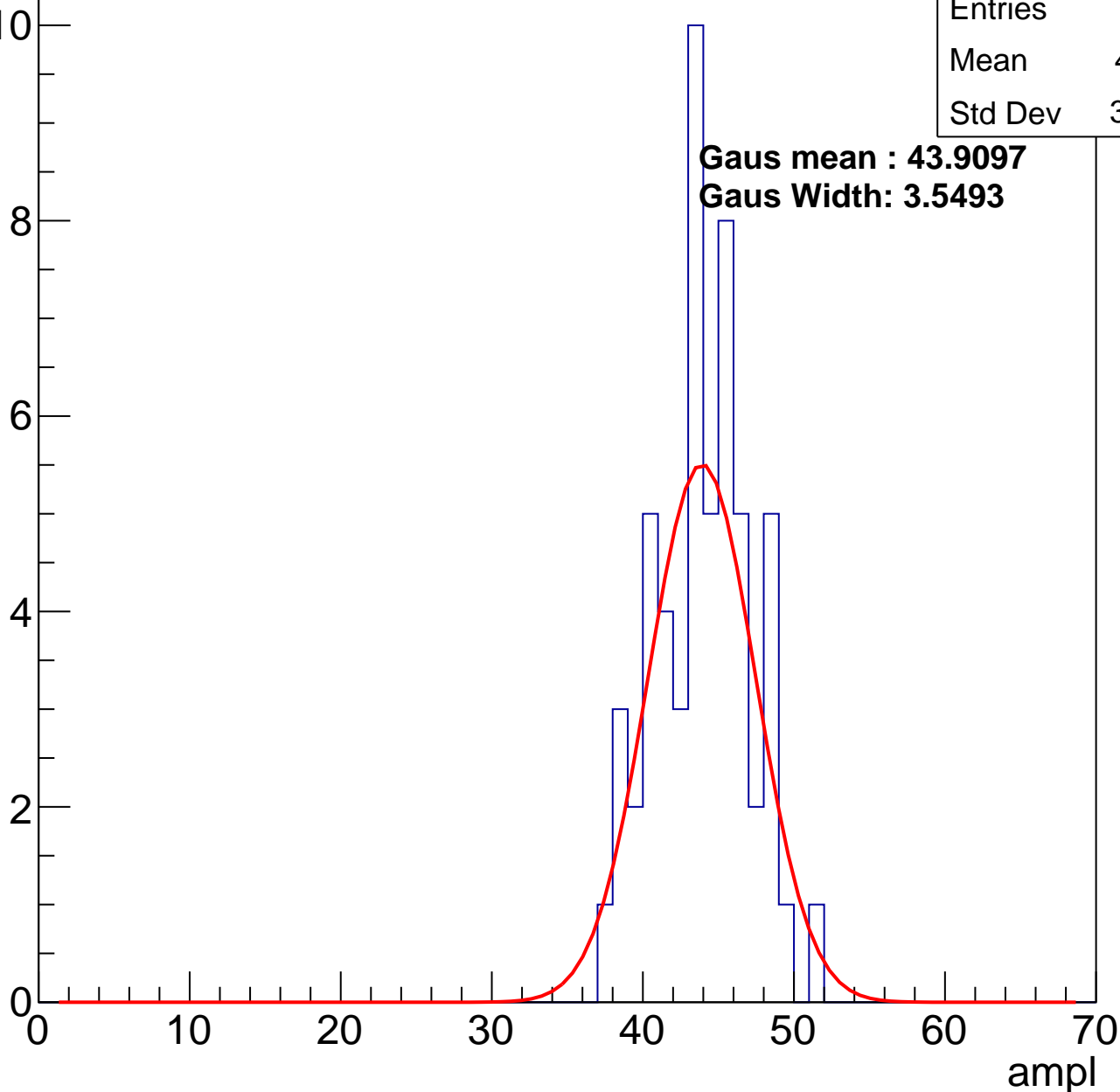
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.51 |
| Std Dev | 3.109 |

Gaus mean : 43.9097

Gaus Width: 3.5493



B1L100S, U7-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 49.81 |
| Std Dev | 3.621 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

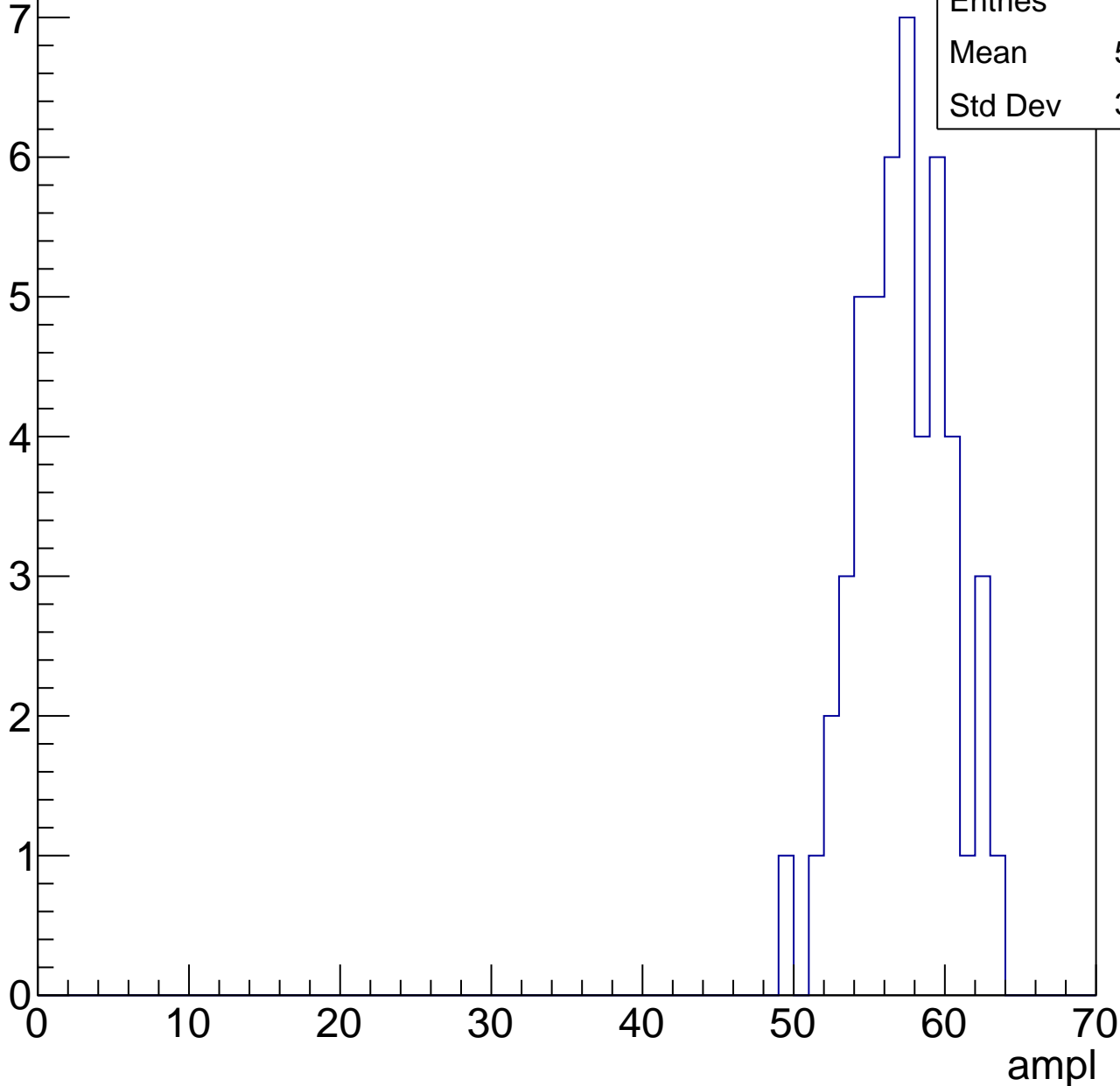
10

B1L100S, U7-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 56.71 |
| Std Dev | 3.051 |

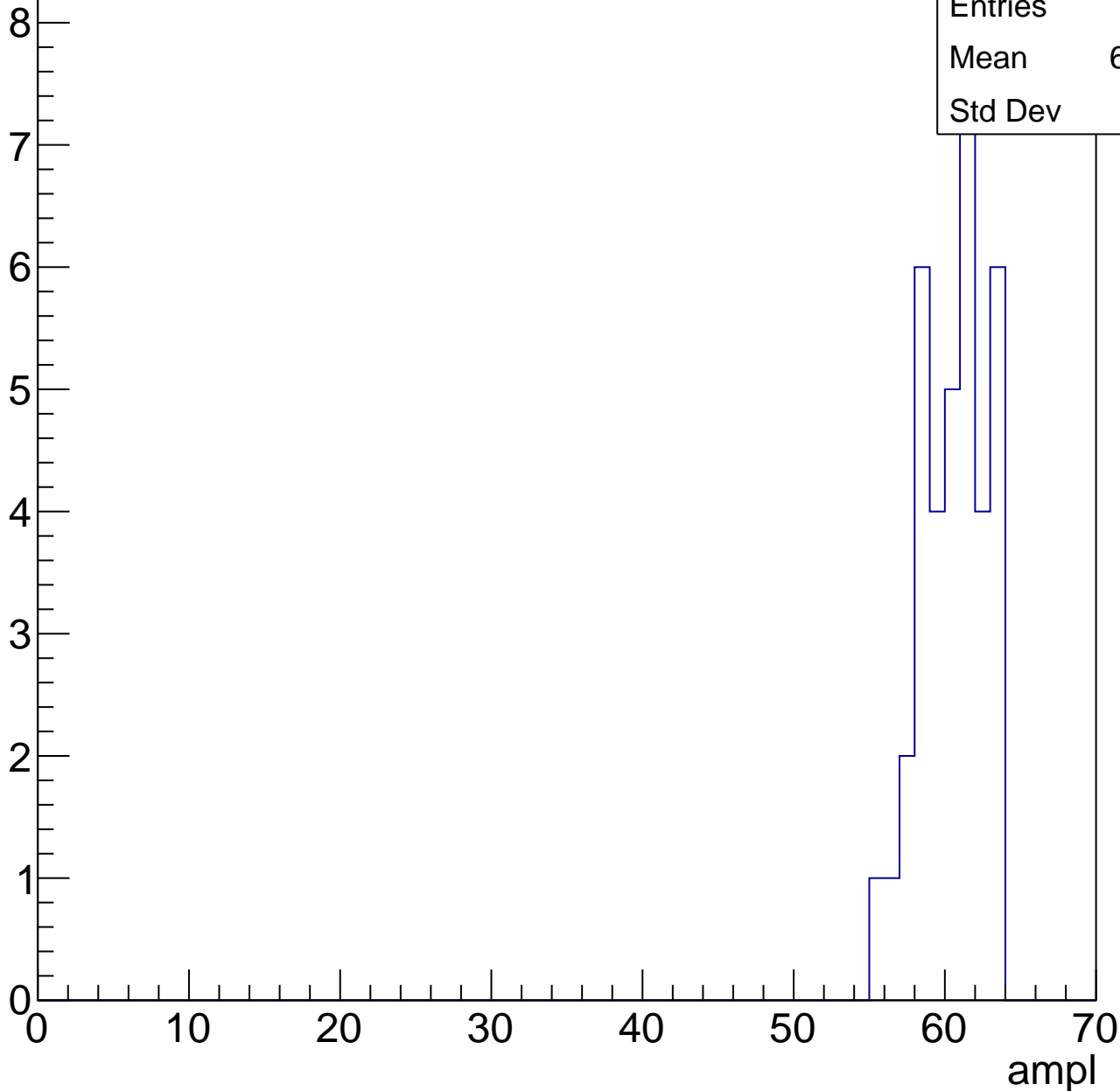


B1L100S, U7-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 60.08 |
| Std Dev | 2.11 |



B1L100S, U7-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 7 |
| Mean | 53 |
| Std Dev | 21.66 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U7-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch69, adc0

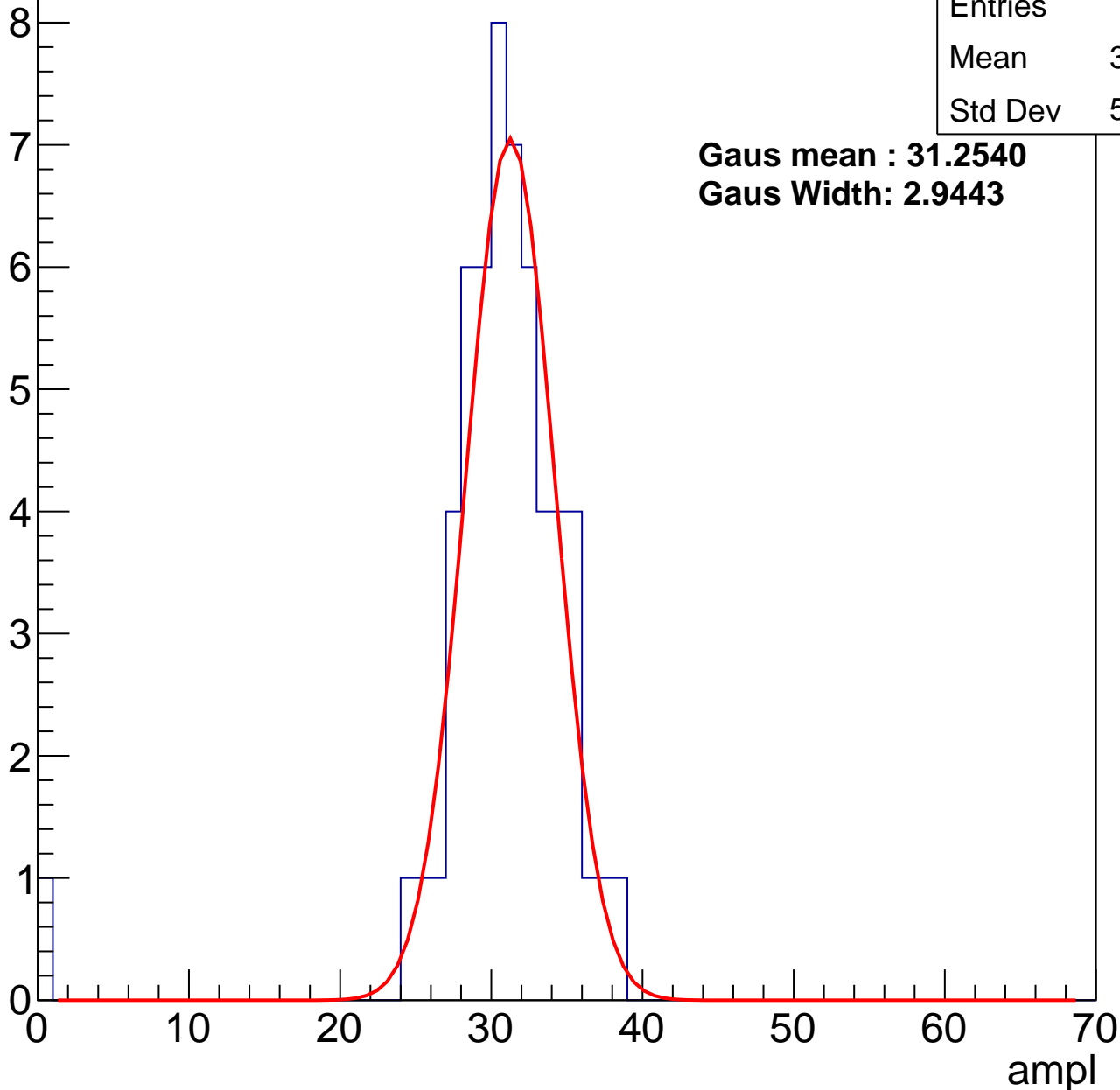
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 30.23 |
| Std Dev | 5.036 |

Gaus mean : 31.2540

Gaus Width: 2.9443



B1L100S, U7-ch69, adc1

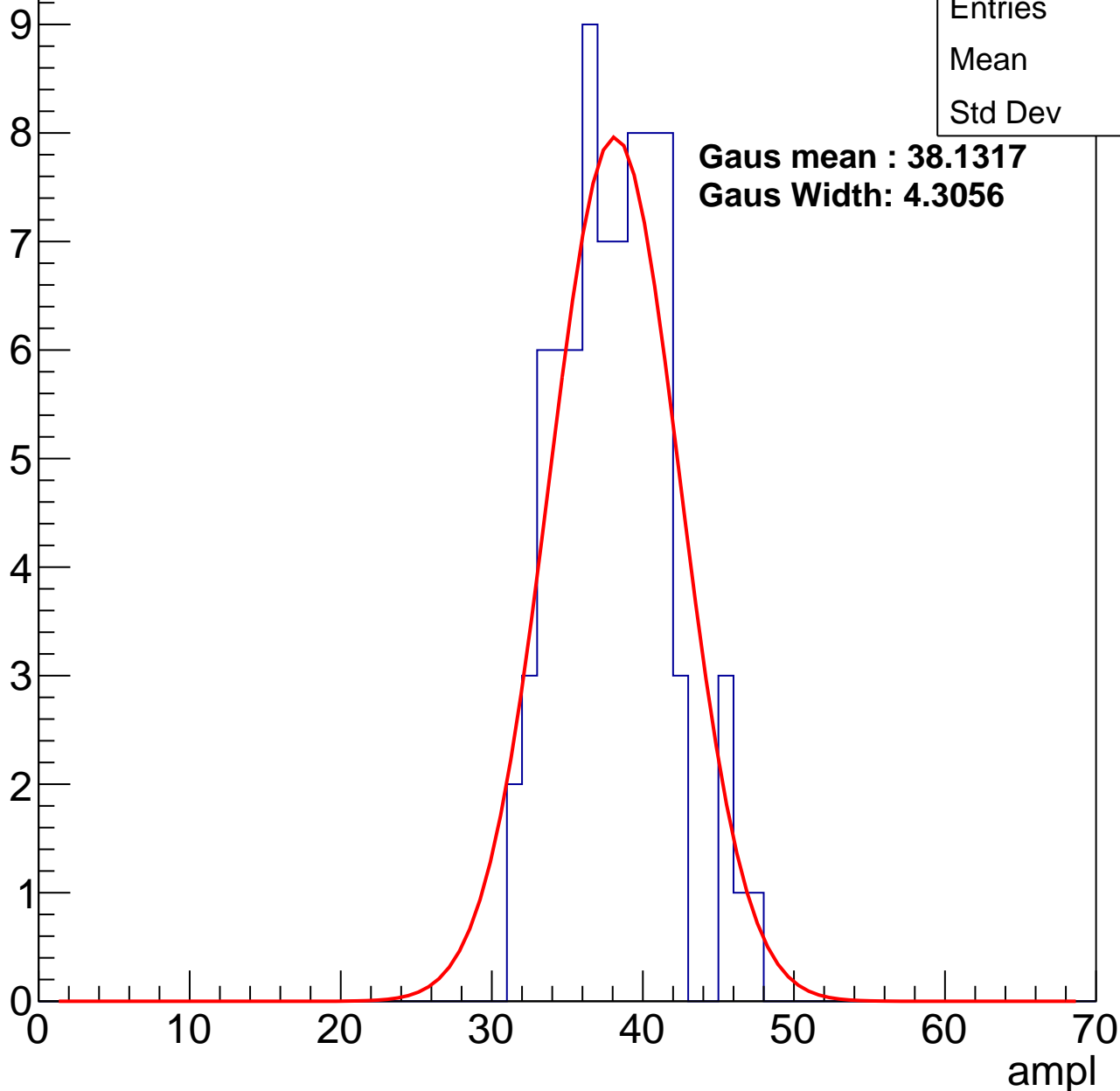
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 78 |
| Mean | 37.6 |
| Std Dev | 3.56 |

Gaus mean : 38.1317

Gaus Width: 4.3056



B1L100S, U7-ch69, adc2

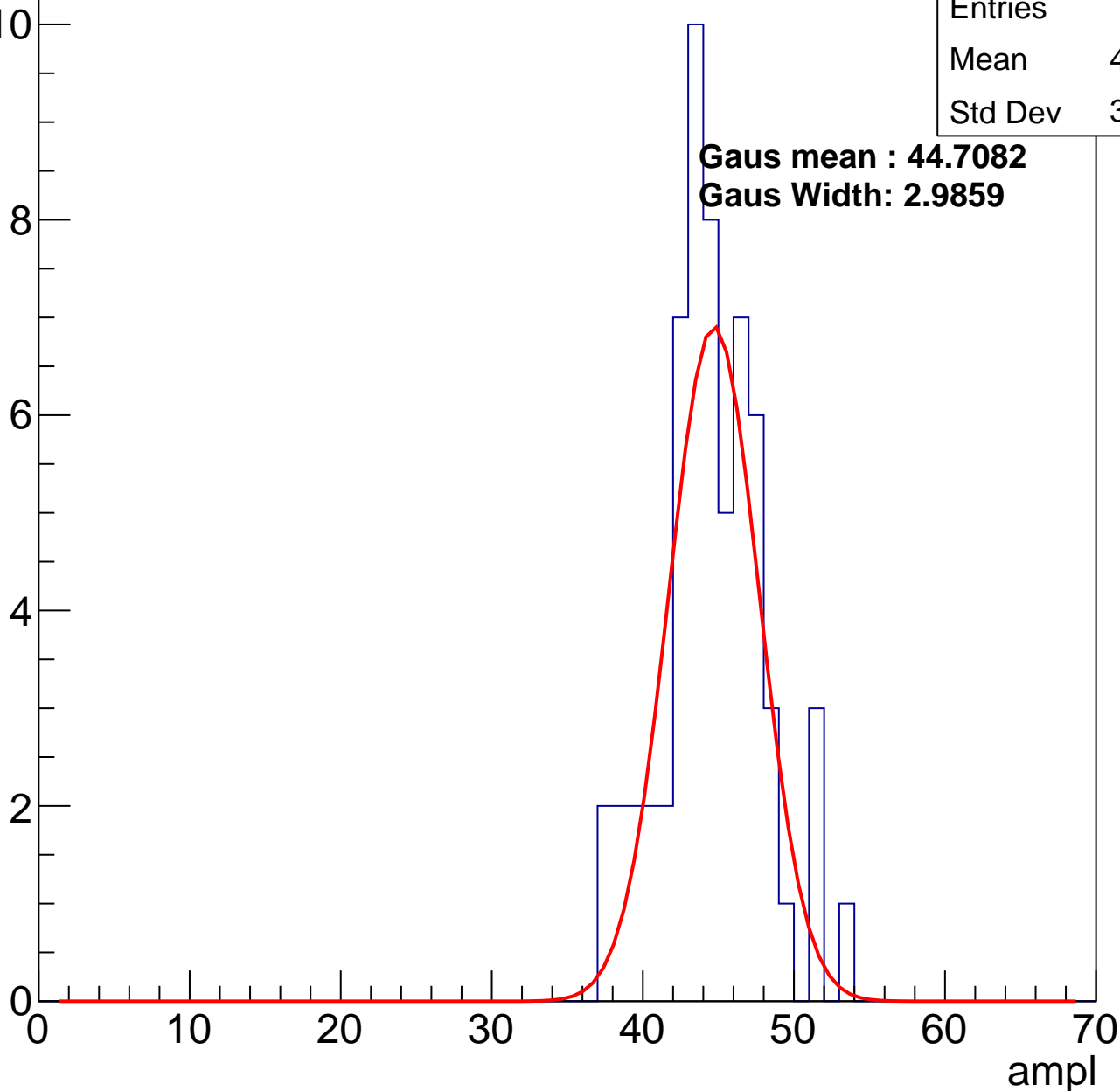
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 44.16 |
| Std Dev | 3.374 |

Gaus mean : 44.7082

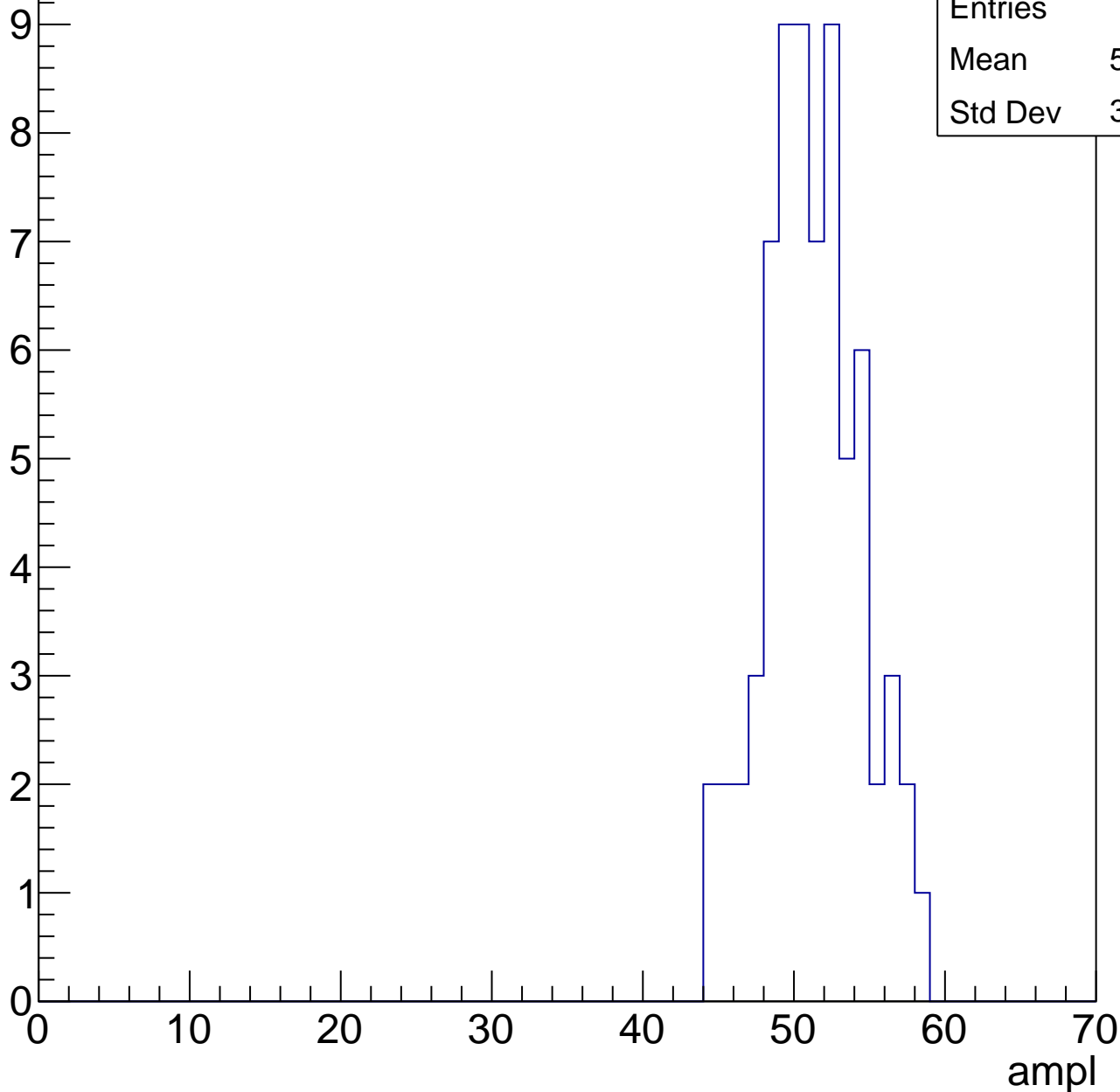
Gaus Width: 2.9859



B1L100S, U7-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

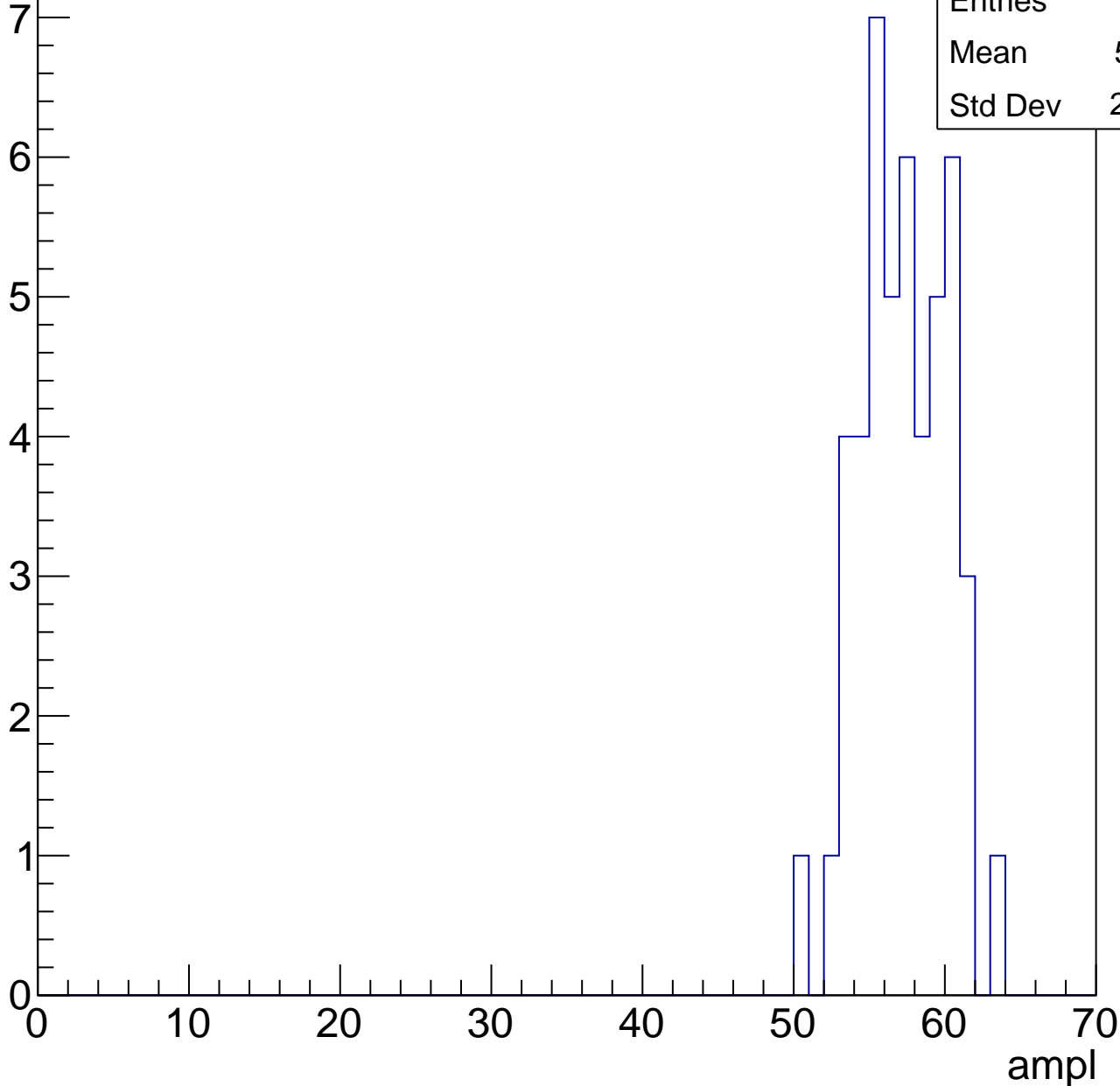


B1L100S, U7-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

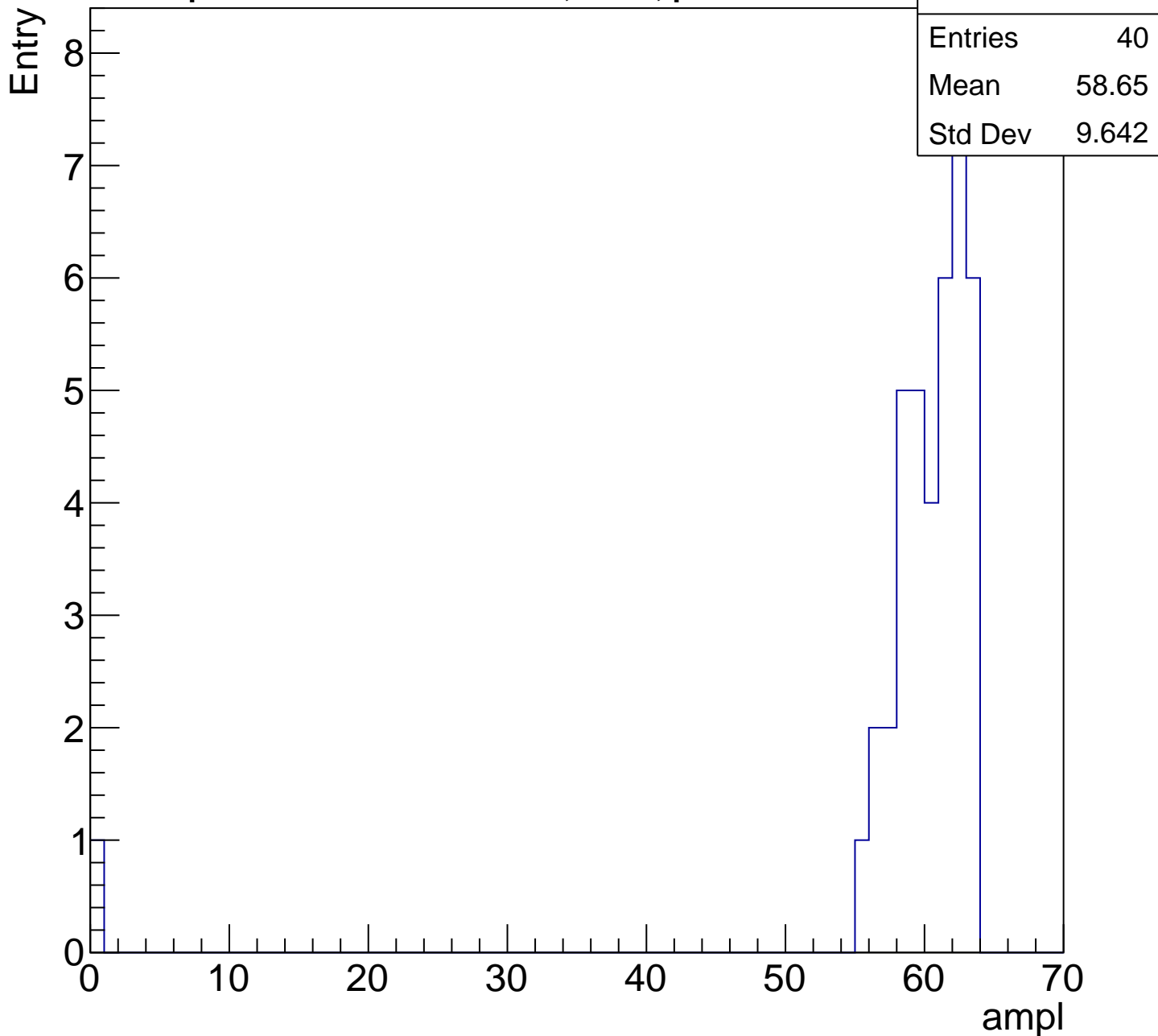
Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 56.81 |
| Std Dev | 2.795 |



B1L100S, U7-ch69, adc5

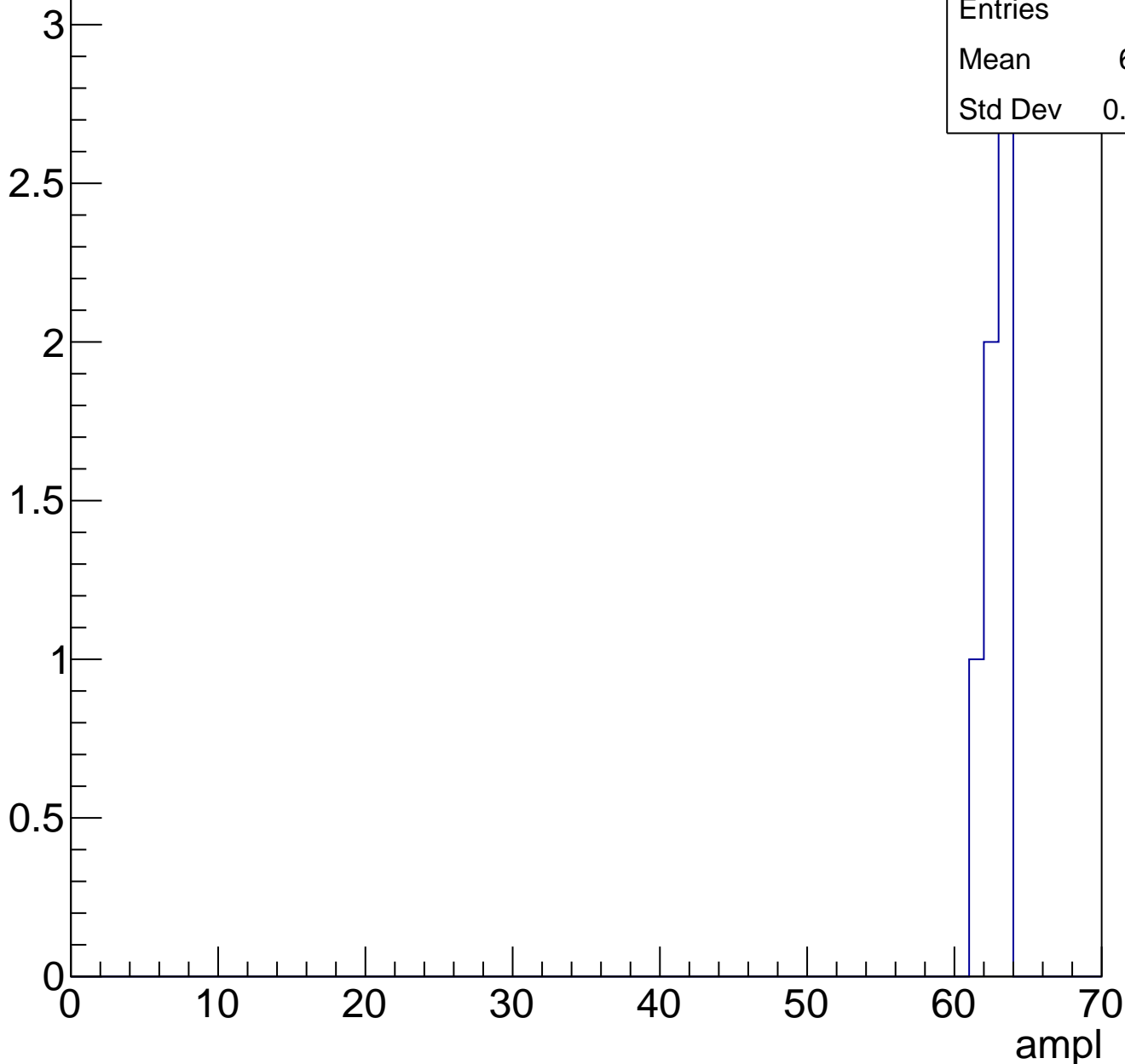
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|--------|
| Entries | 6 |
| Mean | 62.33 |
| Std Dev | 0.7454 |

B1L100S, U7-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 24 |
| Std Dev | 0 |

ampl

B1L100S, U7-ch70, adc0

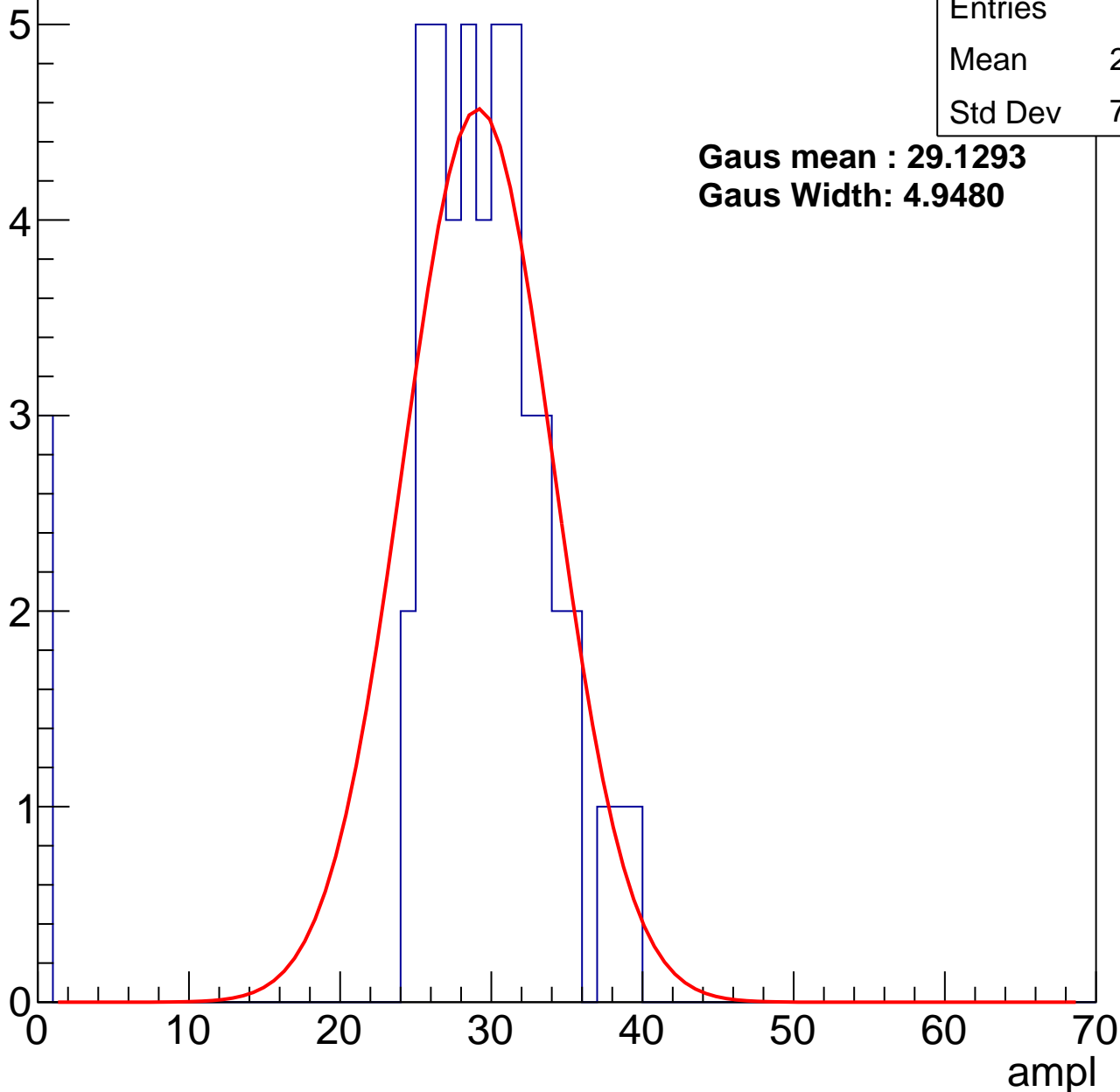
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 27.82 |
| Std Dev | 7.816 |

Gaus mean : 29.1293

Gaus Width: 4.9480



B1L100S, U7-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 98 |
| Mean | 36.28 |
| Std Dev | 4.167 |

Gaus mean : 36.5027

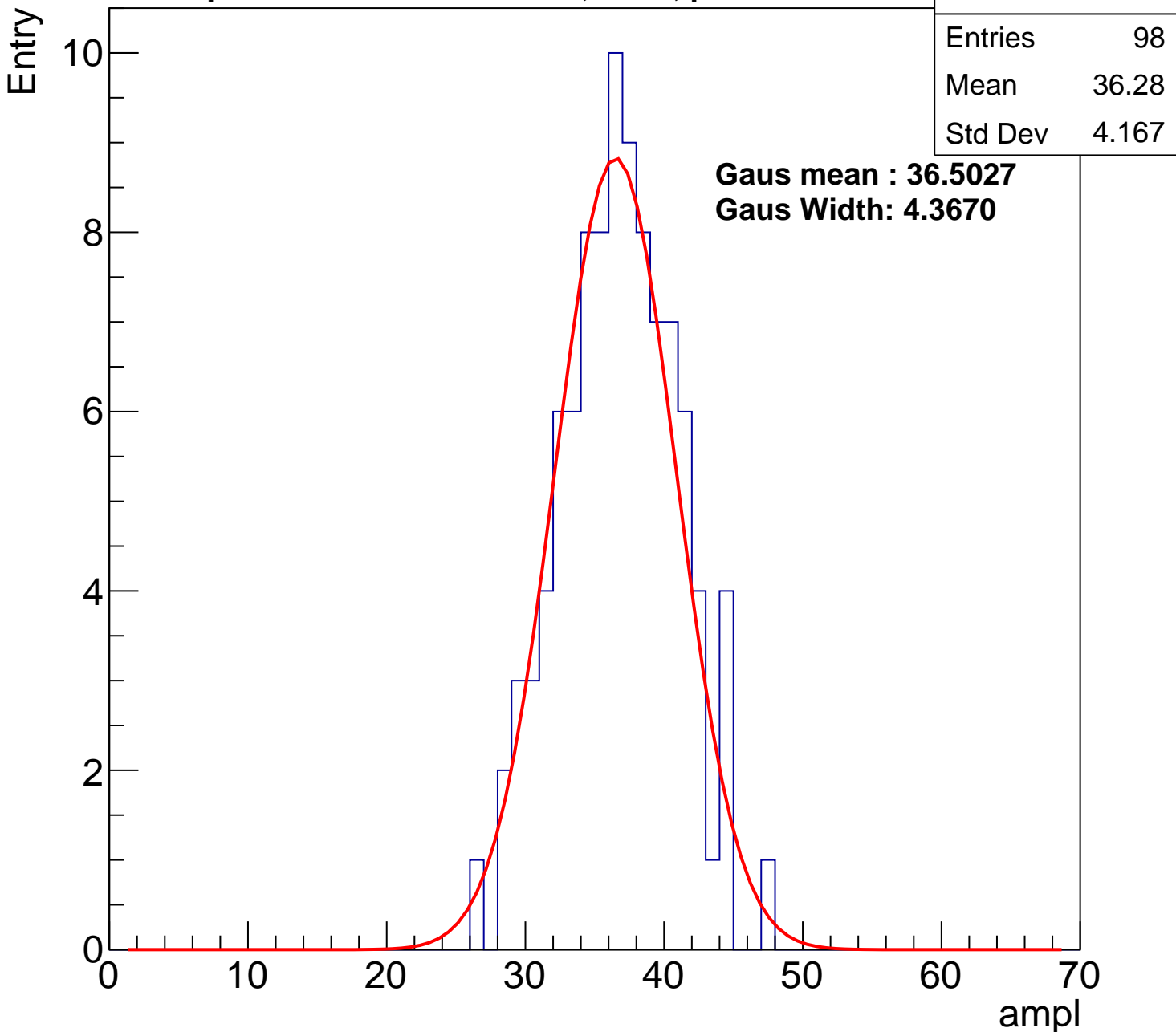
Gaus Width: 4.3670

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch70, adc2

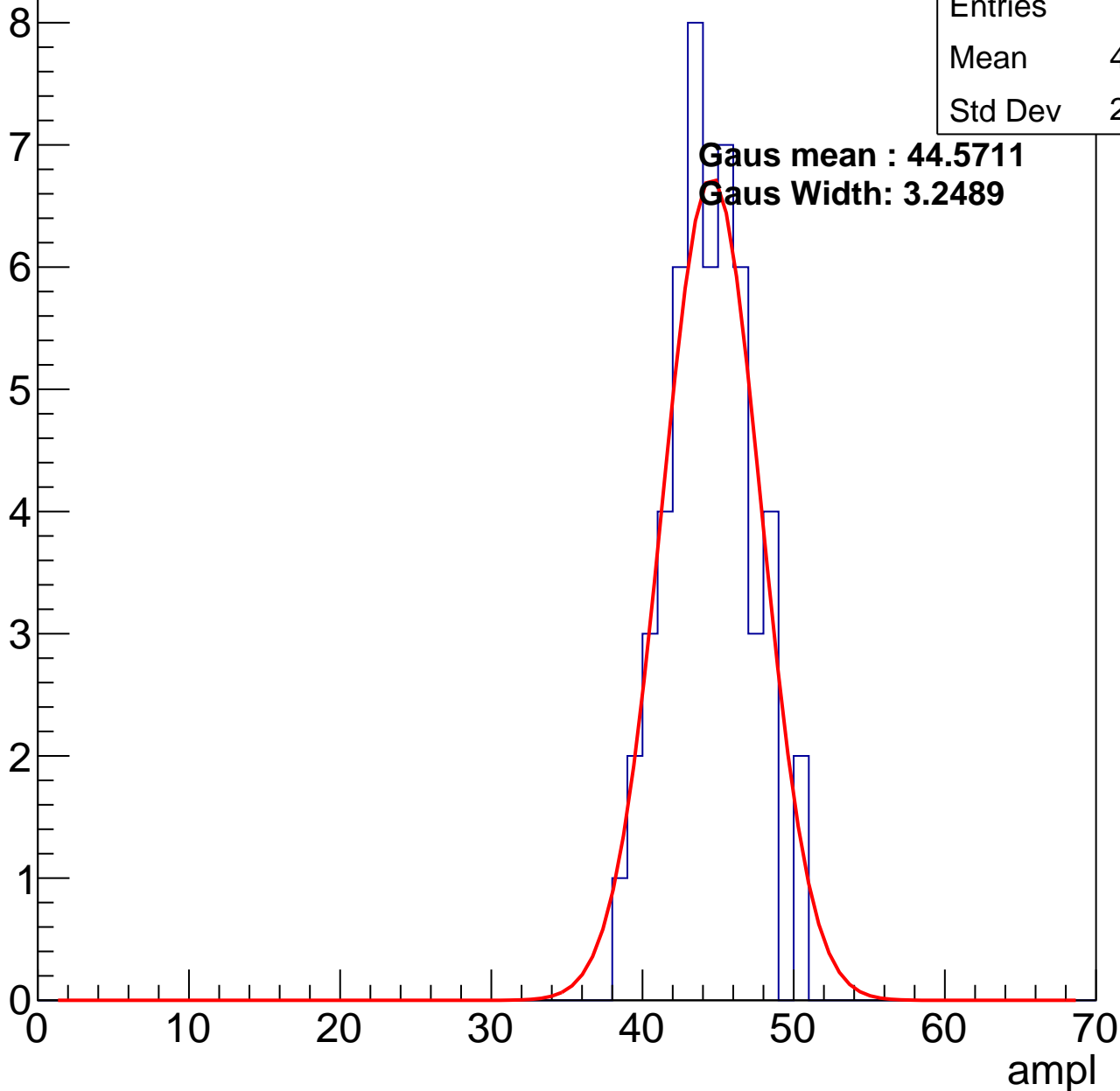
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 43.92 |
| Std Dev | 2.759 |

Gaus mean : 44.5711

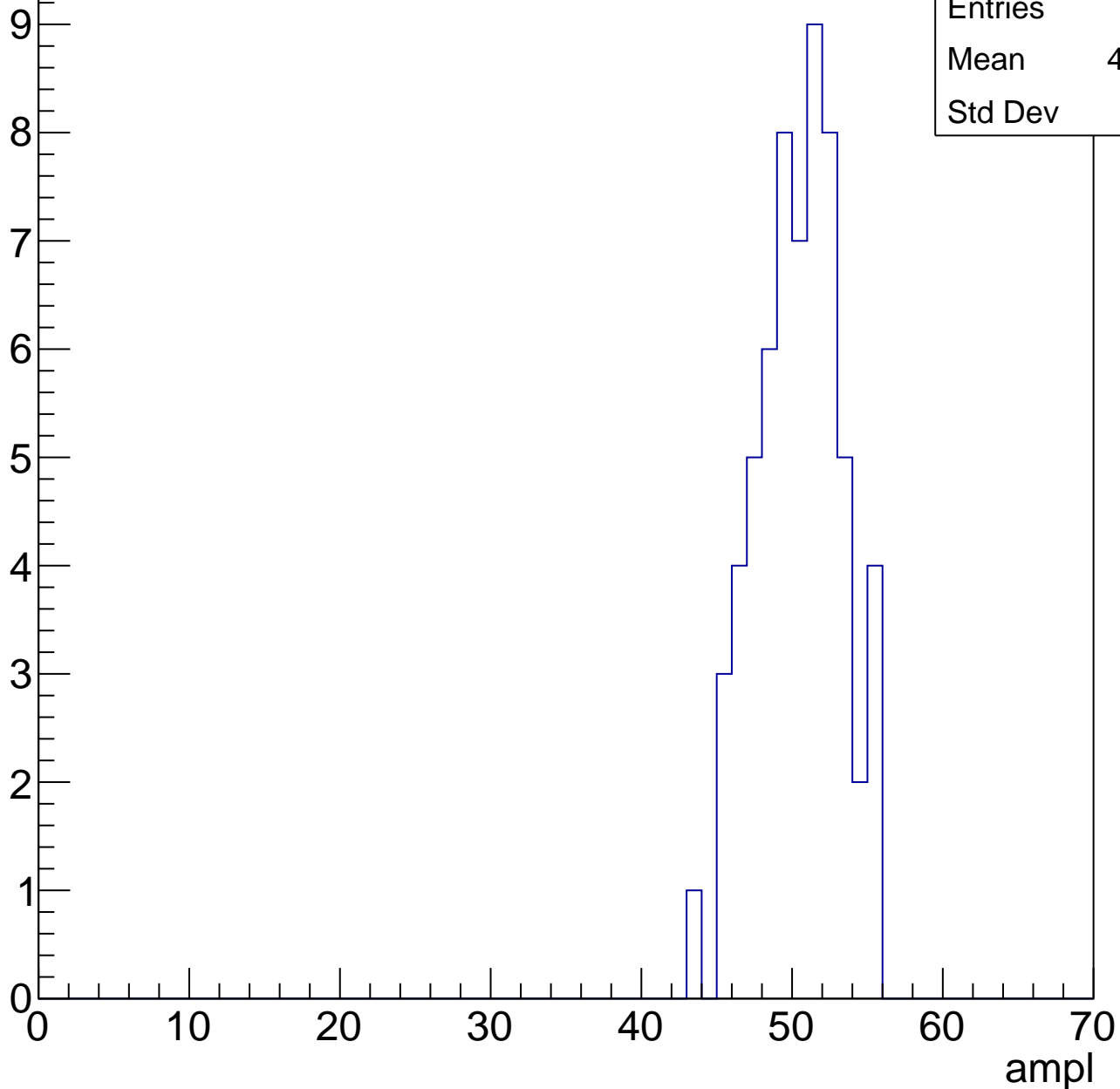
Gaus Width: 3.2489



B1L100S, U7-ch70, adc3

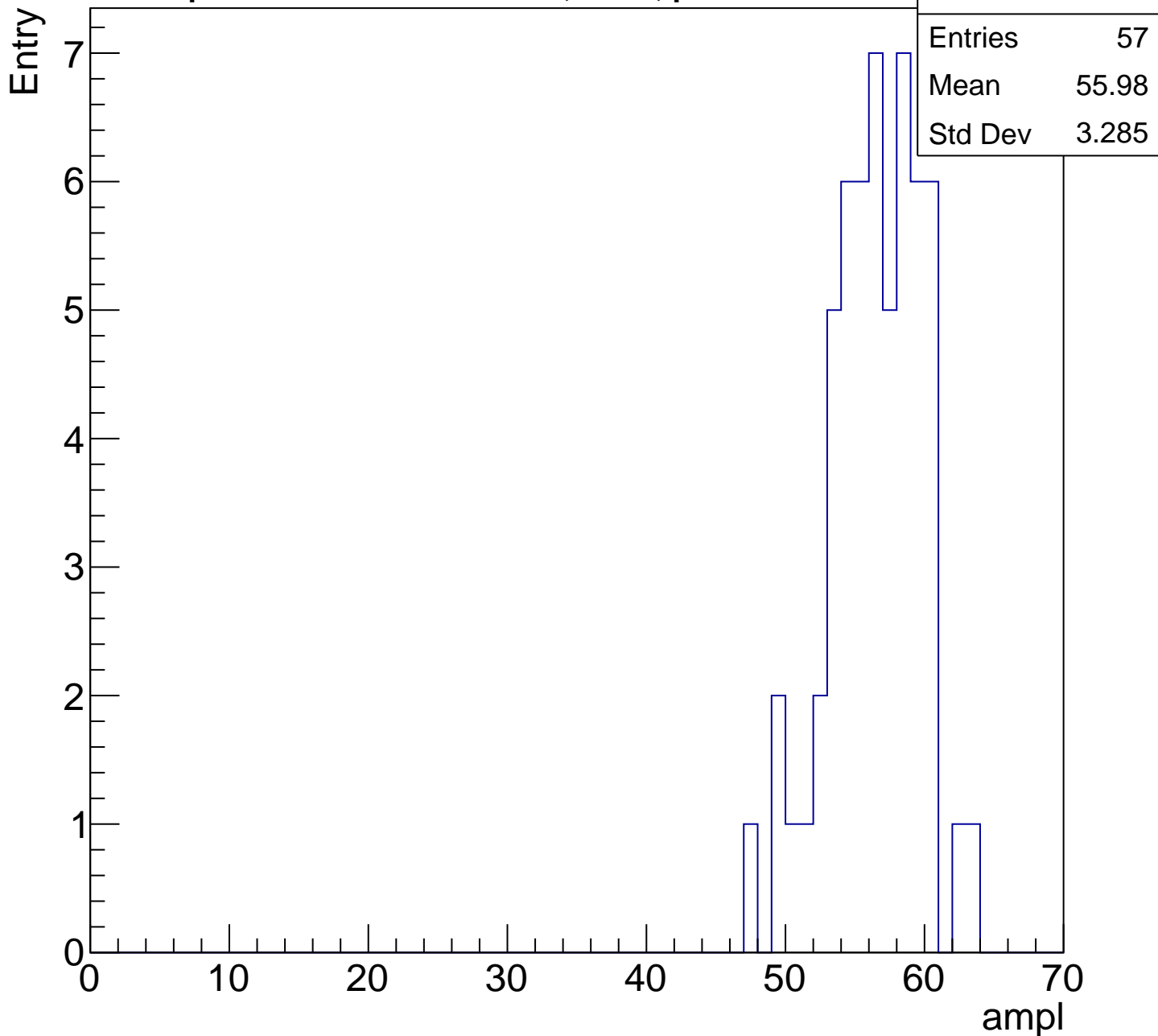
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

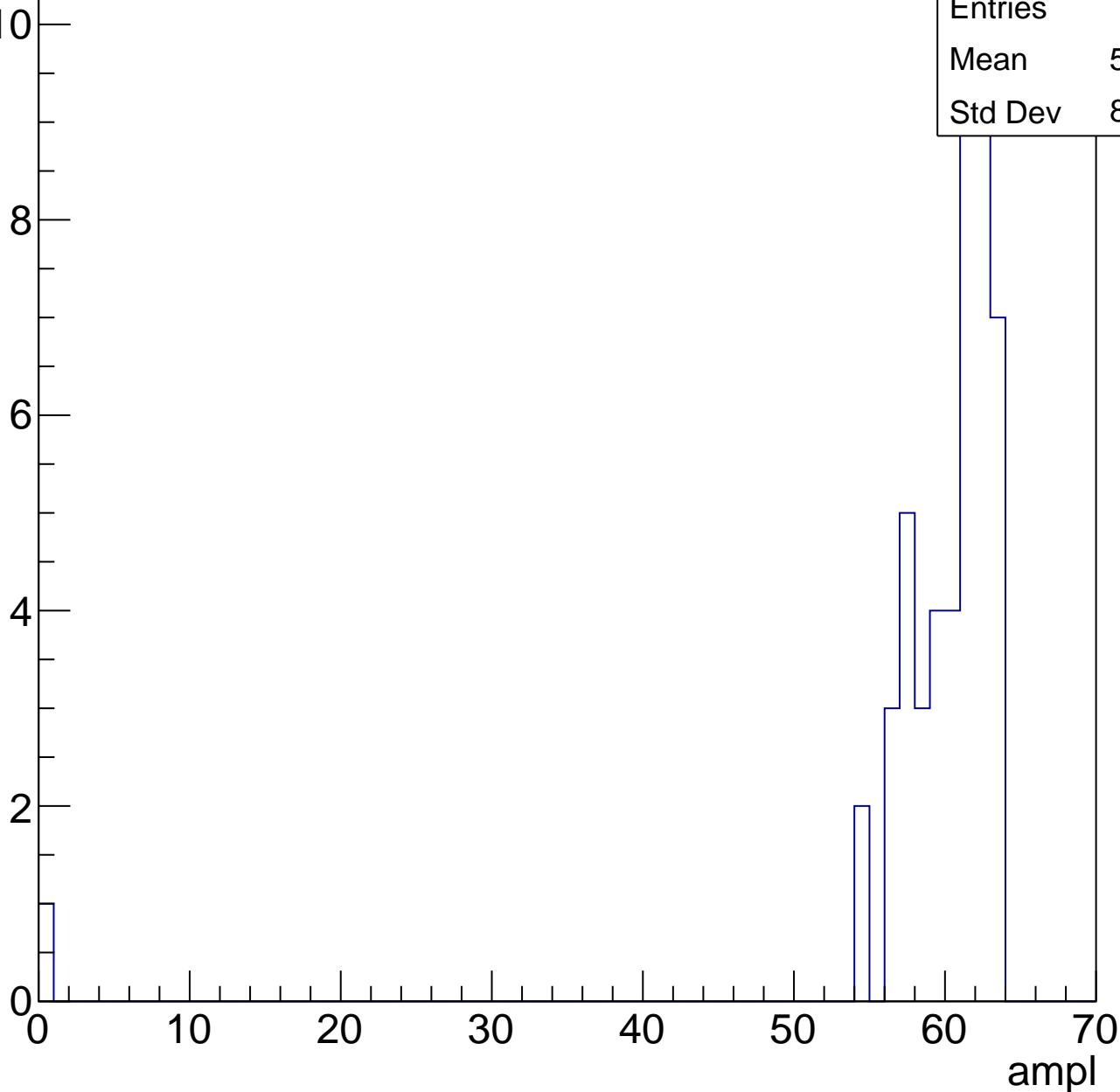


B1L100S, U7-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.75 |
| Std Dev | 8.915 |



B1L100S, U7-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

ampl

B1L100S, U7-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch71, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 30.35 |
| Std Dev | 3.258 |

Gaus mean : 30.6860

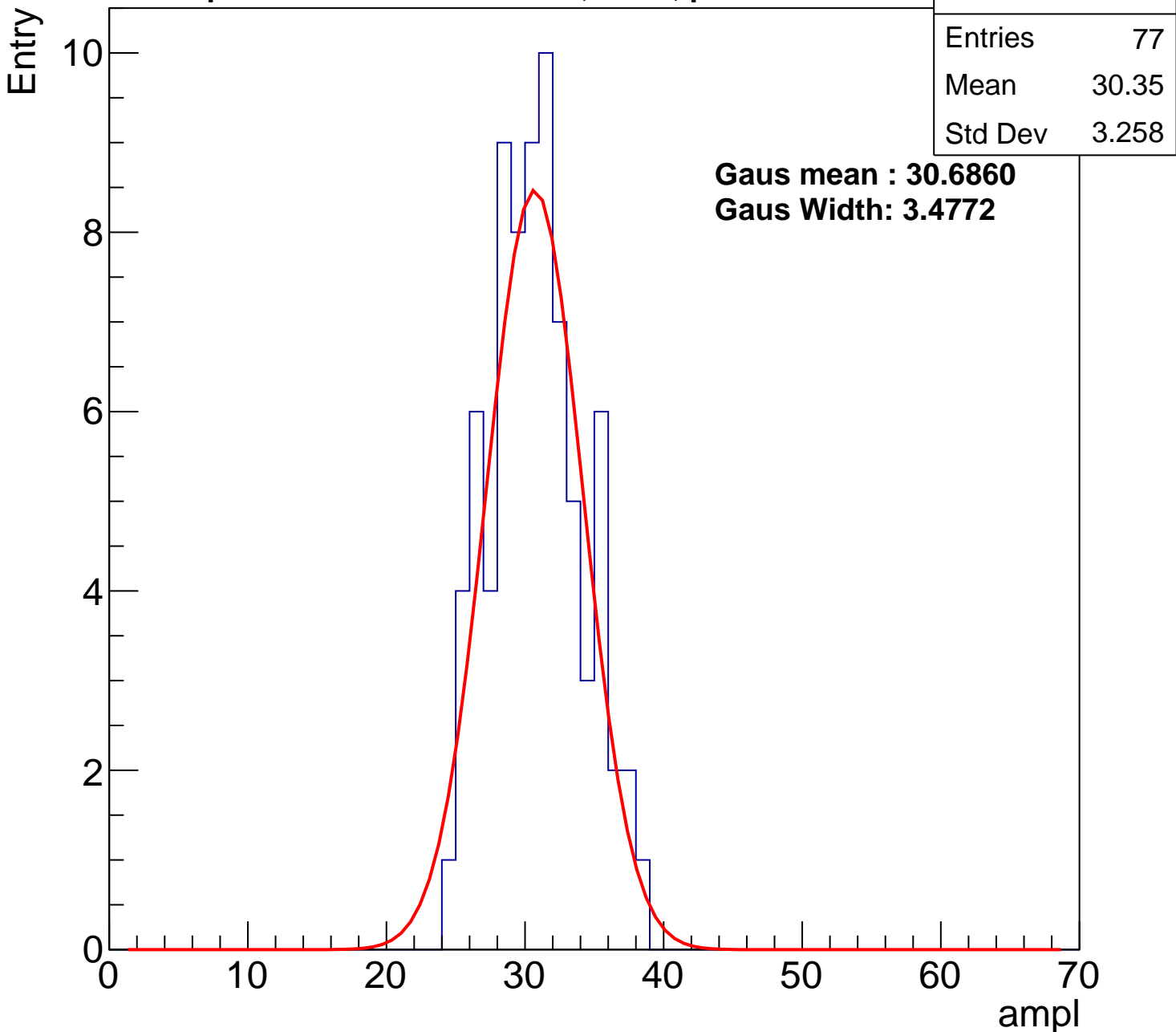
Gaus Width: 3.4772

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch71, adc1

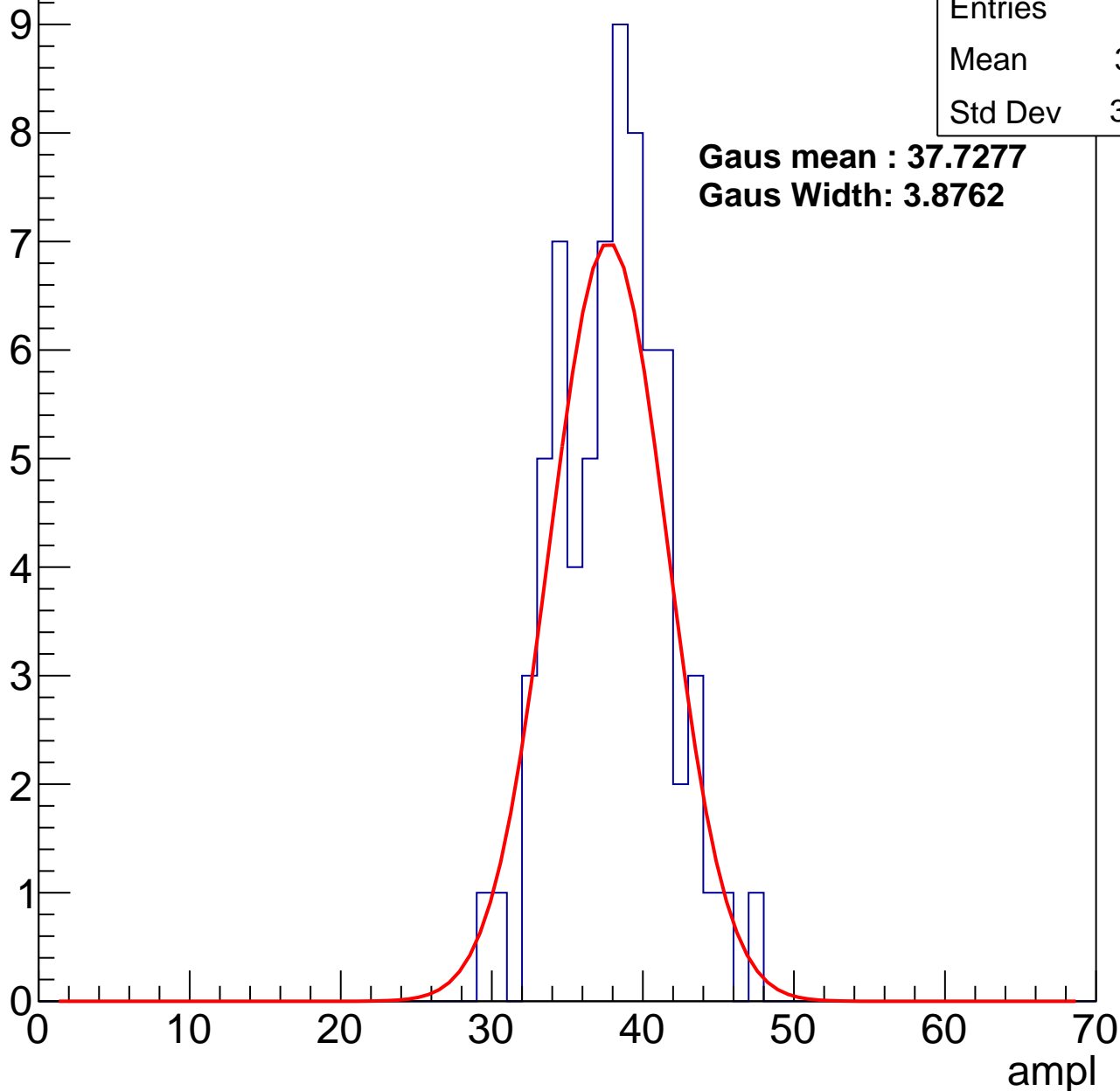
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.51 |
| Std Dev | 3.577 |

Gaus mean : 37.7277

Gaus Width: 3.8762



B1L100S, U7-ch71, adc2

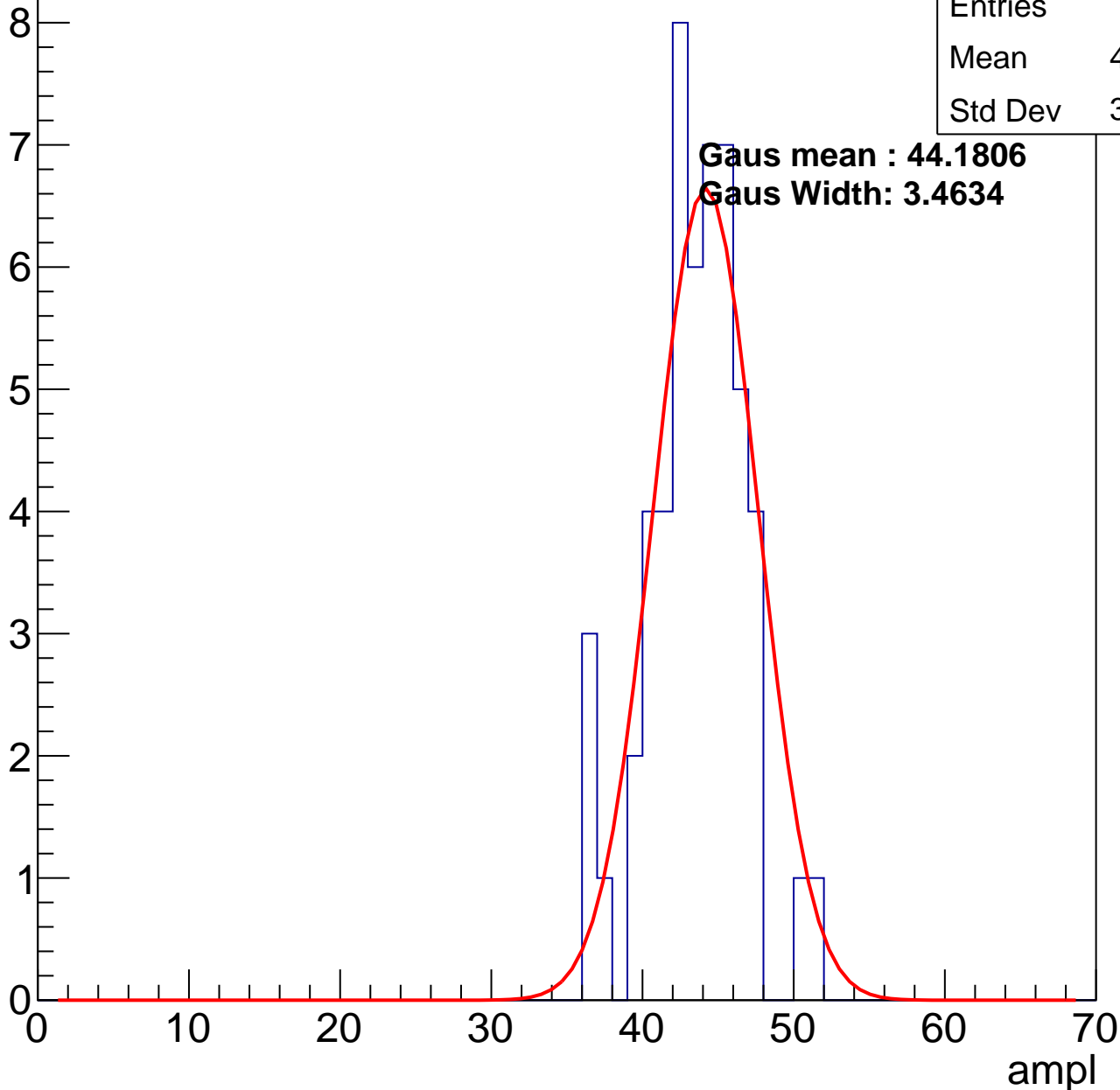
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 43.08 |
| Std Dev | 3.167 |

Gaus mean : 44.1806

Gaus Width: 3.4634



B1L100S, U7-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

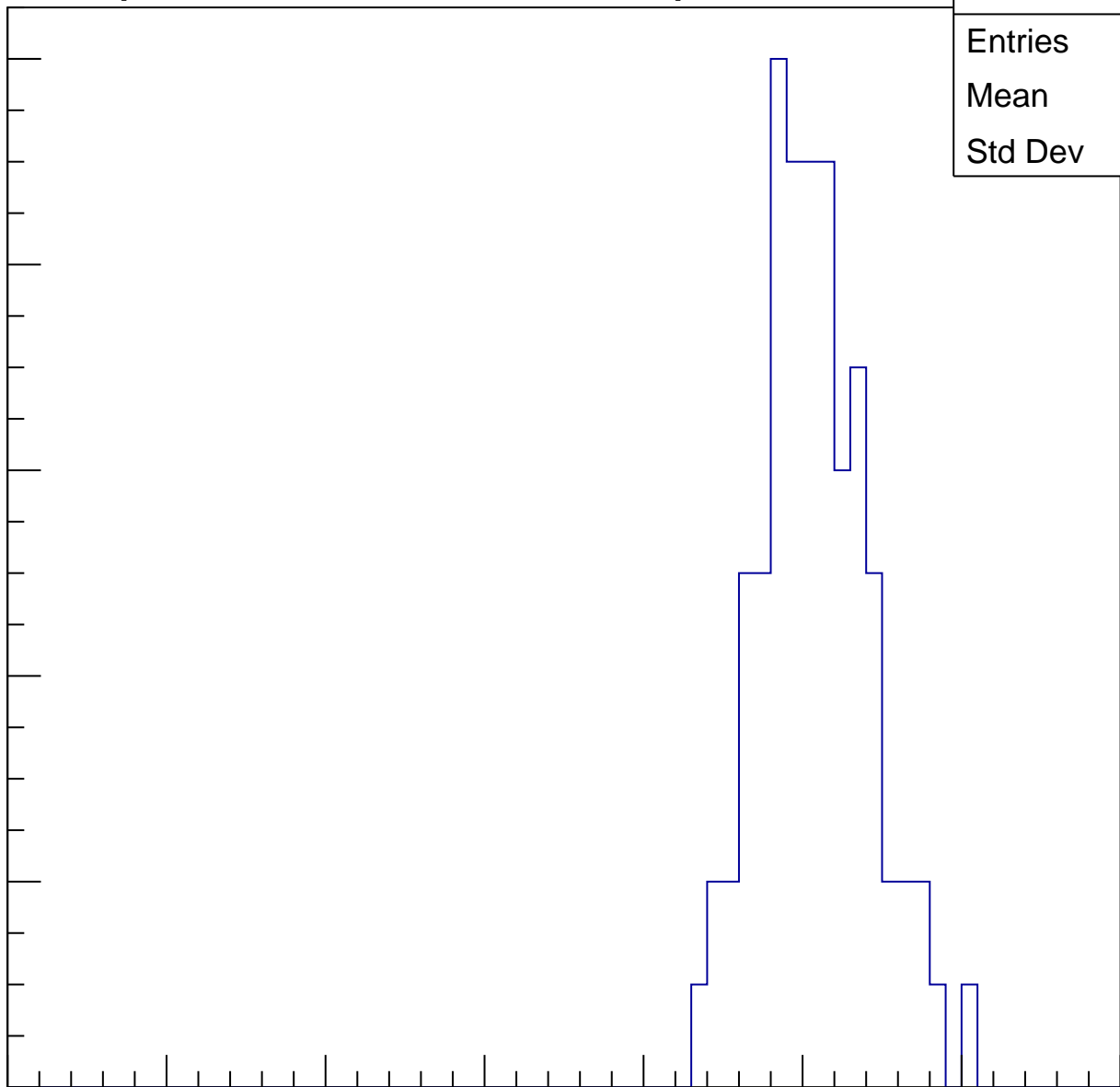
| | |
|---------|-------|
| Entries | 78 |
| Mean | 50.29 |
| Std Dev | 3.393 |

Entry

10
8
6
4
2
0

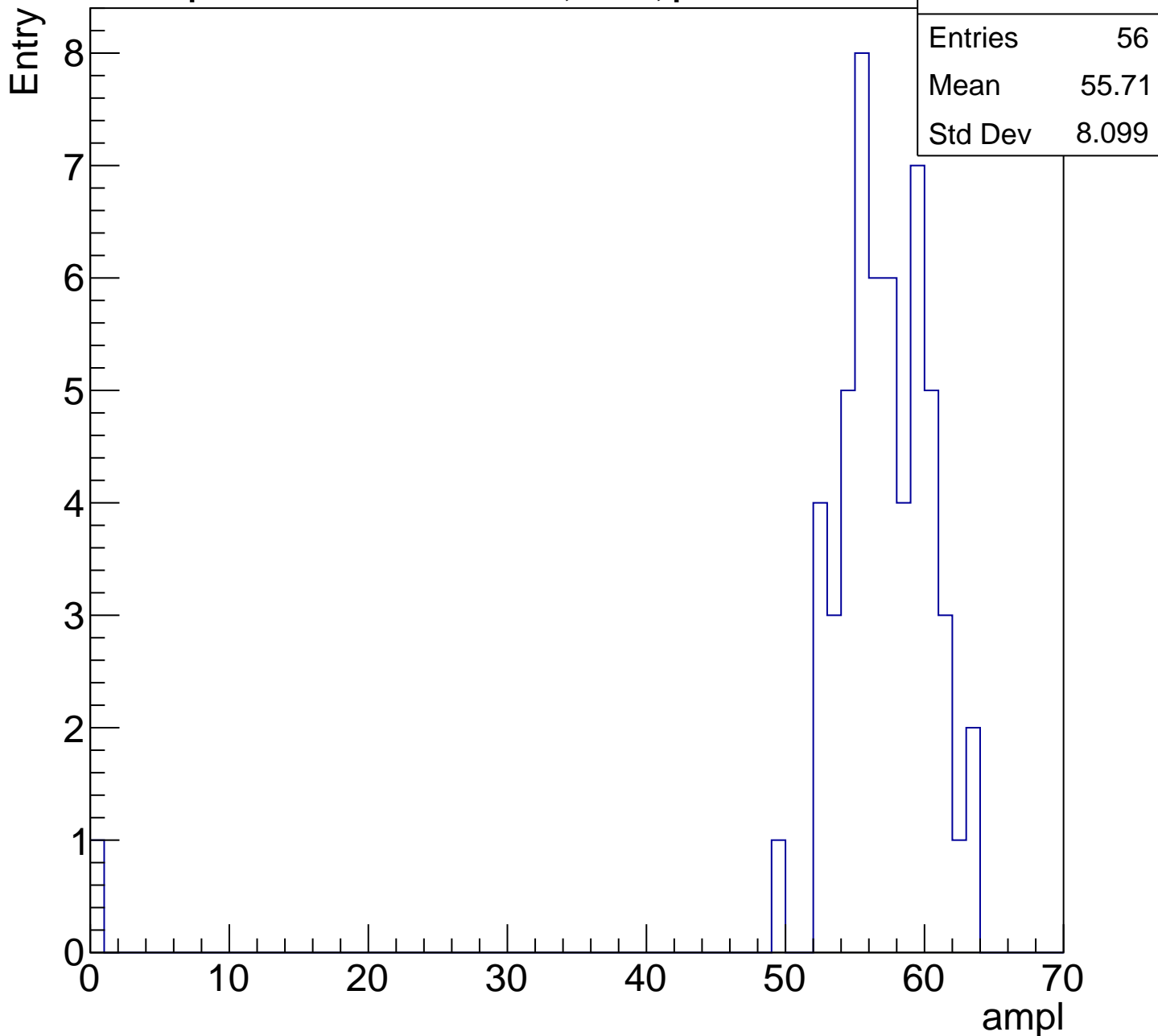
0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

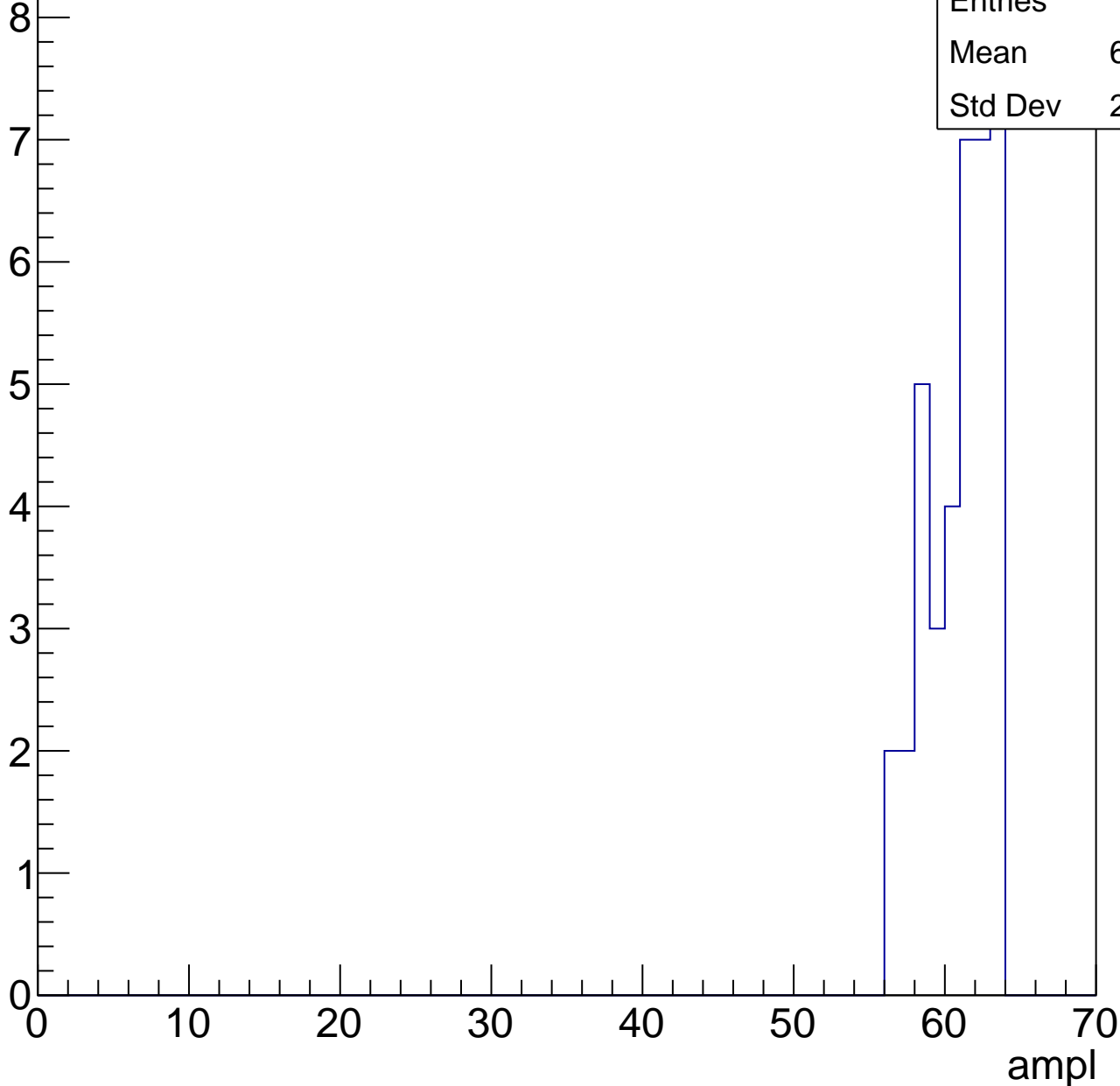


B1L100S, U7-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 60.47 |
| Std Dev | 2.124 |



B1L100S, U7-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch72, adc0

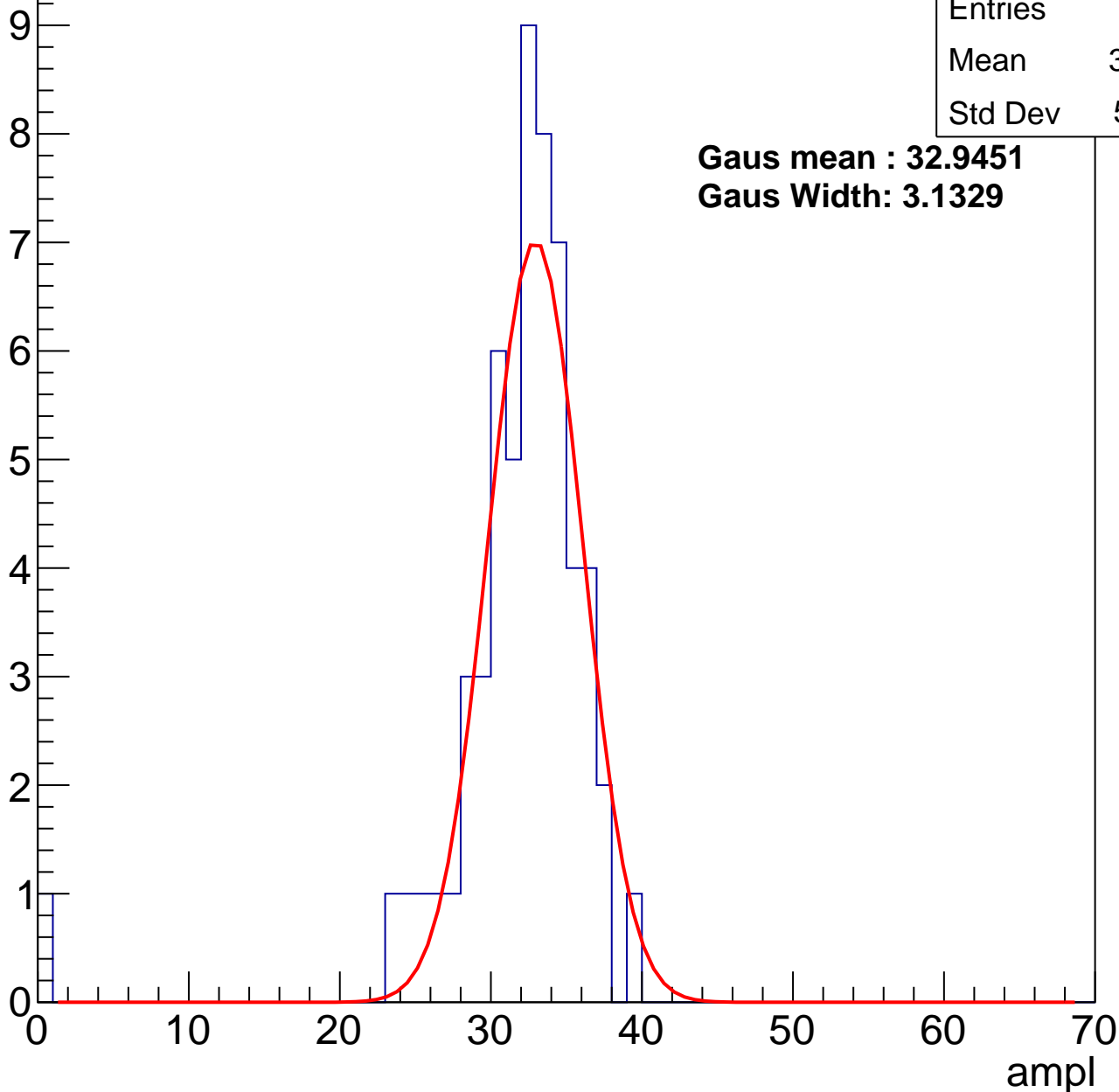
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 31.34 |
| Std Dev | 5.241 |

Gaus mean : 32.9451

Gaus Width: 3.1329



B1L100S, U7-ch72, adc1

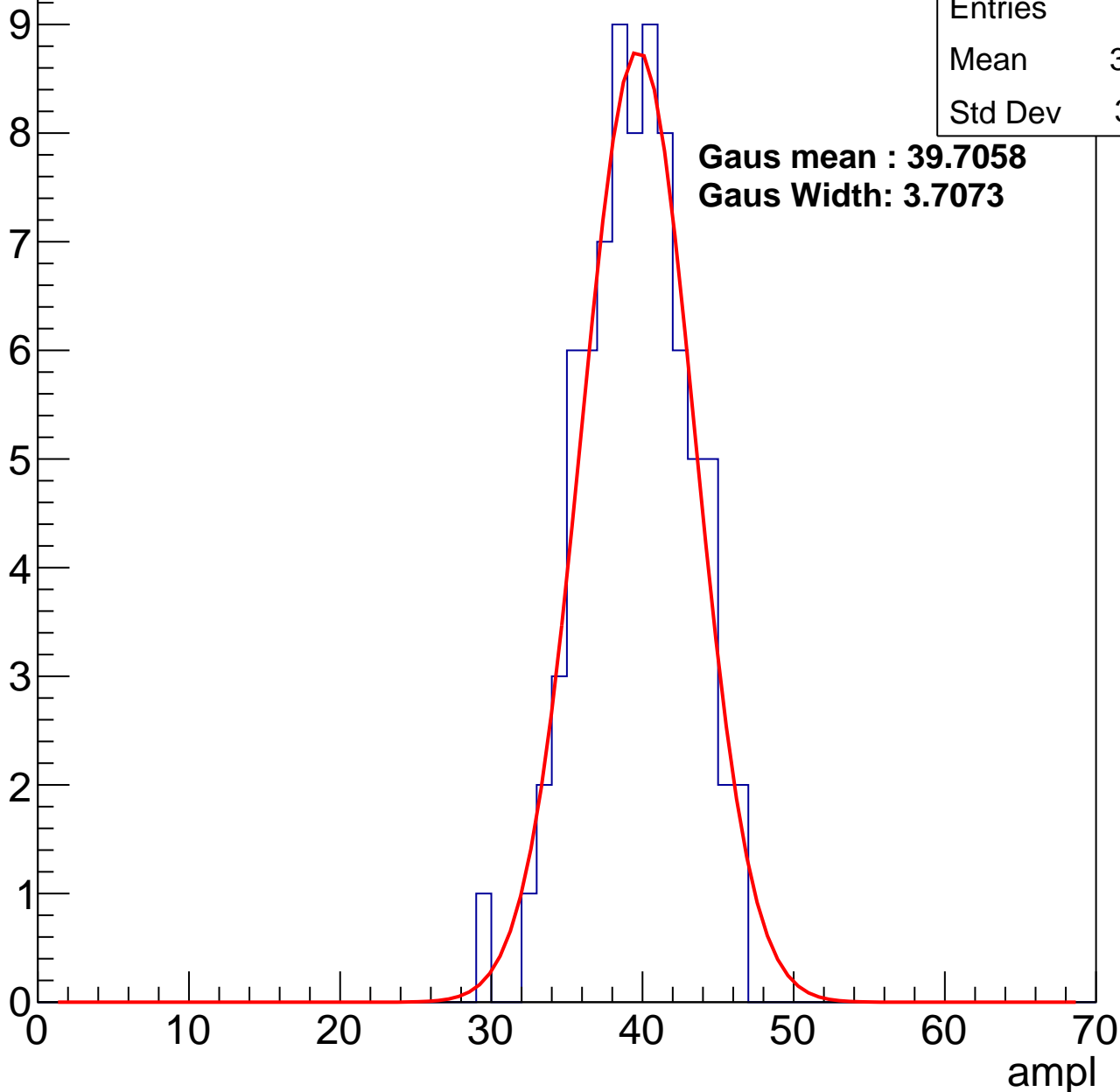
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 39.06 |
| Std Dev | 3.451 |

Gaus mean : 39.7058

Gaus Width: 3.7073



B1L100S, U7-ch72, adc2

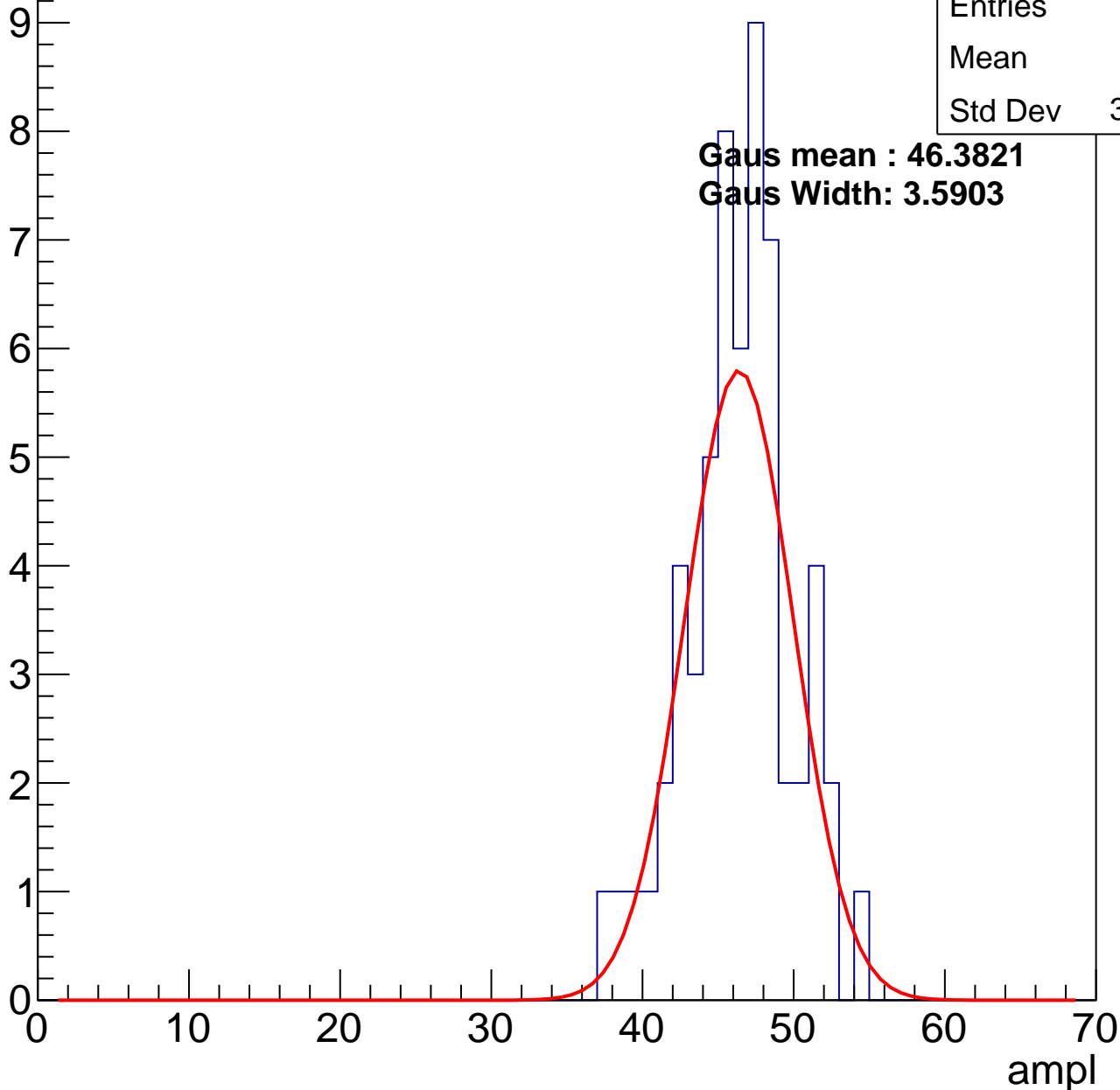
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 45.9 |
| Std Dev | 3.497 |

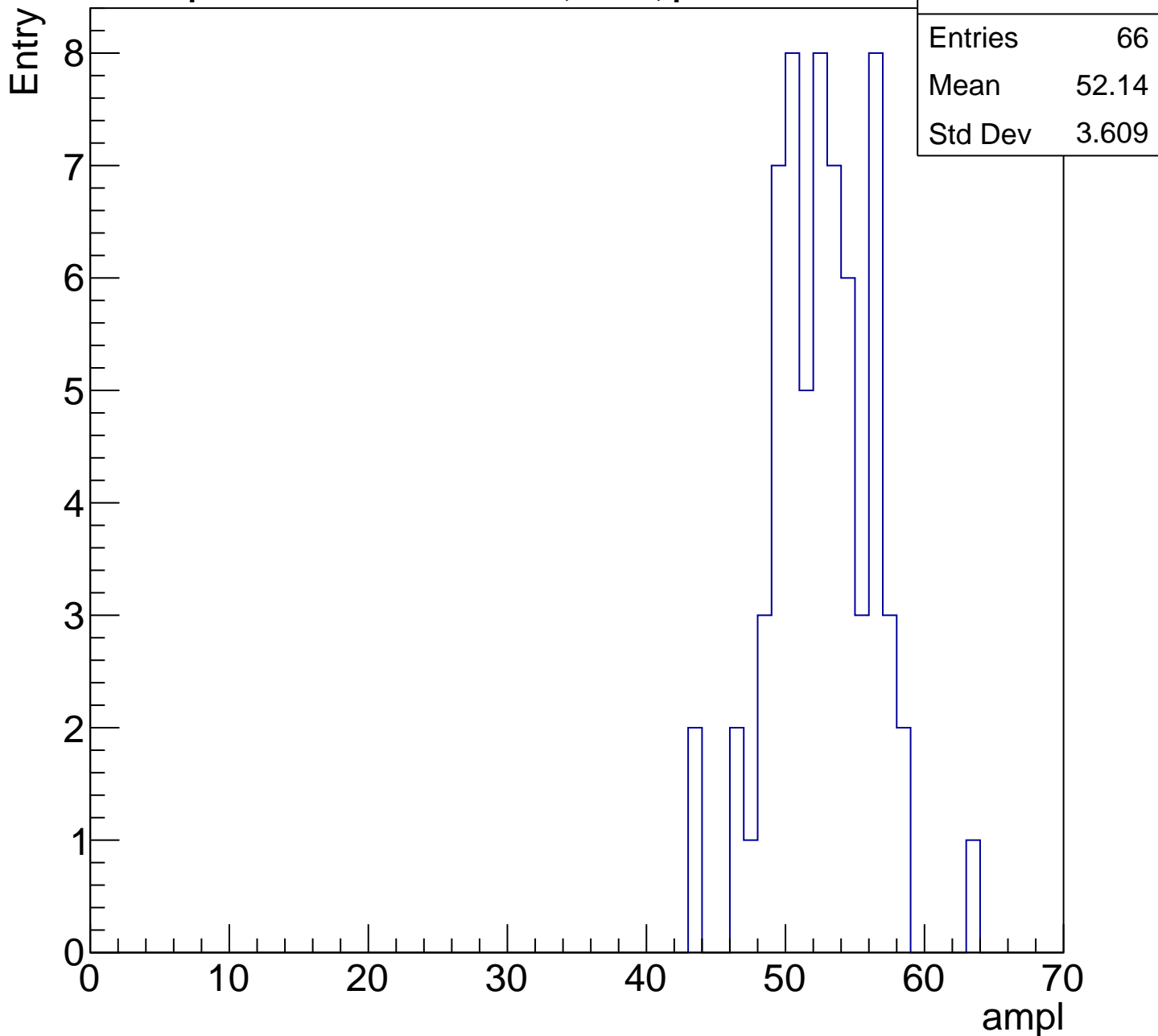
Gaus mean : 46.3821

Gaus Width: 3.5903



B1L100S, U7-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.37 |
| Std Dev | 2.96 |

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

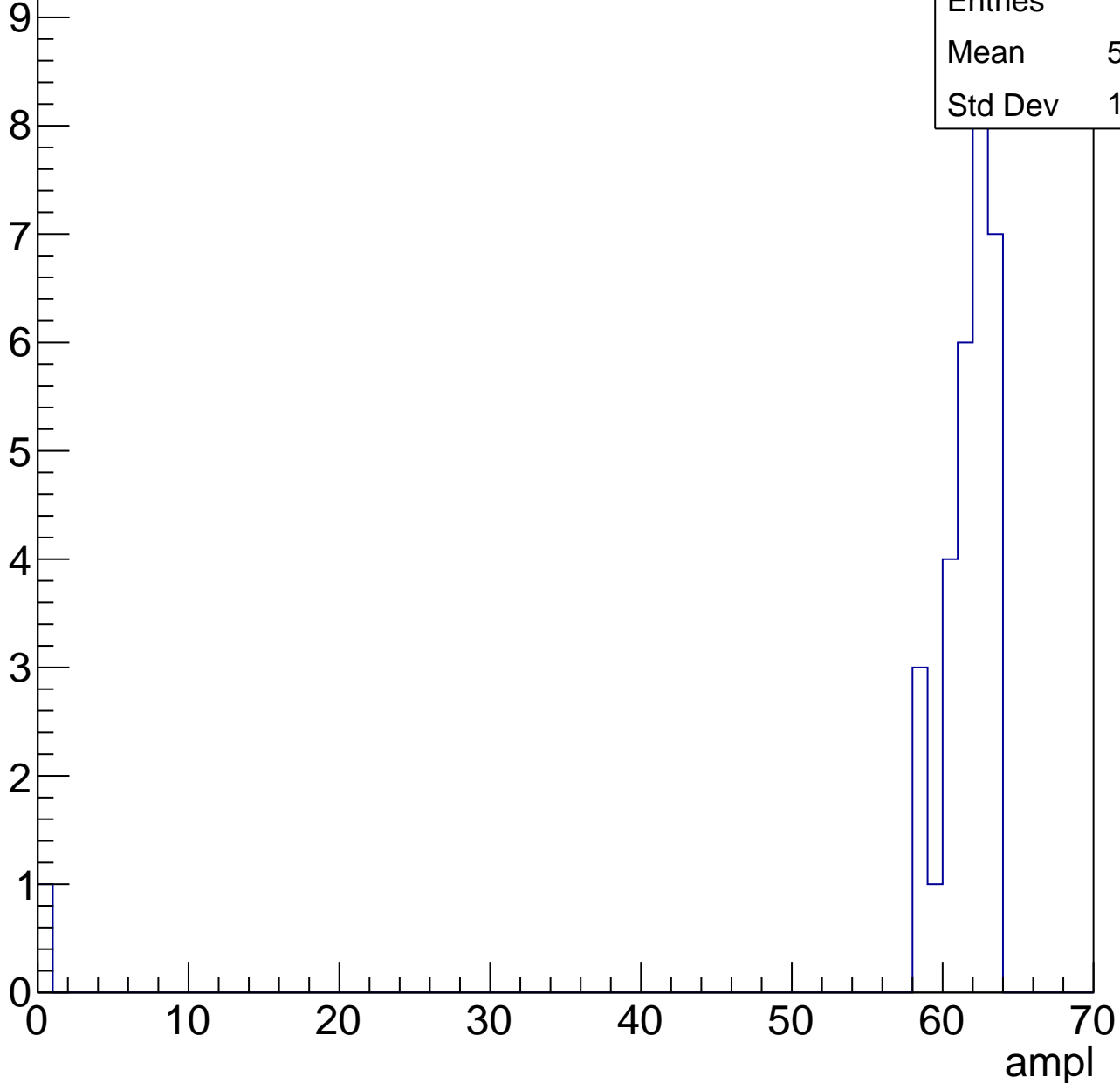
9

B1L100S, U7-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 31 |
| Mean | 59.29 |
| Std Dev | 10.93 |



B1L100S, U7-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|------|
| Entries | 2 |
| Mean | 59.5 |
| Std Dev | 2.5 |

B1L100S, U7-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch73, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 29.86 |
| Std Dev | 4.651 |

Gaus mean : 30.8079

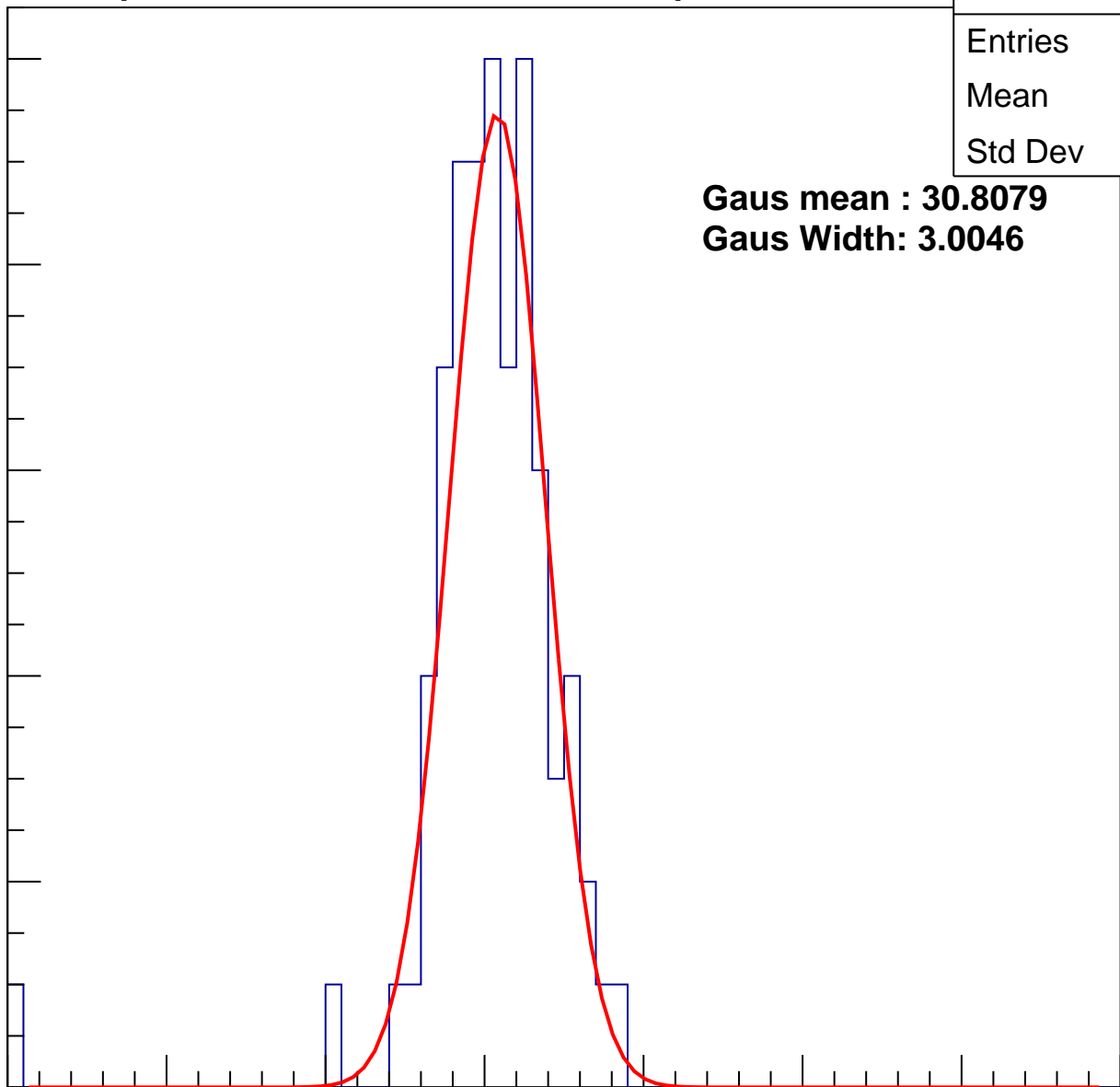
Gaus Width: 3.0046

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch73, adc1

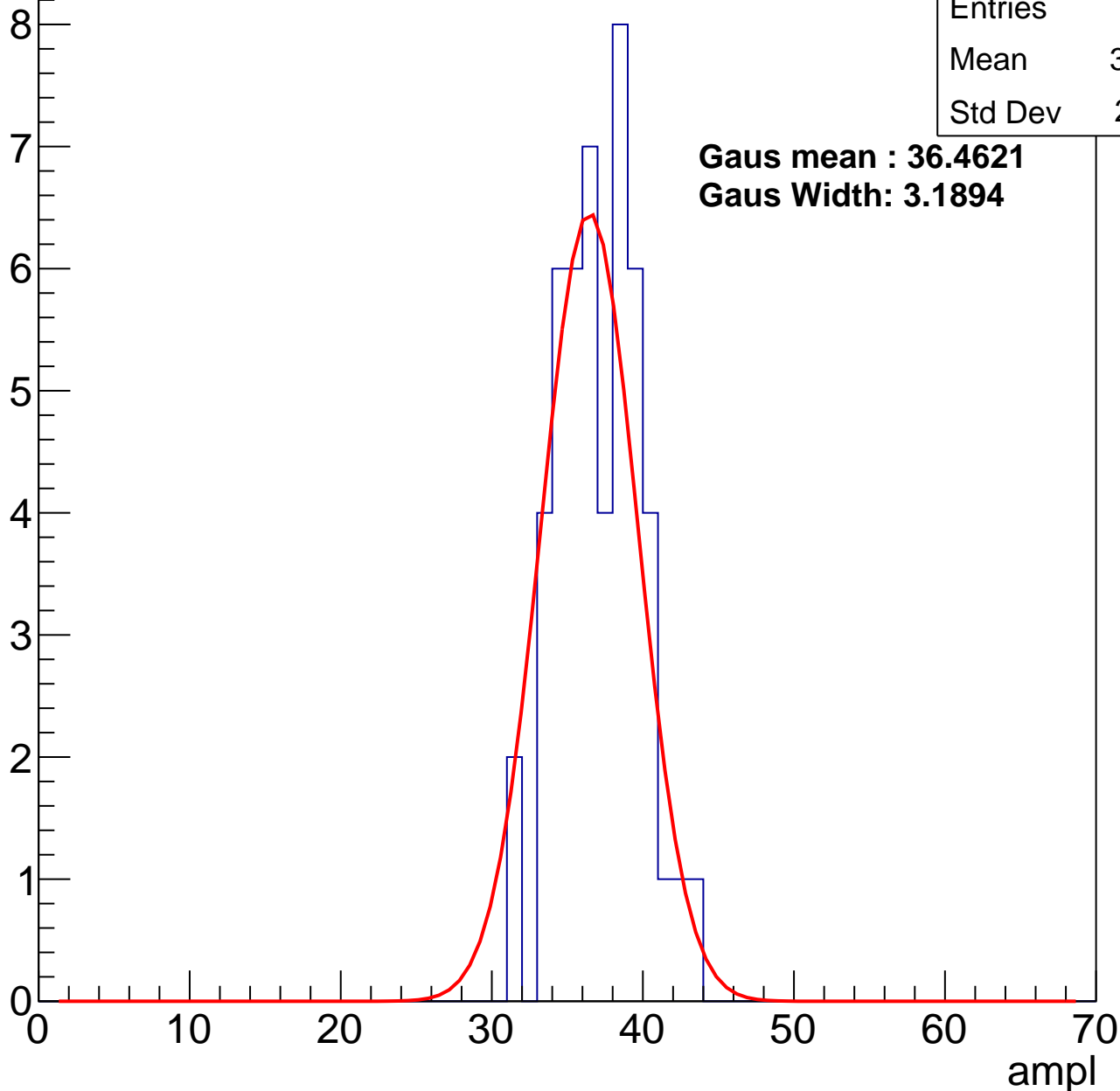
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 36.64 |
| Std Dev | 2.681 |

Gaus mean : 36.4621

Gaus Width: 3.1894



B1L100S, U7-ch73, adc2

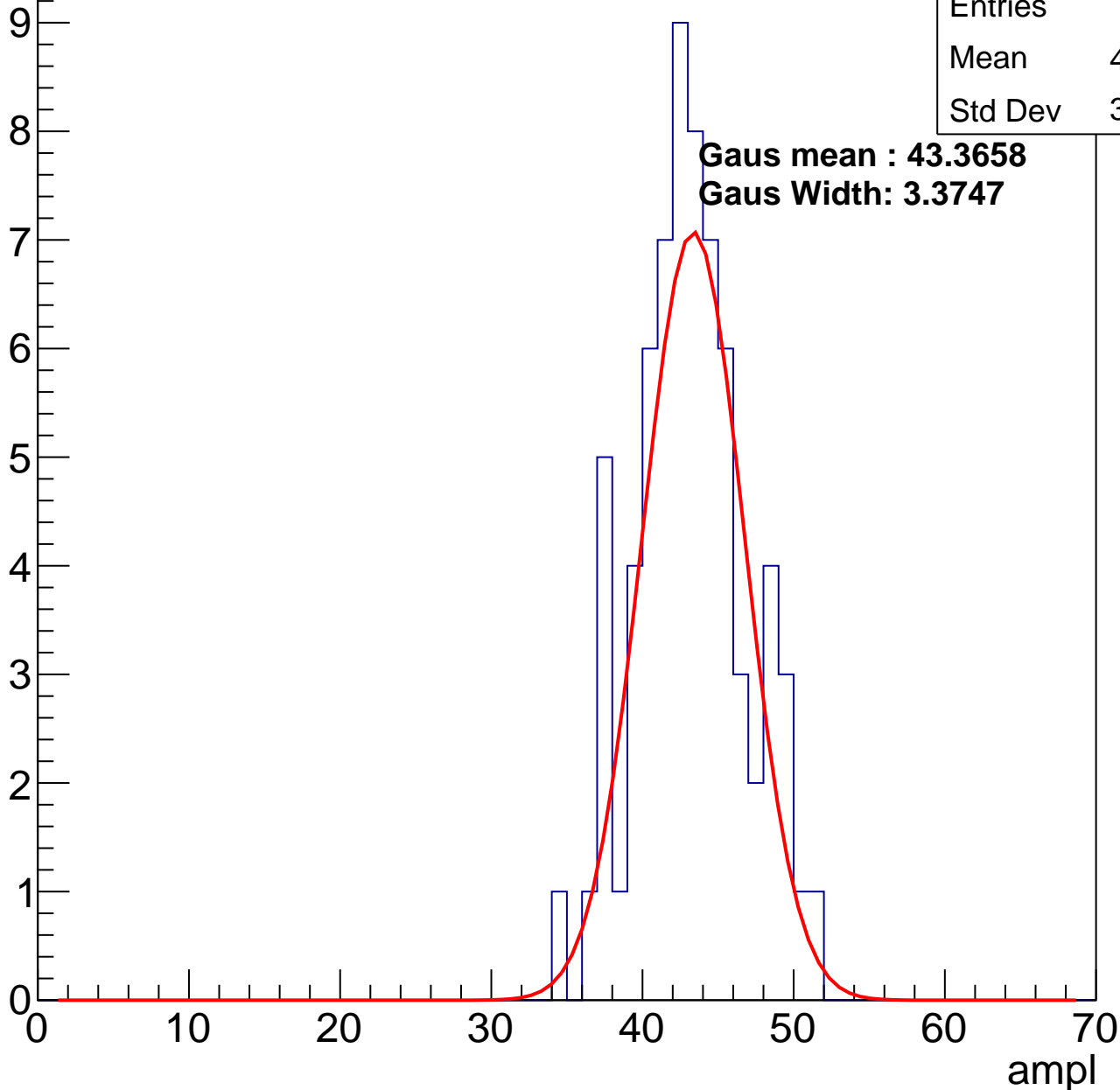
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 42.72 |
| Std Dev | 3.603 |

Gaus mean : 43.3658

Gaus Width: 3.3747

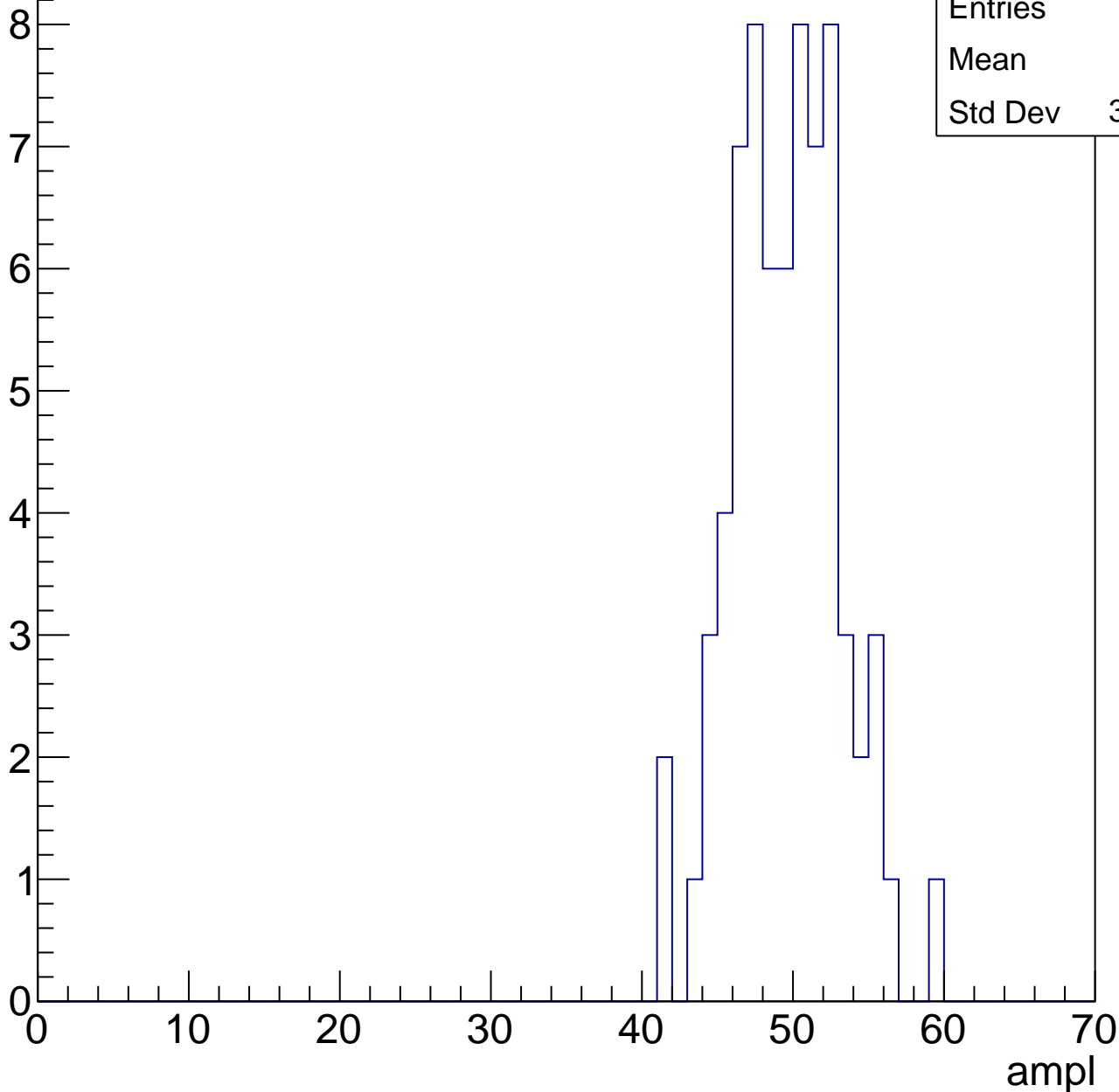


B1L100S, U7-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.1 |
| Std Dev | 3.518 |

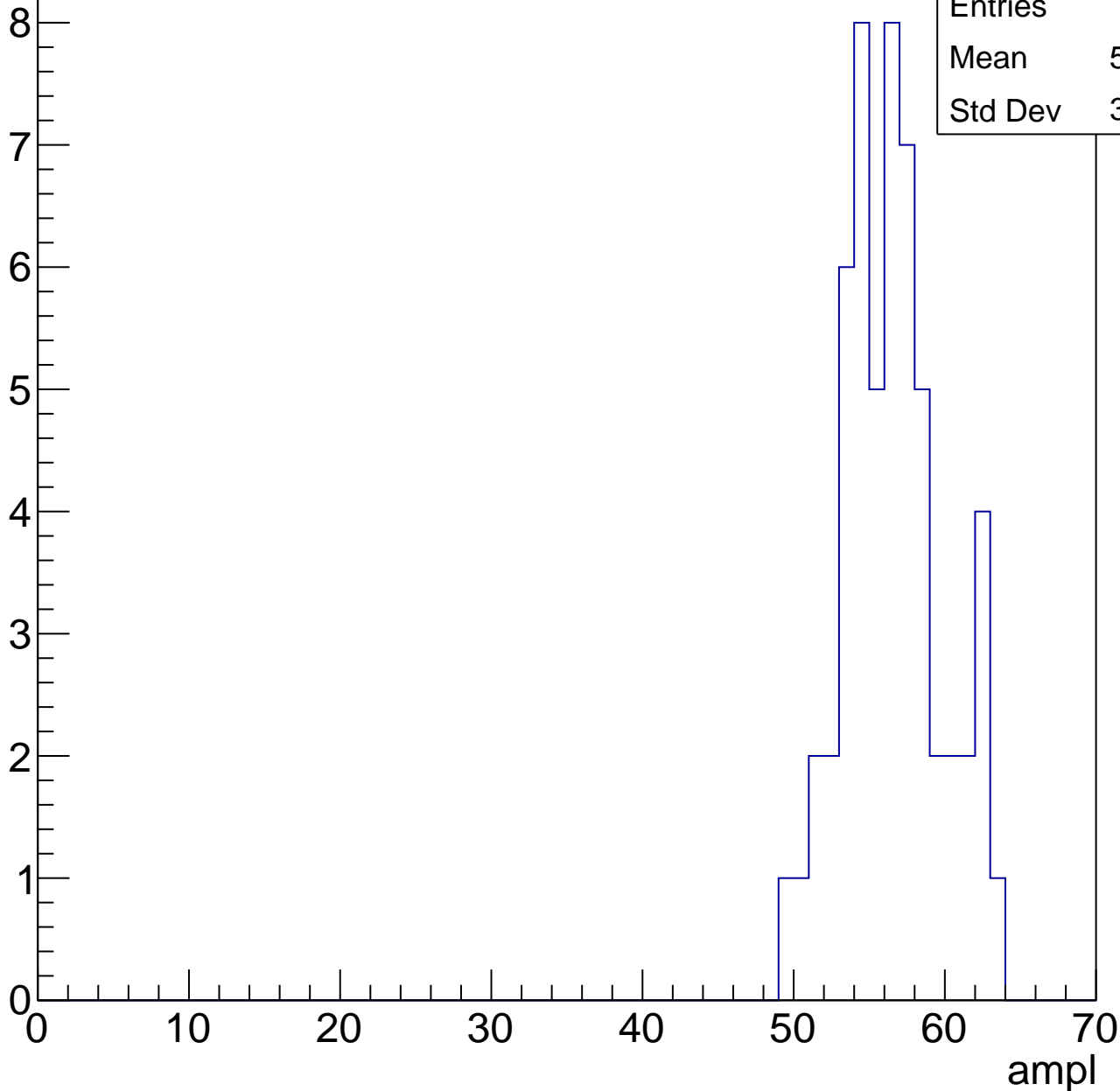


B1L100S, U7-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 56.04 |
| Std Dev | 3.212 |



B1L100S, U7-ch73, adc5

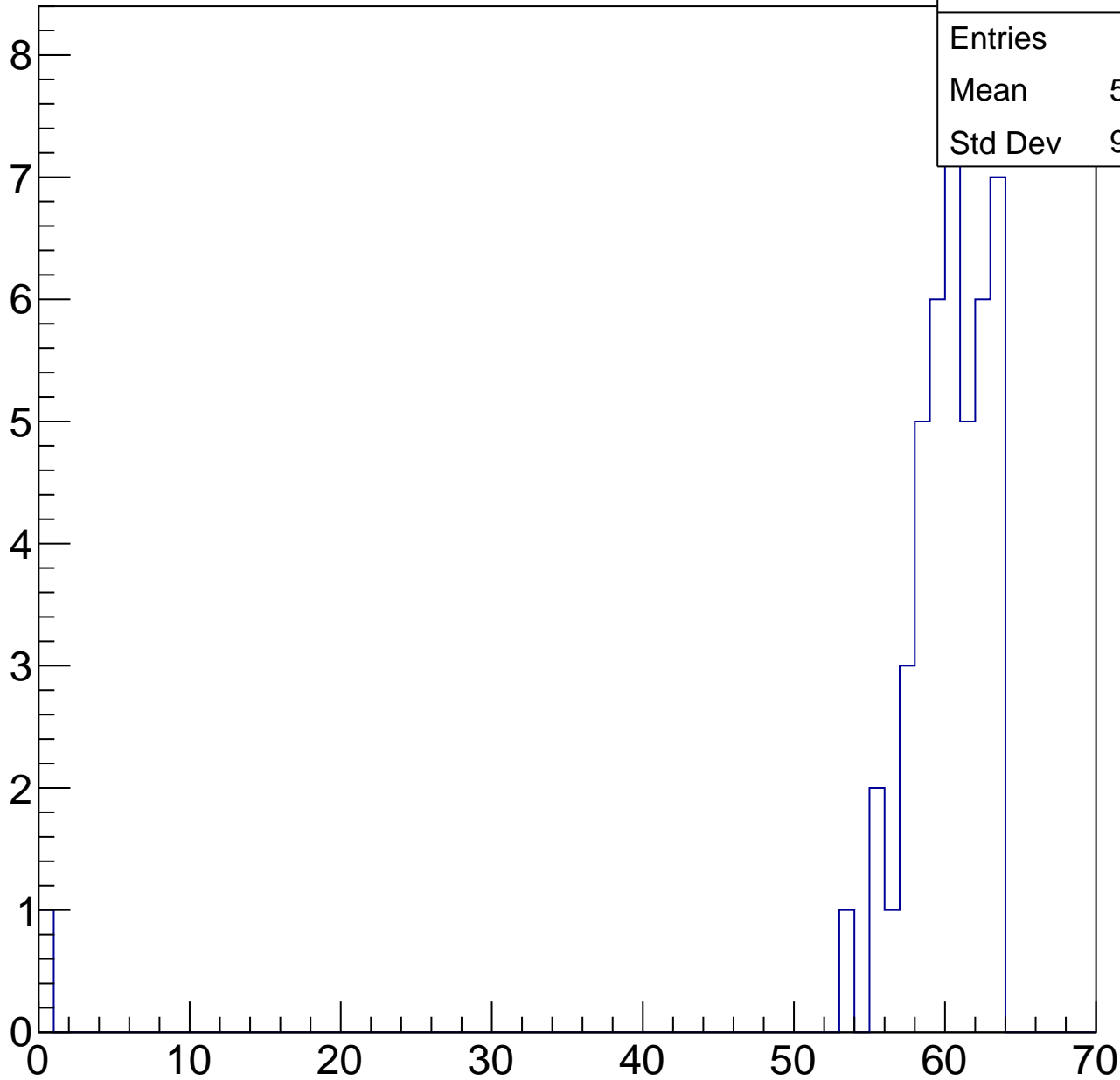
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.49 |
| Std Dev | 9.138 |

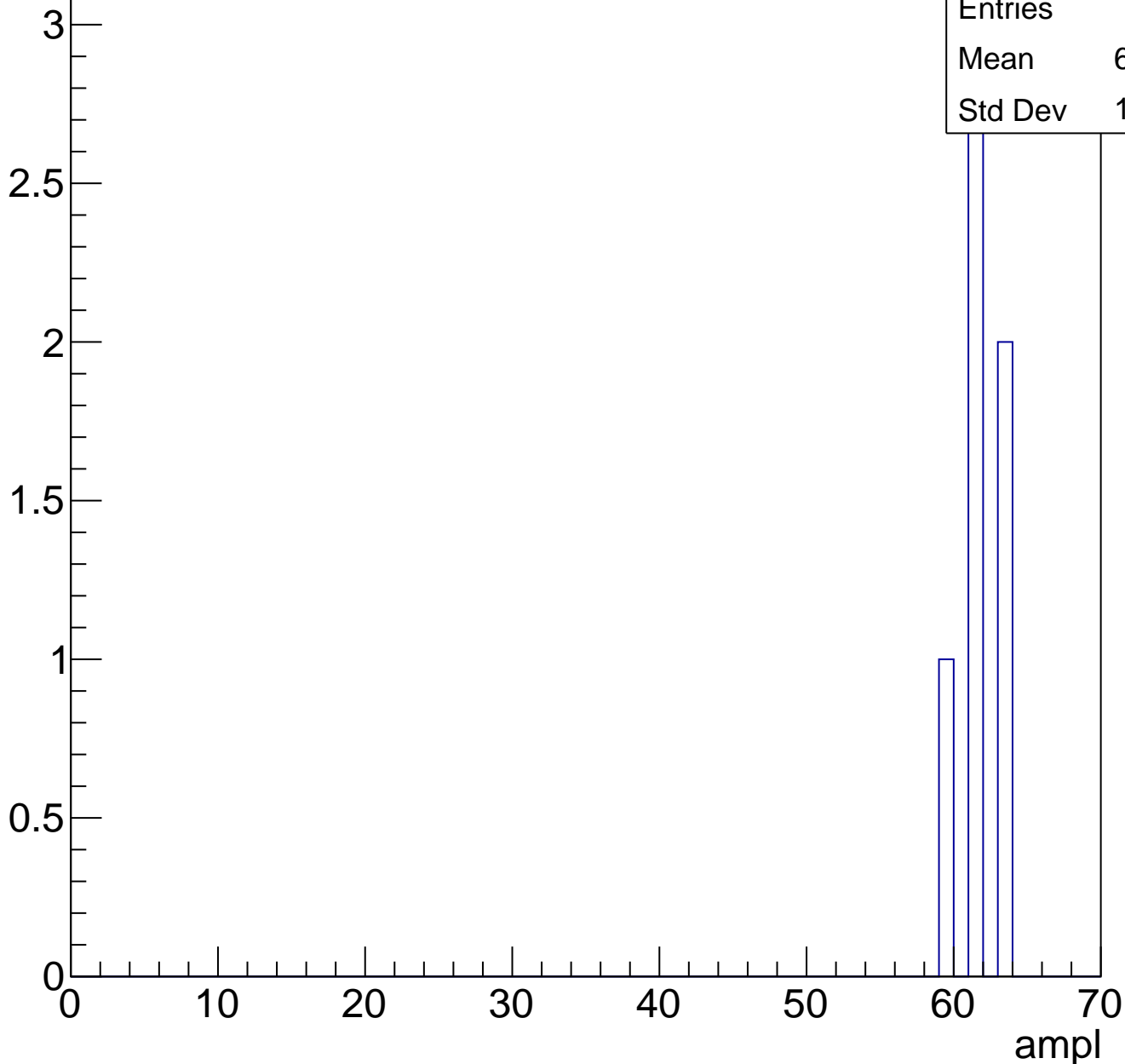
ampl



B1L100S, U7-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch74, adc0

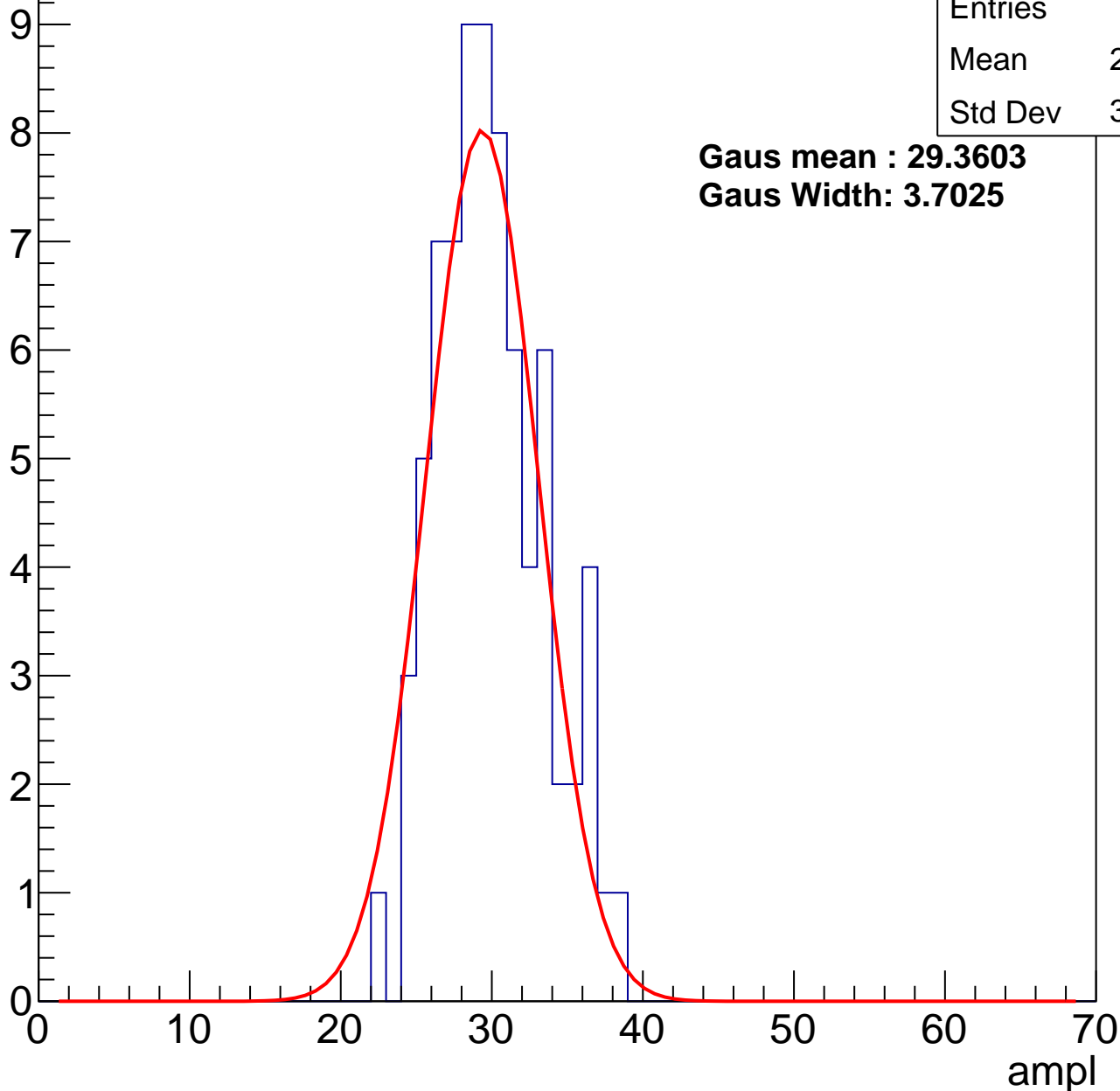
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.49 |
| Std Dev | 3.489 |

Gaus mean : 29.3603

Gaus Width: 3.7025



B1L100S, U7-ch74, adc1

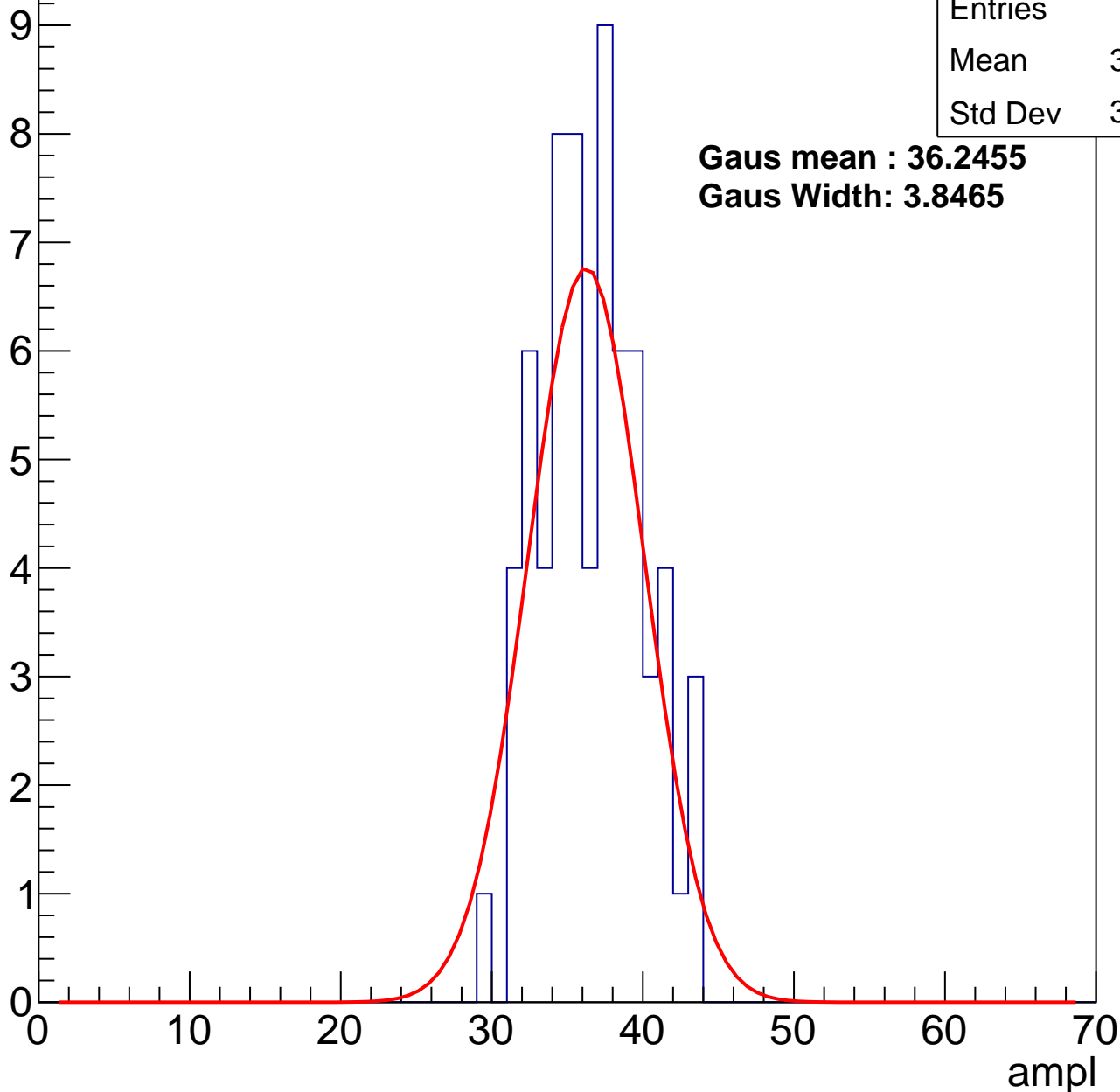
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.16 |
| Std Dev | 3.317 |

Gaus mean : 36.2455

Gaus Width: 3.8465



B1L100S, U7-ch74, adc2

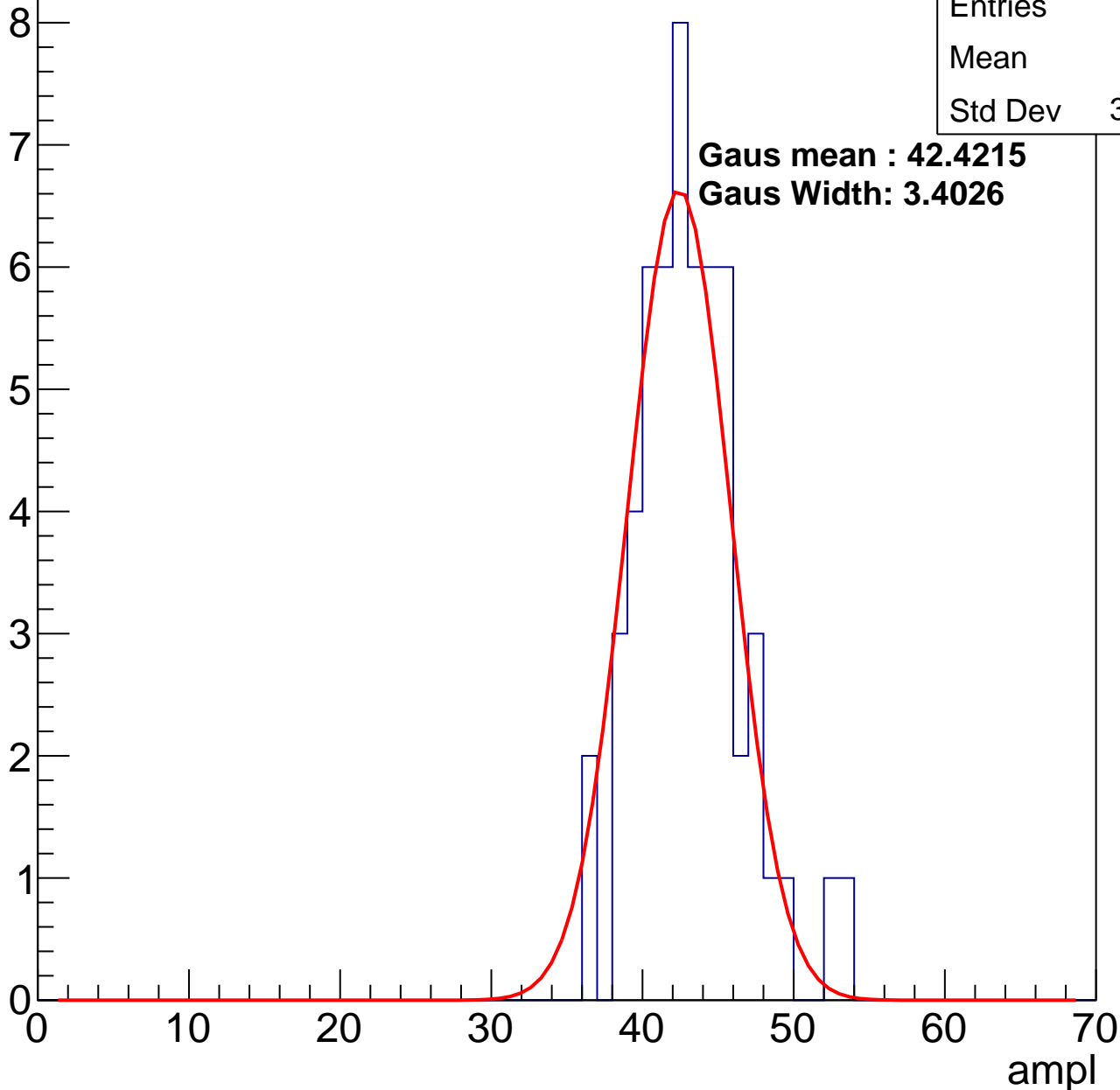
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 42.7 |
| Std Dev | 3.422 |

Gaus mean : 42.4215

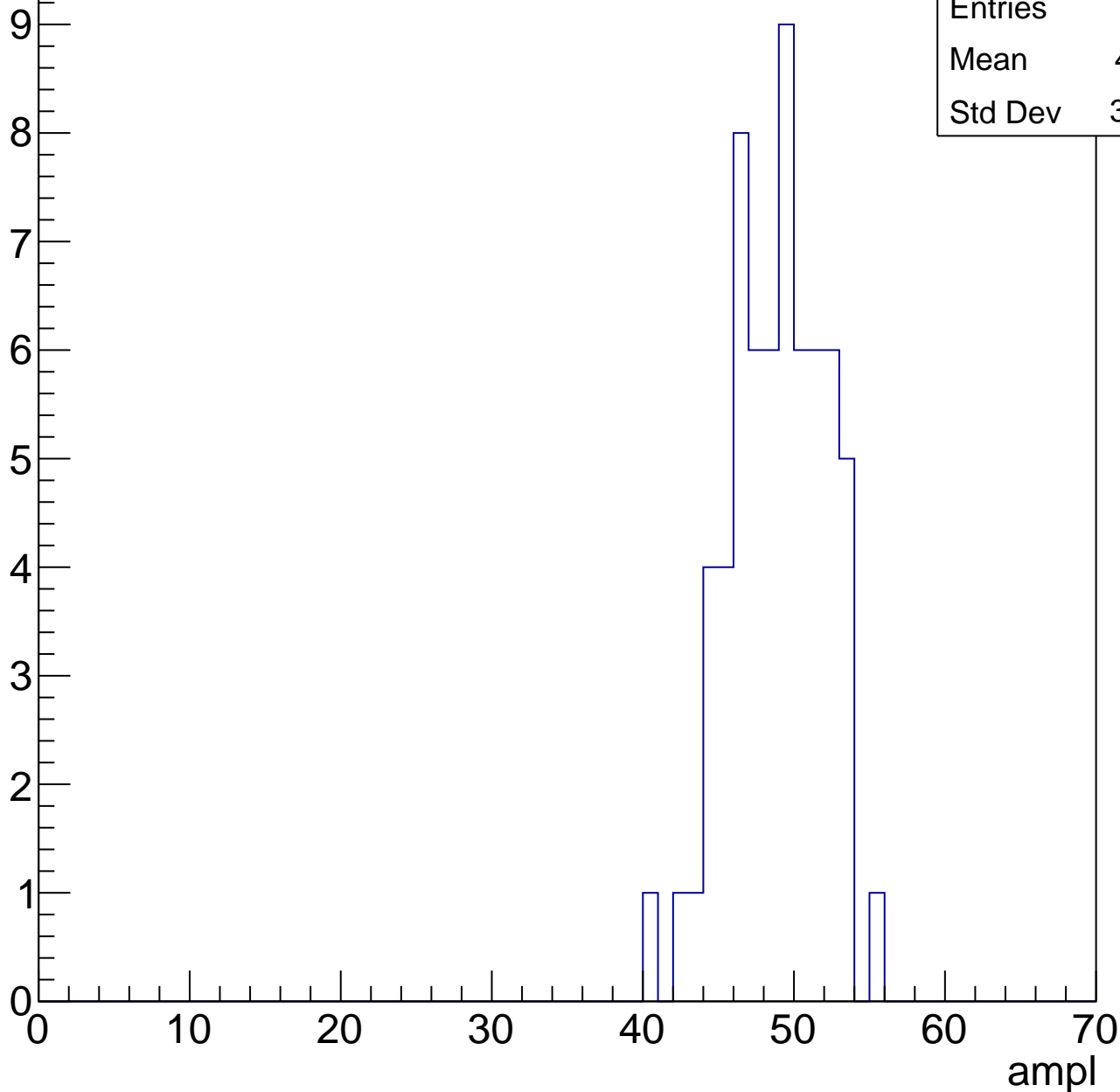
Gaus Width: 3.4026



B1L100S, U7-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



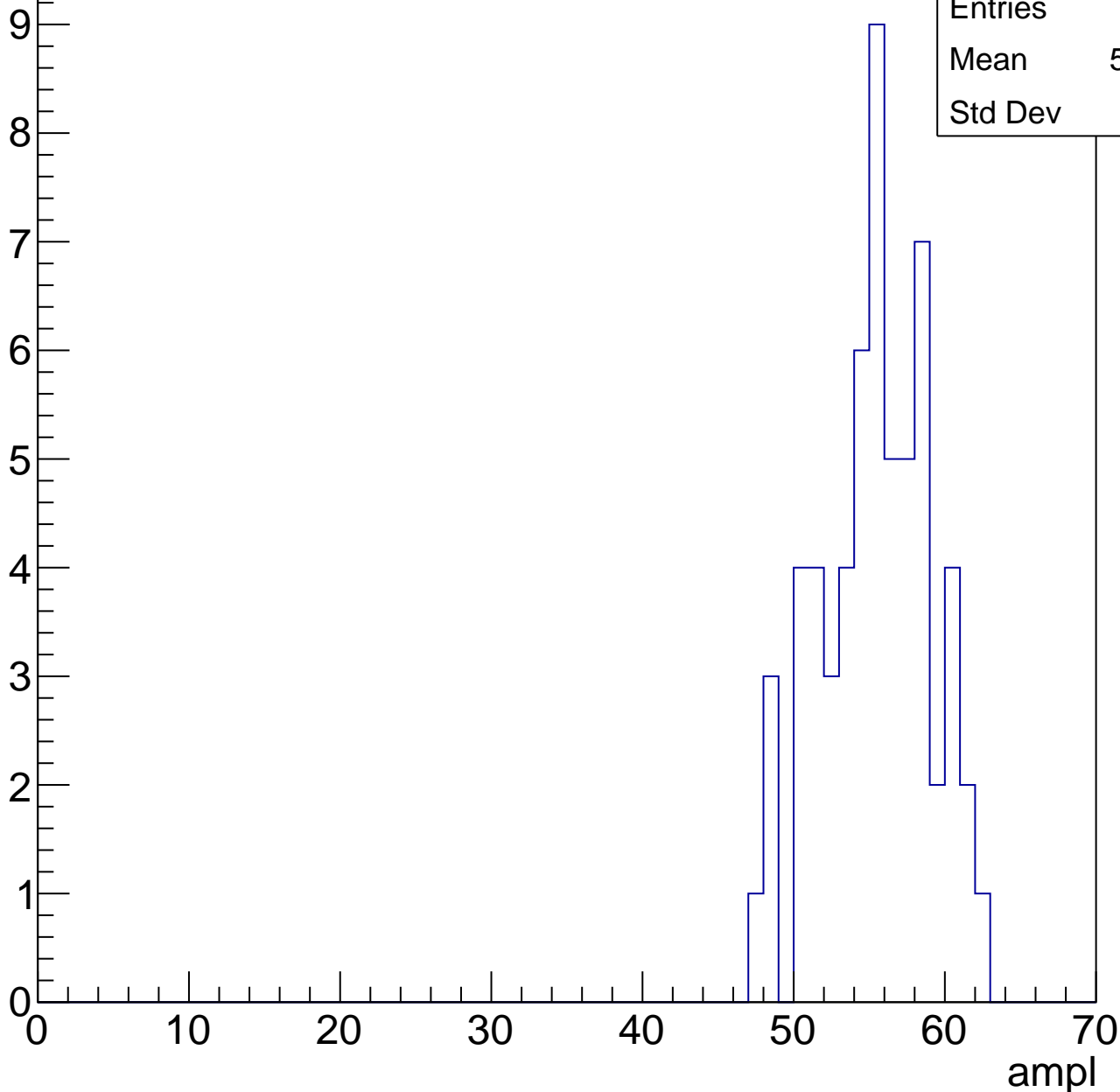
| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.41 |
| Std Dev | 3.086 |

B1L100S, U7-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

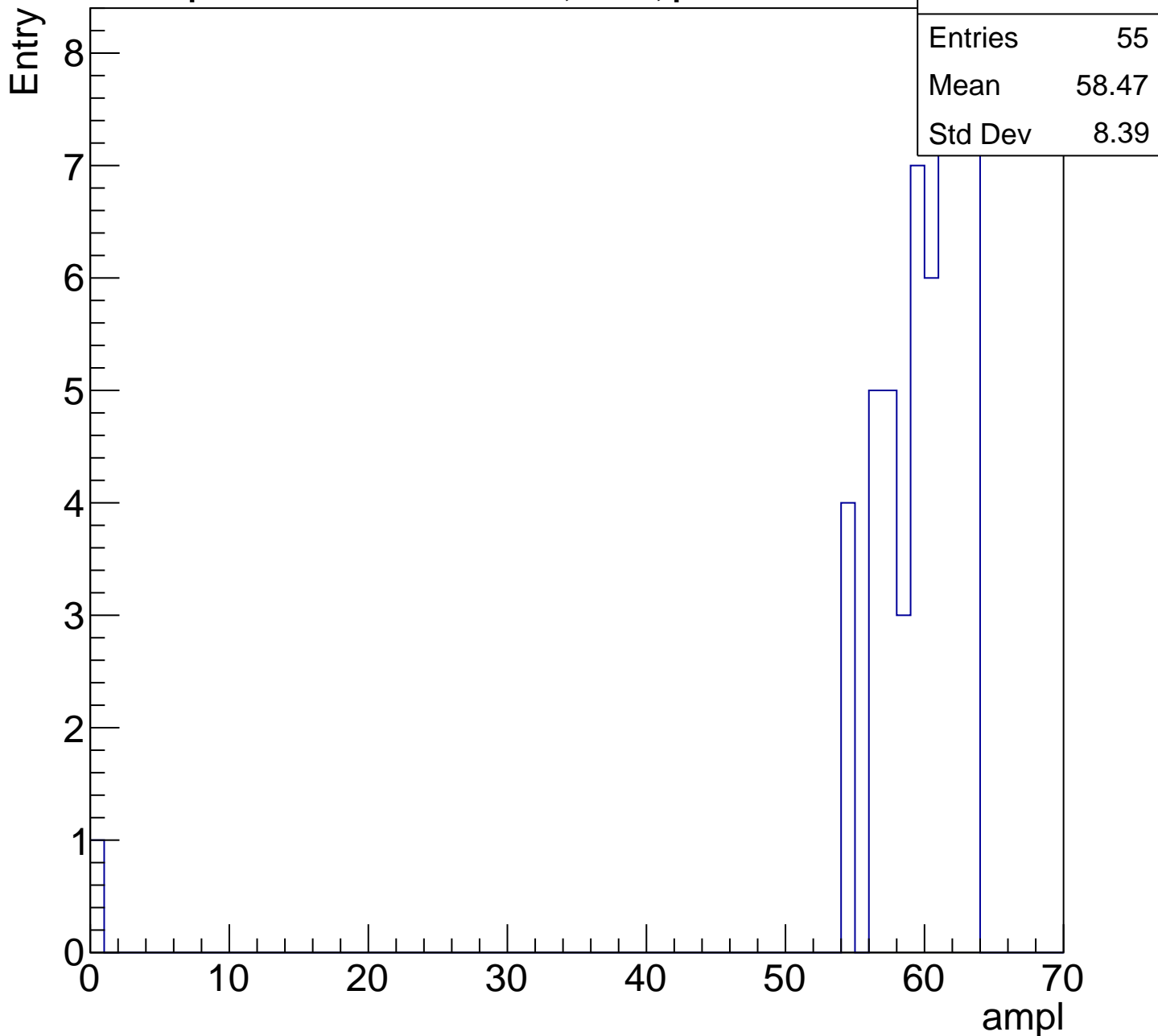
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 54.92 |
| Std Dev | 3.57 |



B1L100S, U7-ch74, adc5

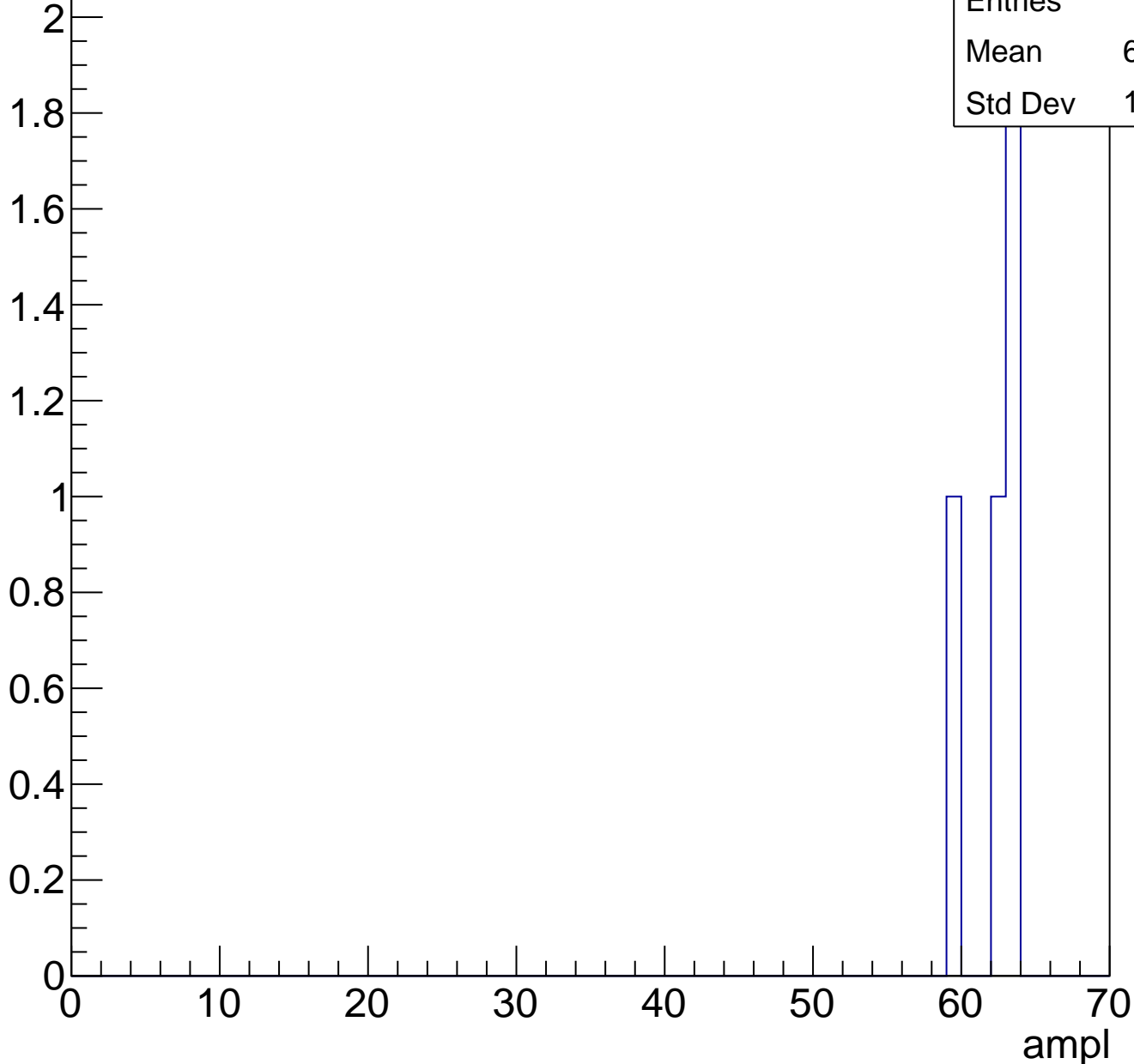
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch75, adc0

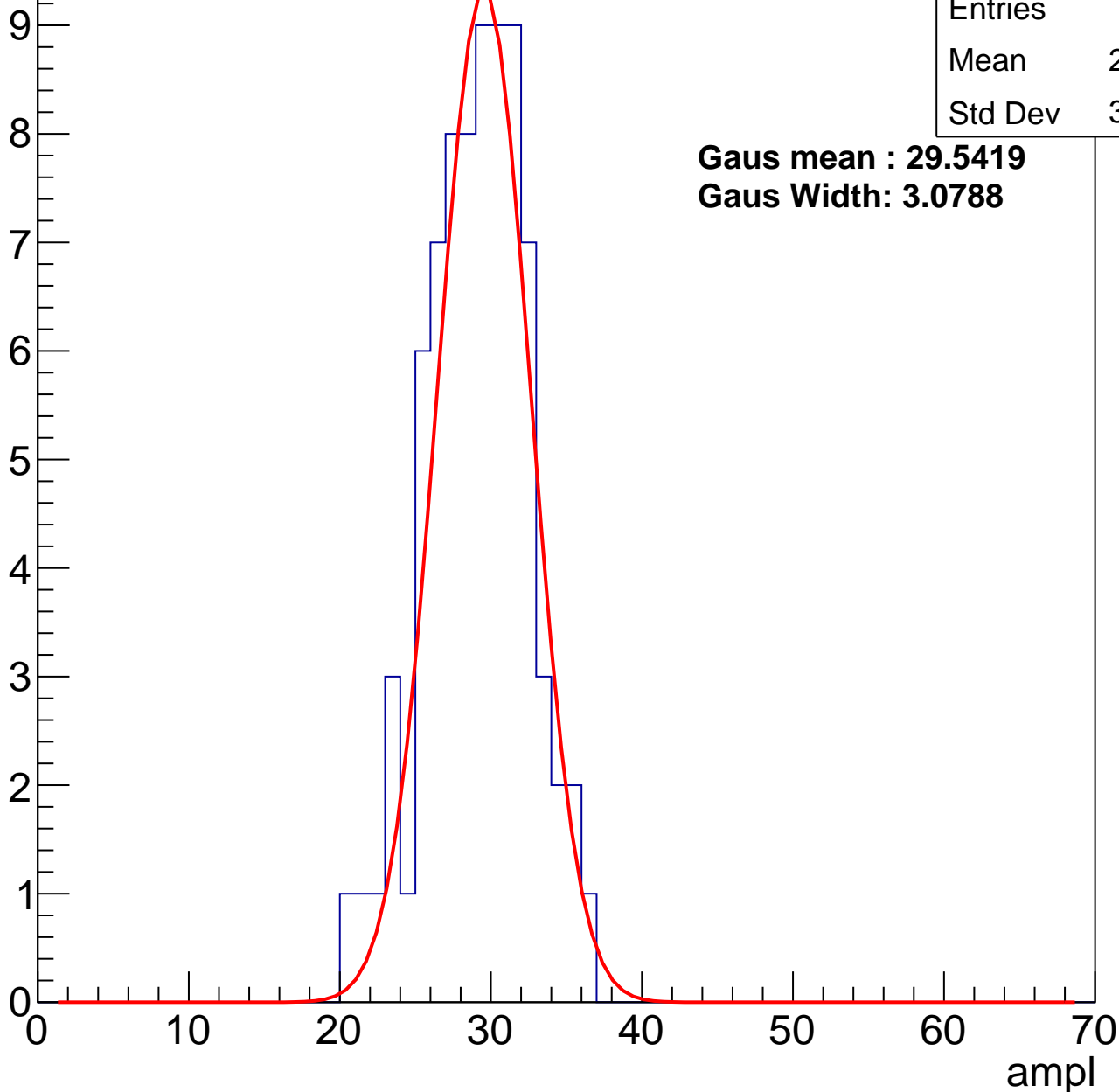
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 28.65 |
| Std Dev | 3.289 |

Gaus mean : 29.5419

Gaus Width: 3.0788



B1L100S, U7-ch75, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 84 |
| Mean | 37.31 |
| Std Dev | 3.366 |

Gaus mean : 37.7962

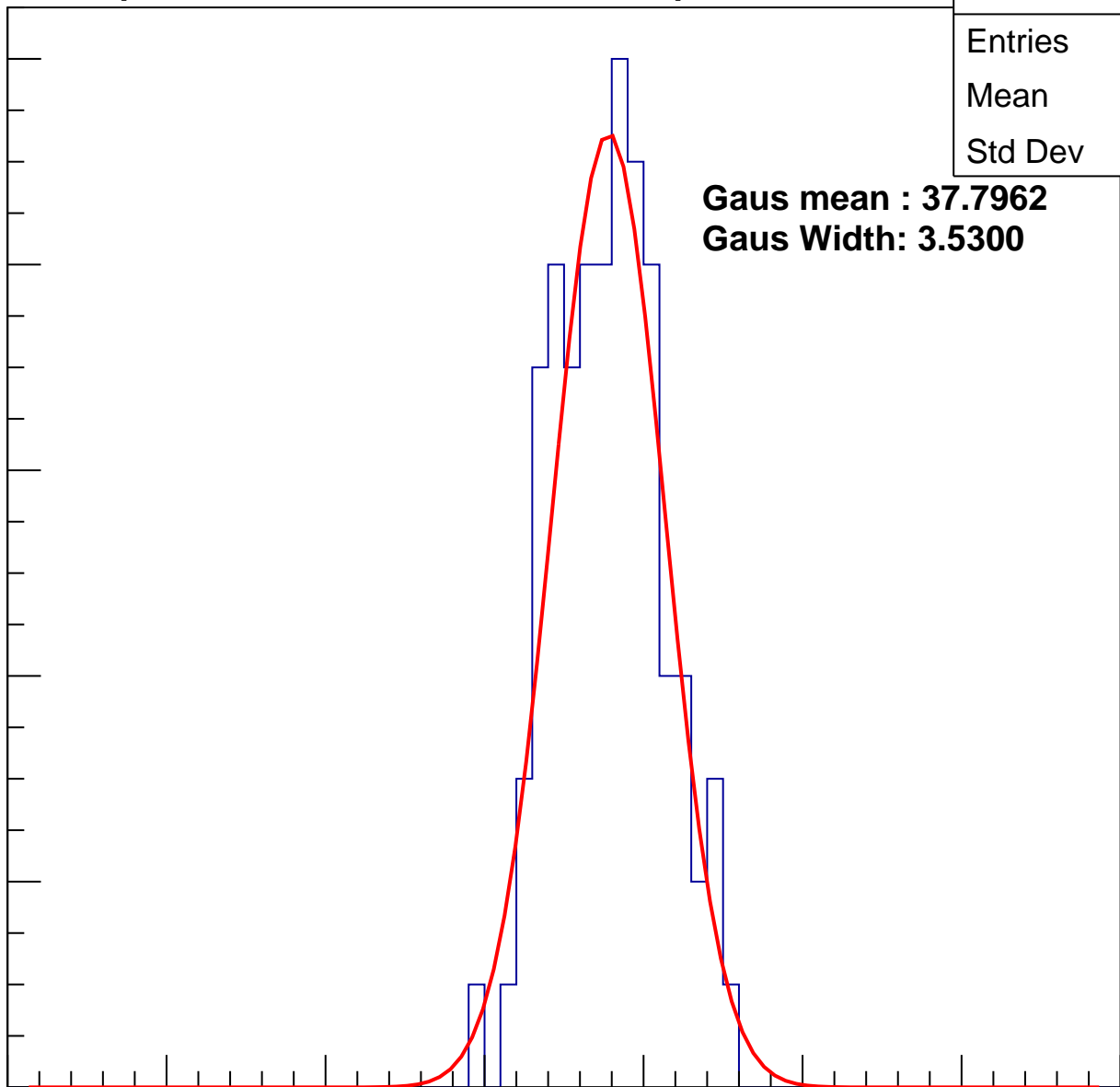
Gaus Width: 3.5300

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch75, adc2

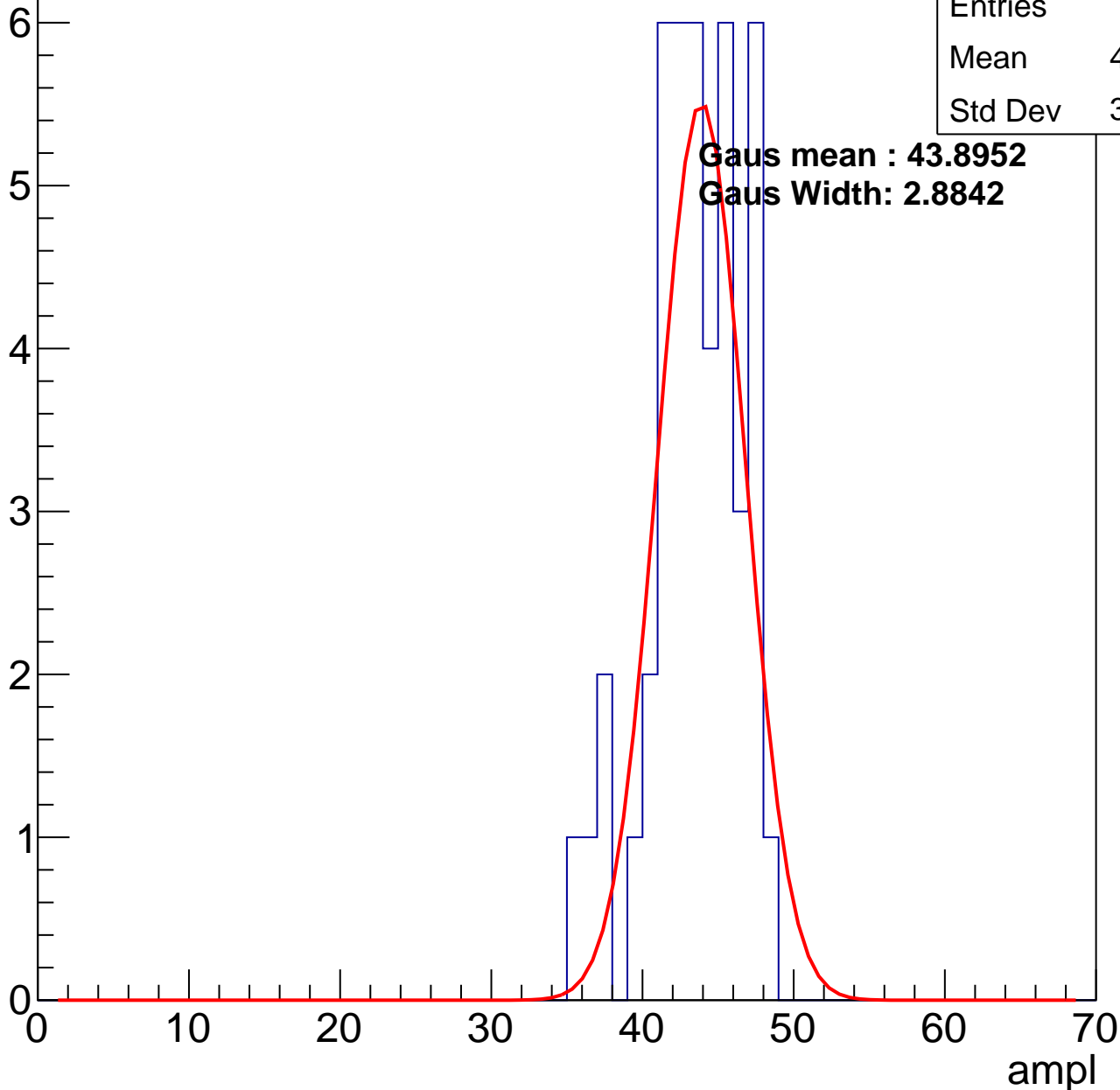
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 42.98 |
| Std Dev | 3.073 |

Gaus mean : 43.8952

Gaus Width: 2.8842



B1L100S, U7-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

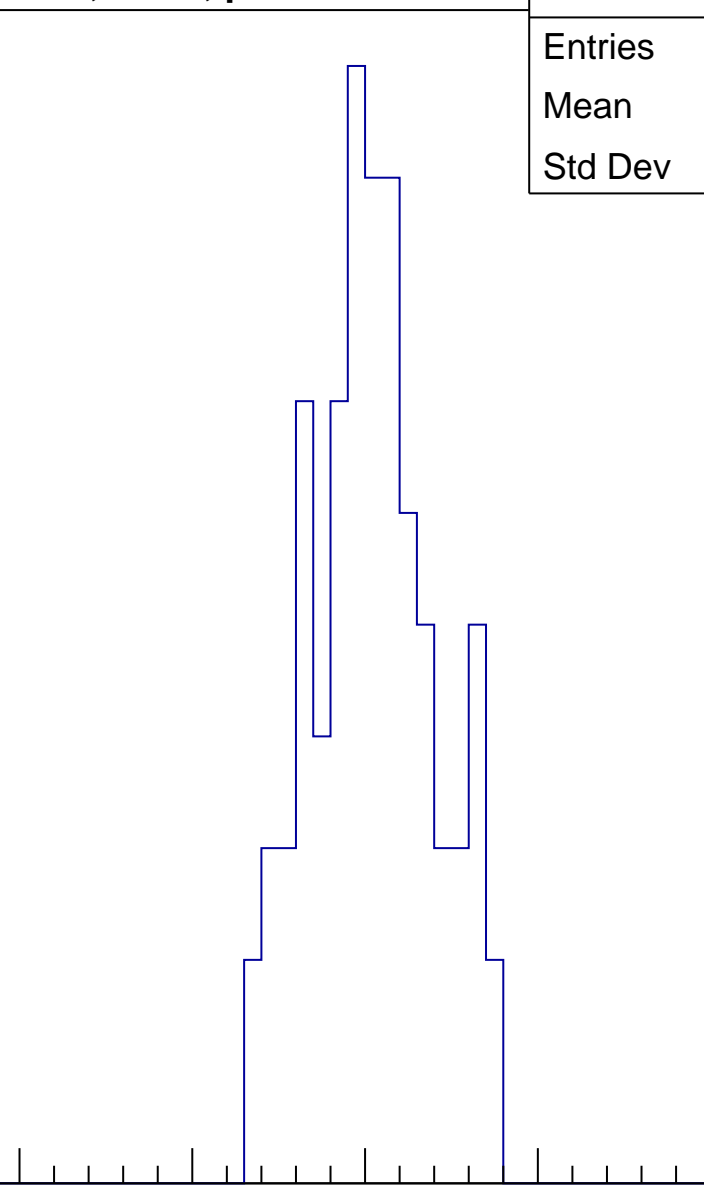
| | |
|---------|-------|
| Entries | 78 |
| Mean | 49.95 |
| Std Dev | 3.482 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

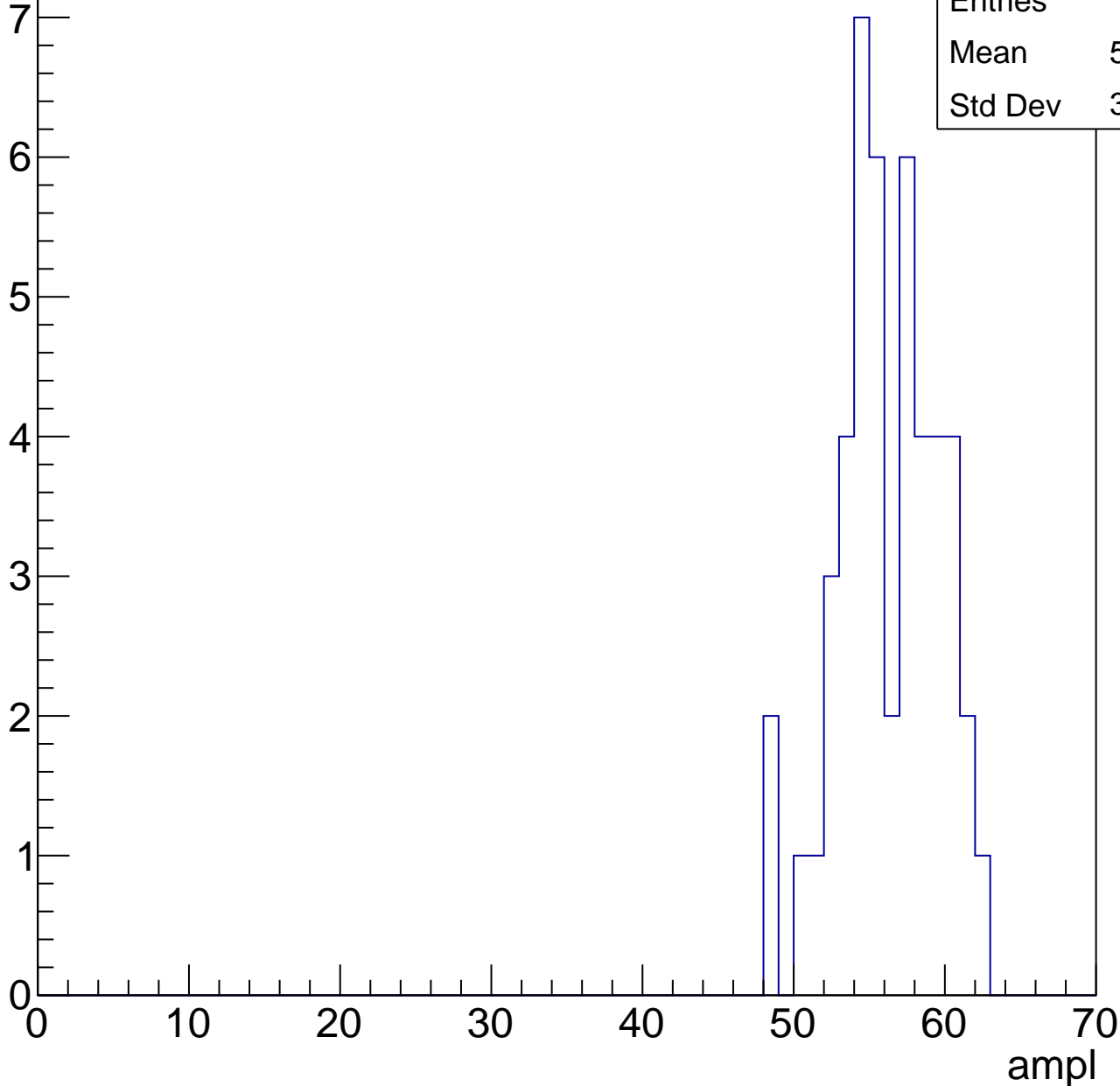


B1L100S, U7-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.72 |
| Std Dev | 3.299 |

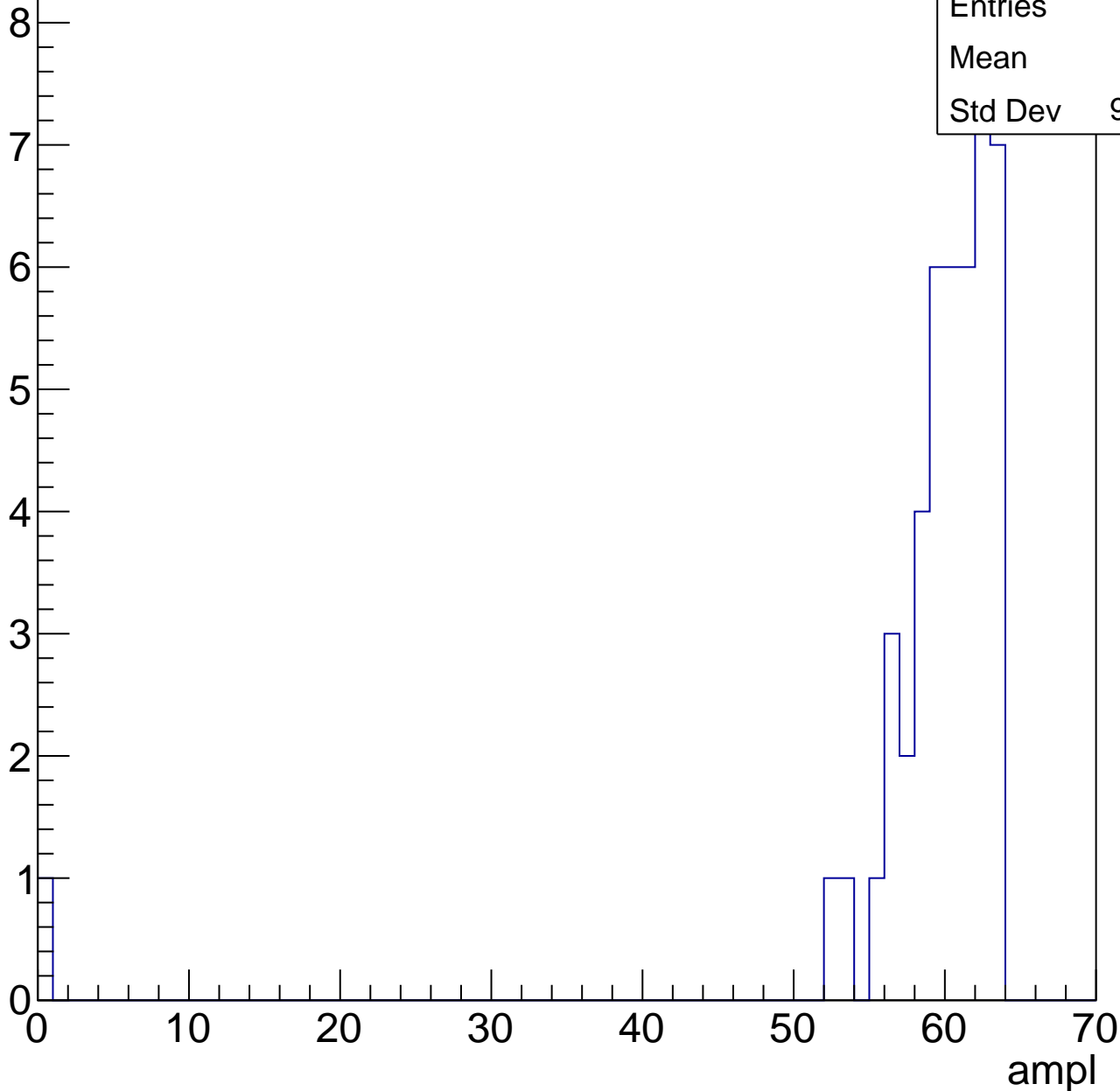


B1L100S, U7-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

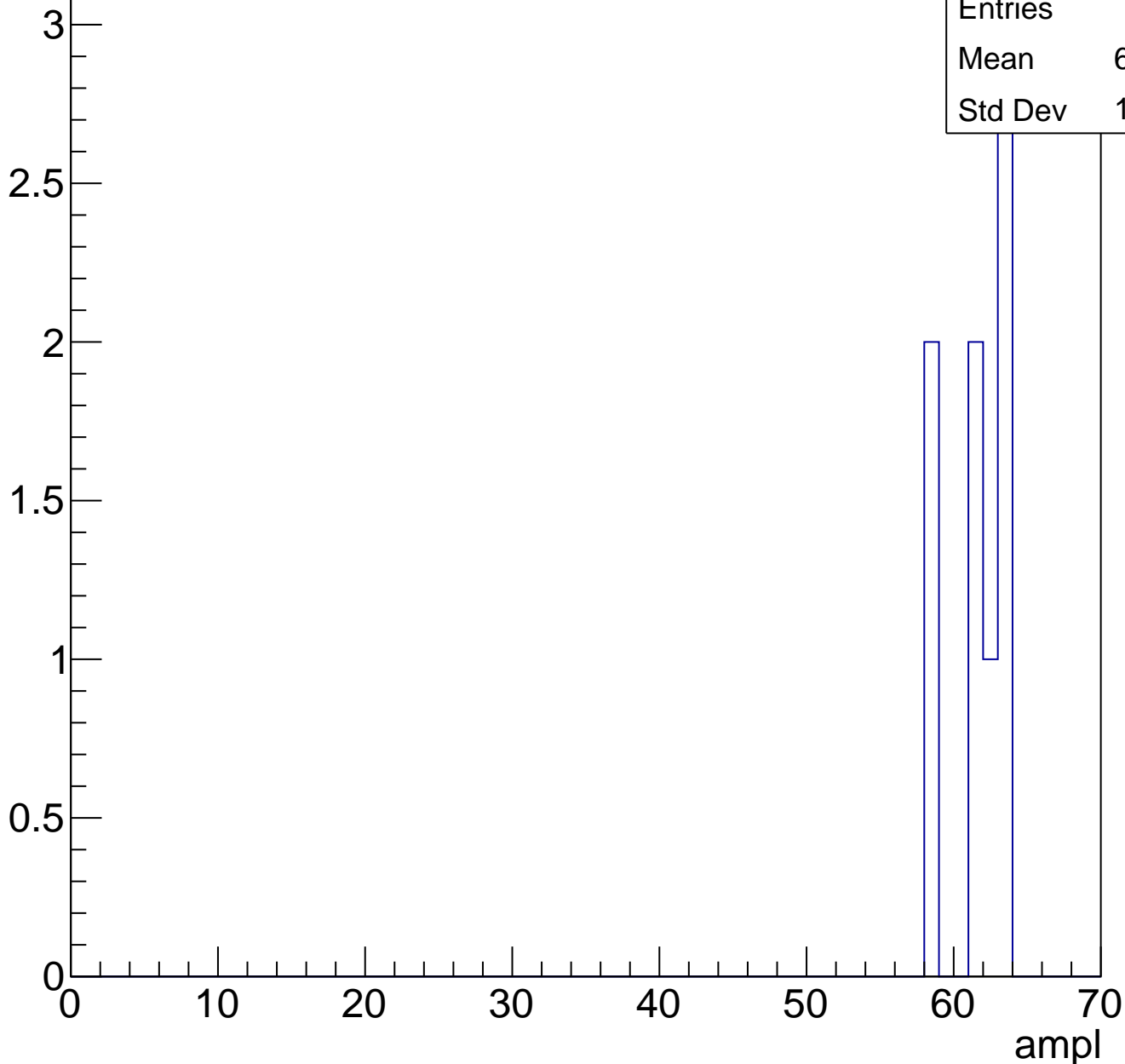
| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.5 |
| Std Dev | 9.117 |



B1L100S, U7-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch76, adc0

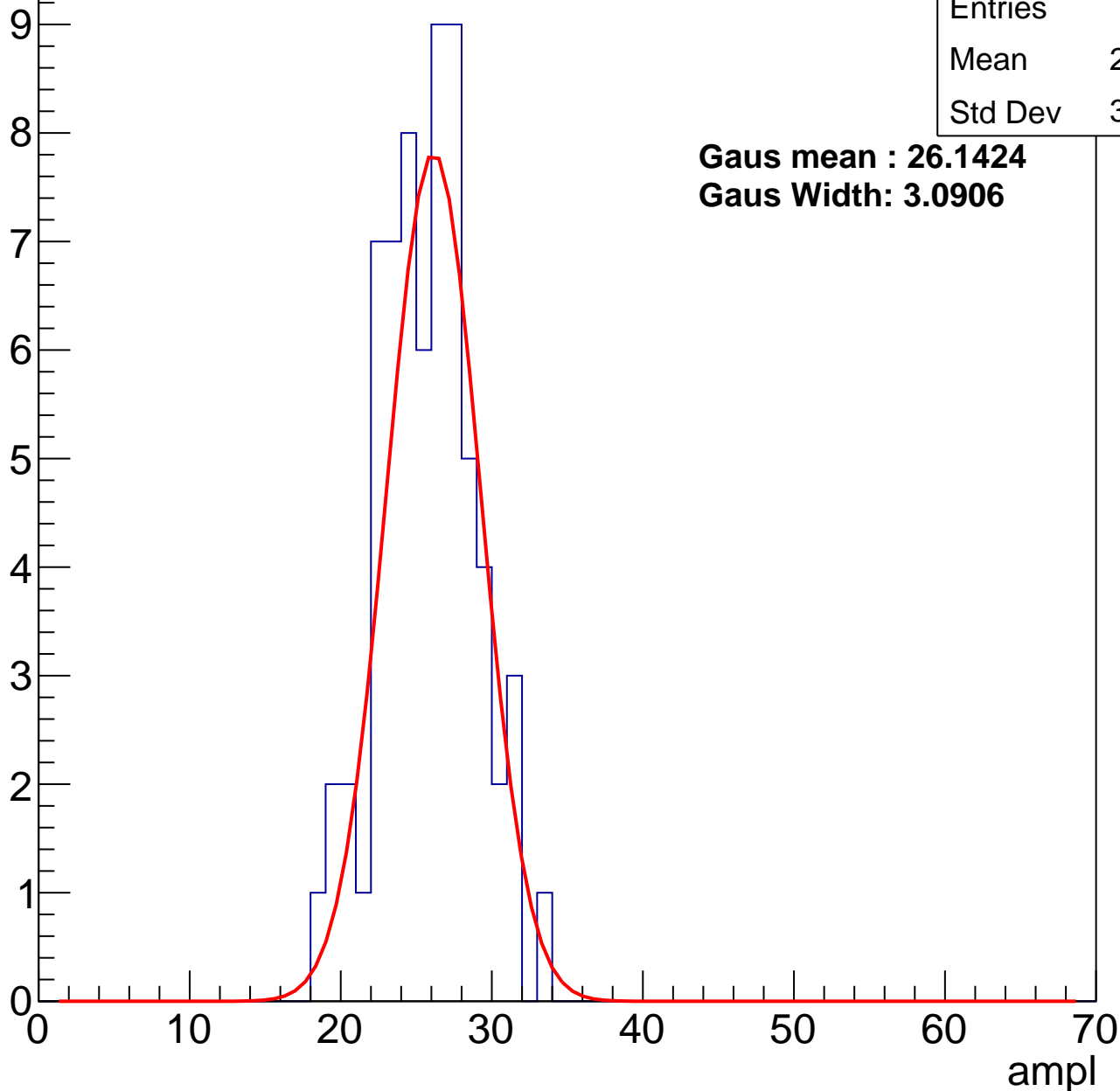
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 25.27 |
| Std Dev | 3.132 |

Gaus mean : 26.1424

Gaus Width: 3.0906



B1L100S, U7-ch76, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 32.32 |
| Std Dev | 3.255 |

Gaus mean : 32.0499

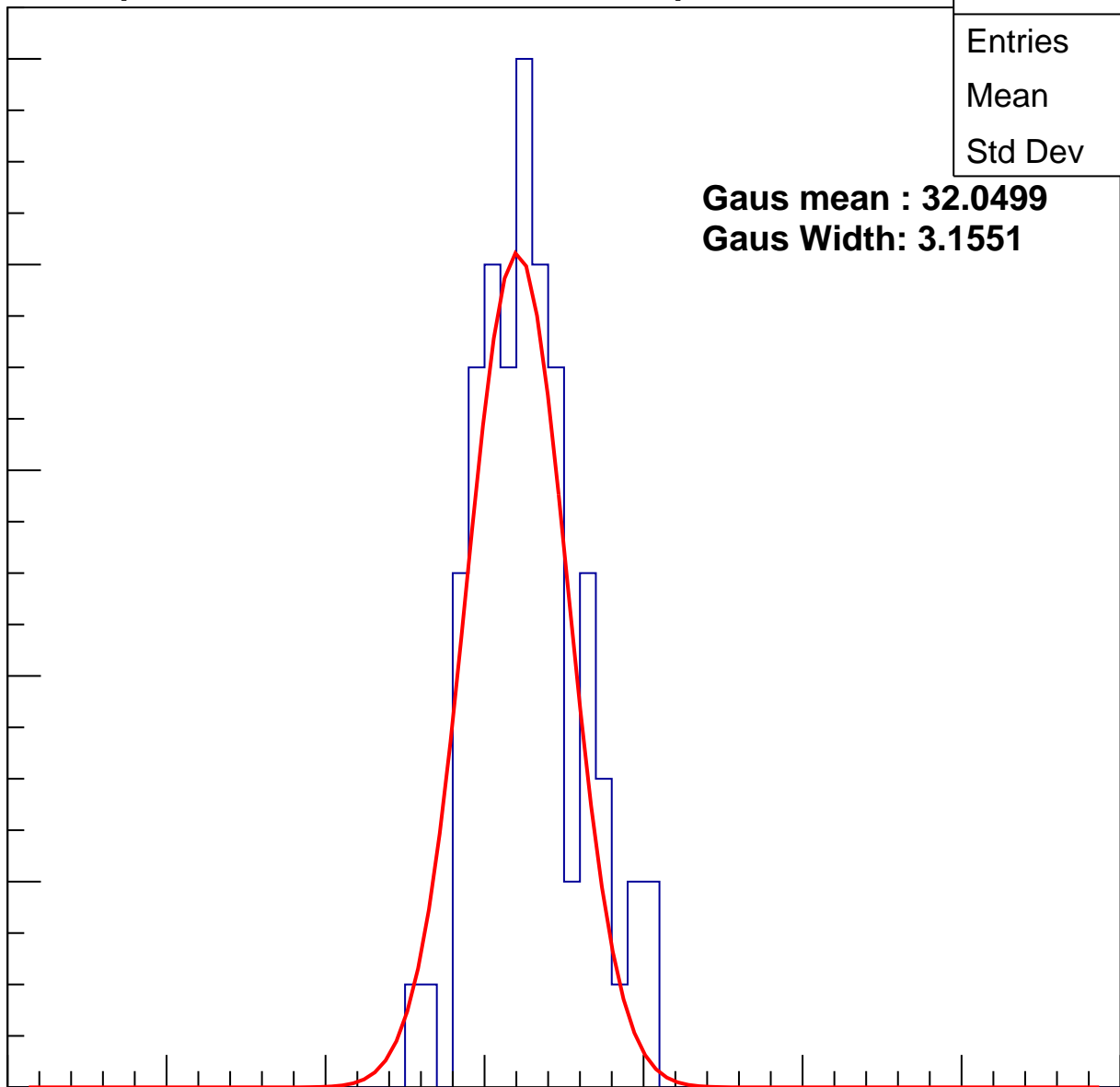
Gaus Width: 3.1551

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch76, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

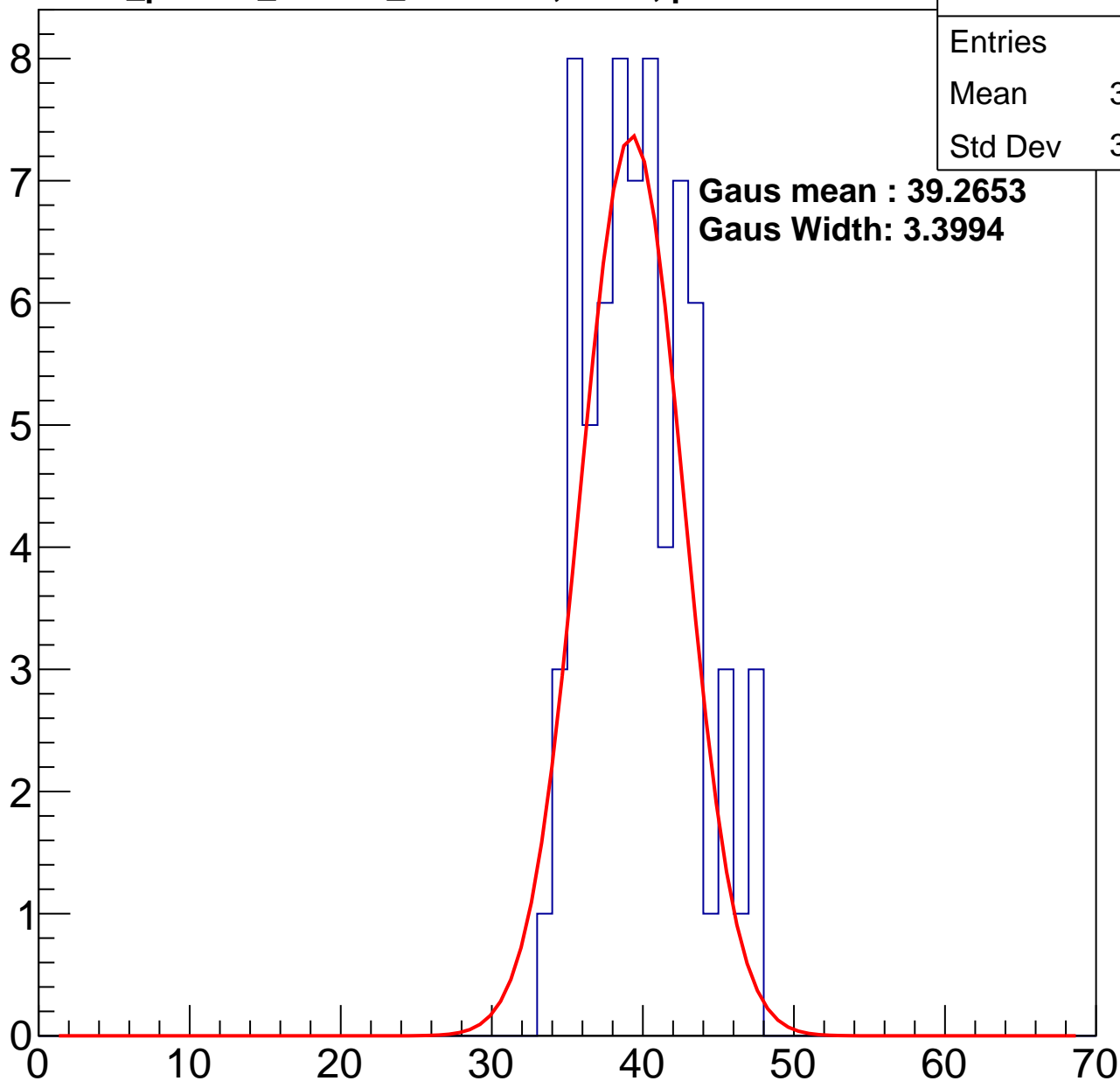
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 71 |
| Mean | 39.38 |
| Std Dev | 3.486 |

Gaus mean : 39.2653

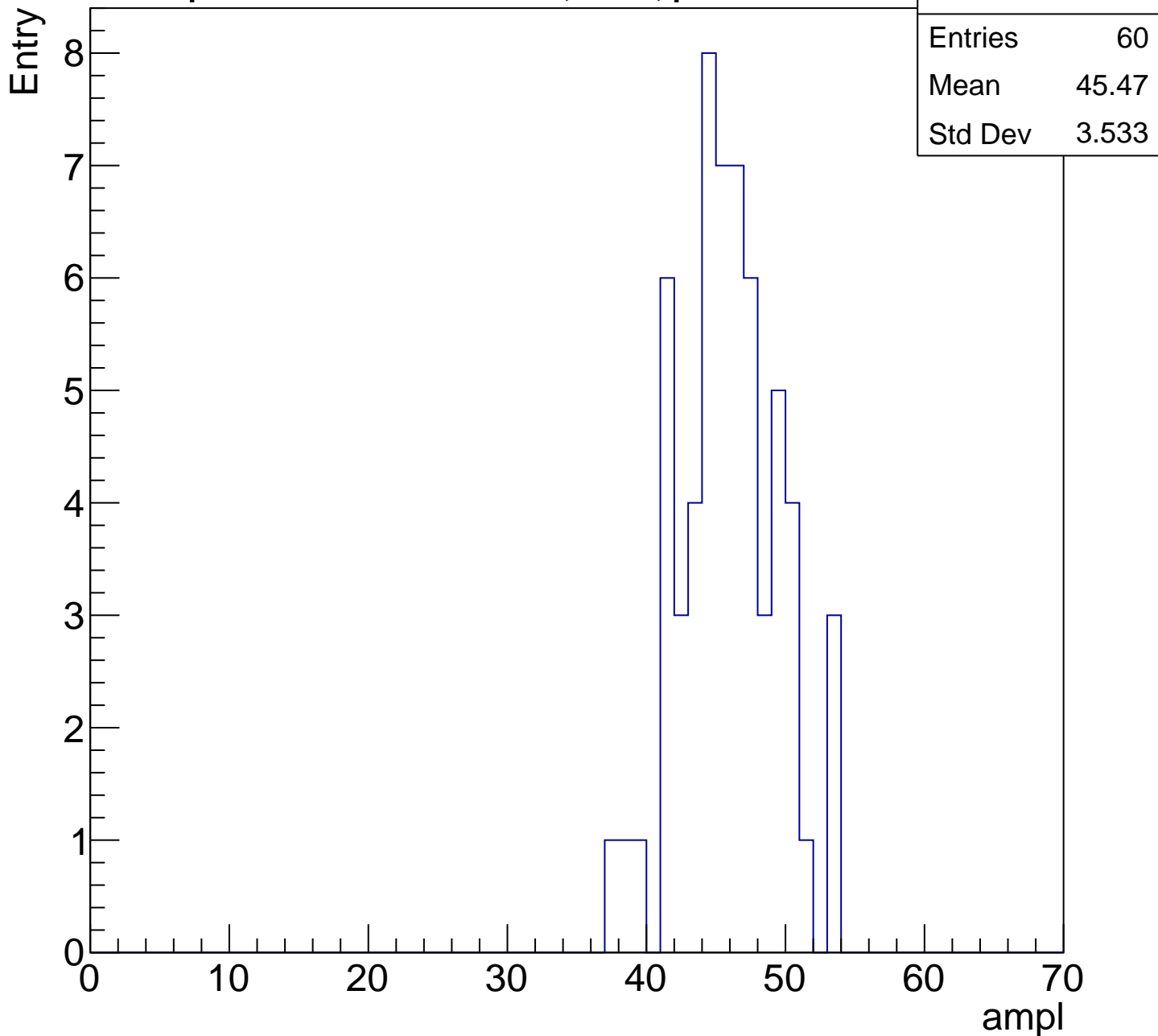
Gaus Width: 3.3994

ampl



B1L100S, U7-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

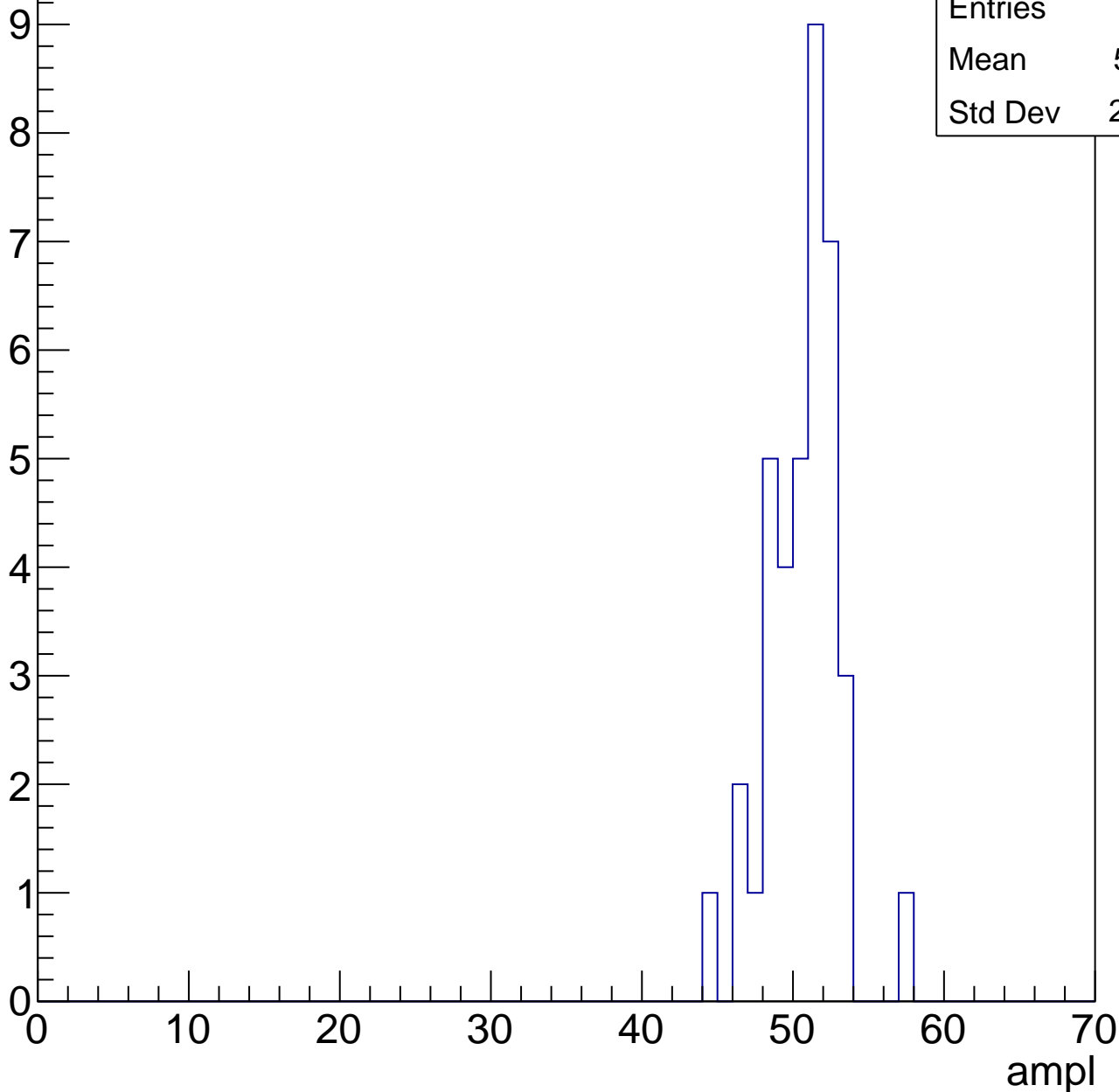


B1L100S, U7-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 50.21 |
| Std Dev | 2.364 |

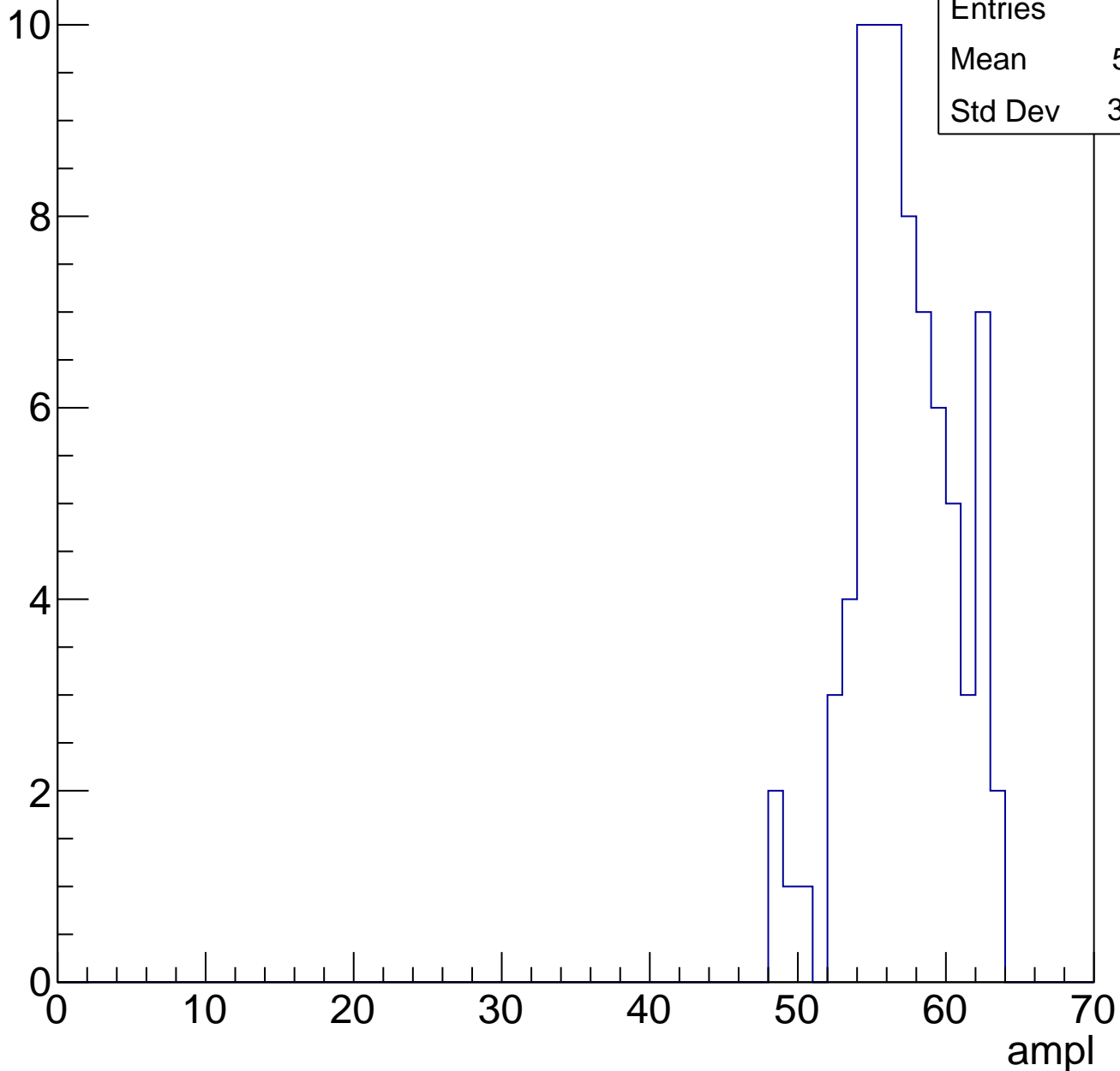


B1L100S, U7-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 56.61 |
| Std Dev | 3.399 |

Entry

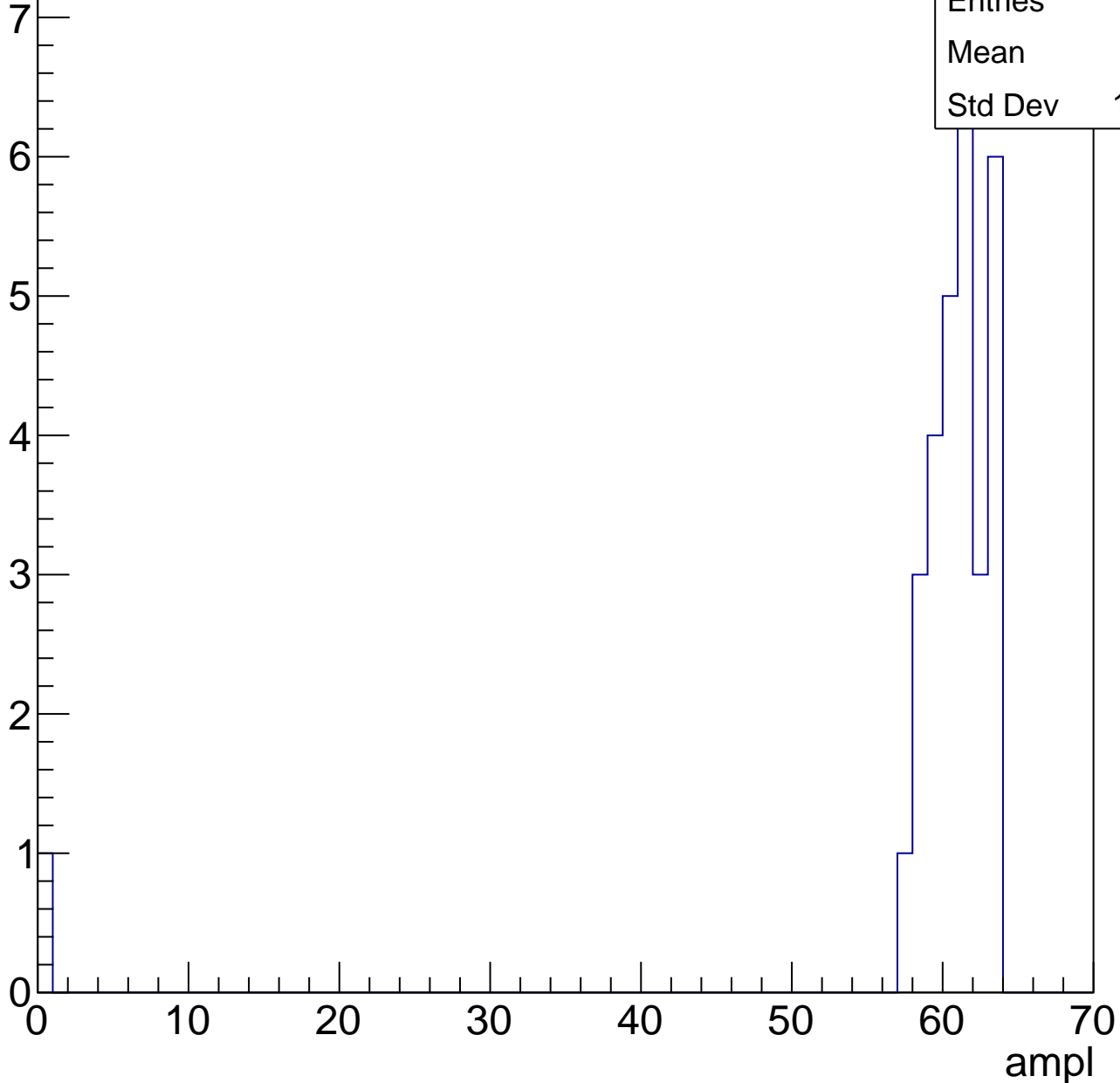


B1L100S, U7-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

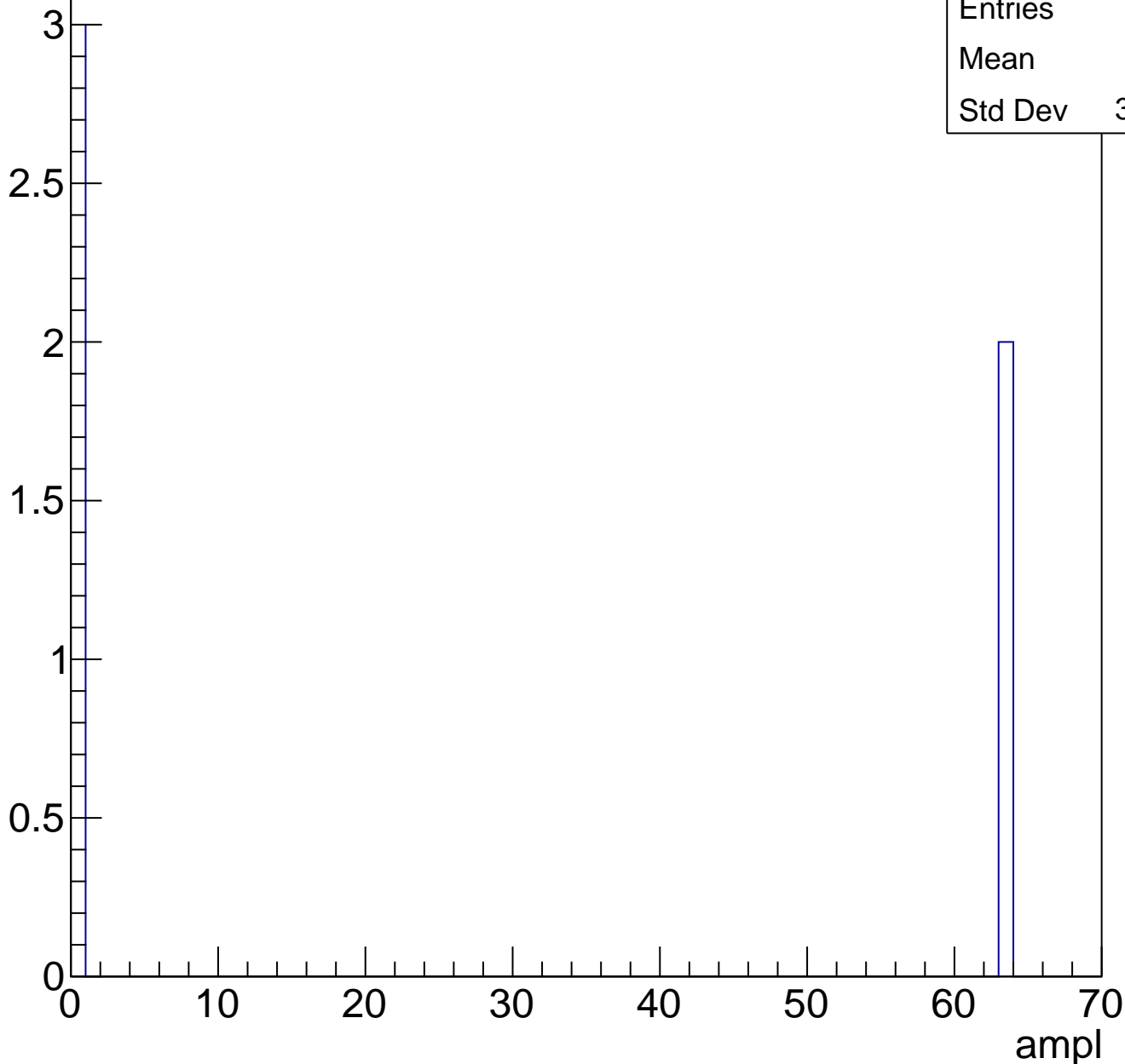
| | |
|---------|-------|
| Entries | 30 |
| Mean | 58.6 |
| Std Dev | 11.01 |



B1L100S, U7-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 5 |
| Mean | 25.2 |
| Std Dev | 30.86 |

B1L100S, U7-ch77, adc0

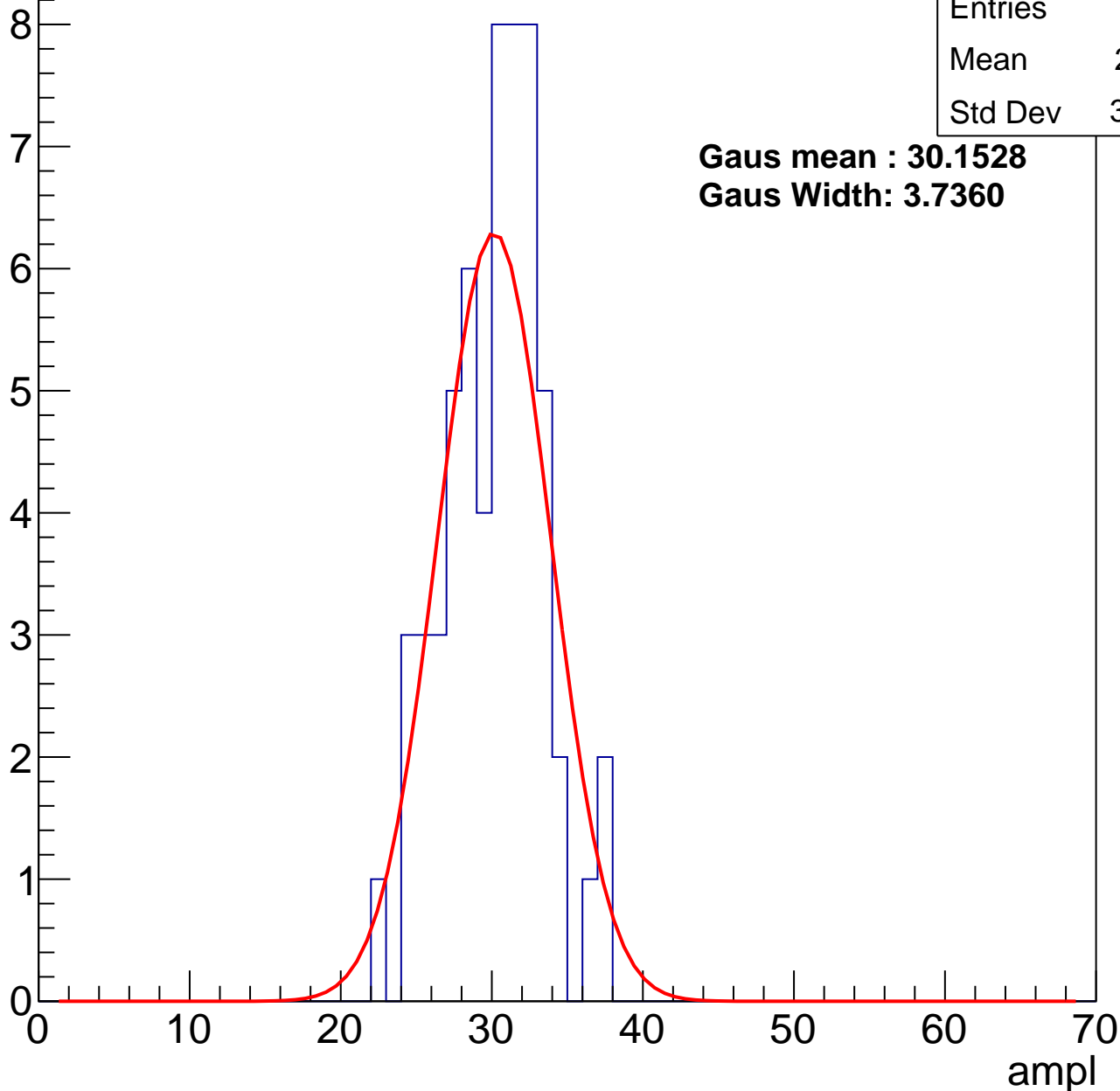
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 29.71 |
| Std Dev | 3.226 |

Gaus mean : 30.1528

Gaus Width: 3.7360



B1L100S, U7-ch77, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.53 |
| Std Dev | 3.827 |

Gaus mean : 37.0879

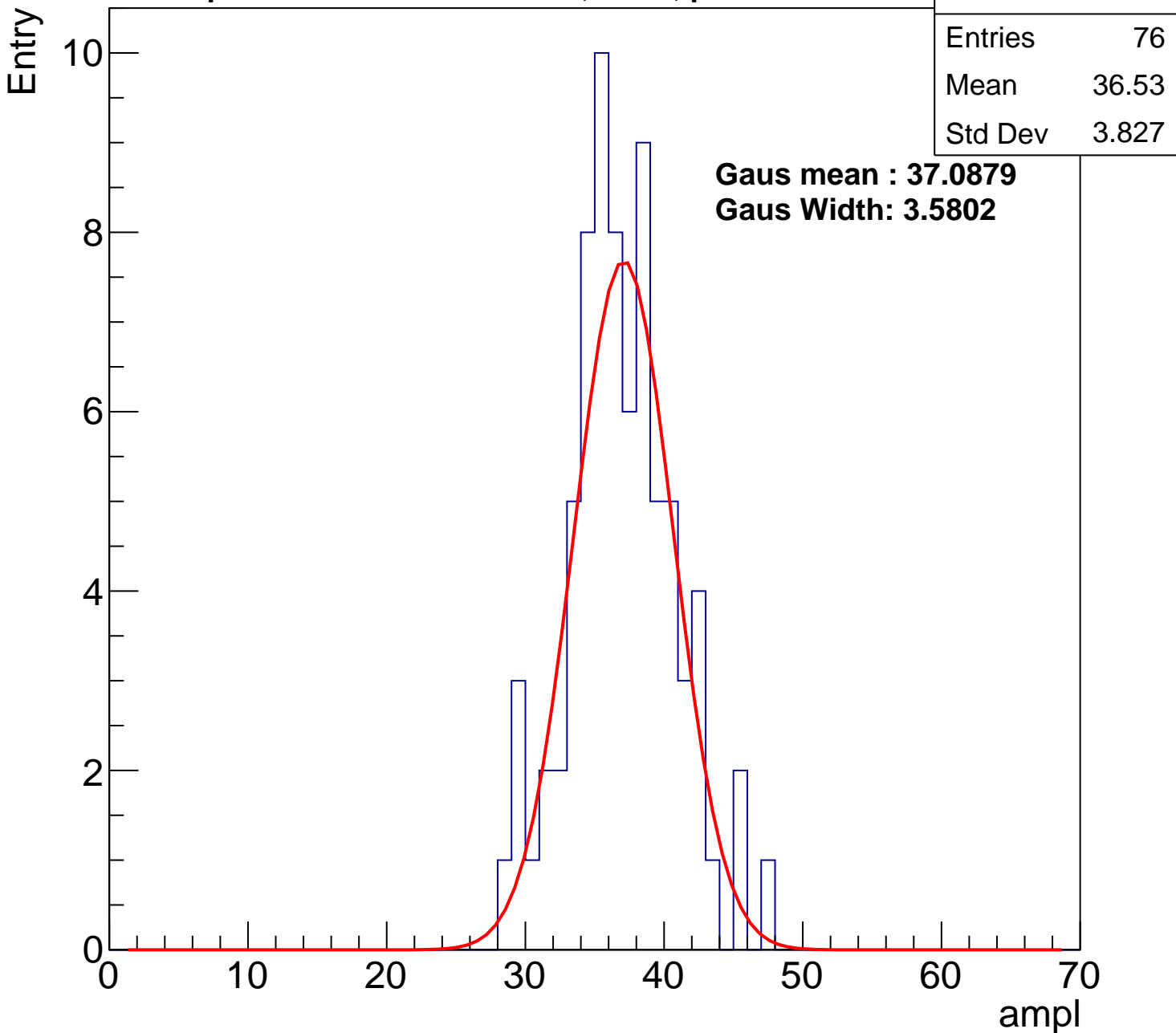
Gaus Width: 3.5802

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch77, adc2

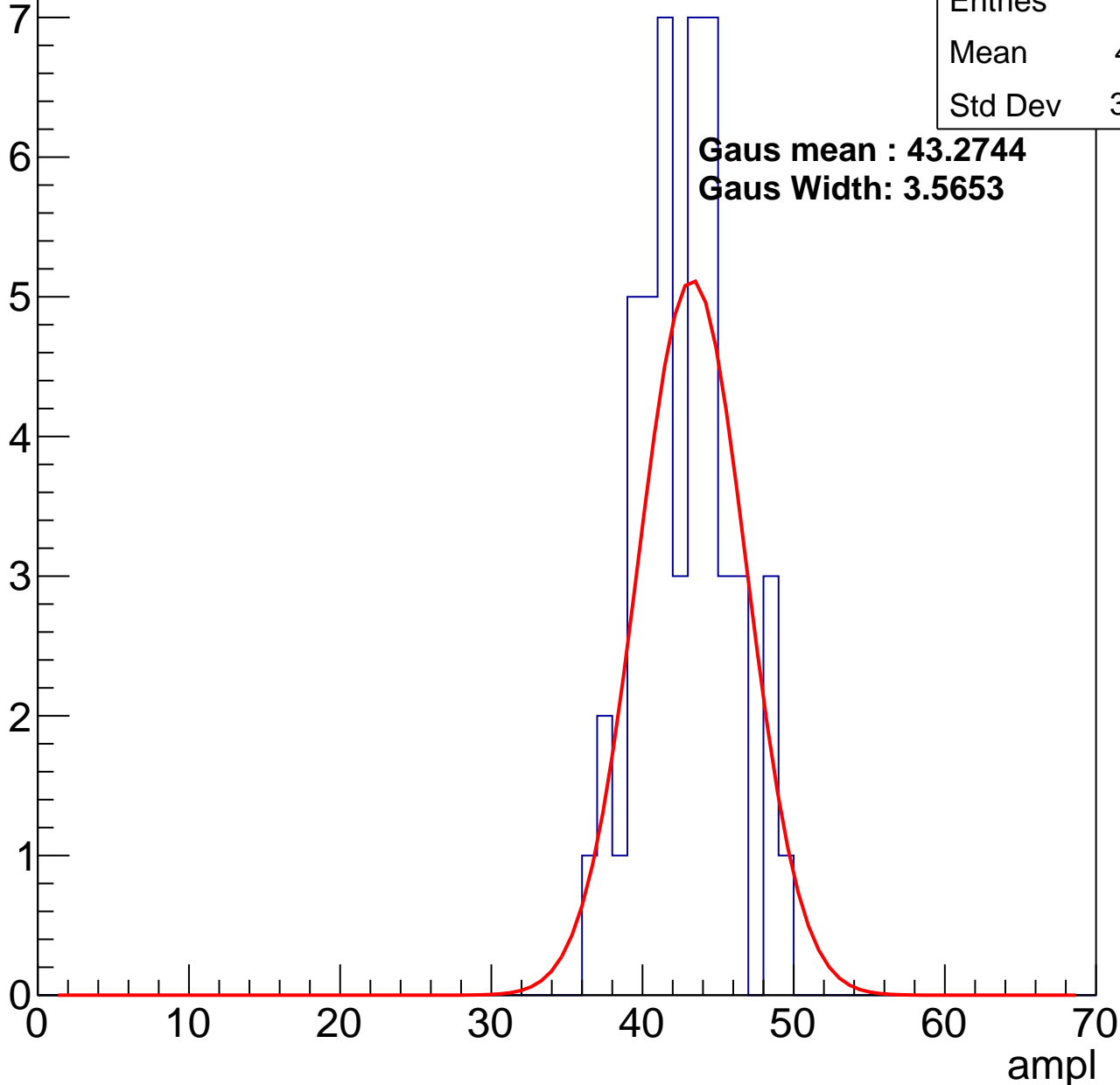
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 42.31 |
| Std Dev | 3.015 |

Gaus mean : 43.2744

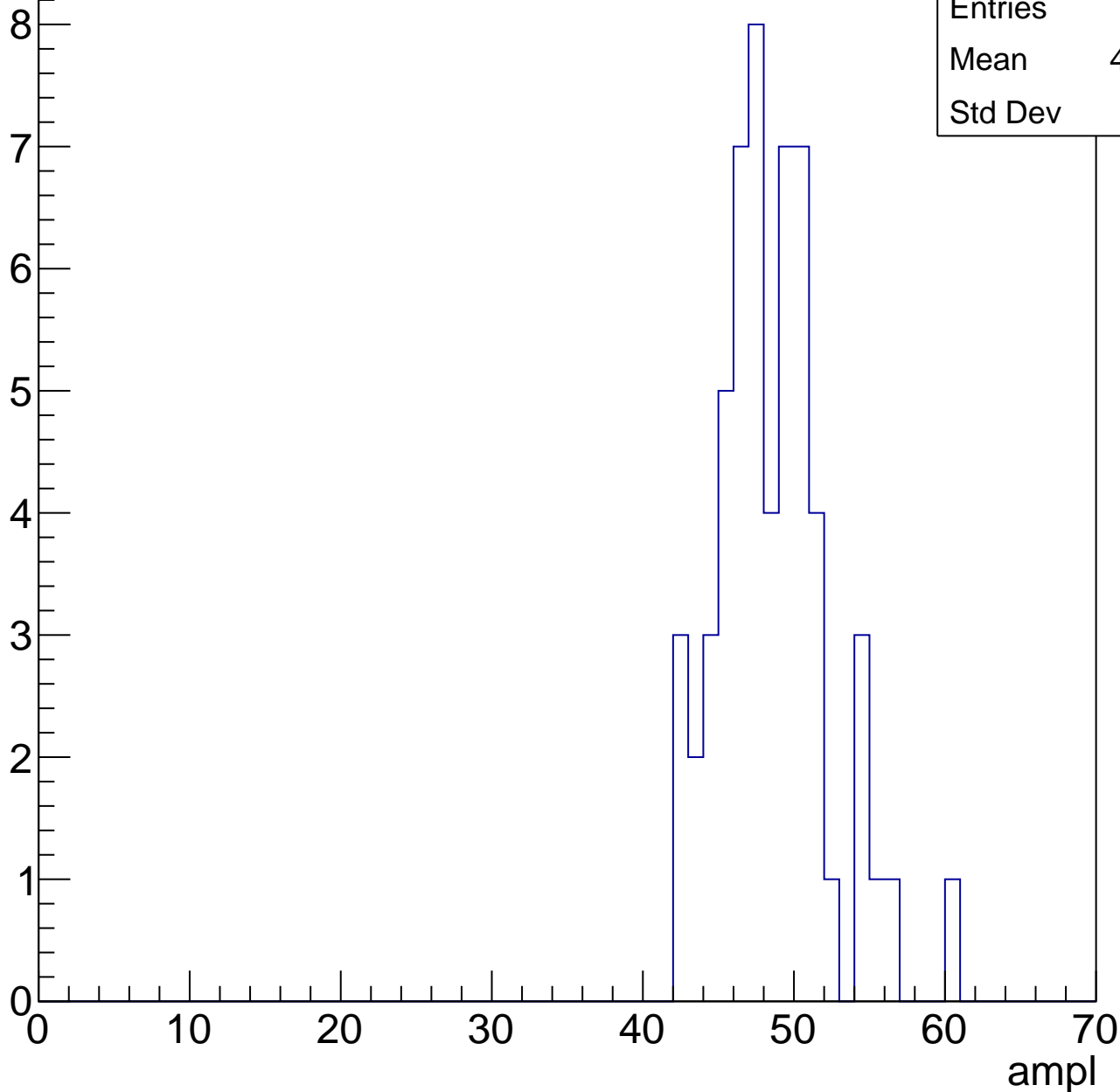
Gaus Width: 3.5653



B1L100S, U7-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

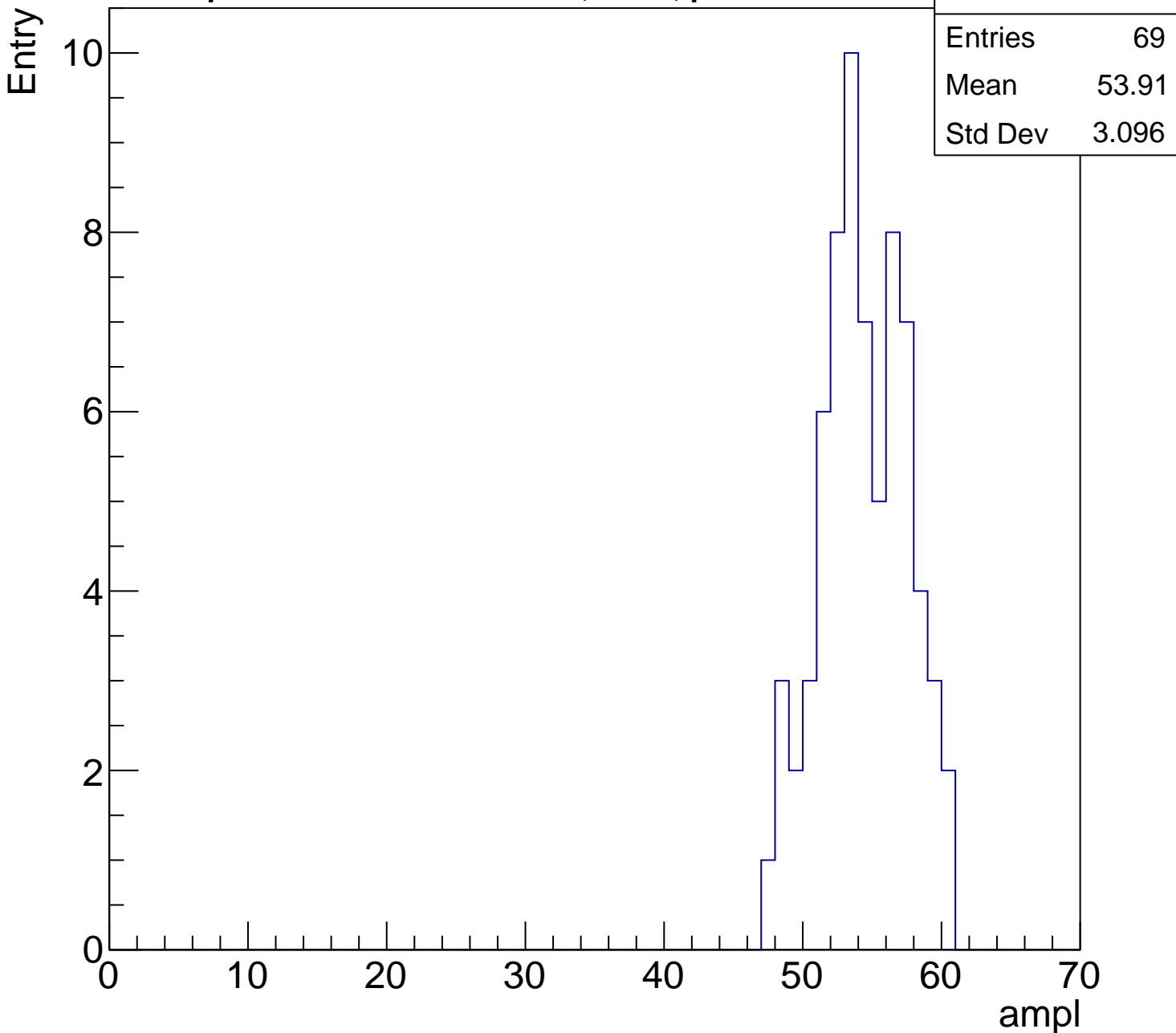
| | |
|---------|-------|
| Entries | 69 |
| Mean | 53.91 |
| Std Dev | 3.096 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

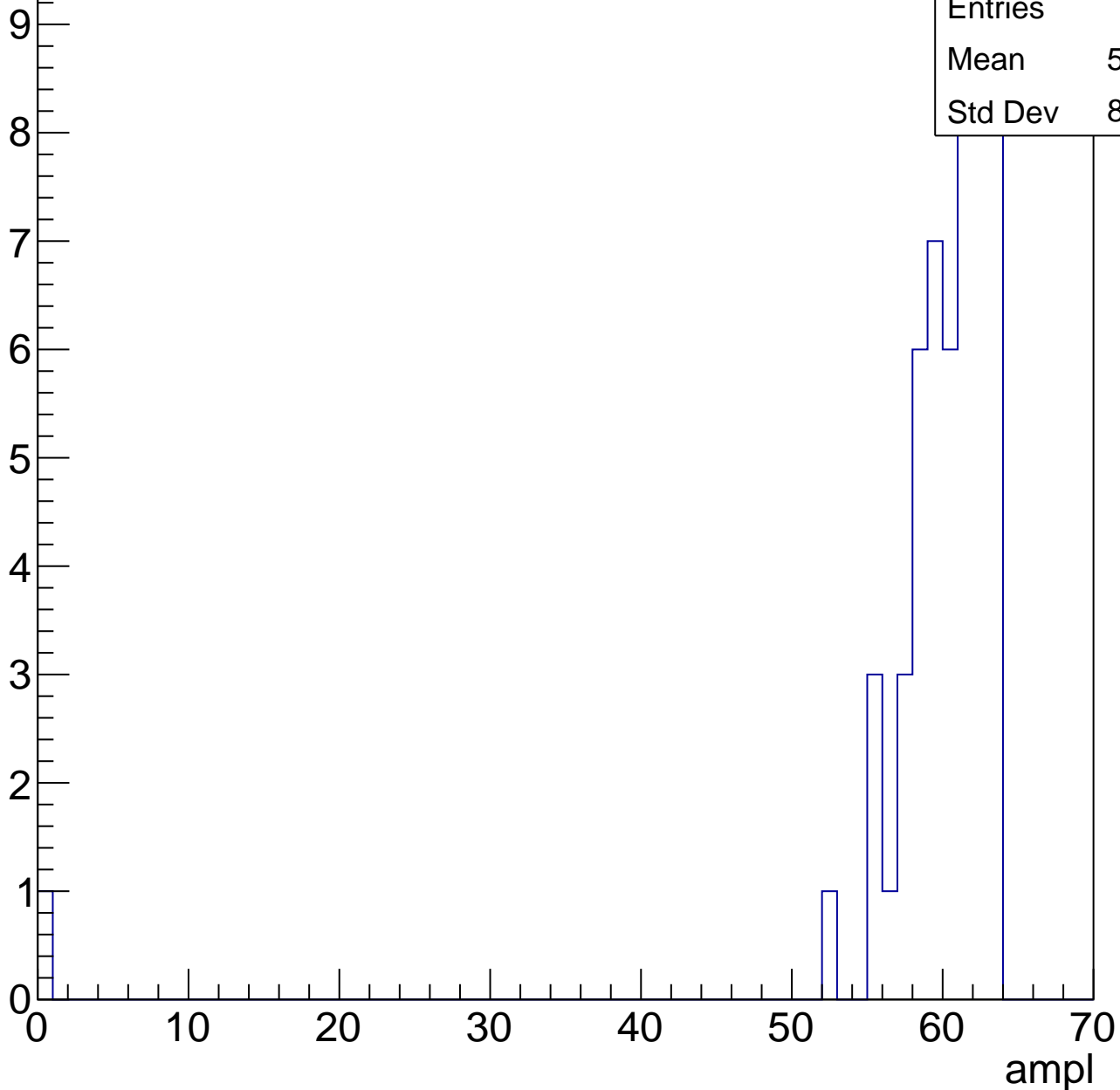


B1L100S, U7-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.83 |
| Std Dev | 8.456 |



B1L100S, U7-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

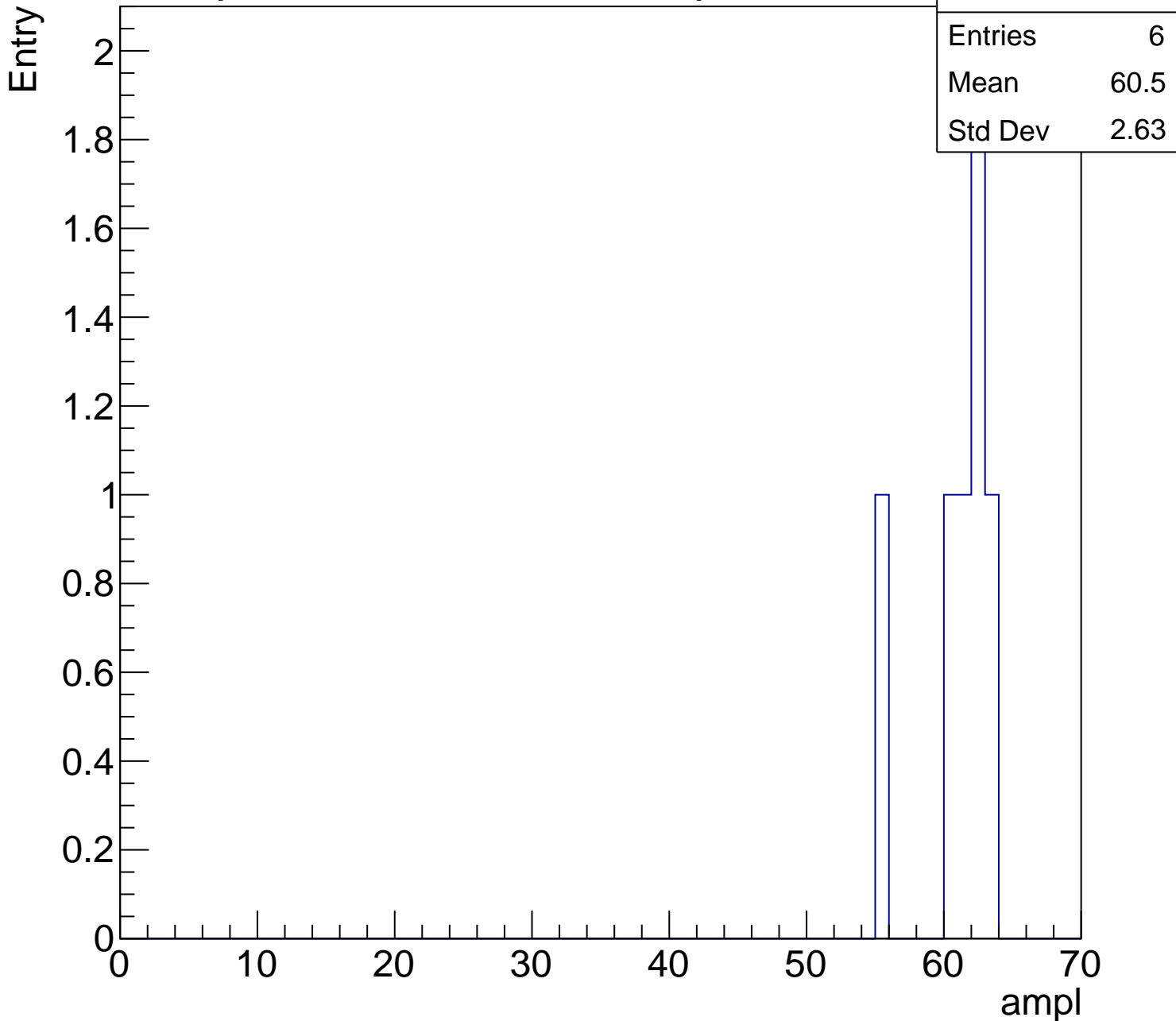
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 6 |
| Mean | 60.5 |
| Std Dev | 2.63 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch78, adc0

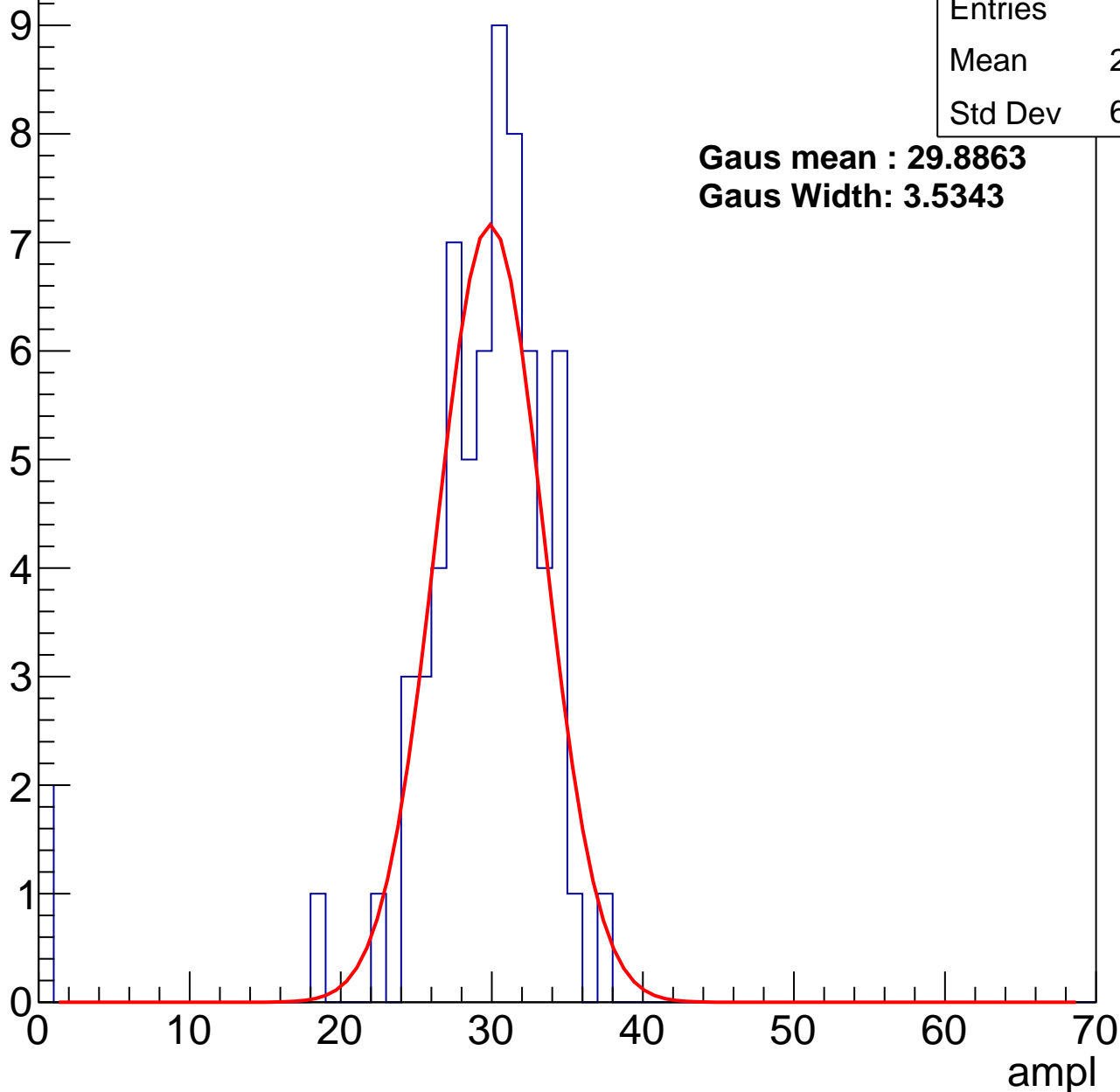
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 28.54 |
| Std Dev | 6.033 |

Gaus mean : 29.8863

Gaus Width: 3.5343



B1L100S, U7-ch78, adc1

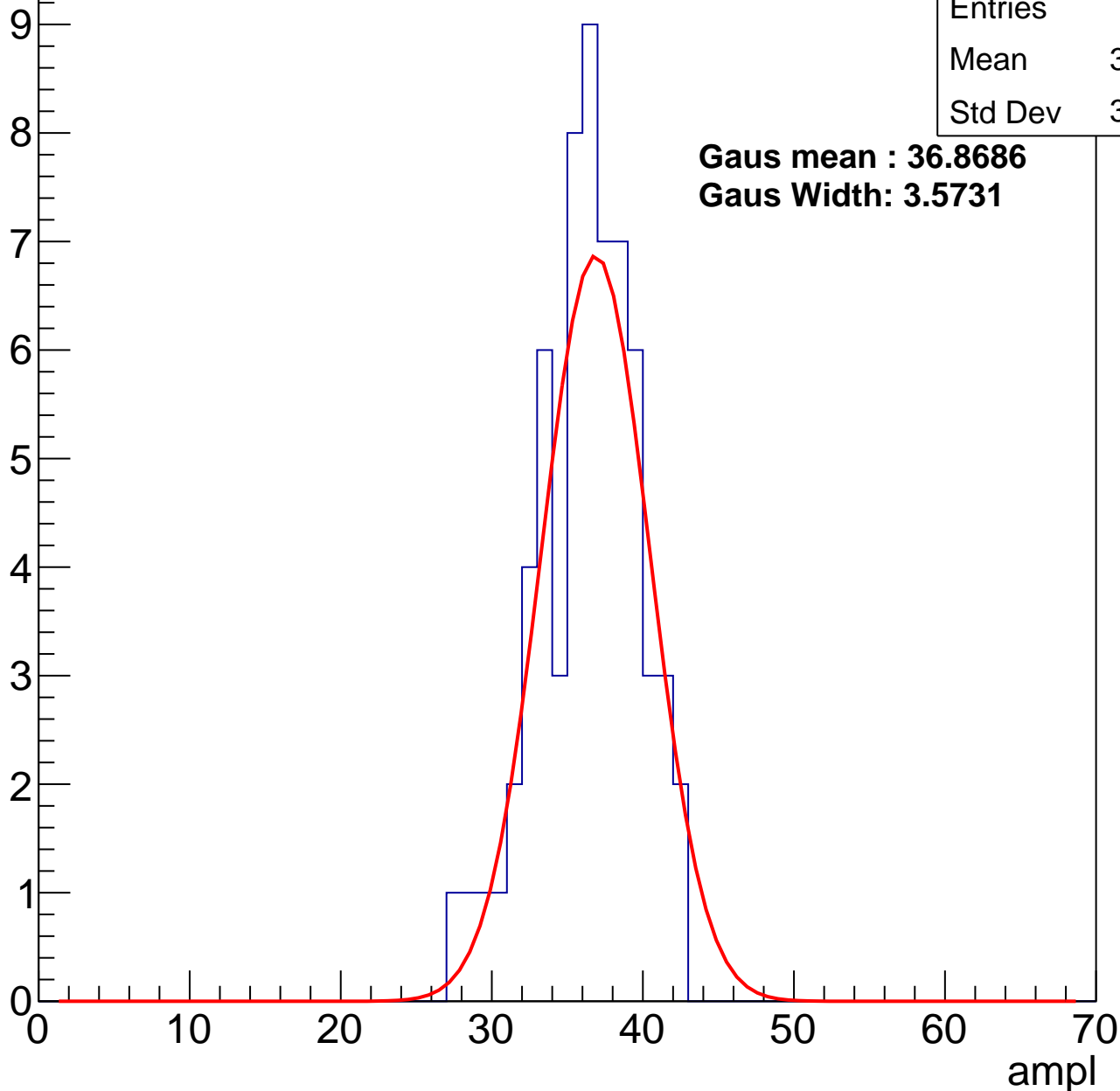
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.84 |
| Std Dev | 3.308 |

Gaus mean : 36.8686

Gaus Width: 3.5731

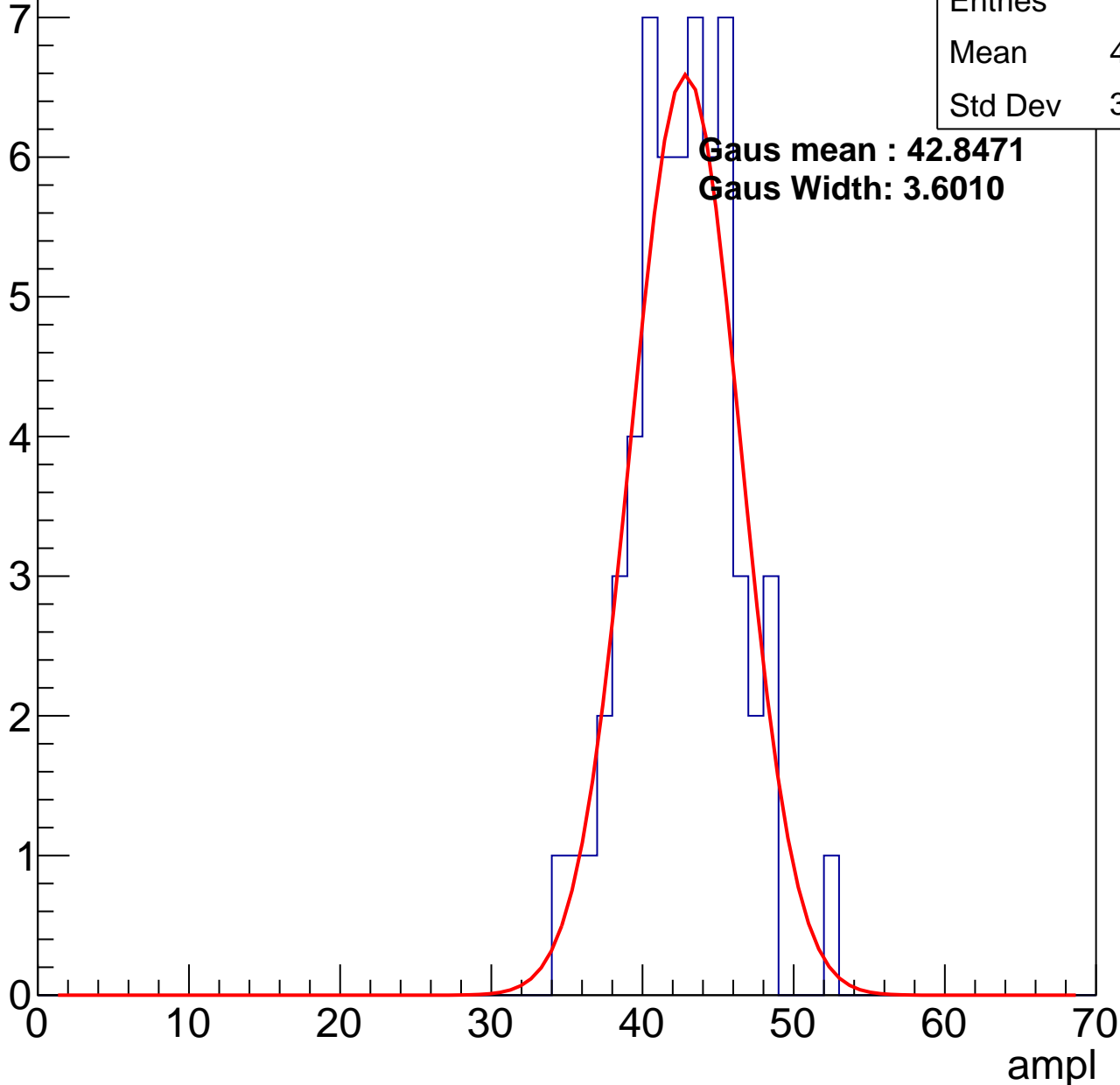


B1L100S, U7-ch78, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.25 |
| Std Dev | 3.448 |

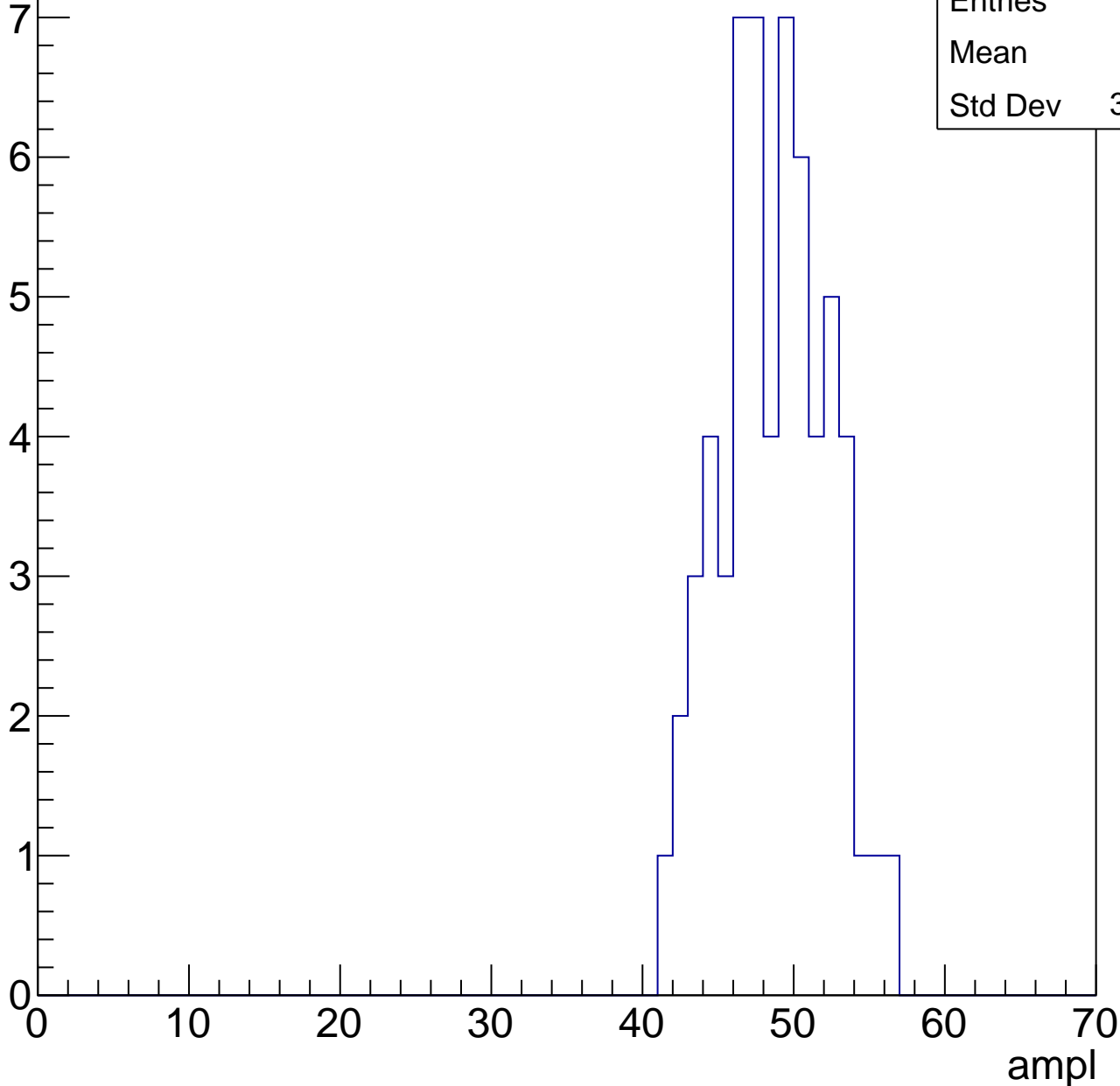


B1L100S, U7-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.2 |
| Std Dev | 3.454 |

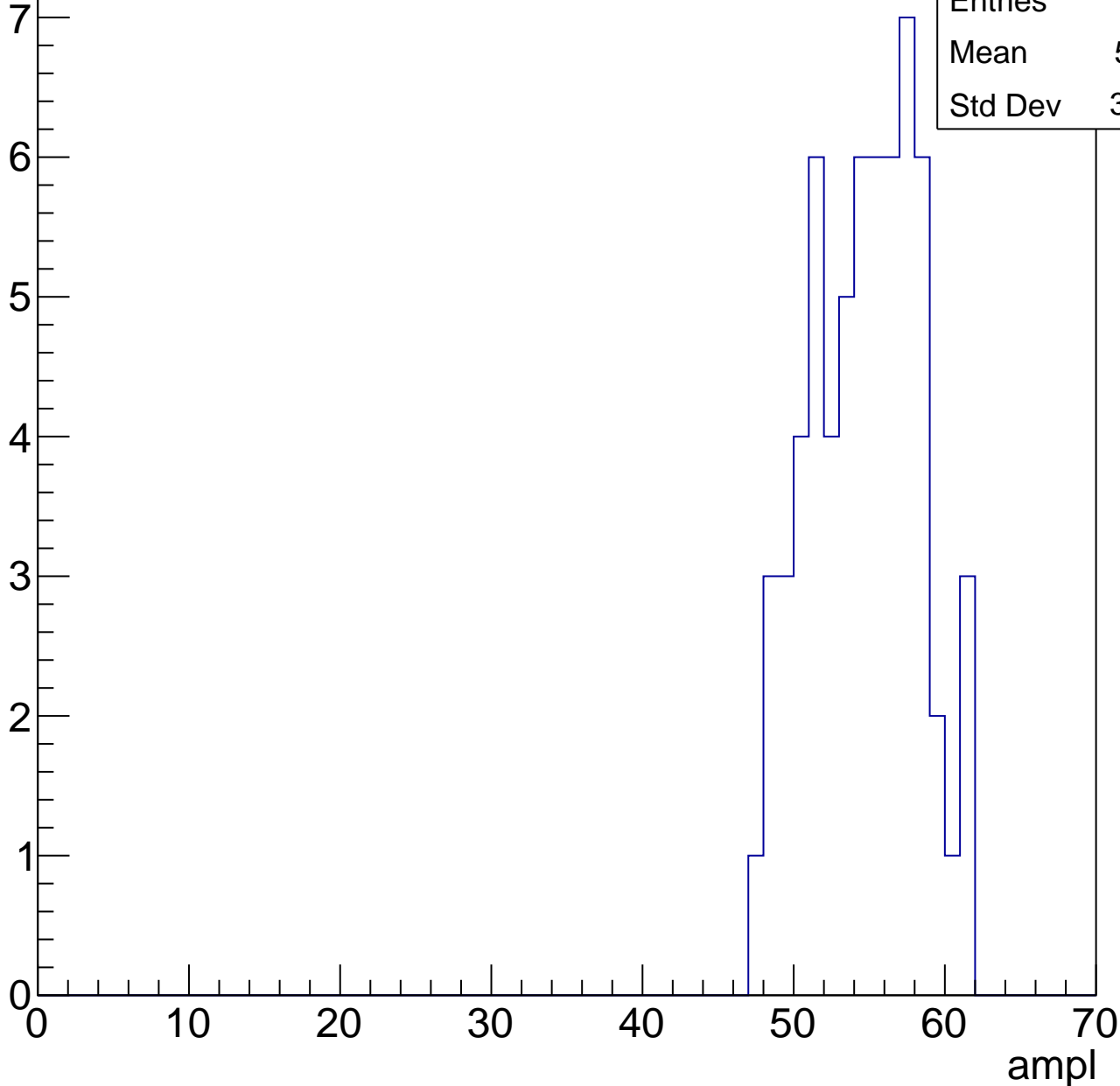


B1L100S, U7-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

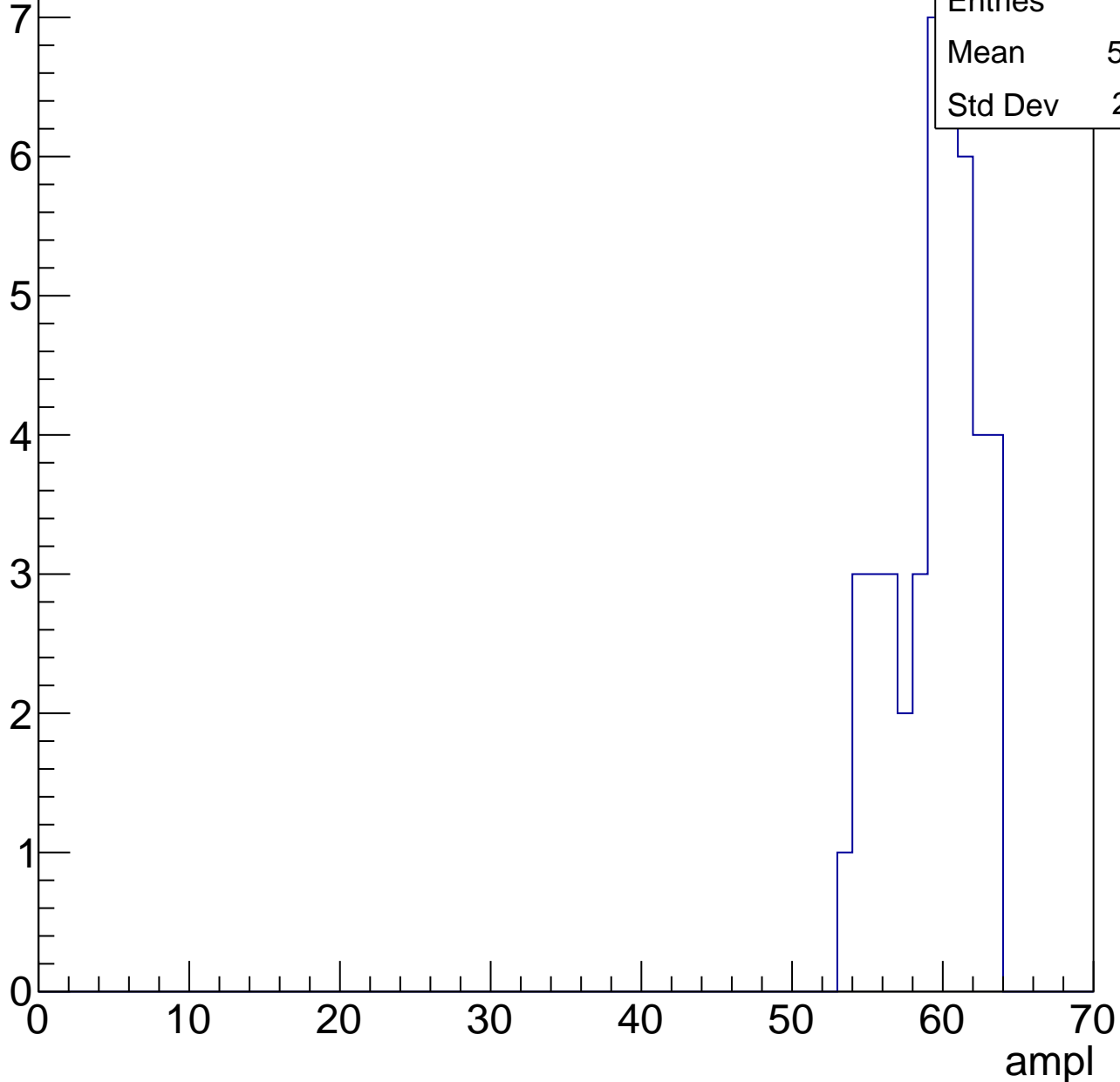
| | |
|---------|-------|
| Entries | 63 |
| Mean | 54.21 |
| Std Dev | 3.542 |



B1L100S, U7-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

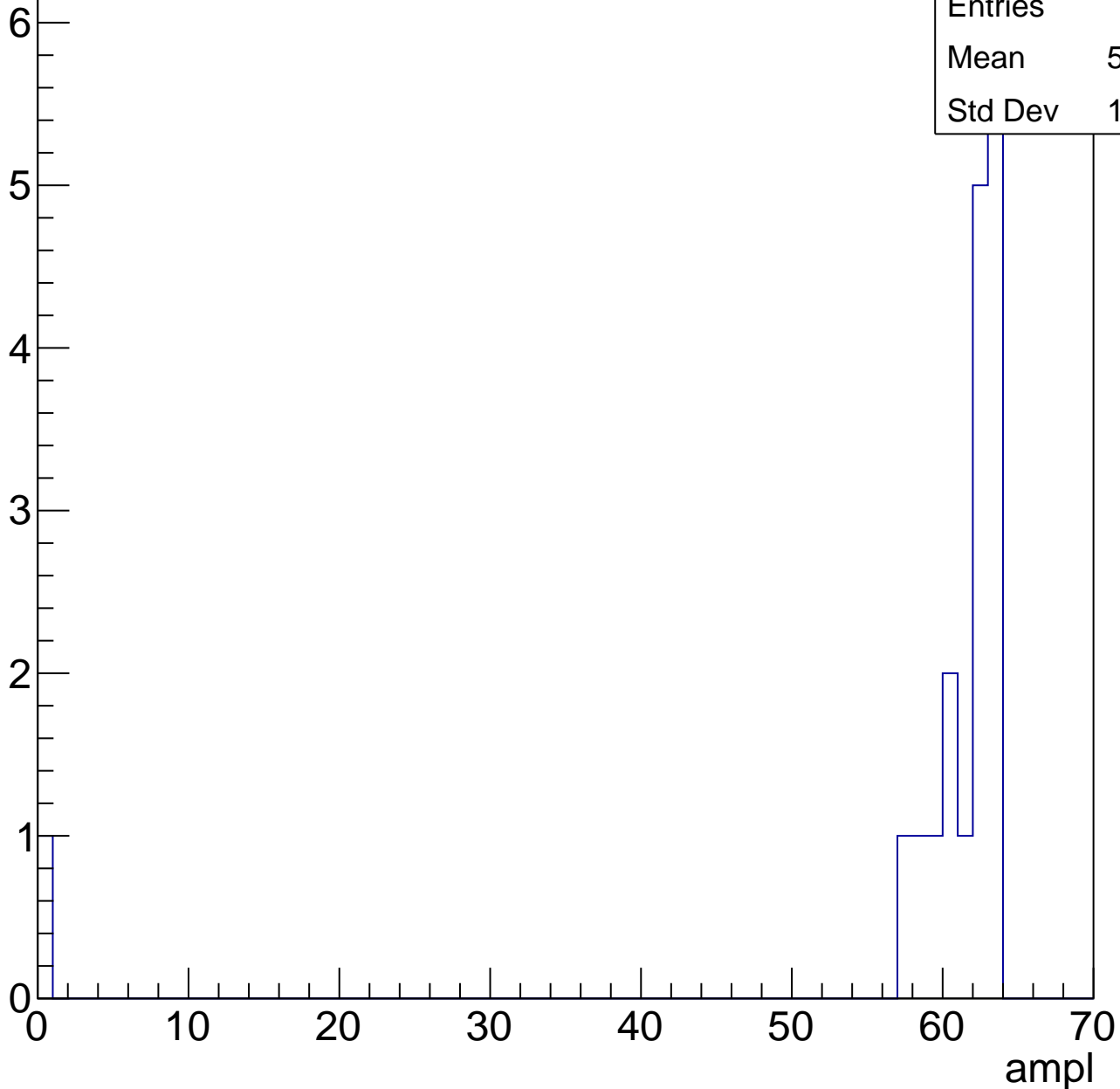


B1L100S, U7-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.94 |
| Std Dev | 14.17 |



B1L100S, U7-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch79, adc0

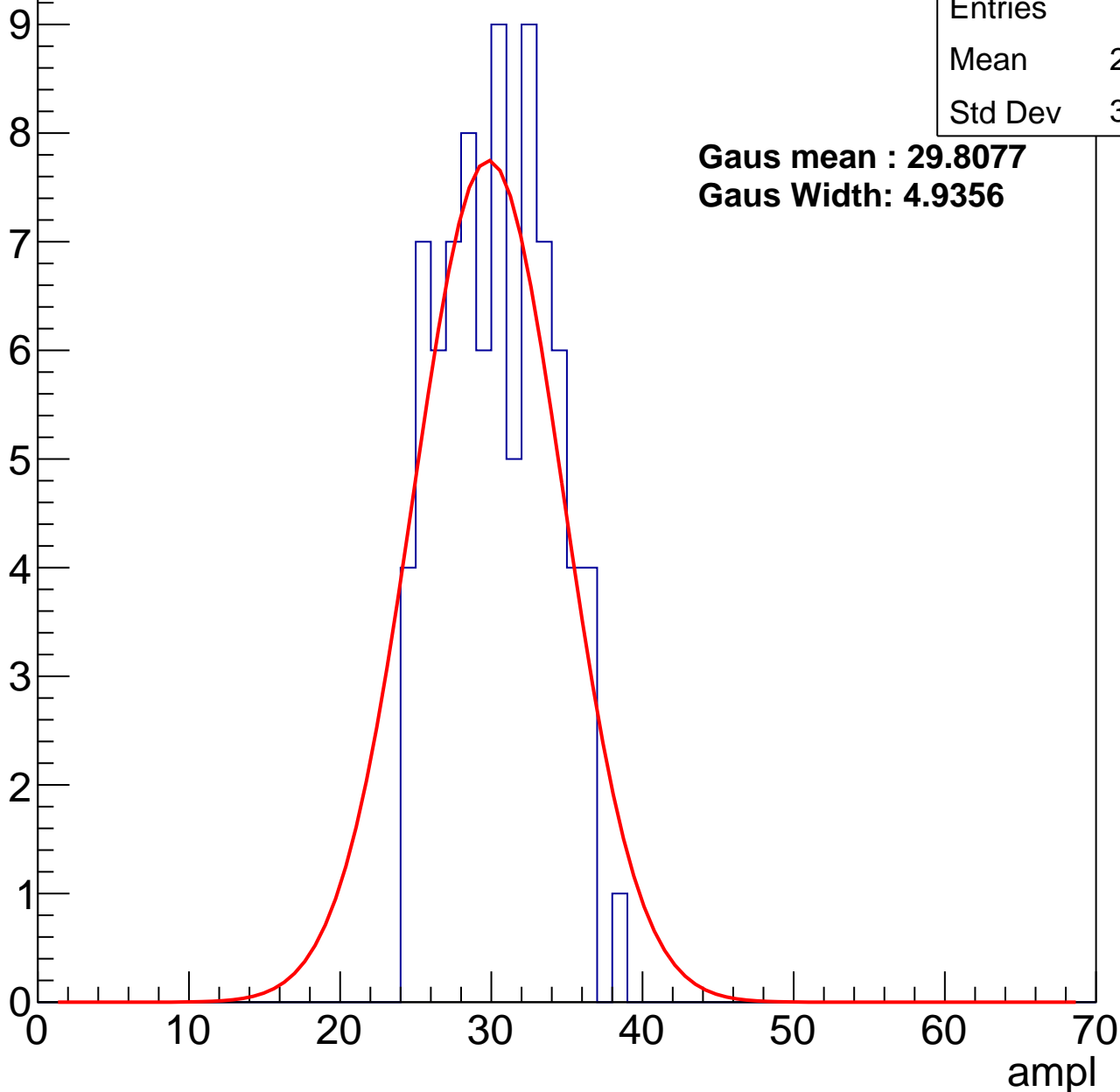
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 29.93 |
| Std Dev | 3.512 |

Gaus mean : 29.8077

Gaus Width: 4.9356



B1L100S, U7-ch79, adc1

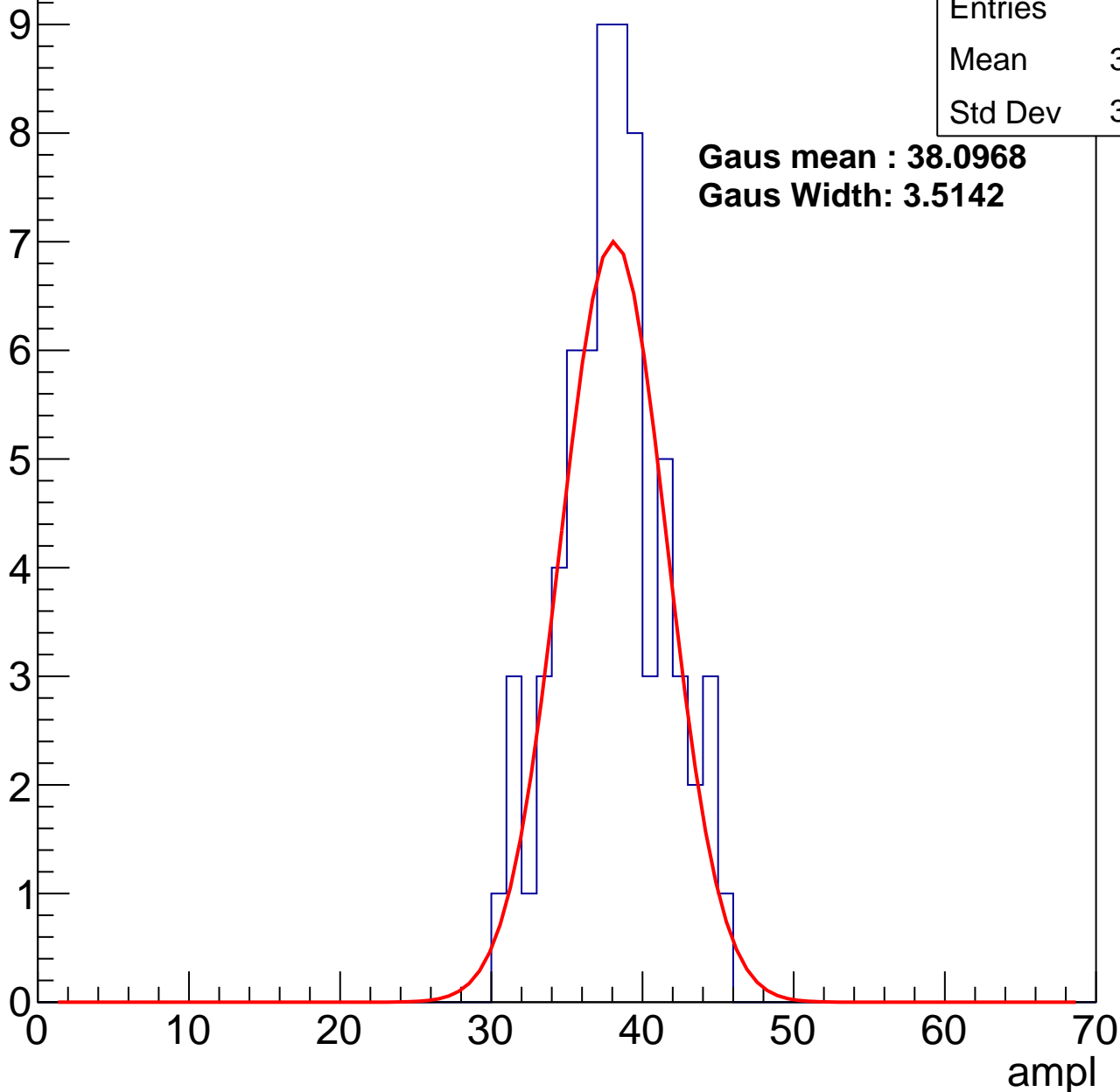
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.57 |
| Std Dev | 3.413 |

Gaus mean : 38.0968

Gaus Width: 3.5142

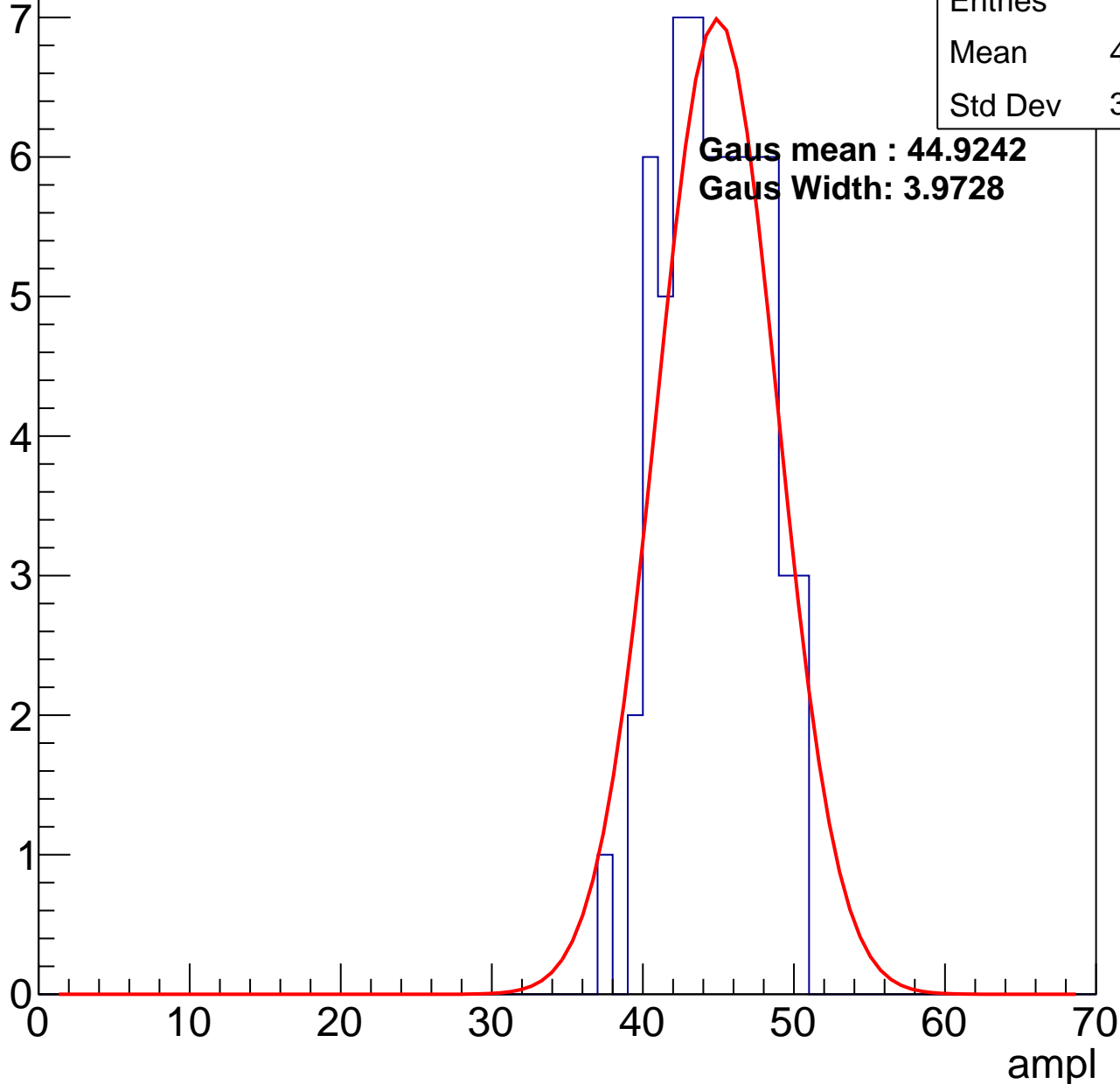


B1L100S, U7-ch79, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 44.25 |
| Std Dev | 3.147 |

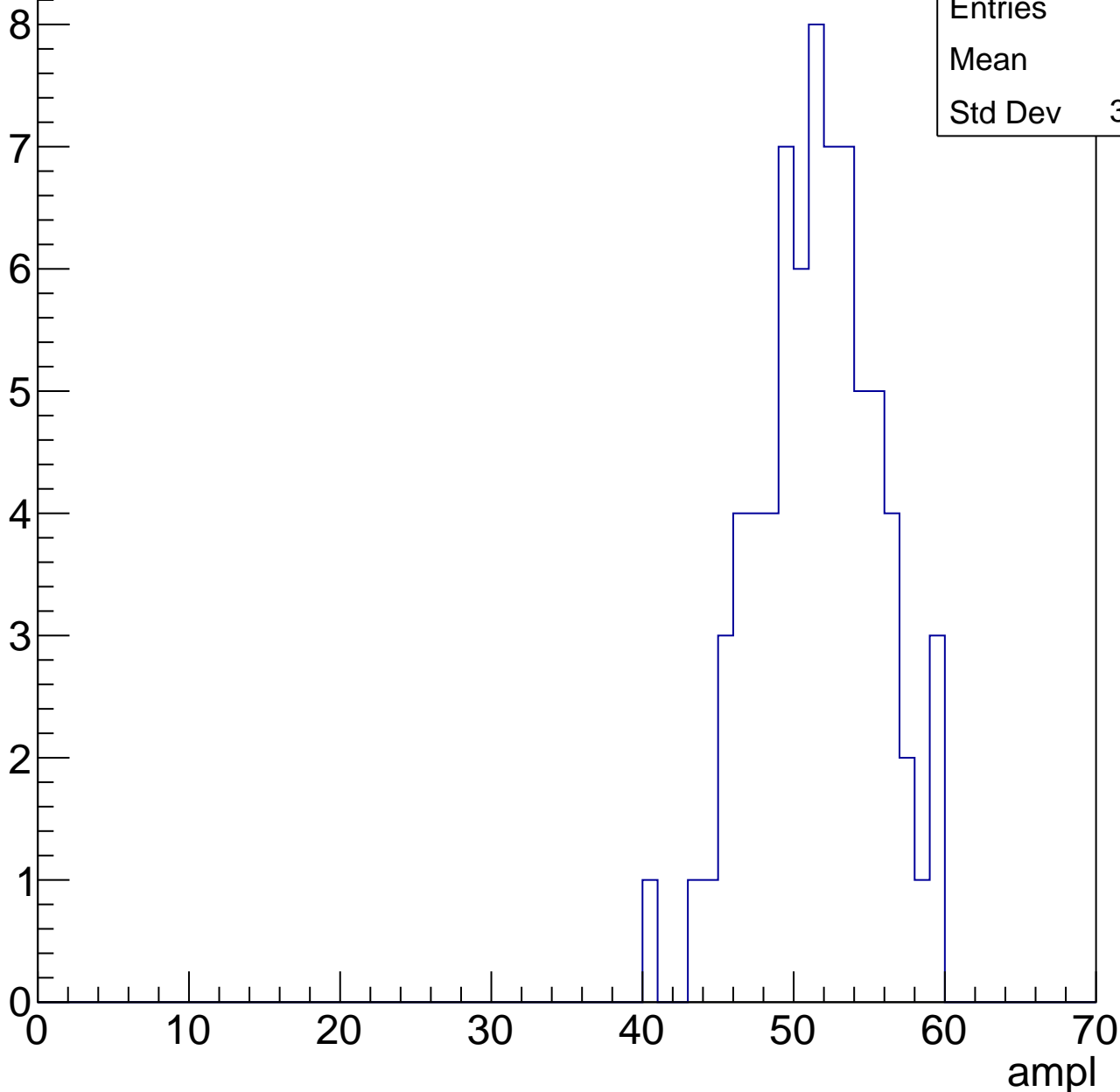


B1L100S, U7-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 51.1 |
| Std Dev | 3.966 |

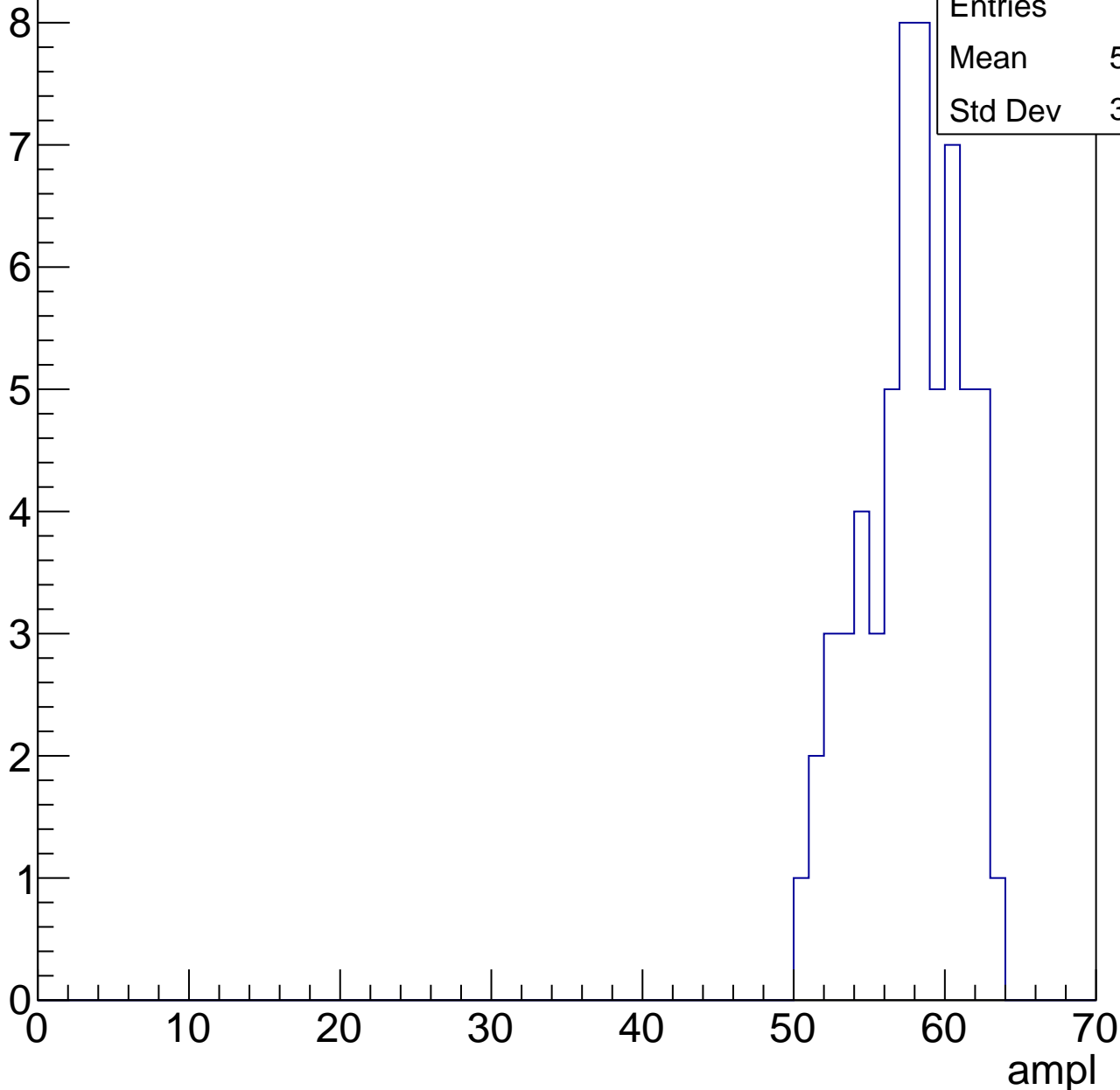


B1L100S, U7-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 57.35 |
| Std Dev | 3.219 |

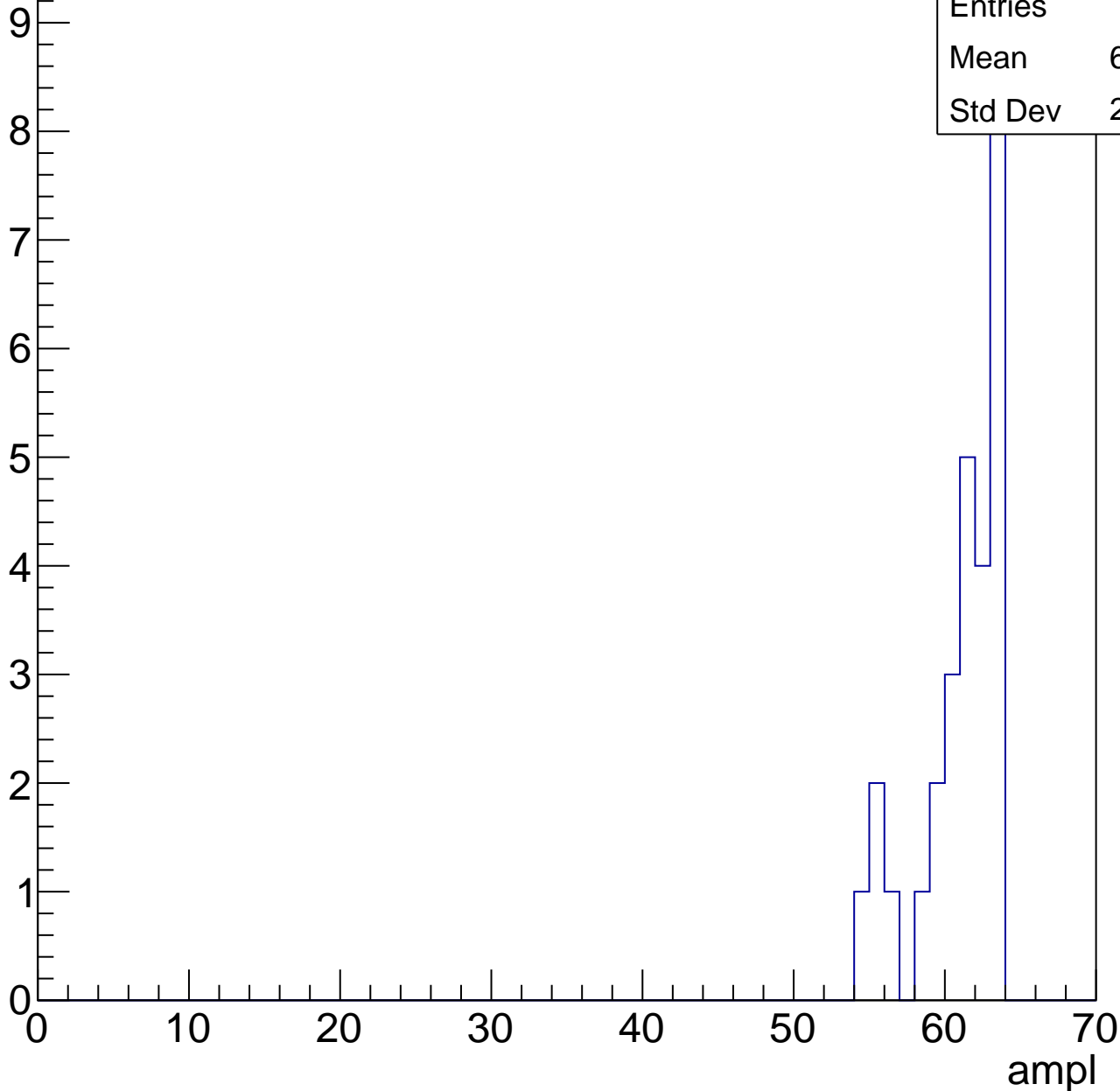


B1L100S, U7-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 60.57 |
| Std Dev | 2.678 |



B1L100S, U7-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch80, adc0

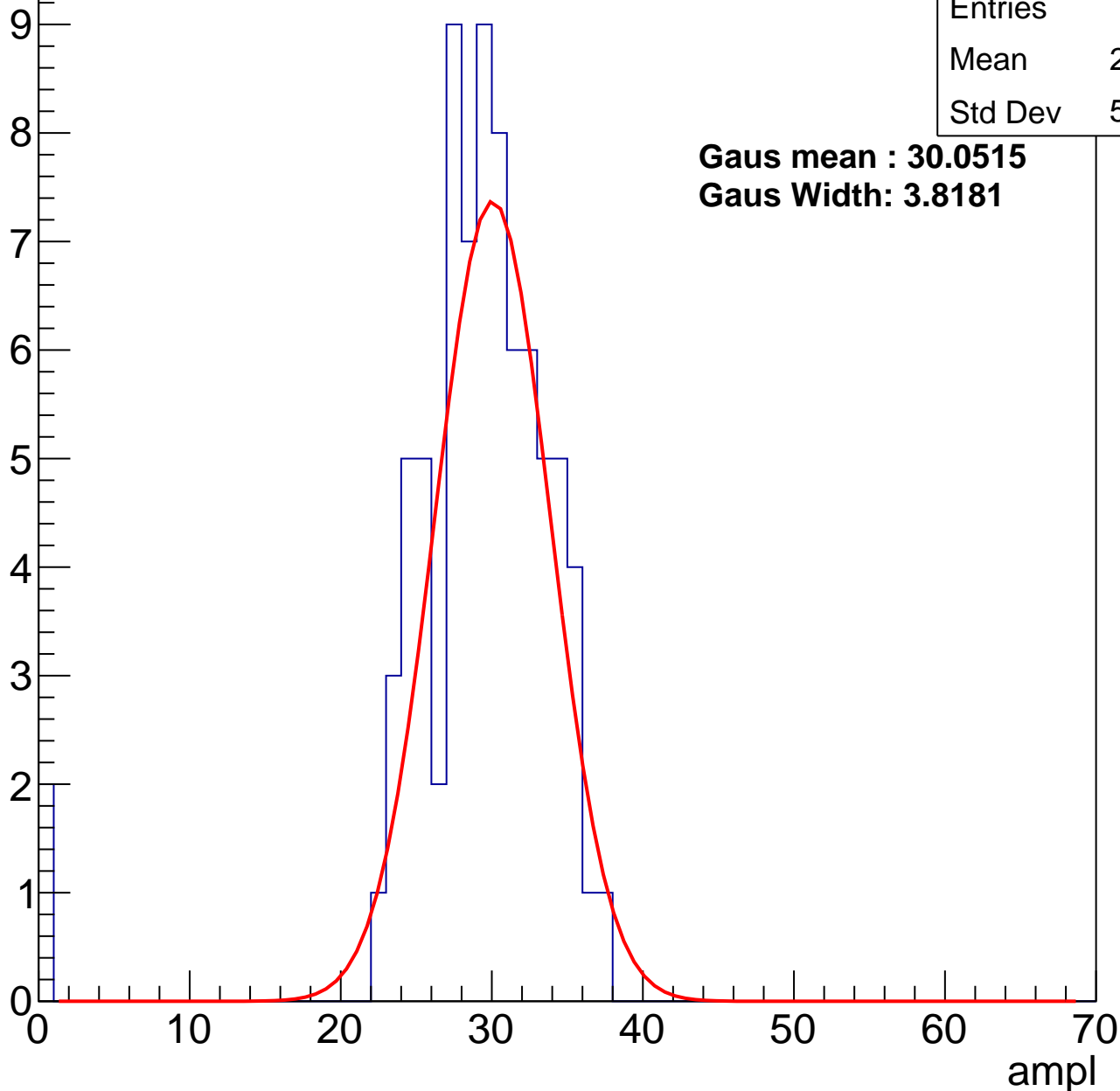
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.53 |
| Std Dev | 5.776 |

Gaus mean : 30.0515

Gaus Width: 3.8181



B1L100S, U7-ch80, adc1

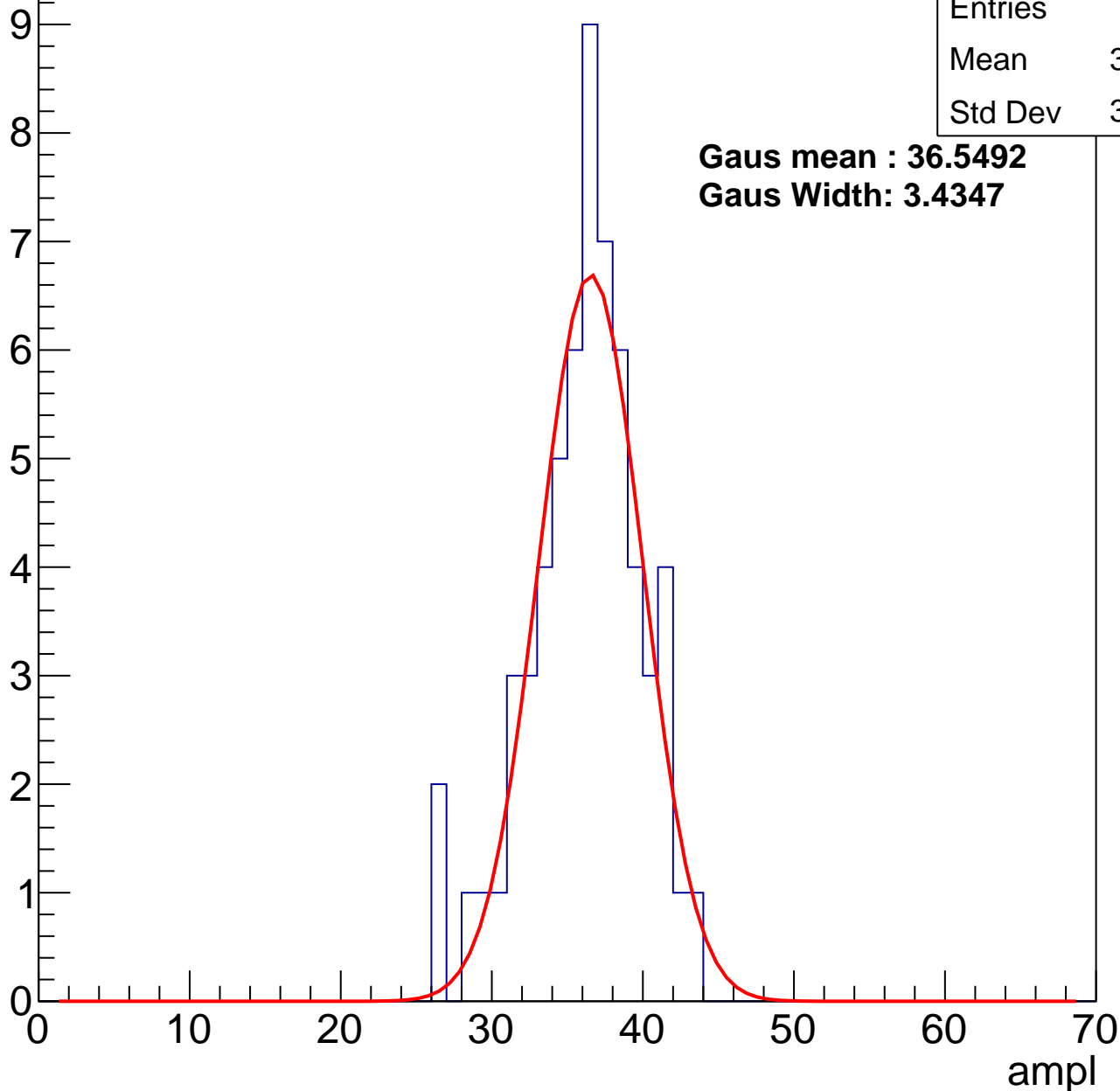
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.67 |
| Std Dev | 3.683 |

Gaus mean : 36.5492

Gaus Width: 3.4347



B1L100S, U7-ch80, adc2

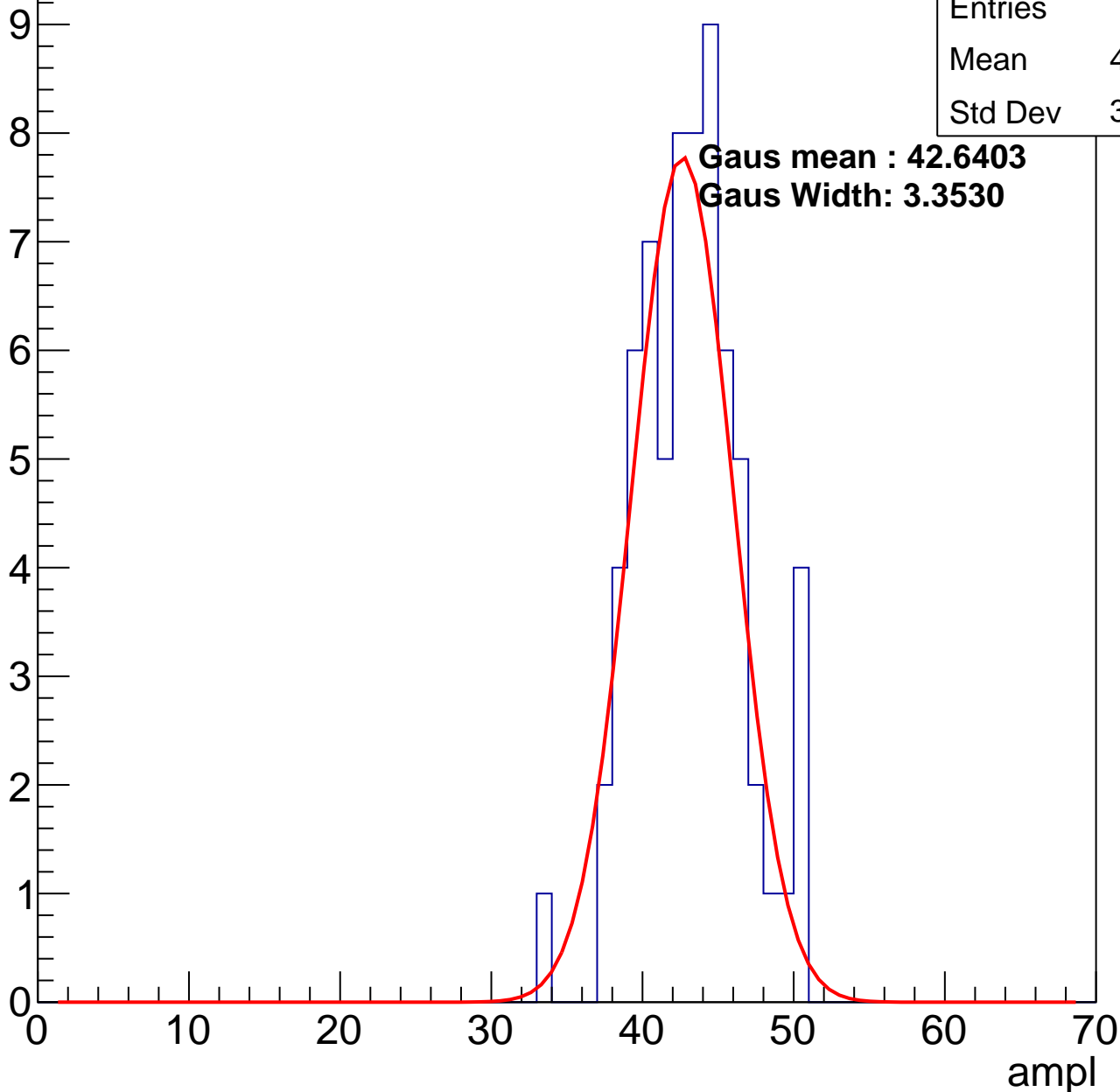
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 42.68 |
| Std Dev | 3.445 |

Gaus mean : 42.6403

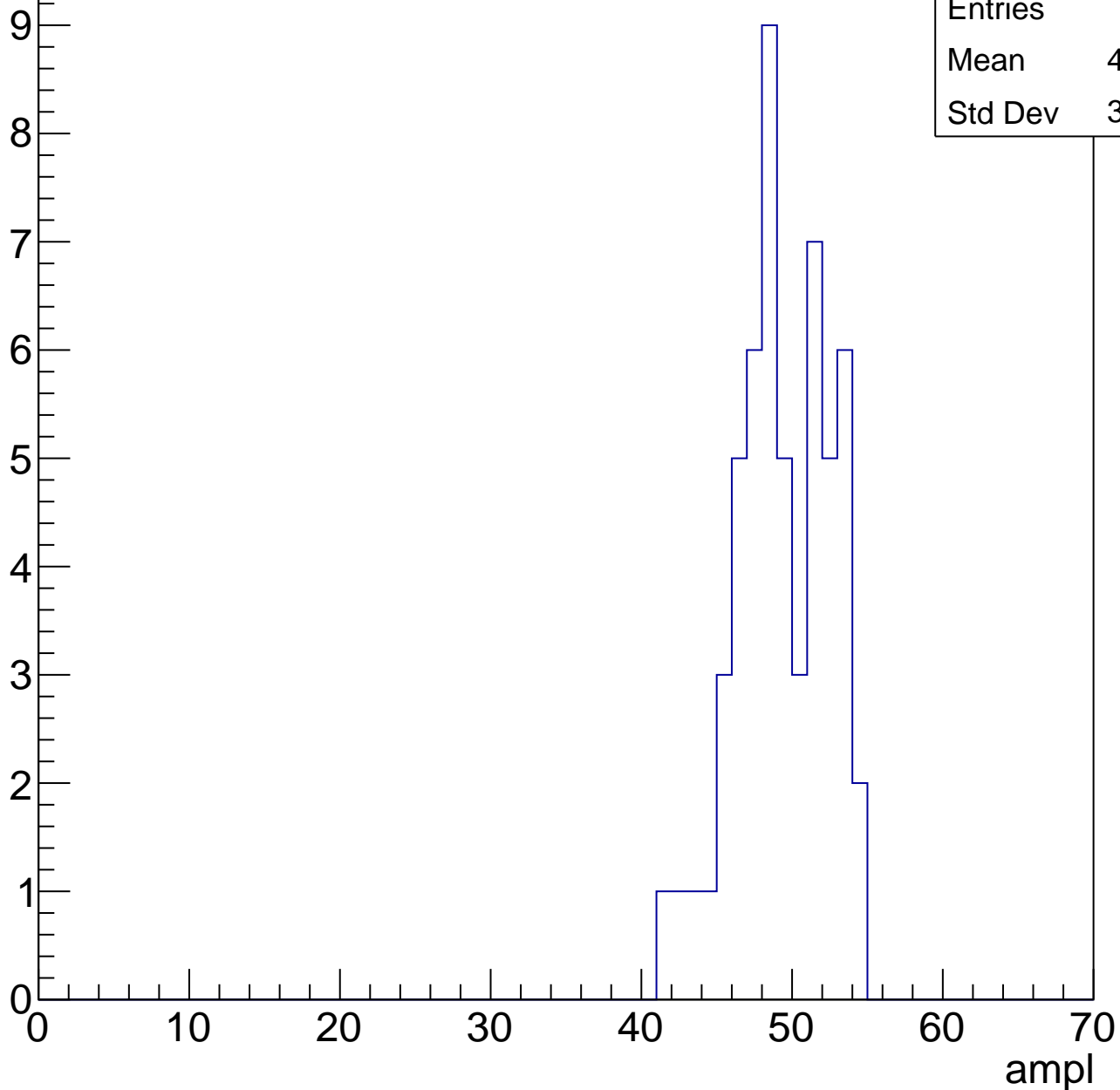
Gaus Width: 3.3530



B1L100S, U7-ch80, adc3

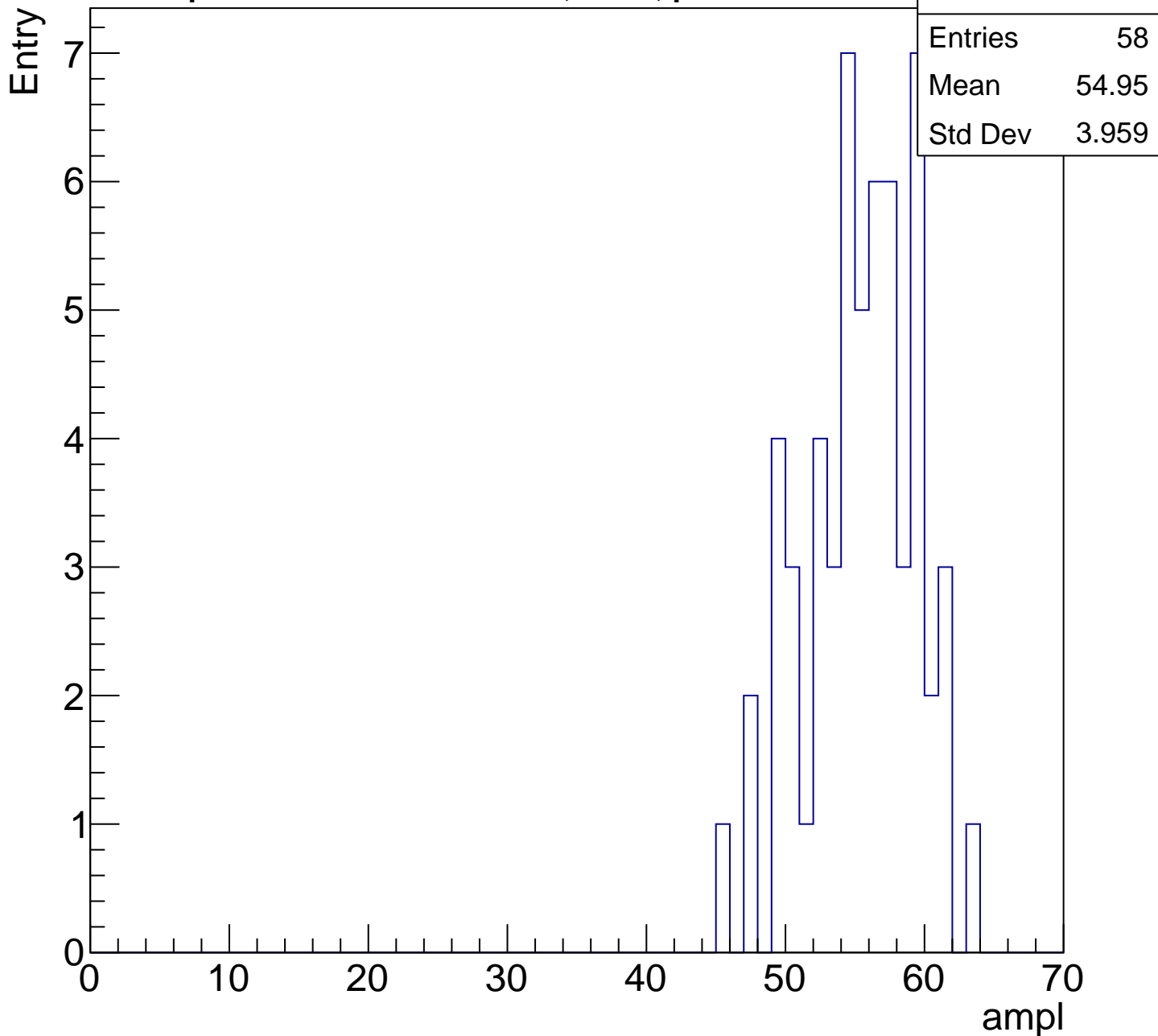
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 59

Mean 58.03

Std Dev 8.299

8

6

4

2

0

0

10

20

30

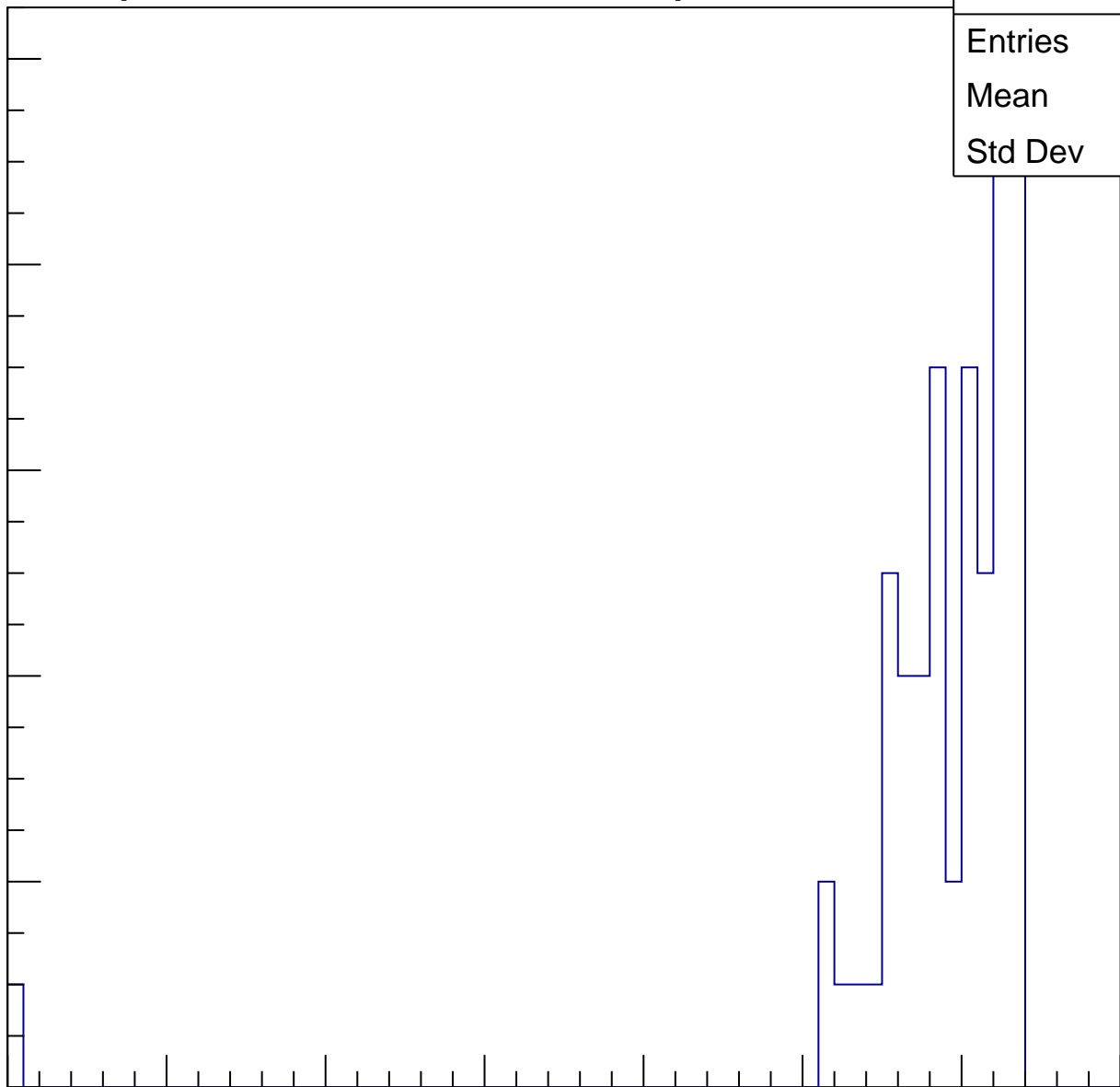
40

50

60

70

ampl



B1L100S, U7-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

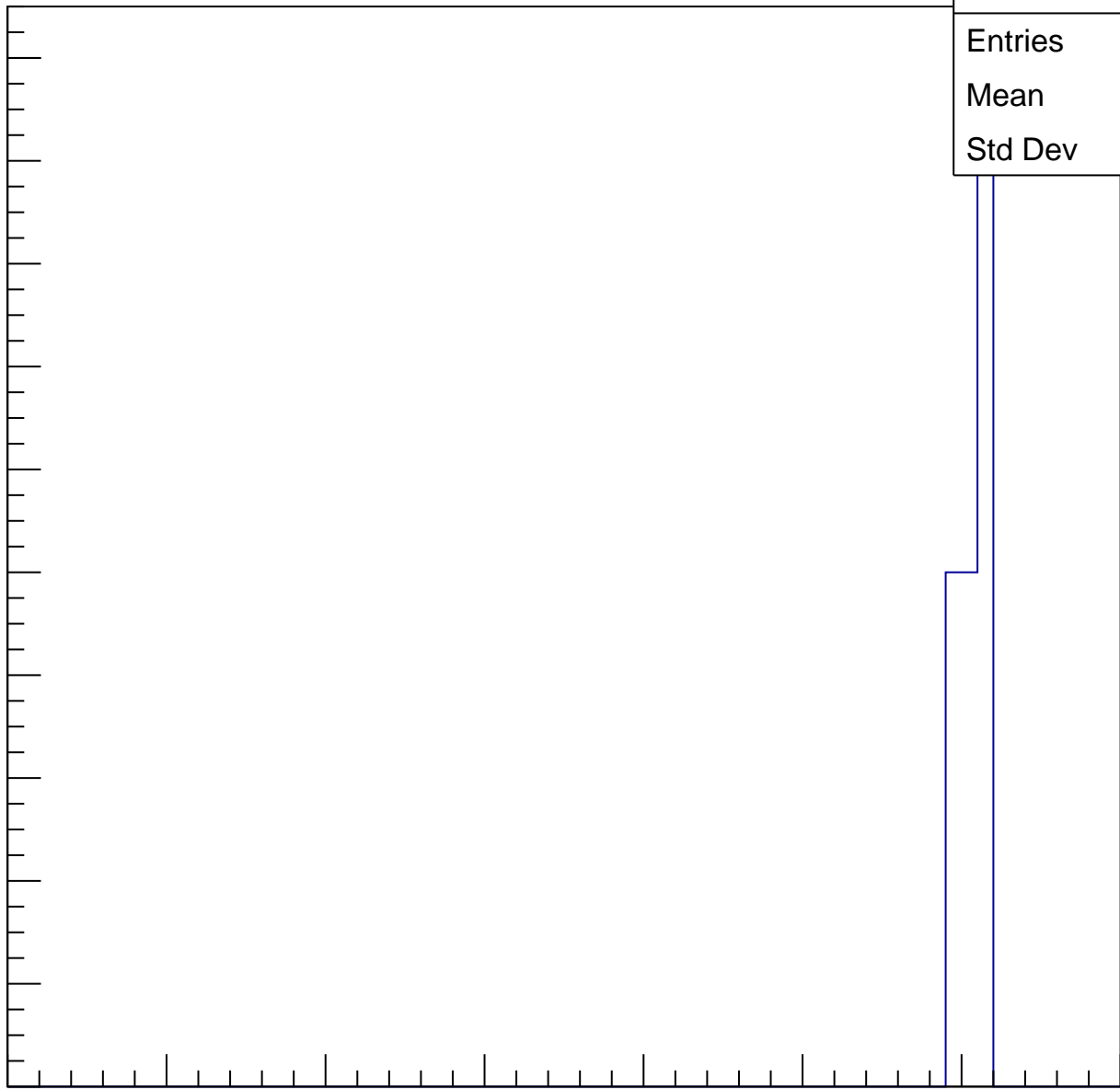
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 4 |
| Mean | 60.25 |
| Std Dev | 0.8292 |

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch81, adc0

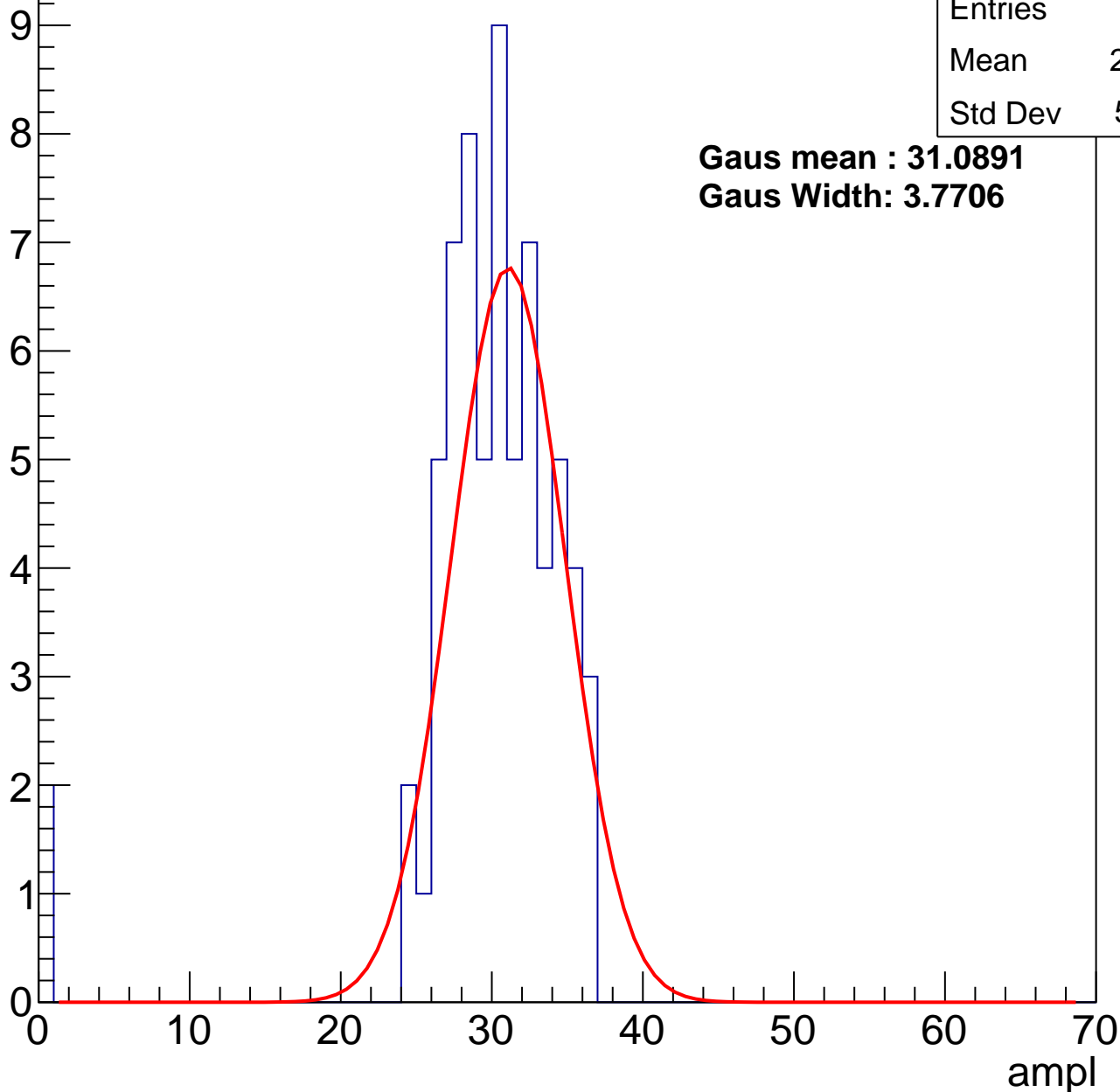
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 29.25 |
| Std Dev | 5.981 |

Gaus mean : 31.0891

Gaus Width: 3.7706



B1L100S, U7-ch81, adc1

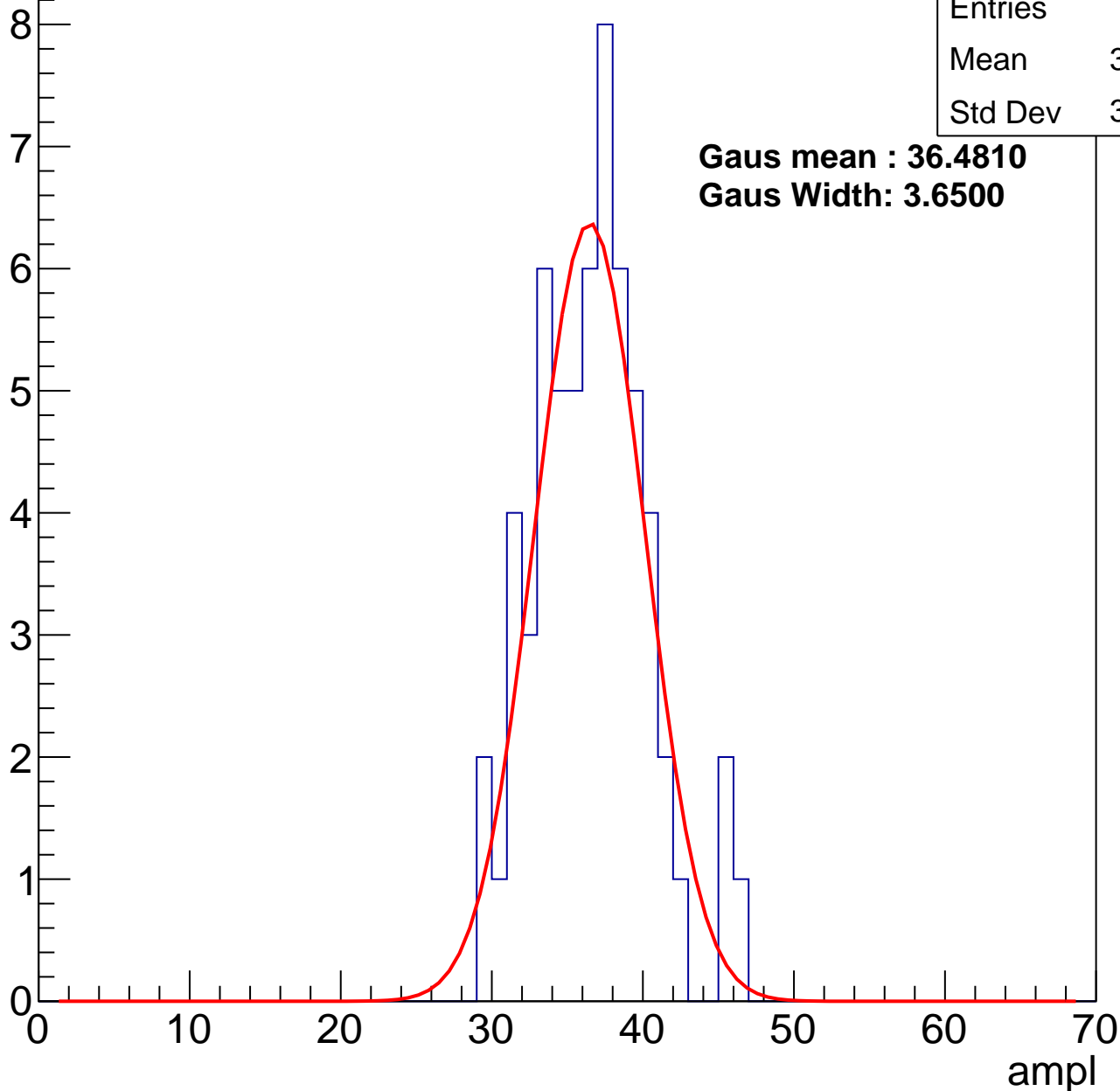
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 36.16 |
| Std Dev | 3.729 |

Gaus mean : 36.4810

Gaus Width: 3.6500



B1L100S, U7-ch81, adc2

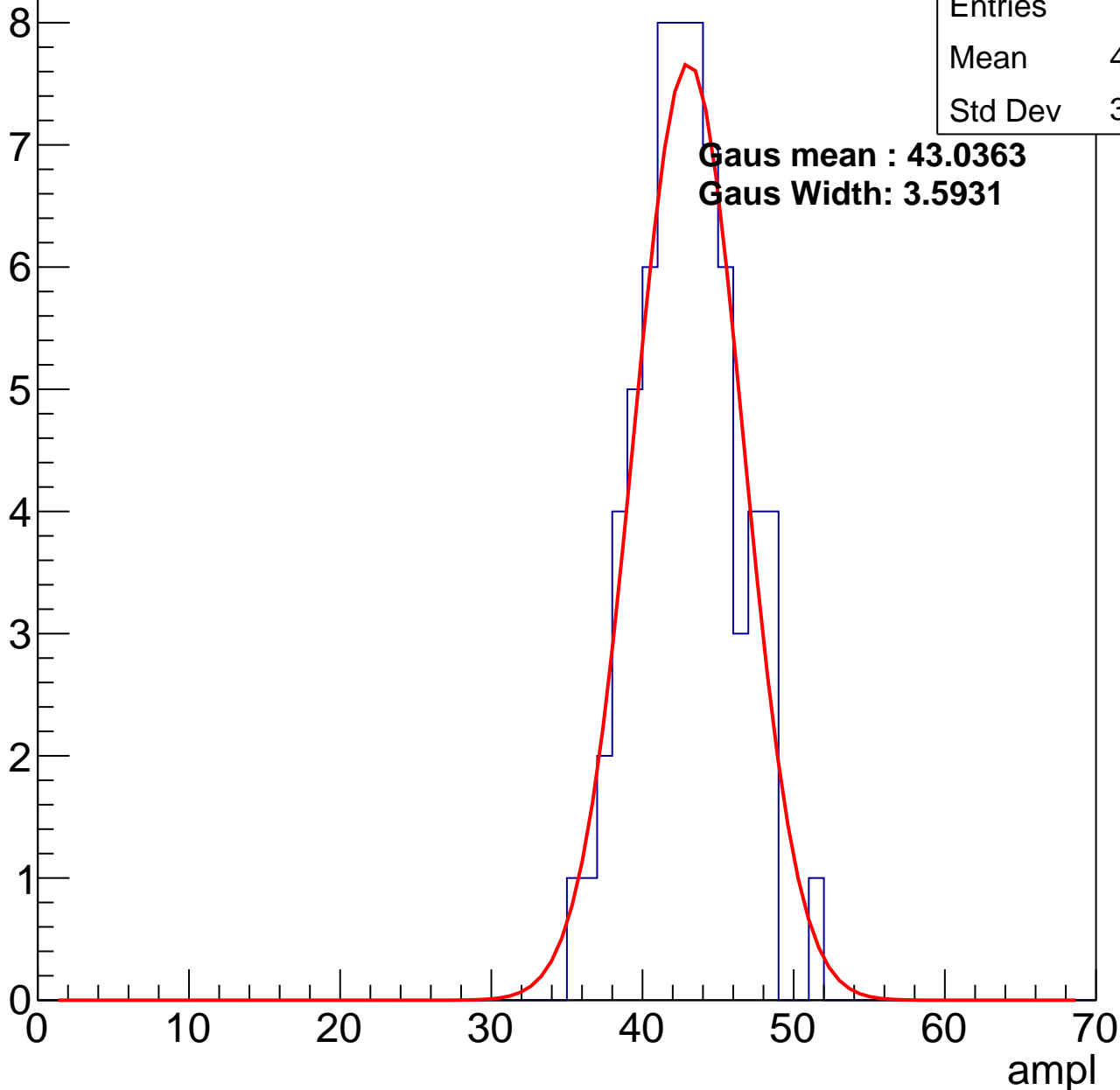
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.46 |
| Std Dev | 3.265 |

Gaus mean : 43.0363

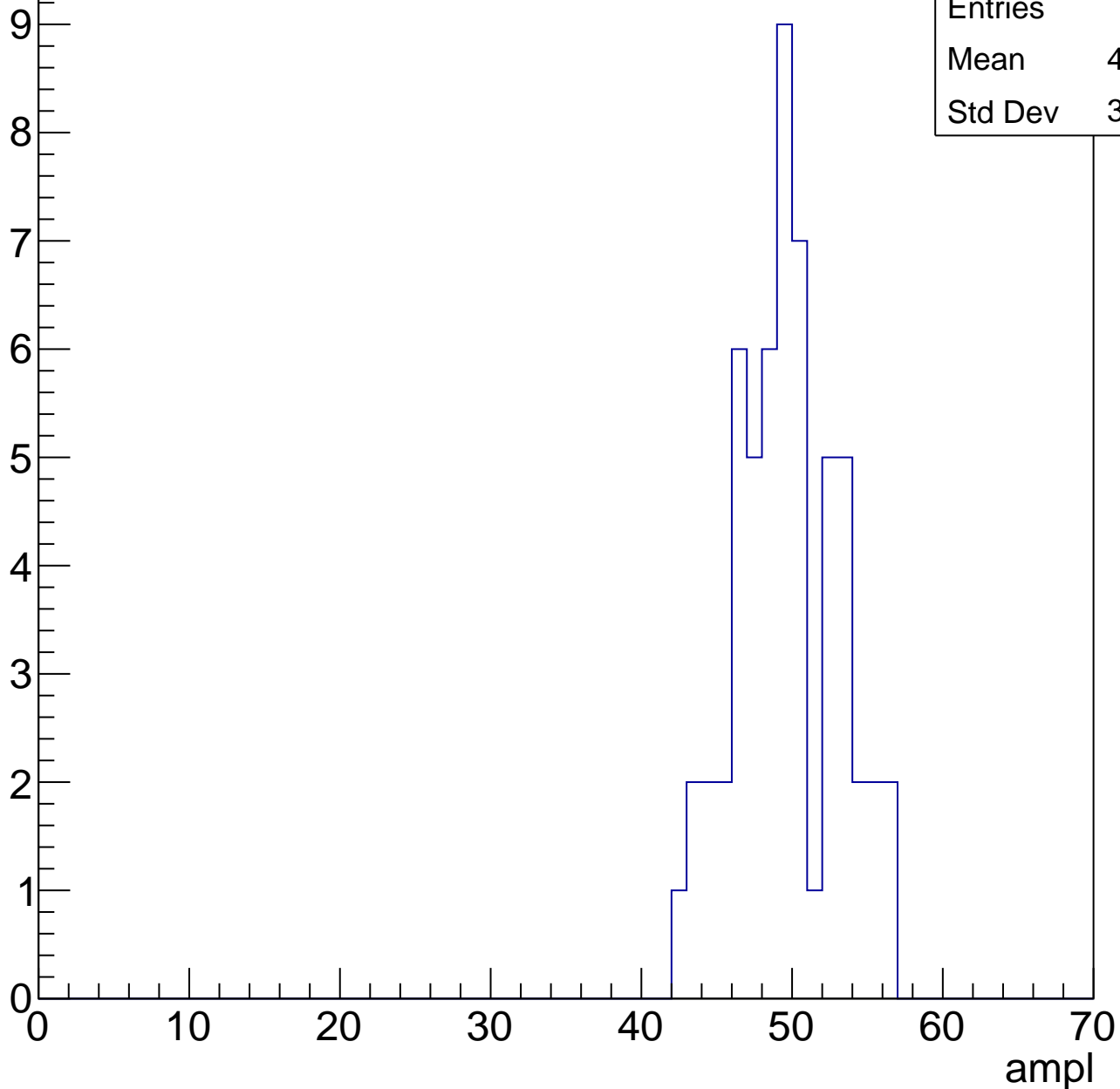
Gaus Width: 3.5931



B1L100S, U7-ch81, adc3

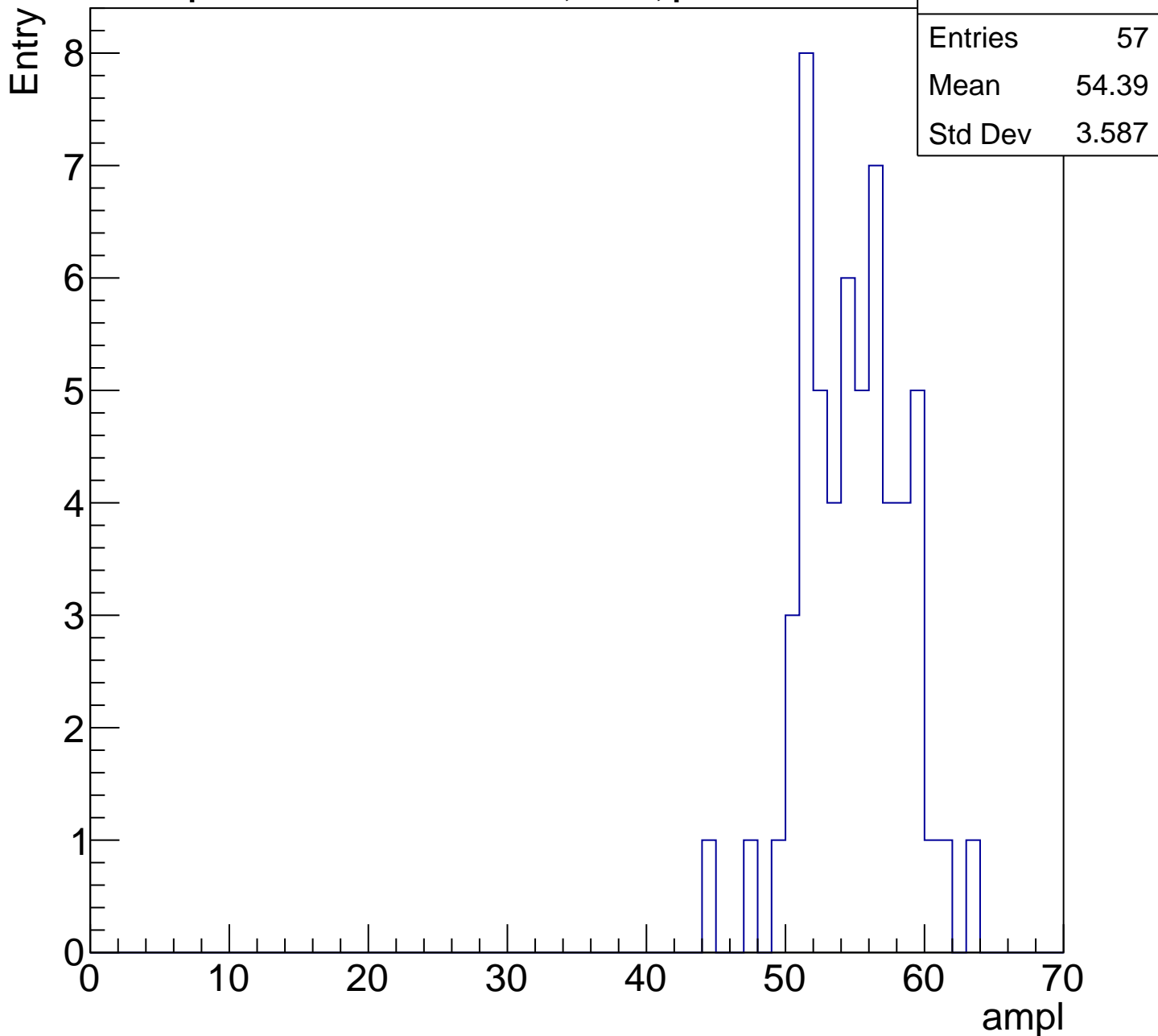
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch81, adc5

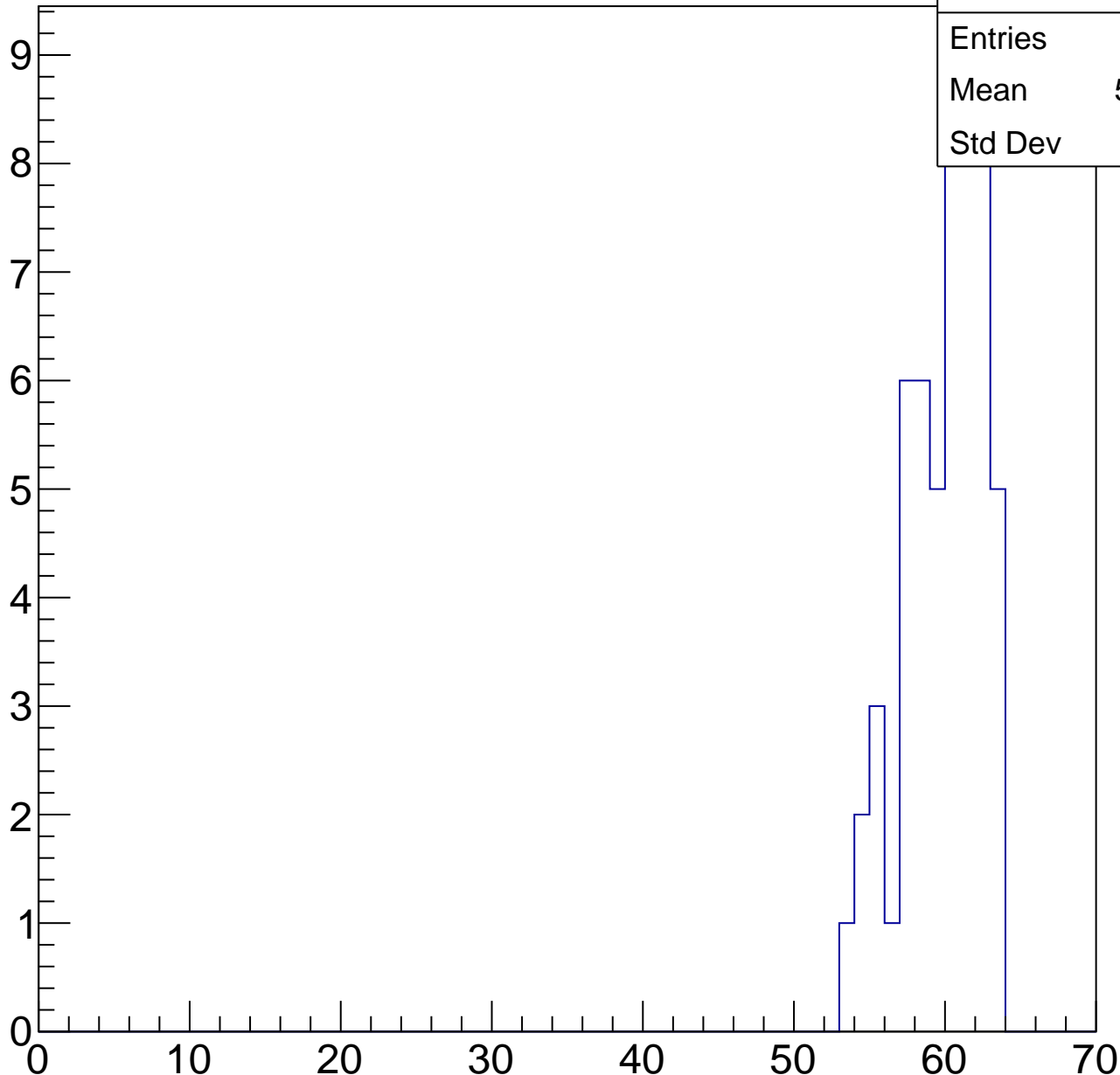
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 54 |
| Mean | 59.41 |
| Std Dev | 2.6 |

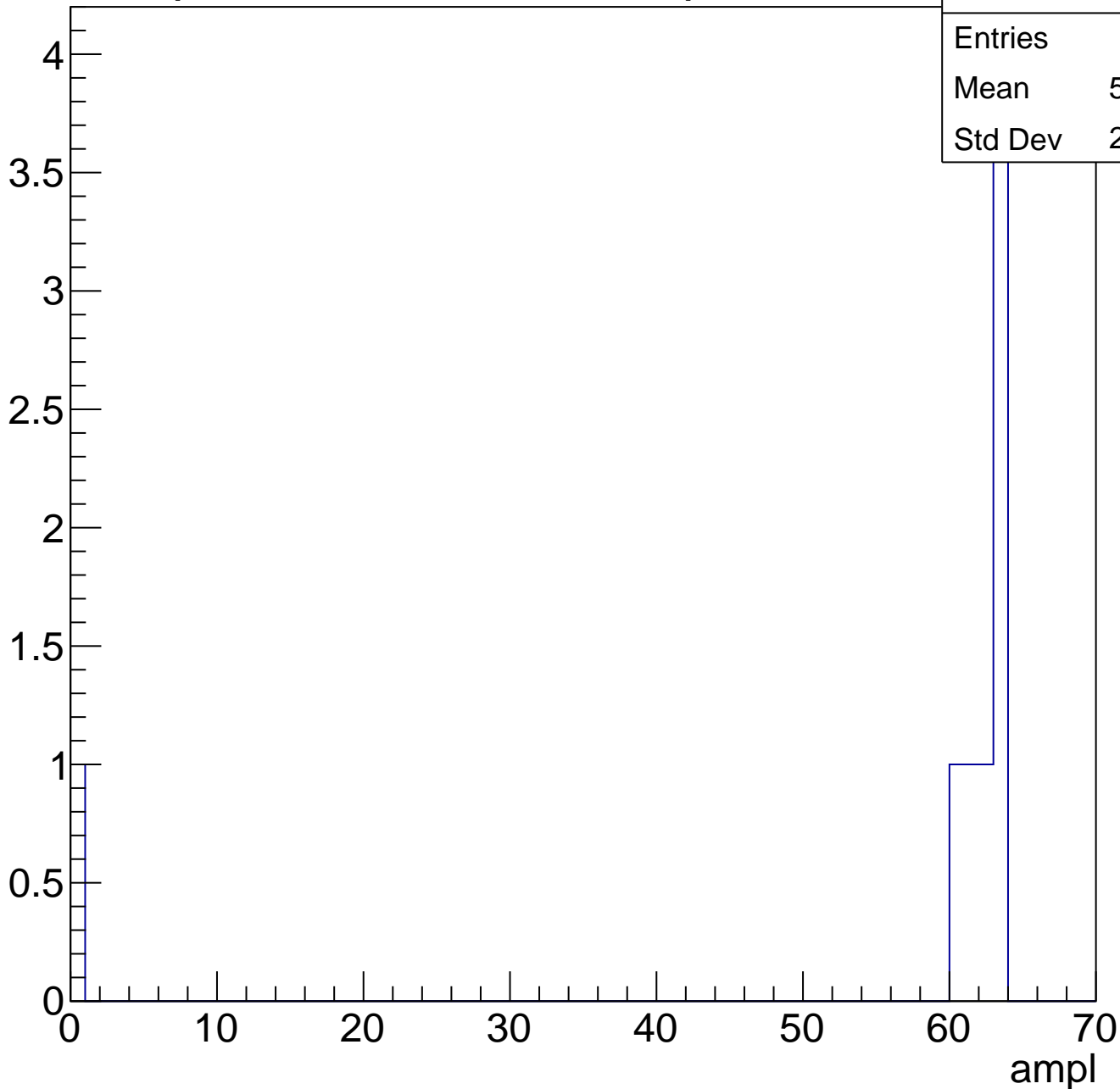
ampl



B1L100S, U7-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch82, adc0

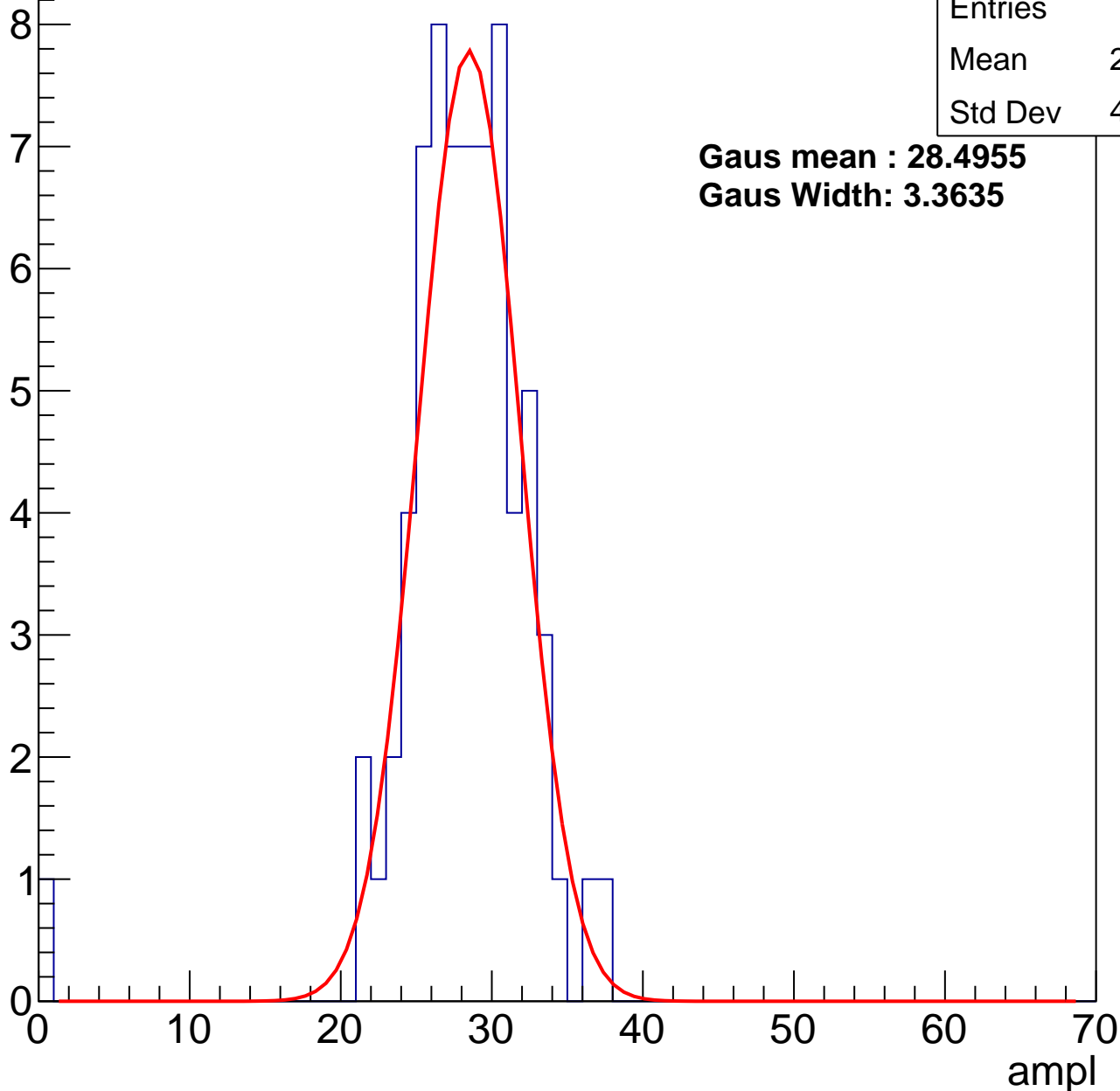
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 27.64 |
| Std Dev | 4.718 |

Gaus mean : 28.4955

Gaus Width: 3.3635



B1L100S, U7-ch82, adc1

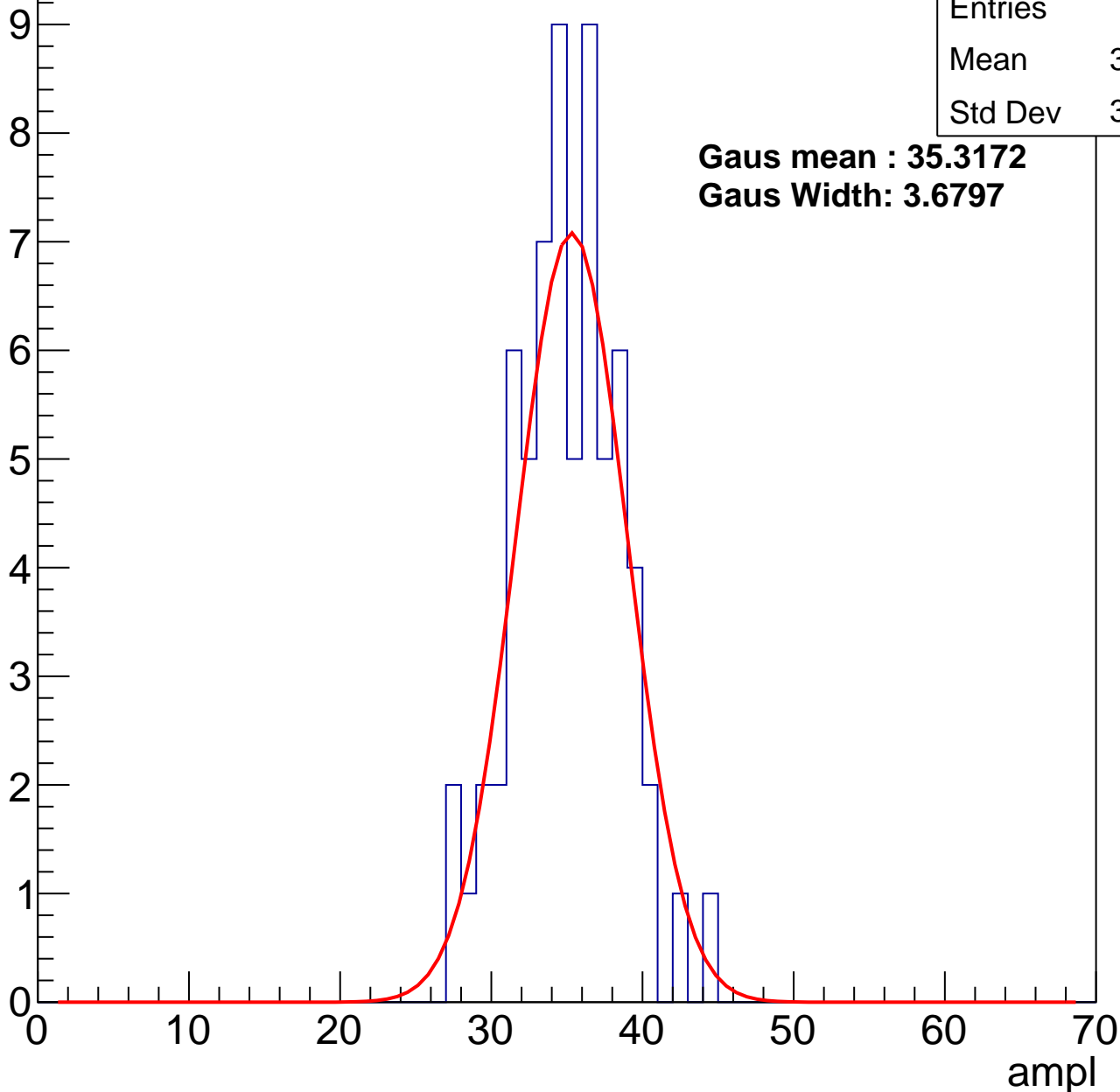
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 34.58 |
| Std Dev | 3.439 |

Gaus mean : 35.3172

Gaus Width: 3.6797



B1L100S, U7-ch82, adc2

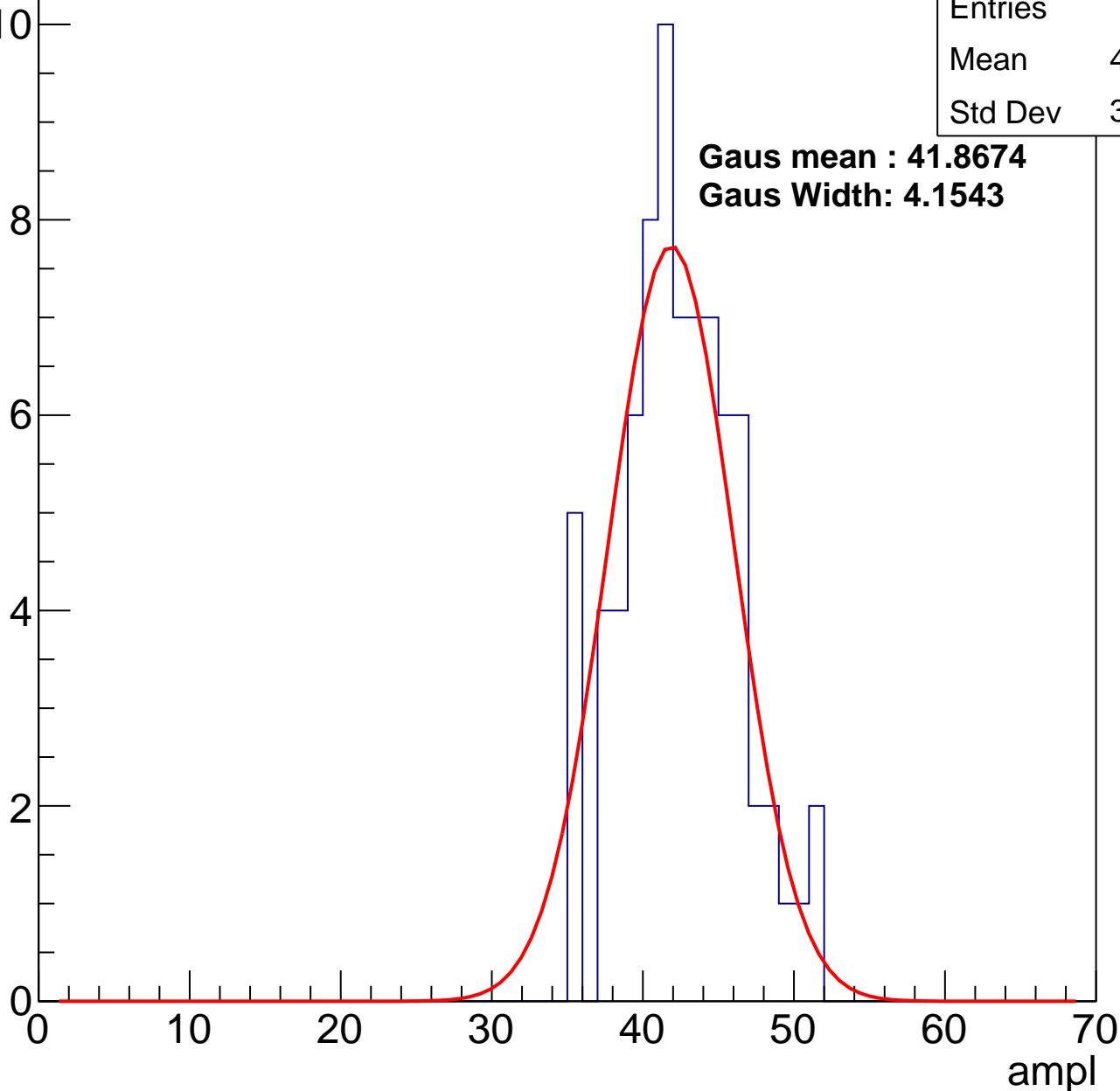
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.04 |
| Std Dev | 3.733 |

Gaus mean : 41.8674

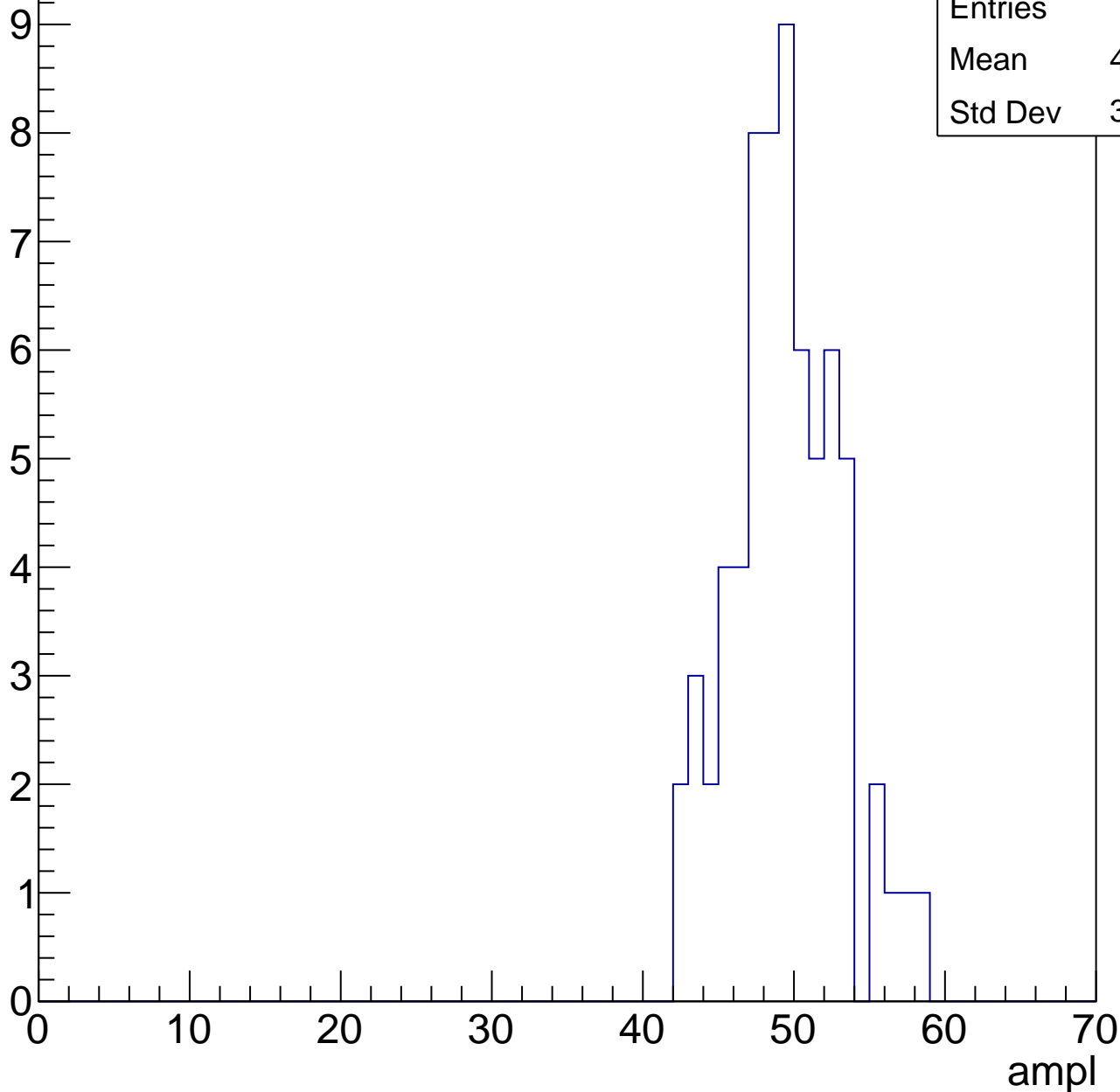
Gaus Width: 4.1543



B1L100S, U7-ch82, adc3

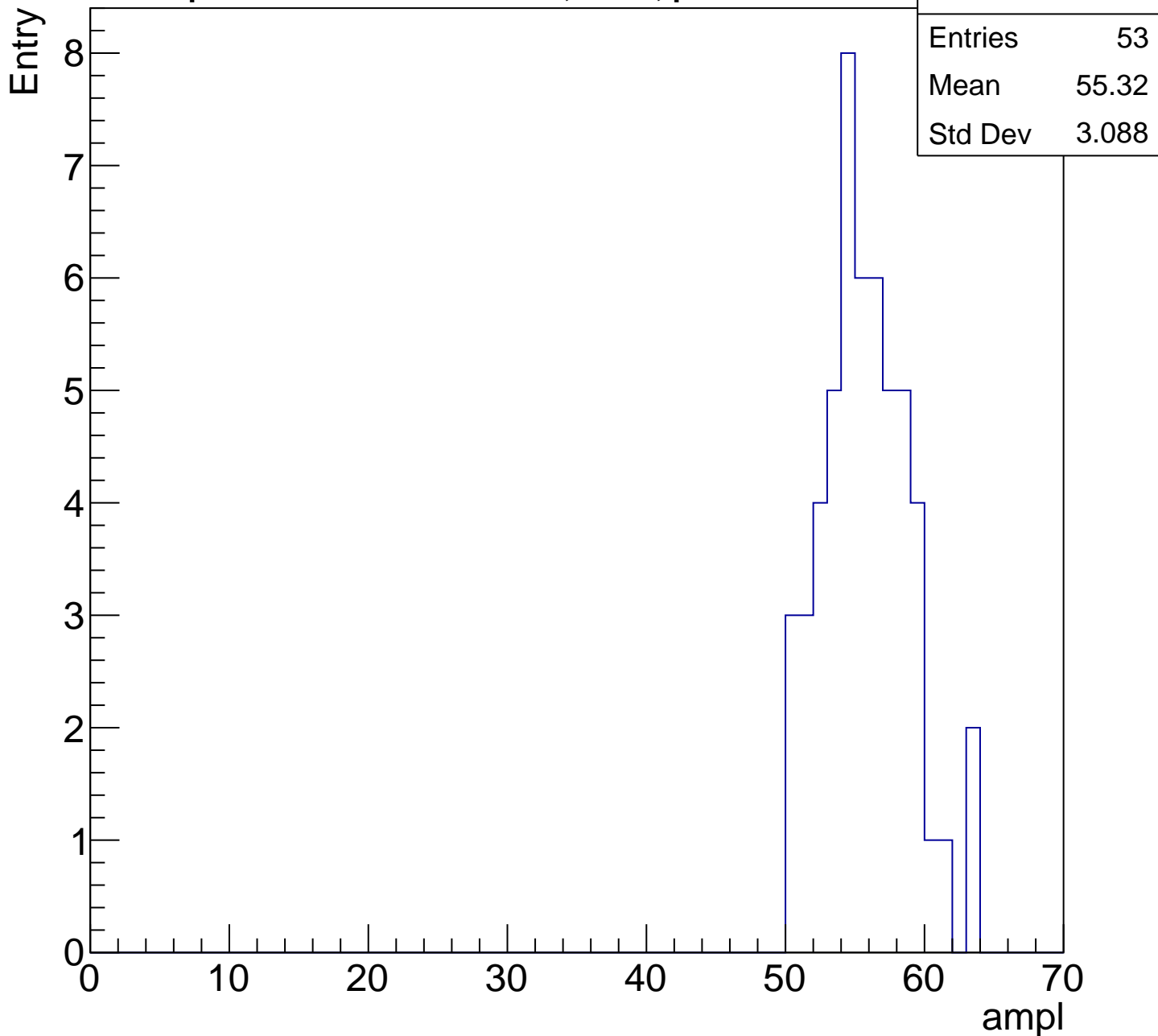
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.27 |
| Std Dev | 8.75 |

ampl

0

10

20

30

40

50

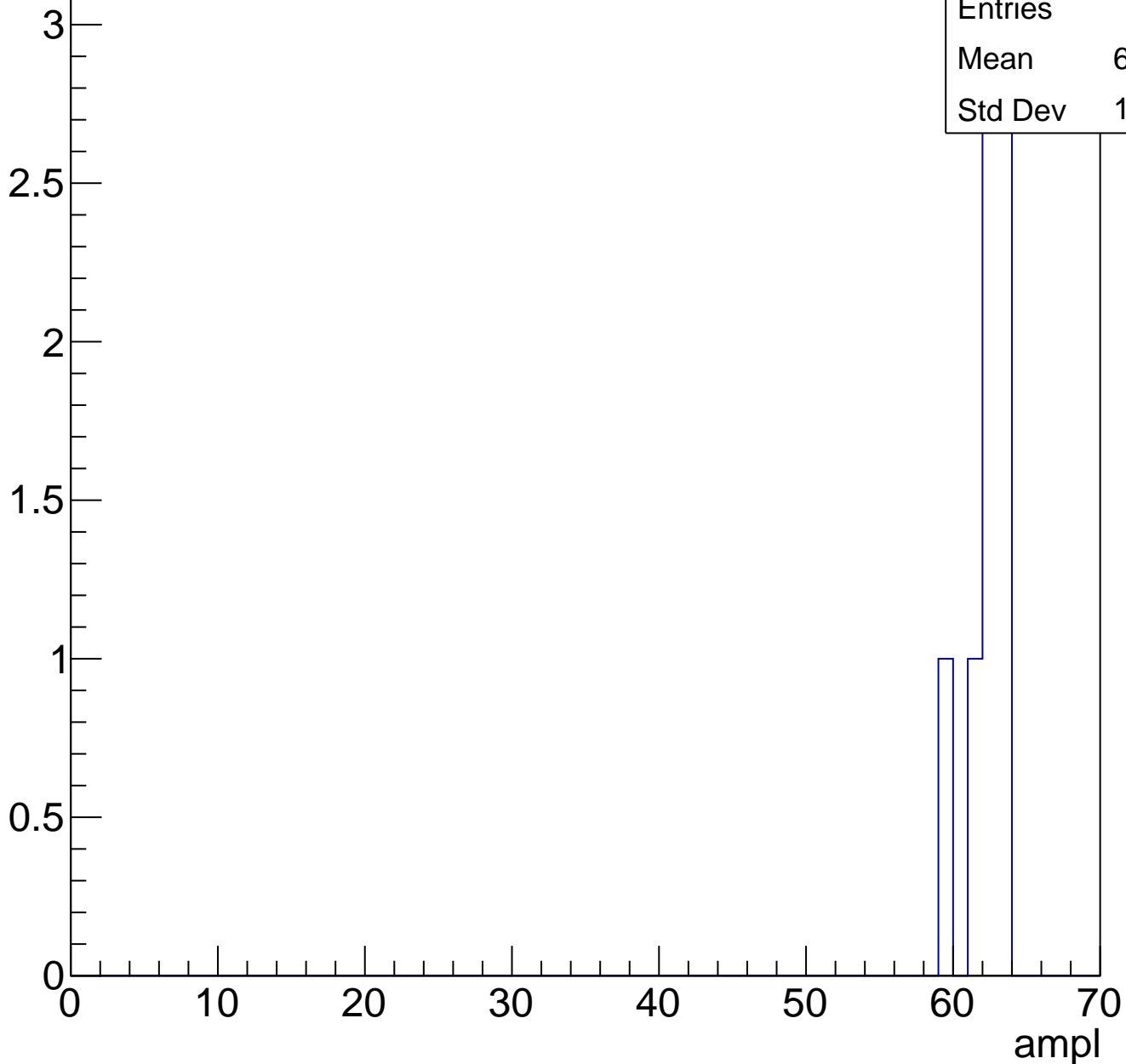
60

70

B1L100S, U7-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

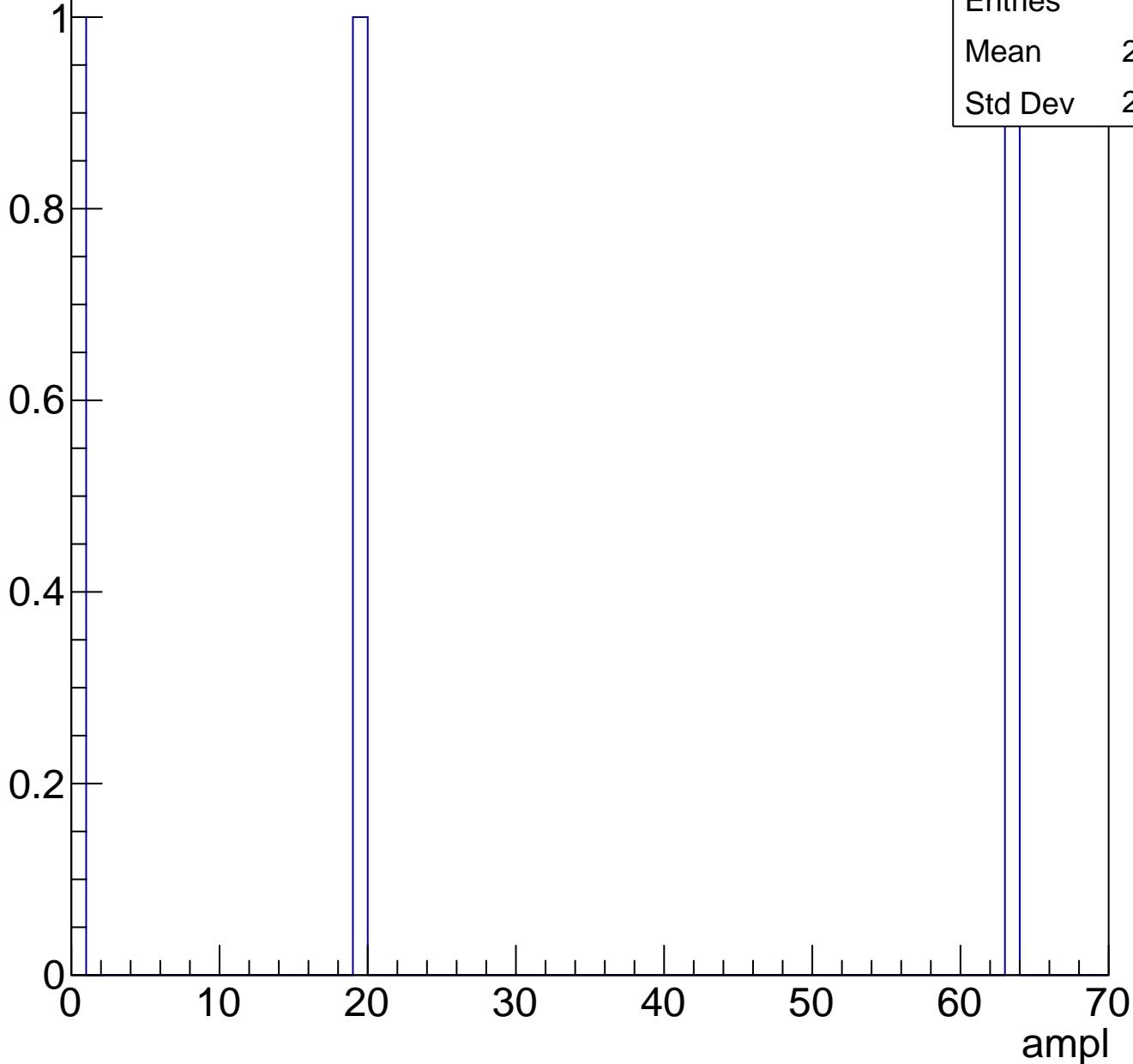
Entry



B1L100S, U7-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch83, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 27.82 |
| Std Dev | 5.897 |

Gaus mean : 29.3464

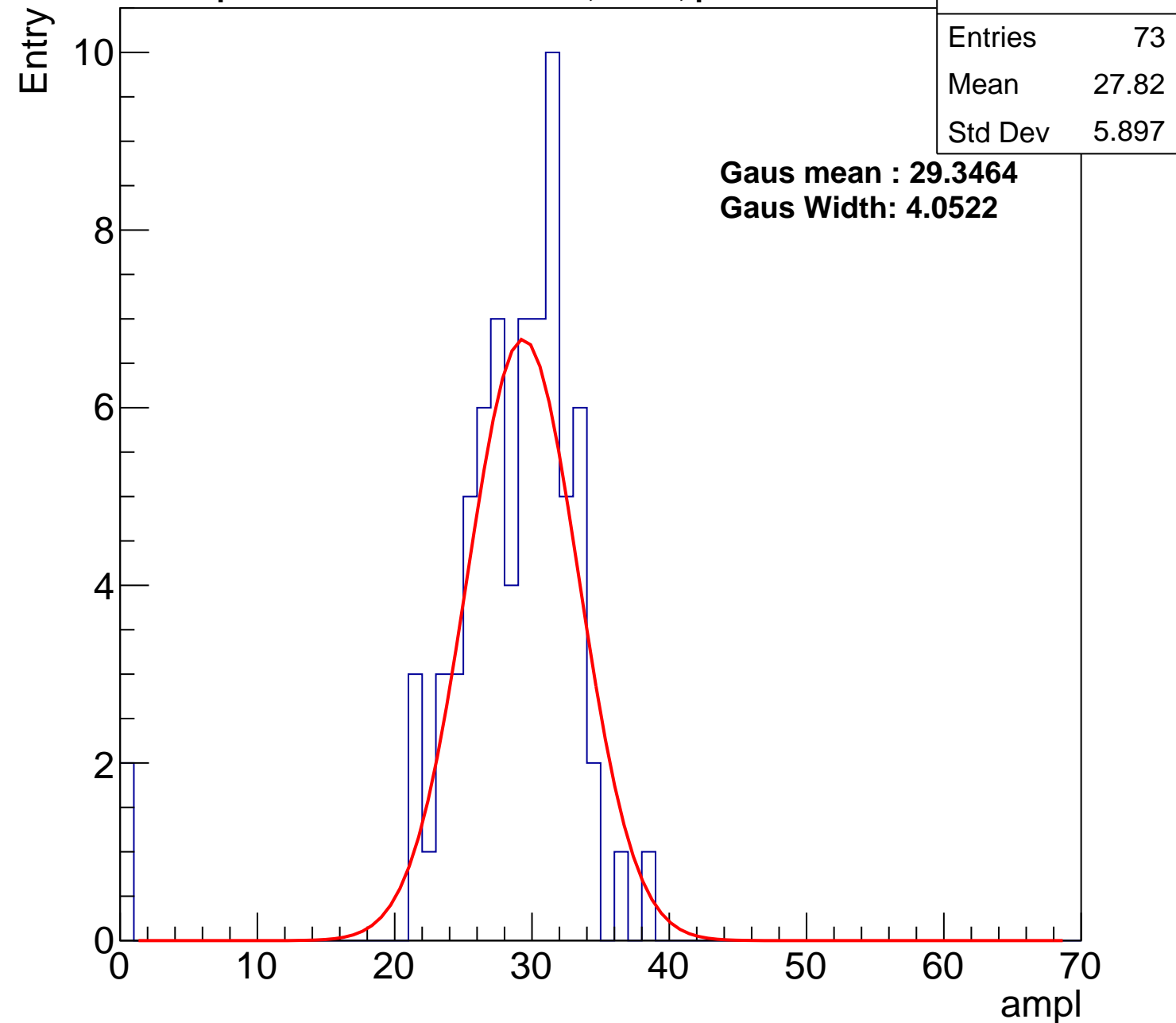
Gaus Width: 4.0522

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch83, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 62 |
| Mean | 34.58 |
| Std Dev | 3.644 |

Gaus mean : 34.8738

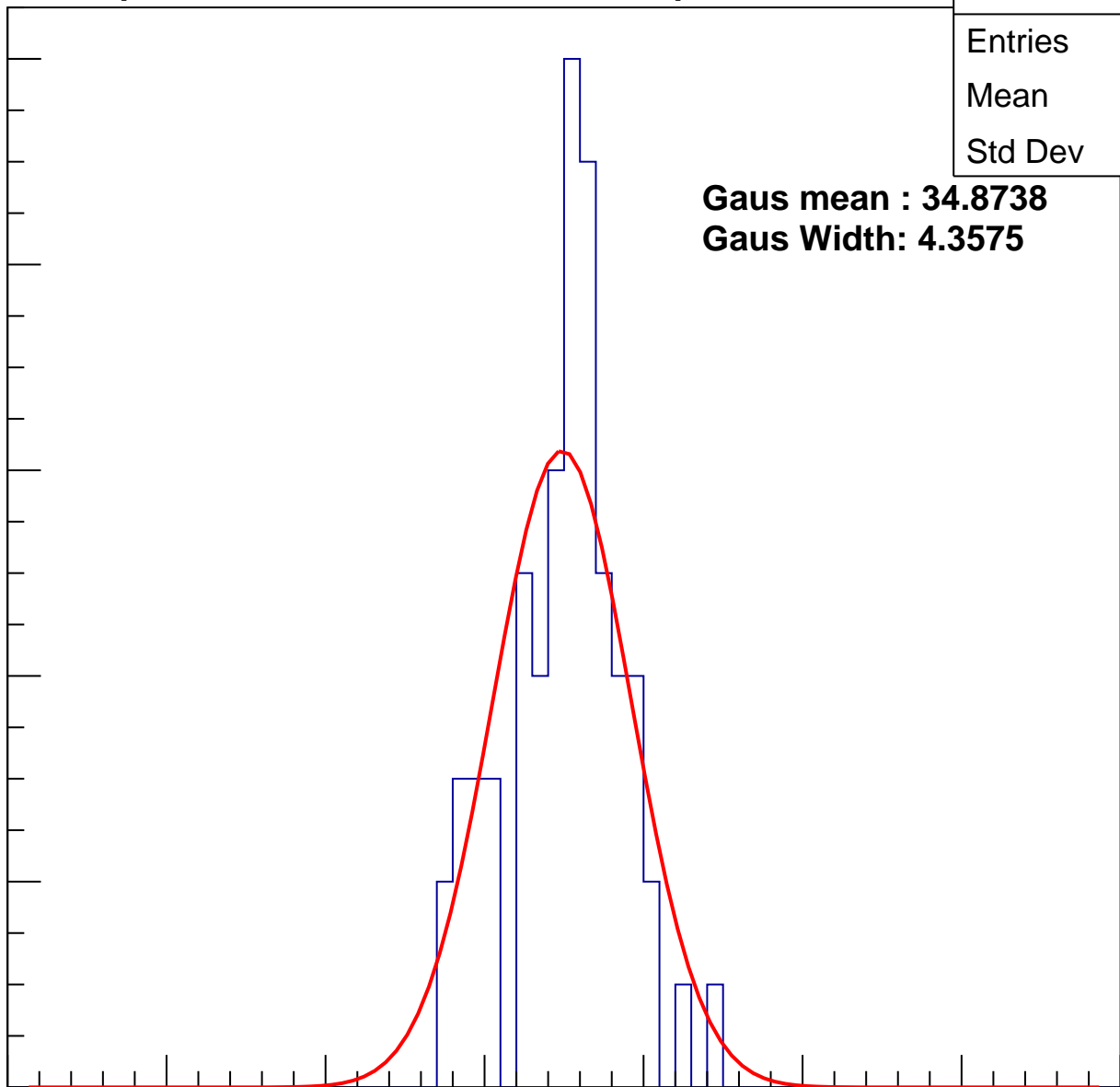
Gaus Width: 4.3575

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch83, adc2

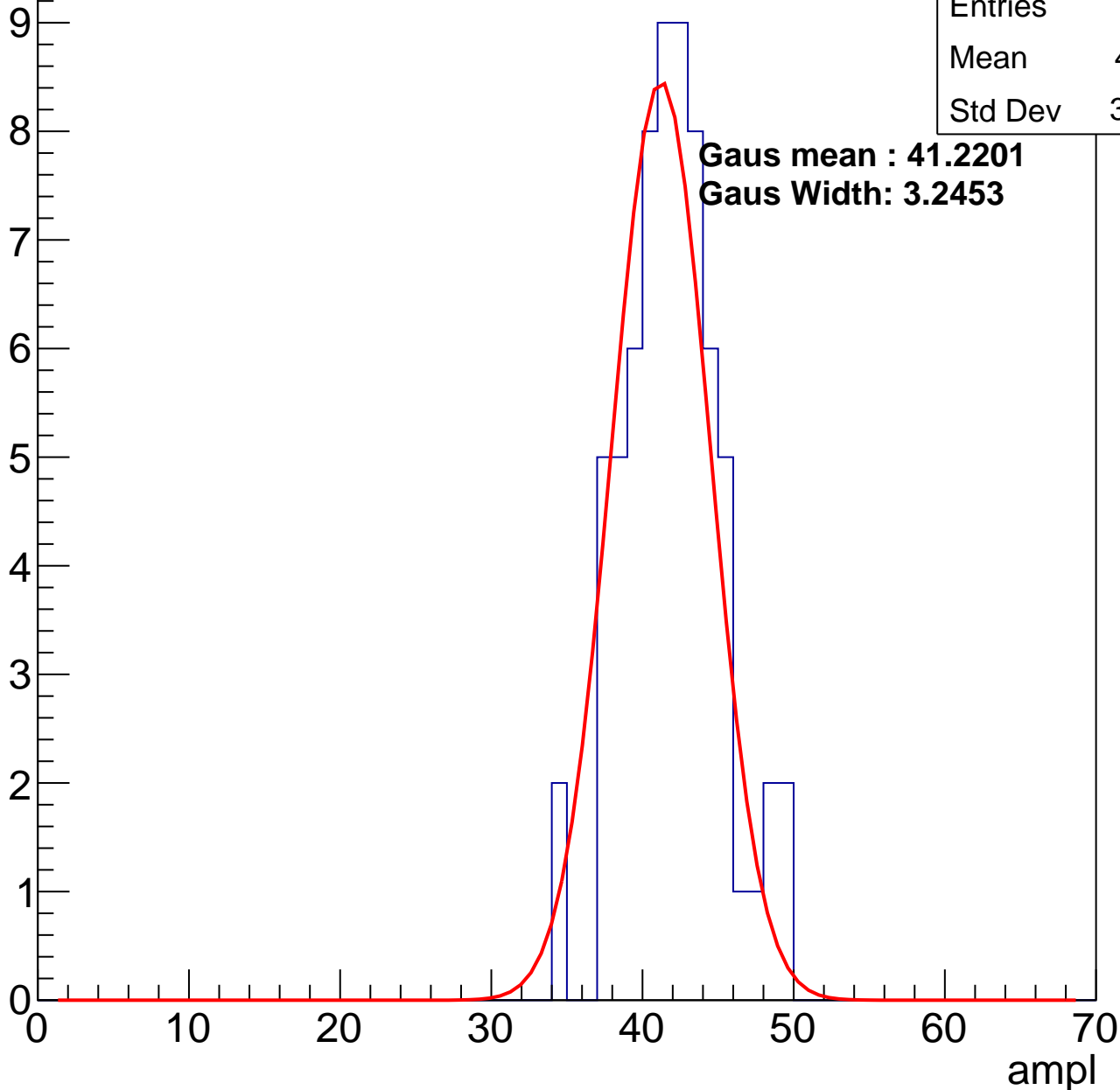
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 41.51 |
| Std Dev | 3.183 |

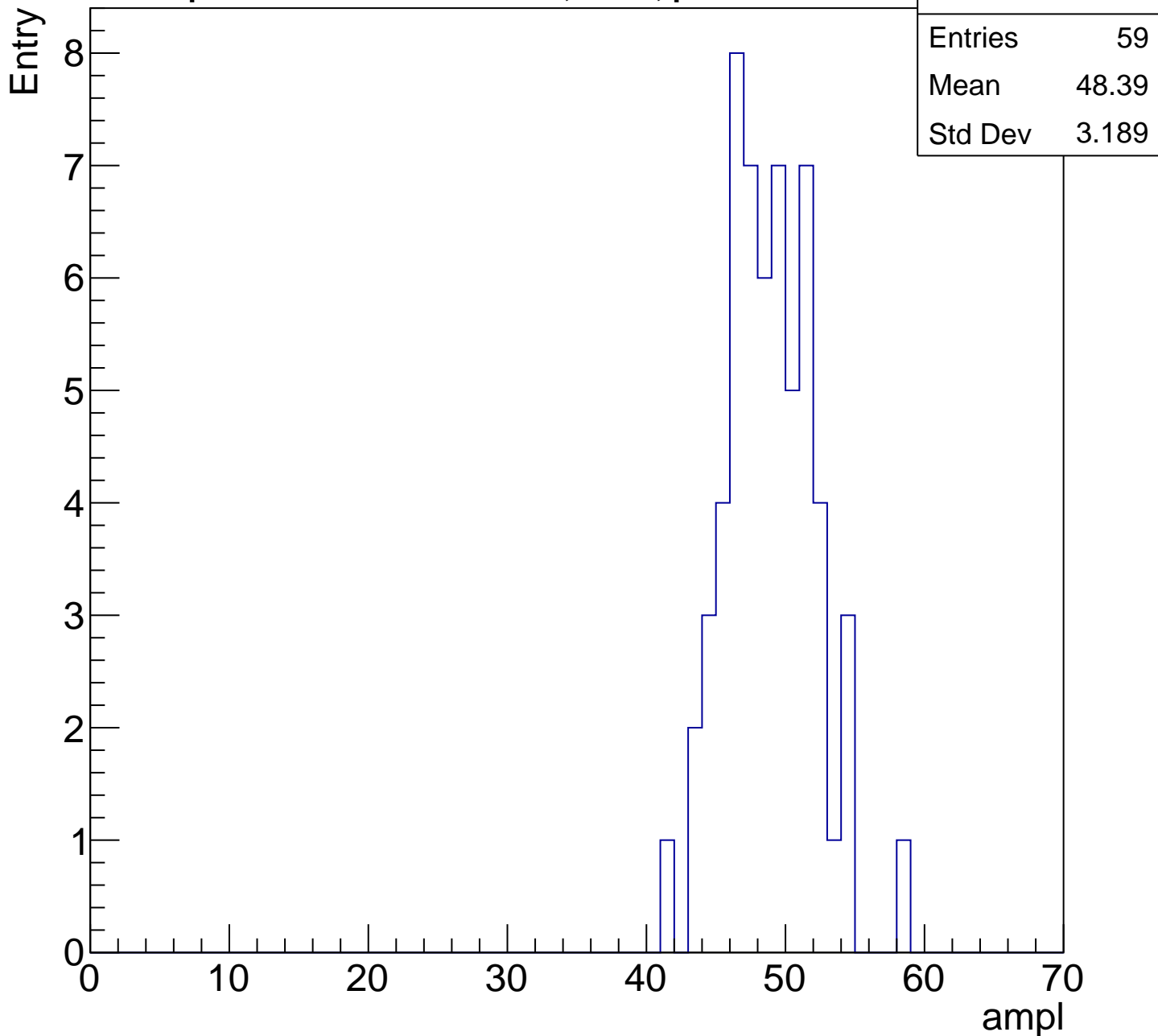
Gaus mean : 41.2201

Gaus Width: 3.2453



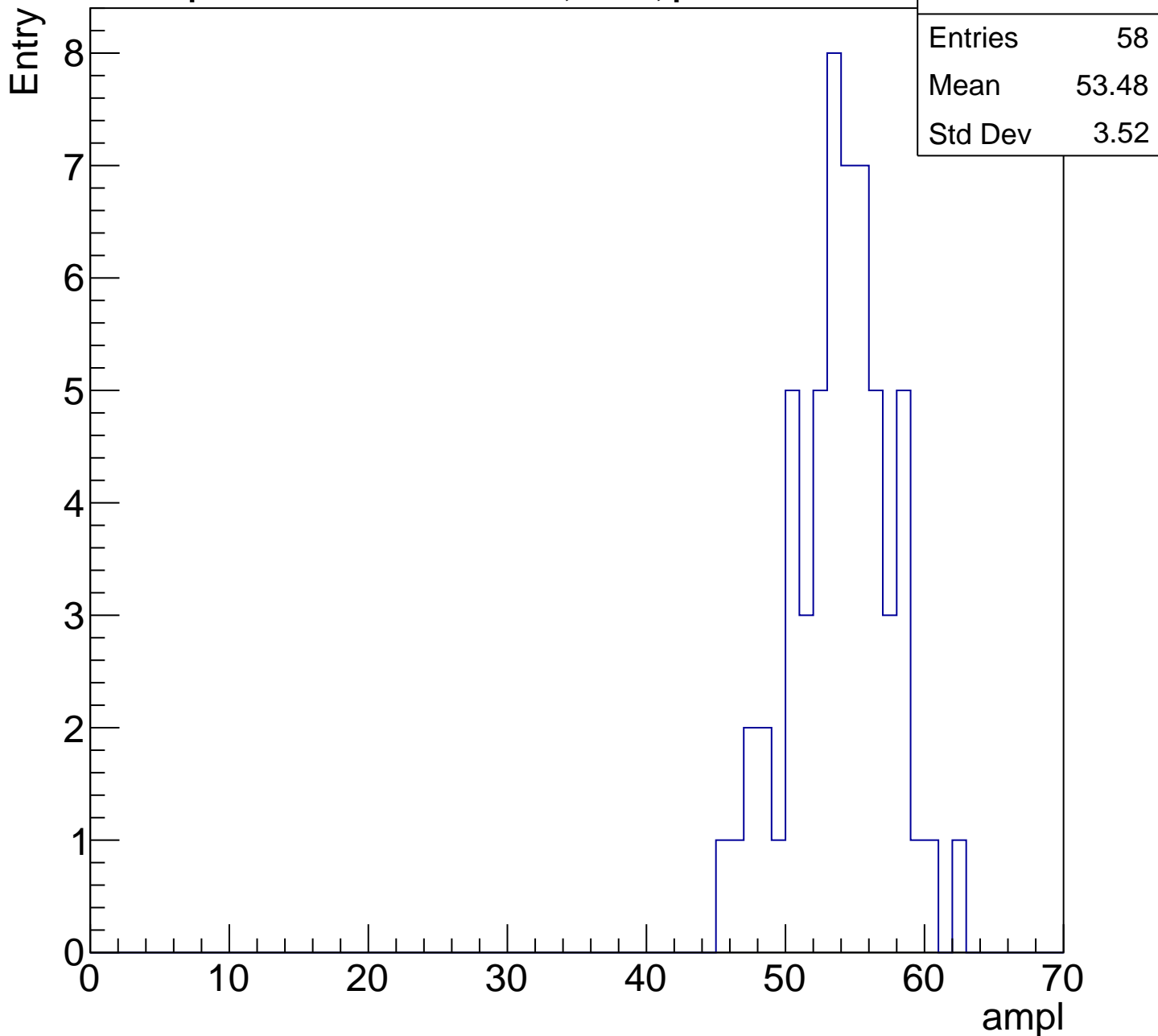
B1L100S, U7-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2



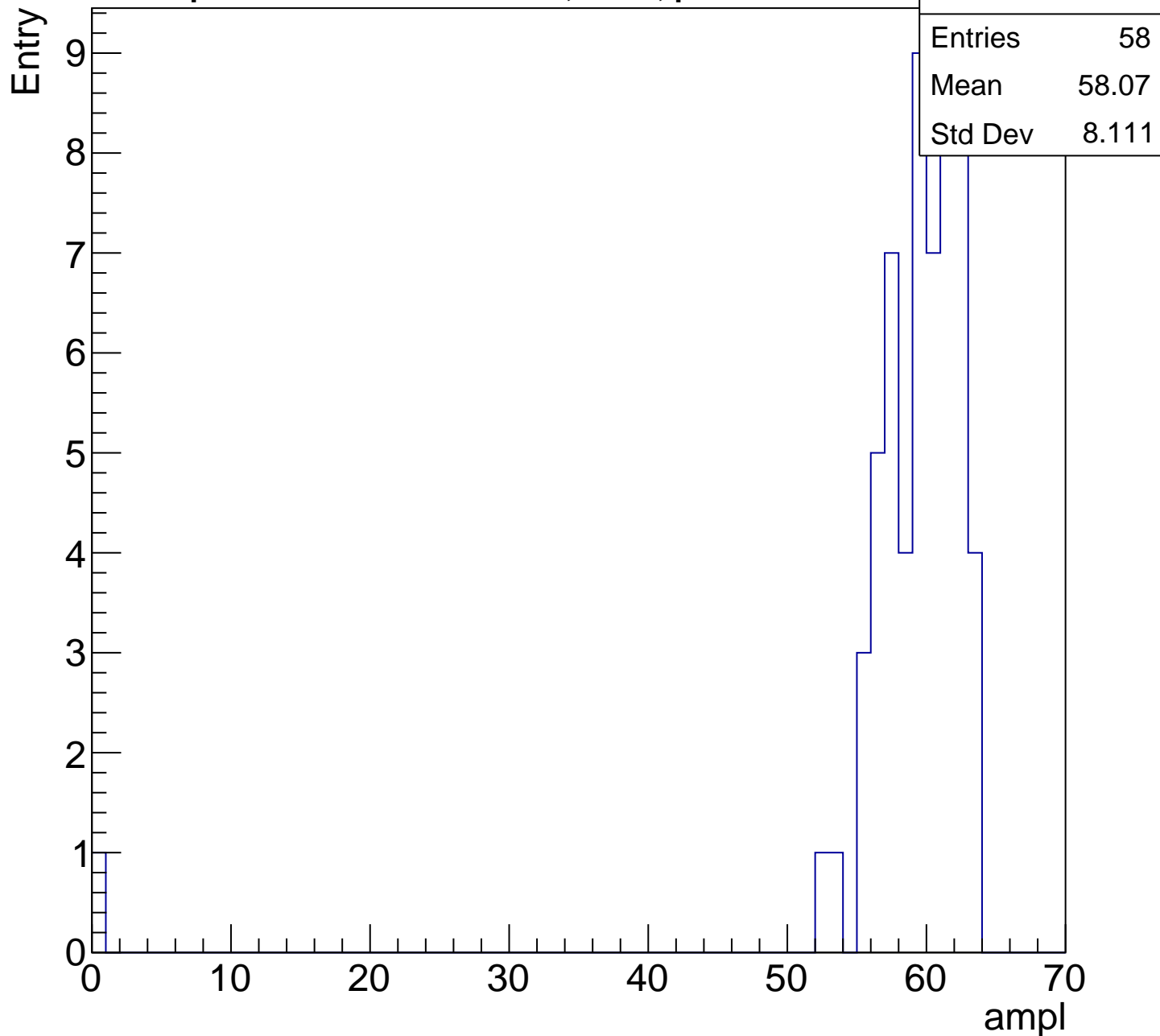
B1L100S, U7-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

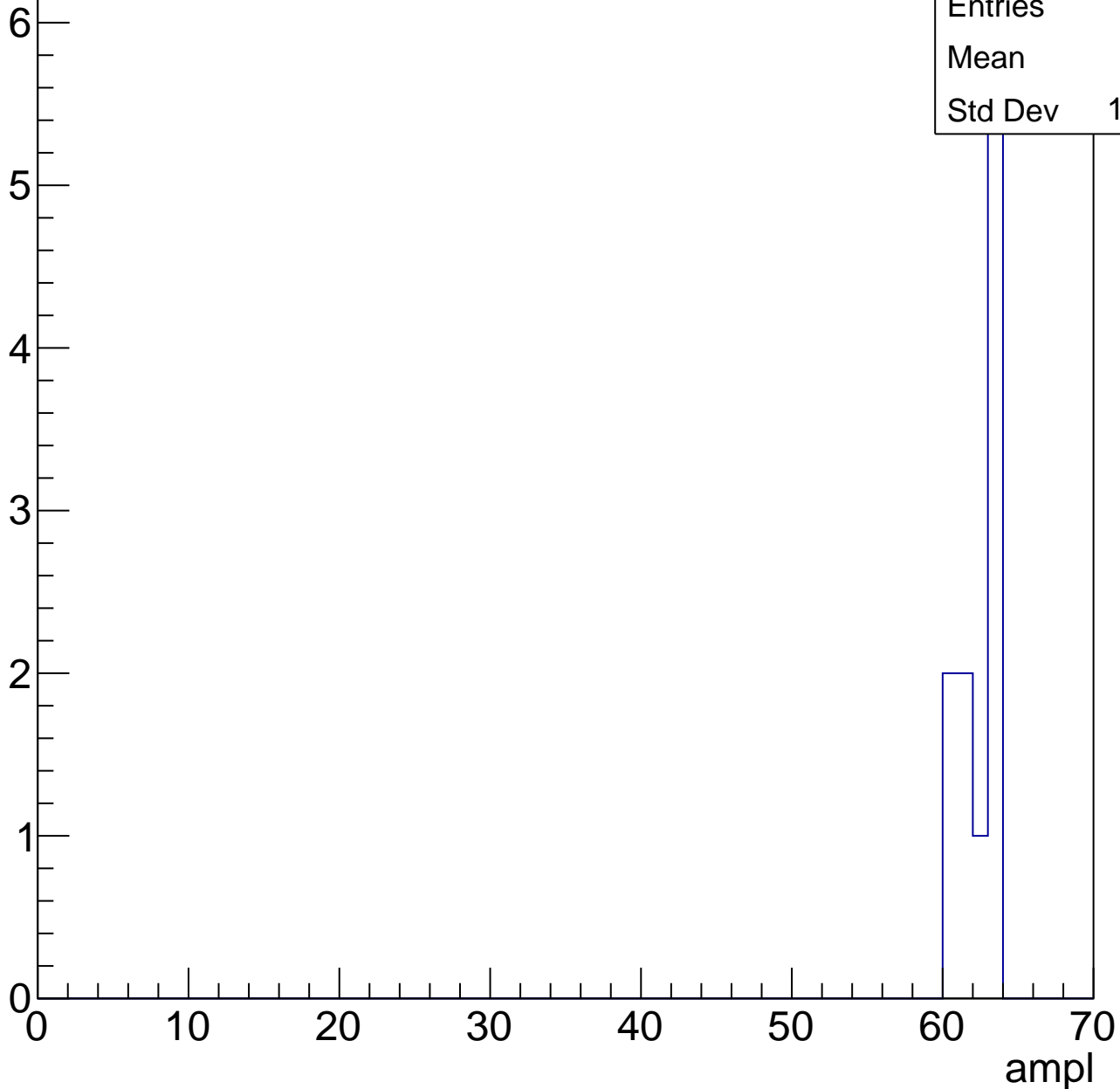


B1L100S, U7-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 62 |
| Std Dev | 1.206 |



B1L100S, U7-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

B1L100S, U7-ch84, adc0

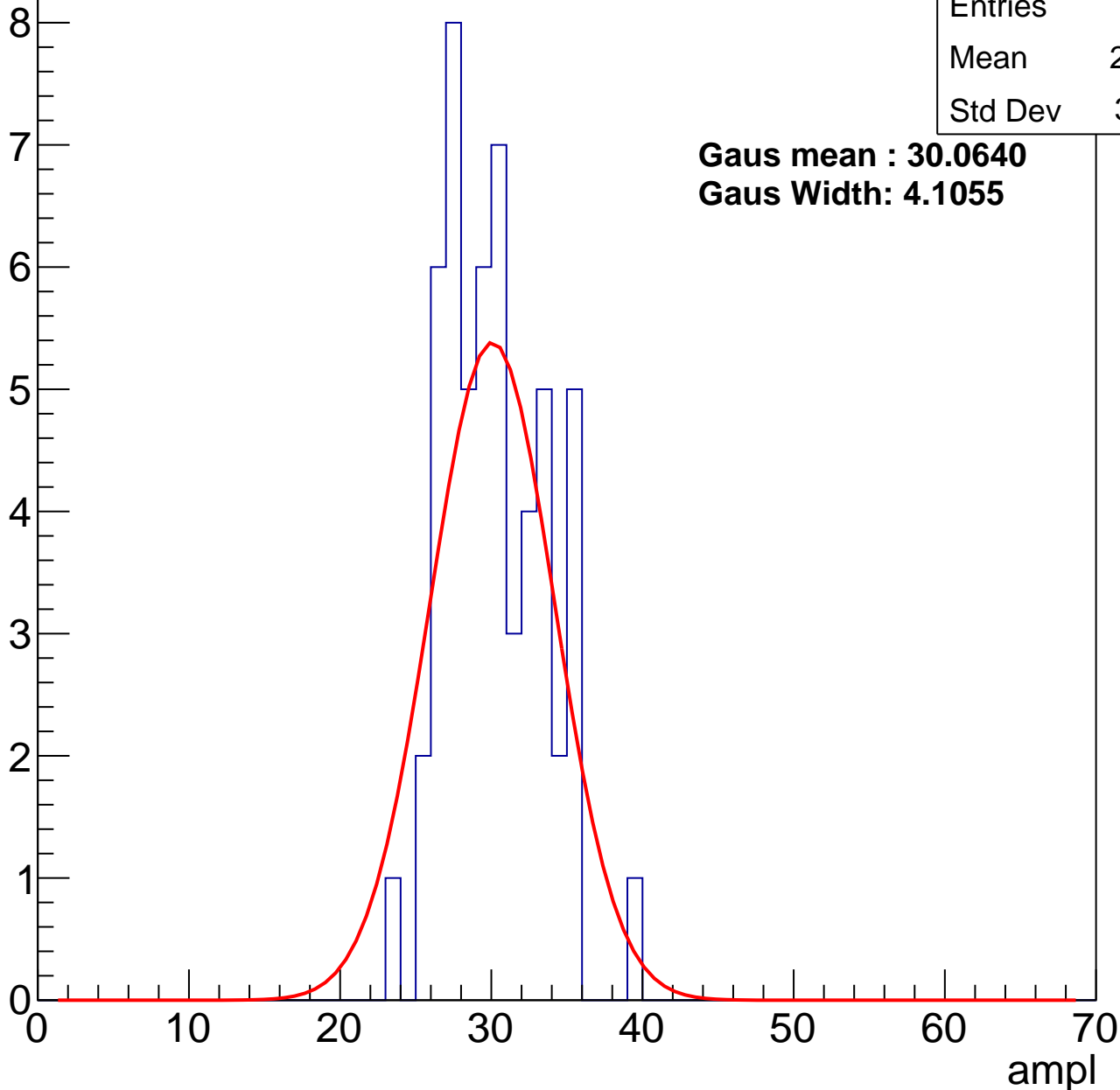
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 29.76 |
| Std Dev | 3.281 |

Gaus mean : 30.0640

Gaus Width: 4.1055



B1L100S, U7-ch84, adc1

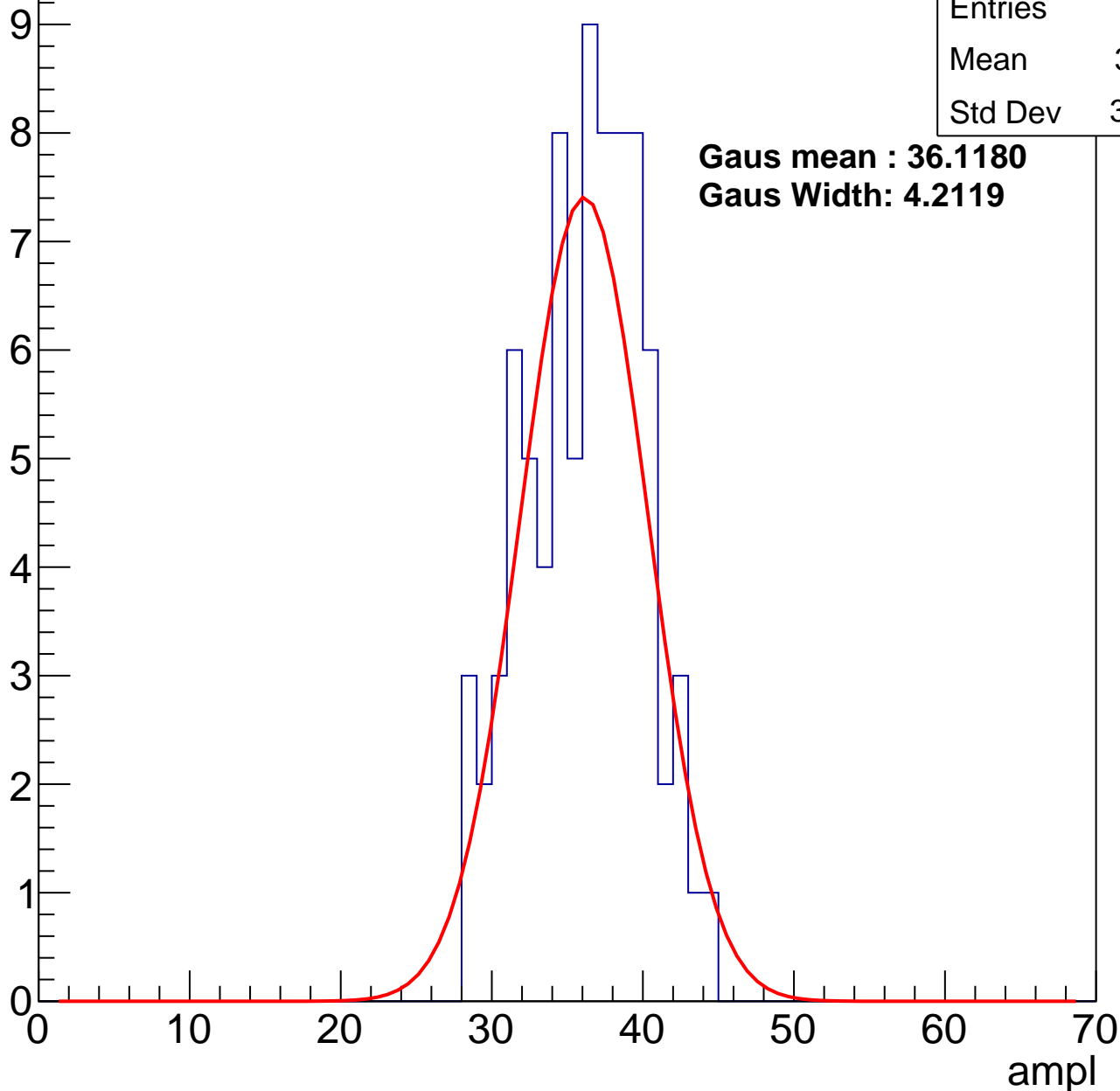
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 35.71 |
| Std Dev | 3.782 |

Gaus mean : 36.1180

Gaus Width: 4.2119



B1L100S, U7-ch84, adc2

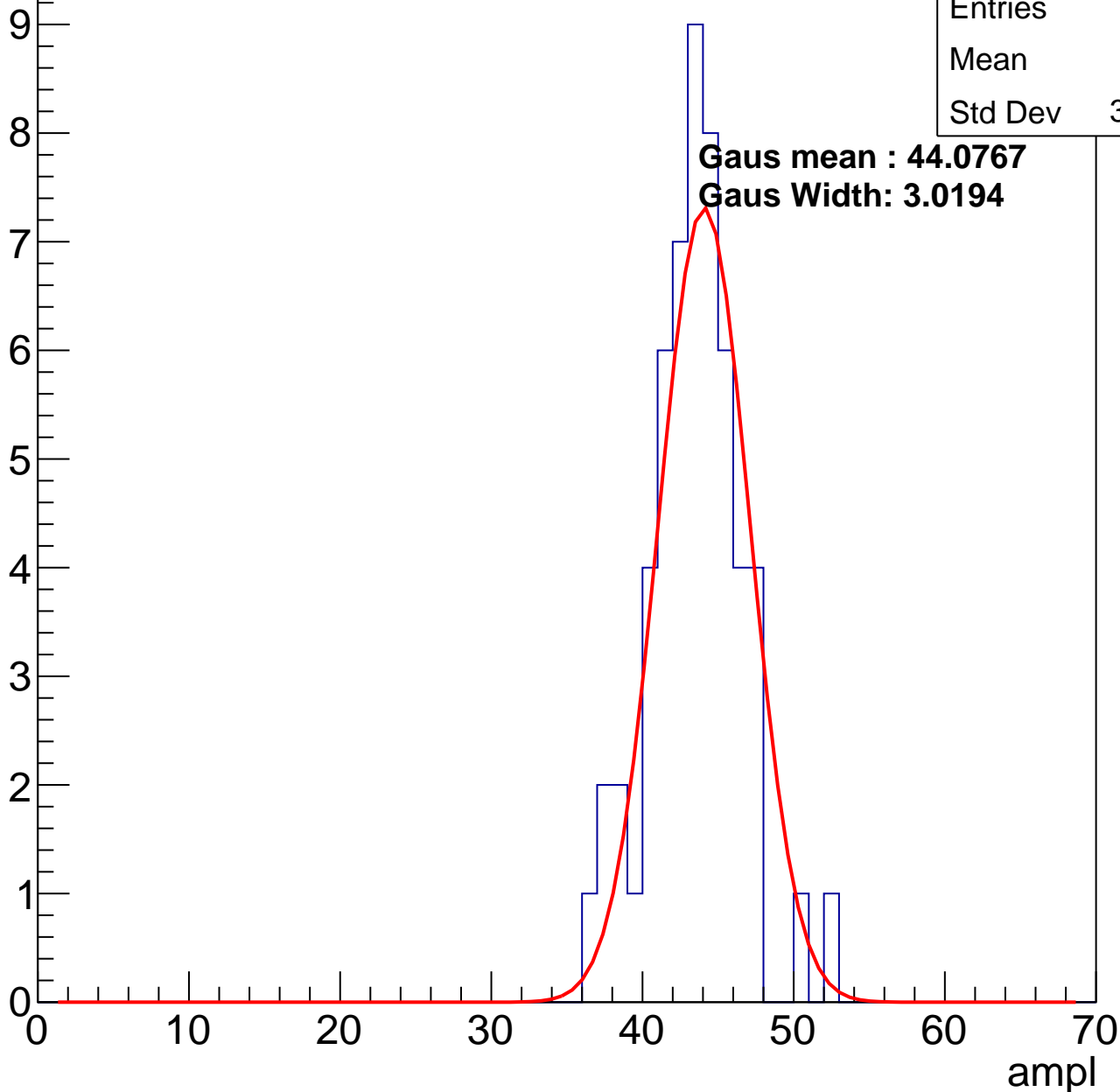
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43 |
| Std Dev | 3.036 |

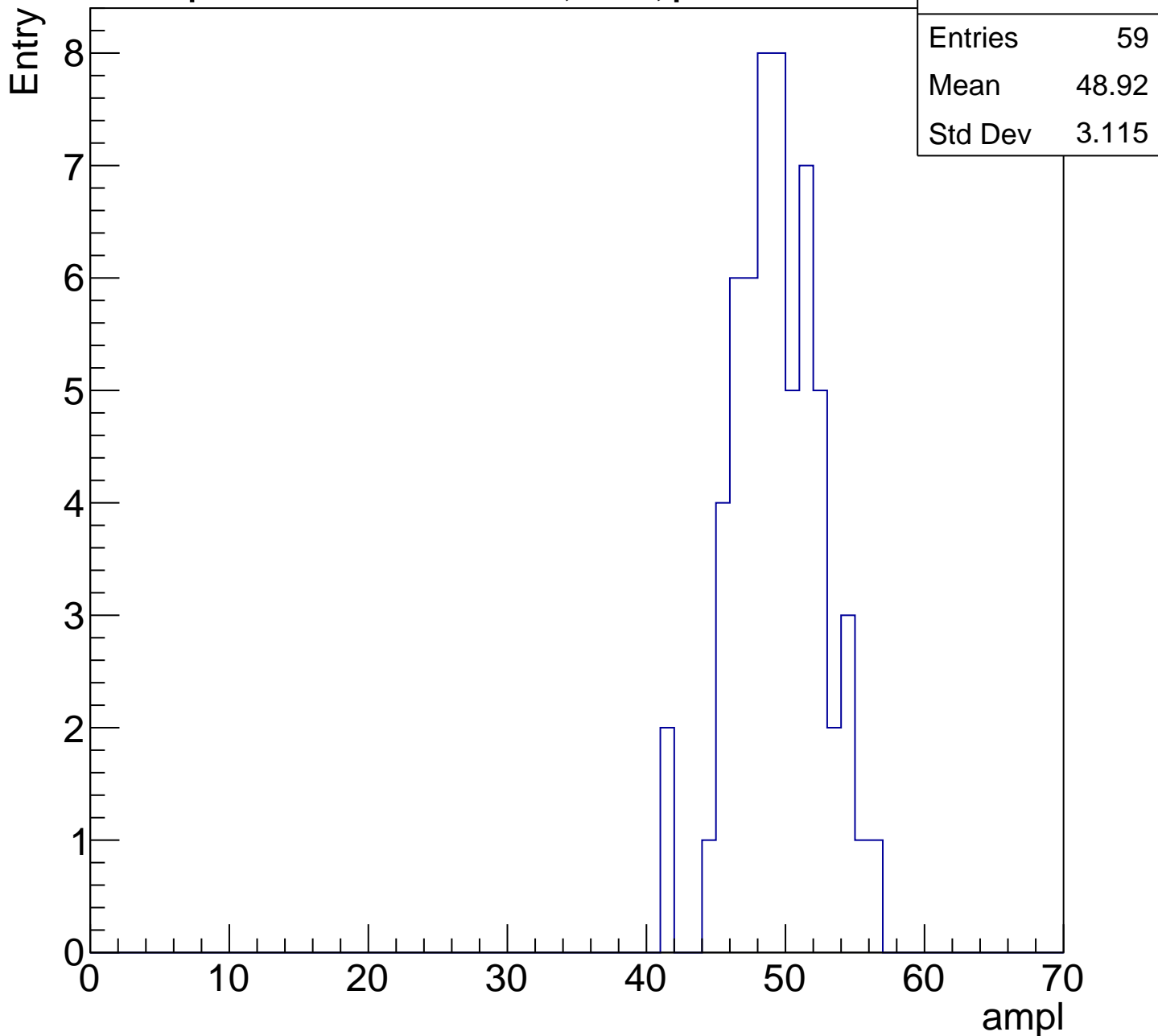
Gaus mean : 44.0767

Gaus Width: 3.0194



B1L100S, U7-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

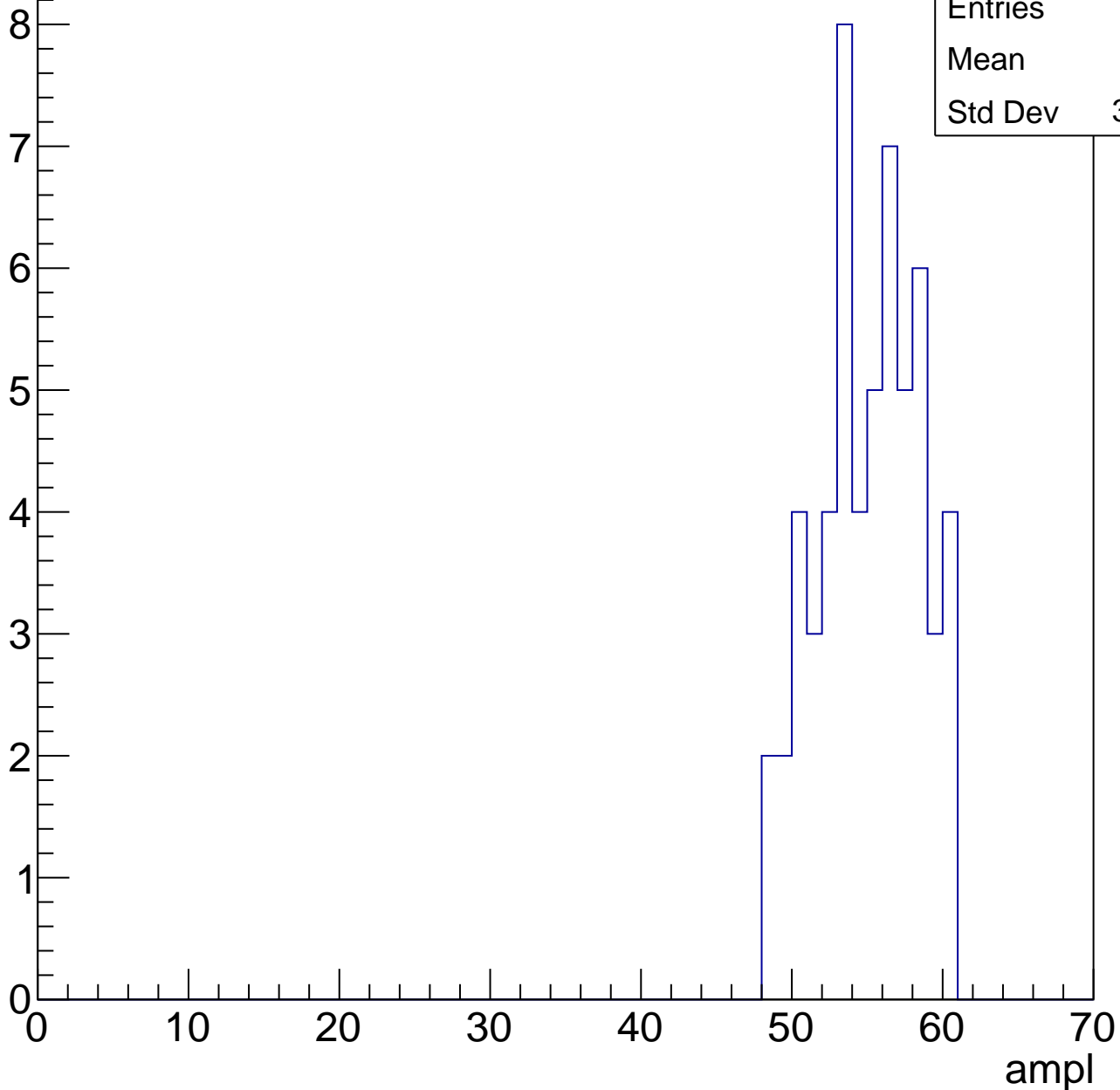


B1L100S, U7-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.6 |
| Std Dev | 3.271 |



B1L100S, U7-ch84, adc5

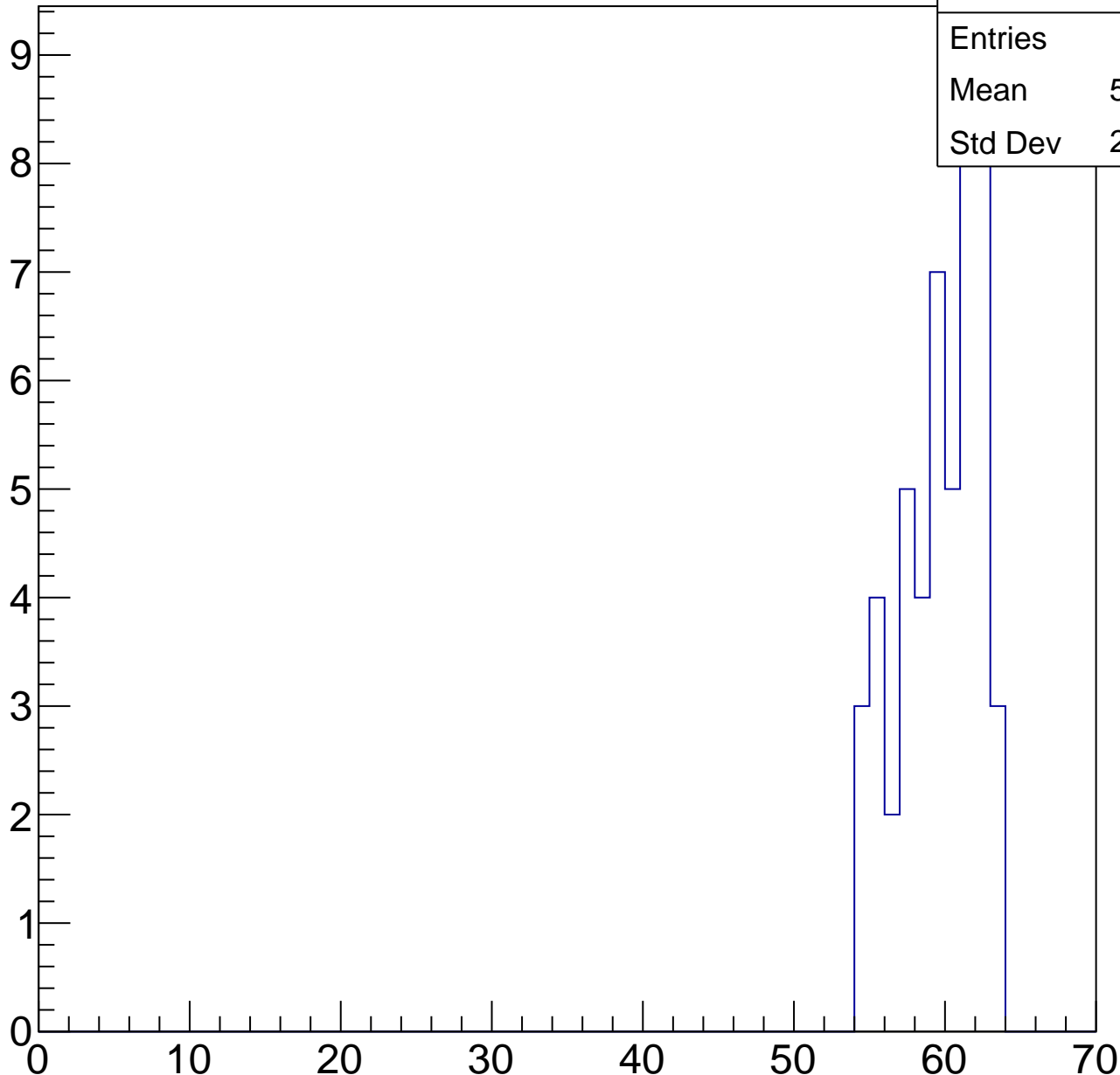
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.22 |
| Std Dev | 2.615 |

ampl



B1L100S, U7-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

10

Mean

62.4

Std Dev

1.02

ampl

0

10

20

30

40

50

60

70

B1L100S, U7-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7 |
| Std Dev | 9.899 |

B1L100S, U7-ch85, adc0

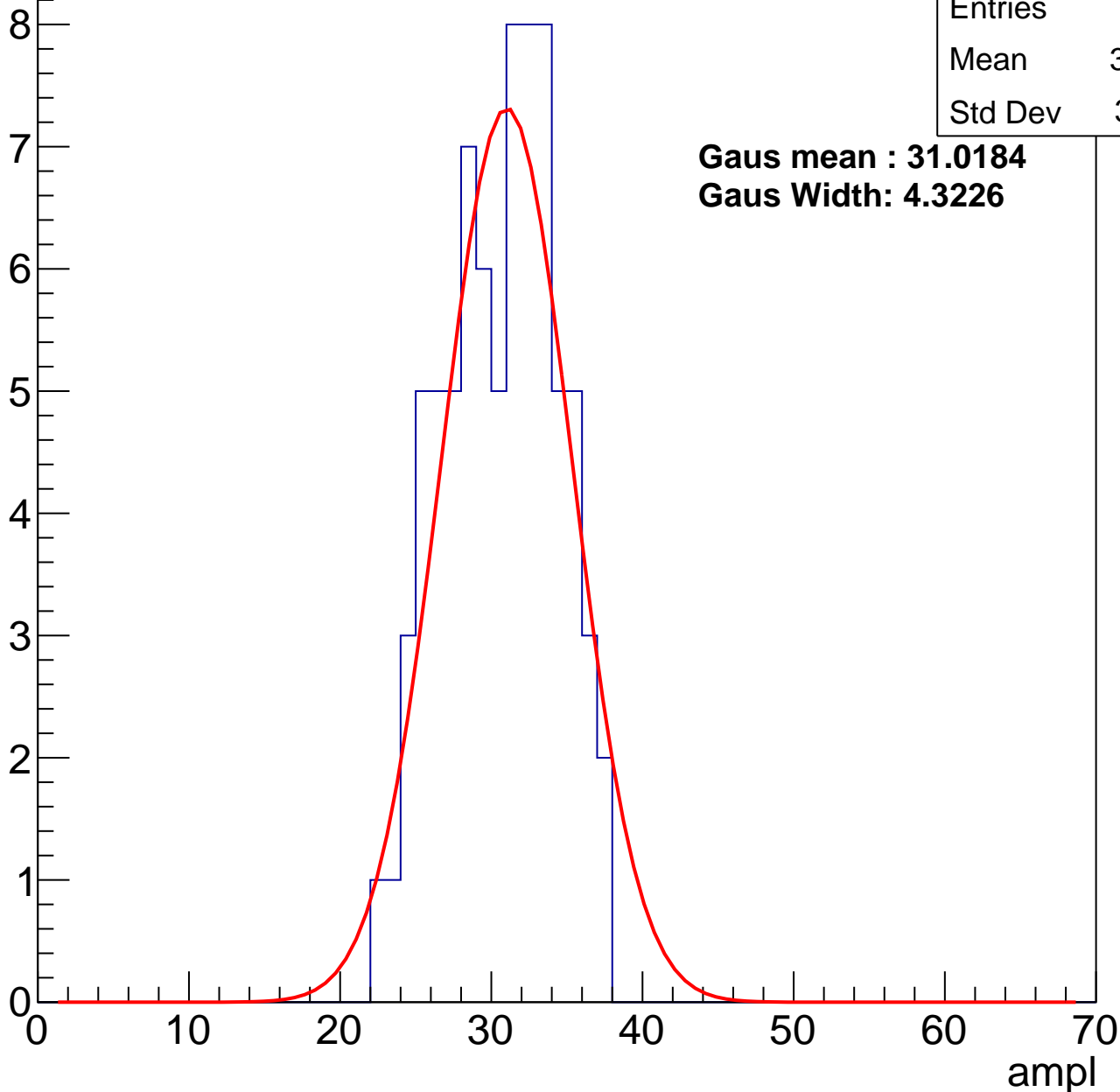
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 30.16 |
| Std Dev | 3.651 |

Gaus mean : 31.0184

Gaus Width: 4.3226



B1L100S, U7-ch85, adc1

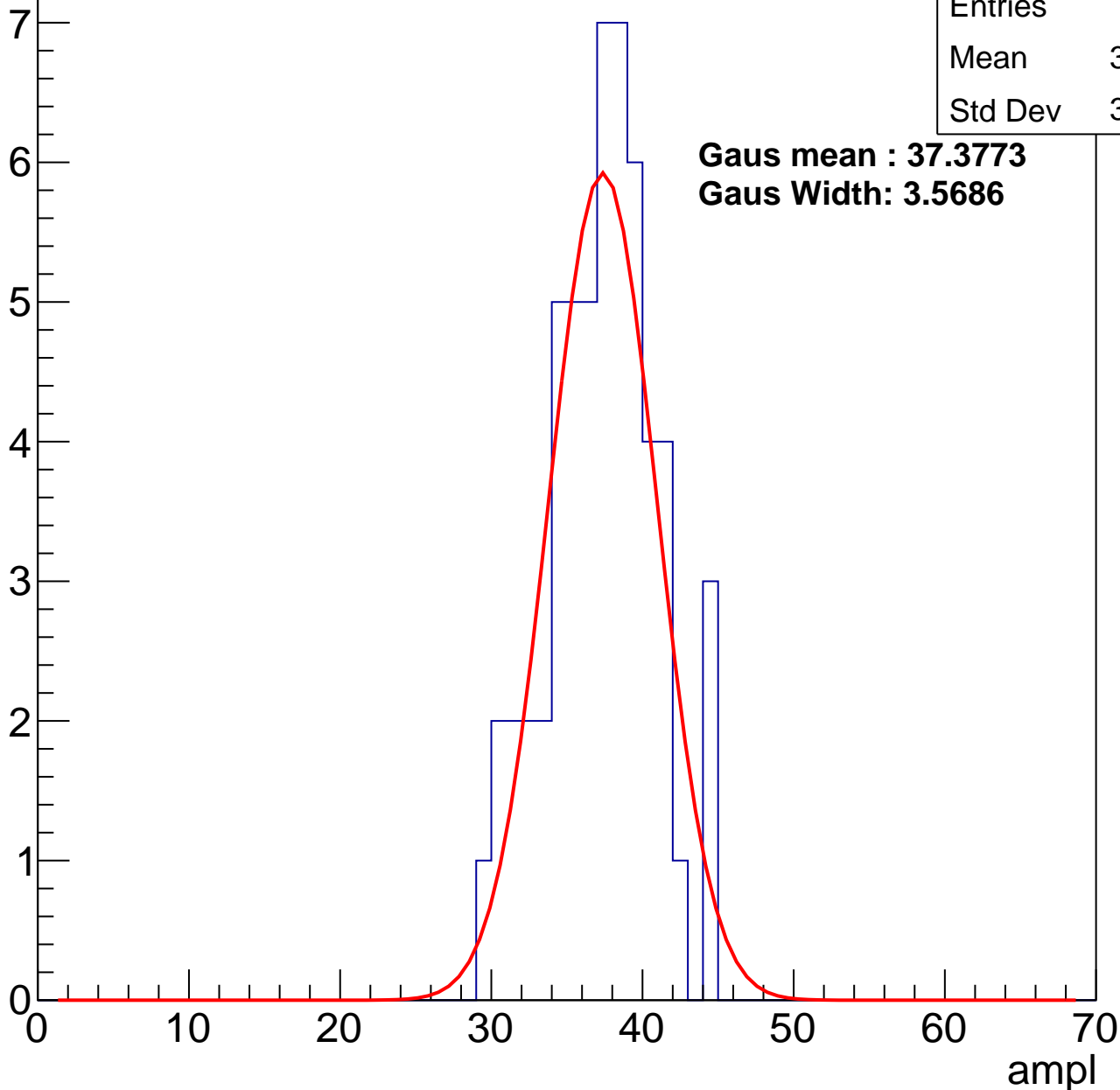
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 36.84 |
| Std Dev | 3.504 |

Gaus mean : 37.3773

Gaus Width: 3.5686



B1L100S, U7-ch85, adc2

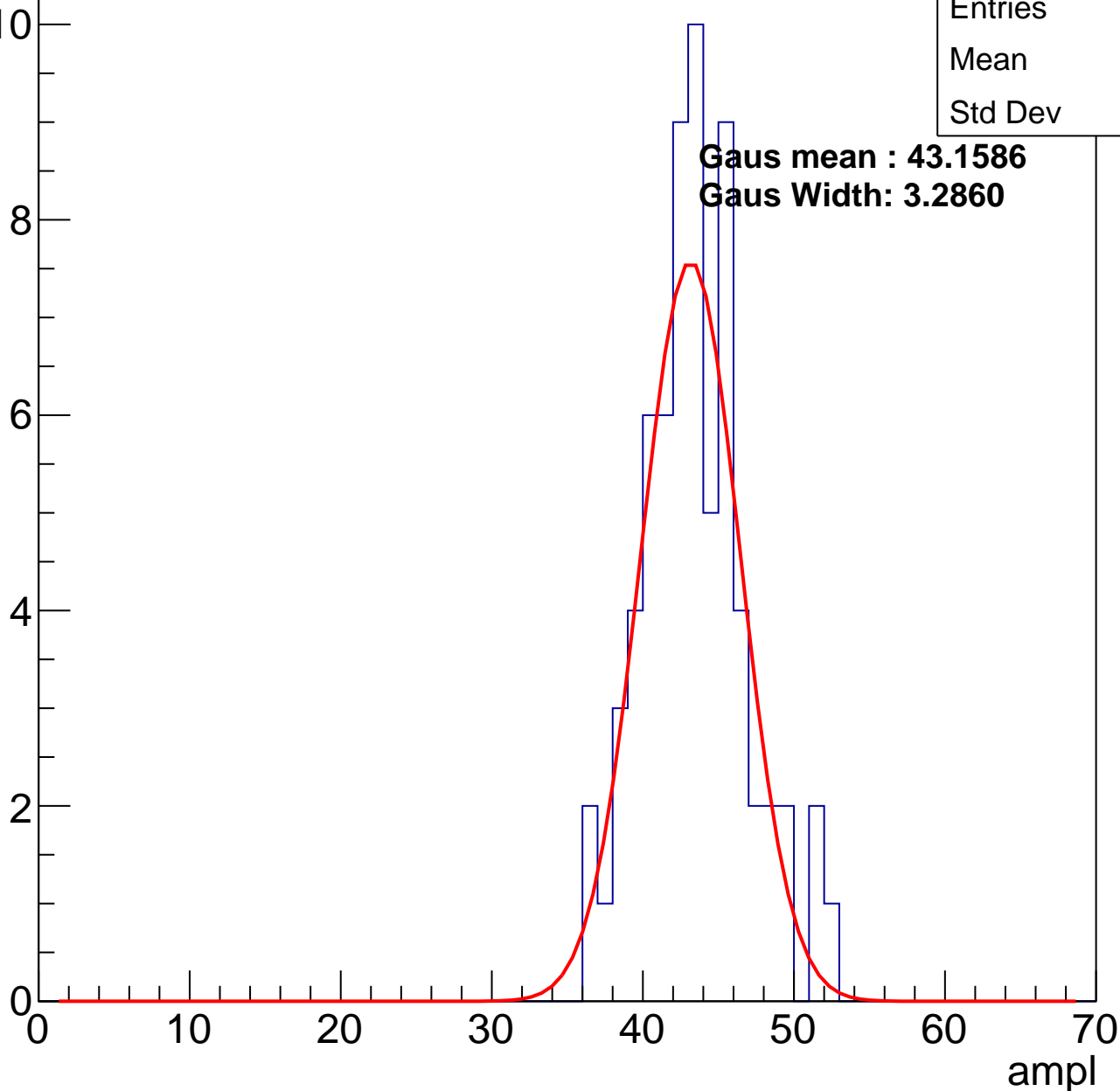
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 68 |
| Mean | 43 |
| Std Dev | 3.43 |

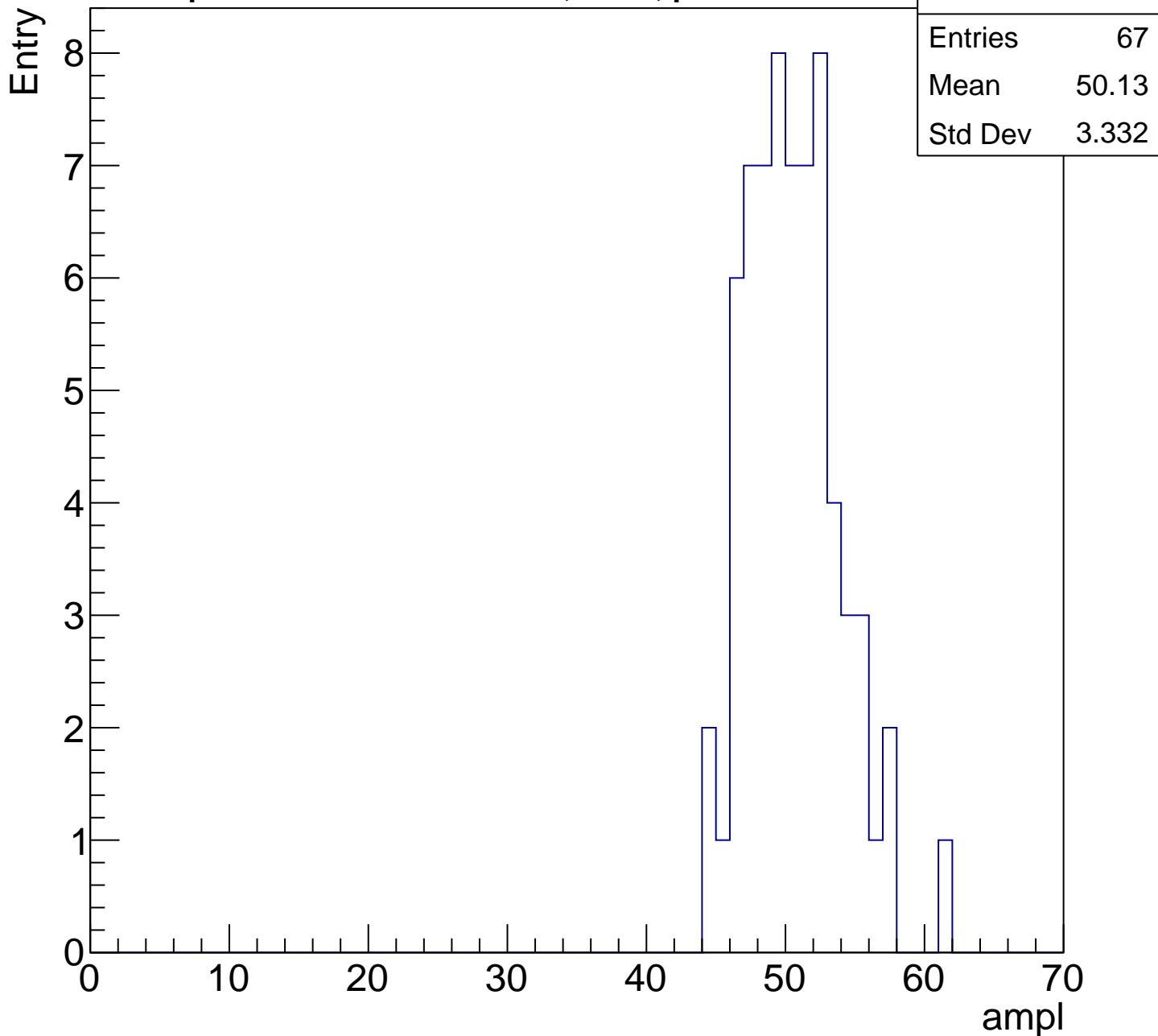
Gaus mean : 43.1586

Gaus Width: 3.2860



B1L100S, U7-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

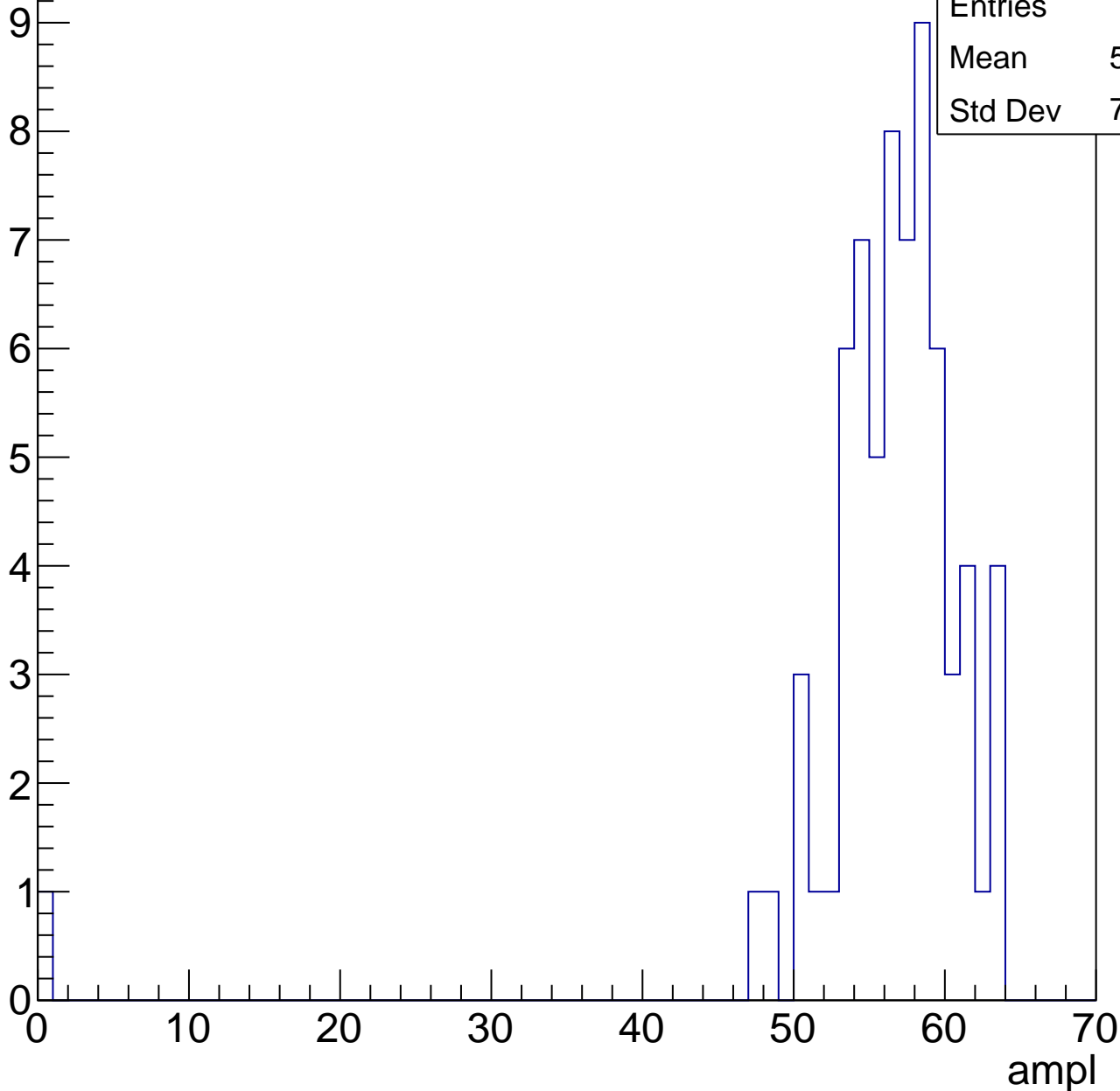


B1L100S, U7-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 55.59 |
| Std Dev | 7.653 |



B1L100S, U7-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

32

Mean

60.31

Std Dev

2.228

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

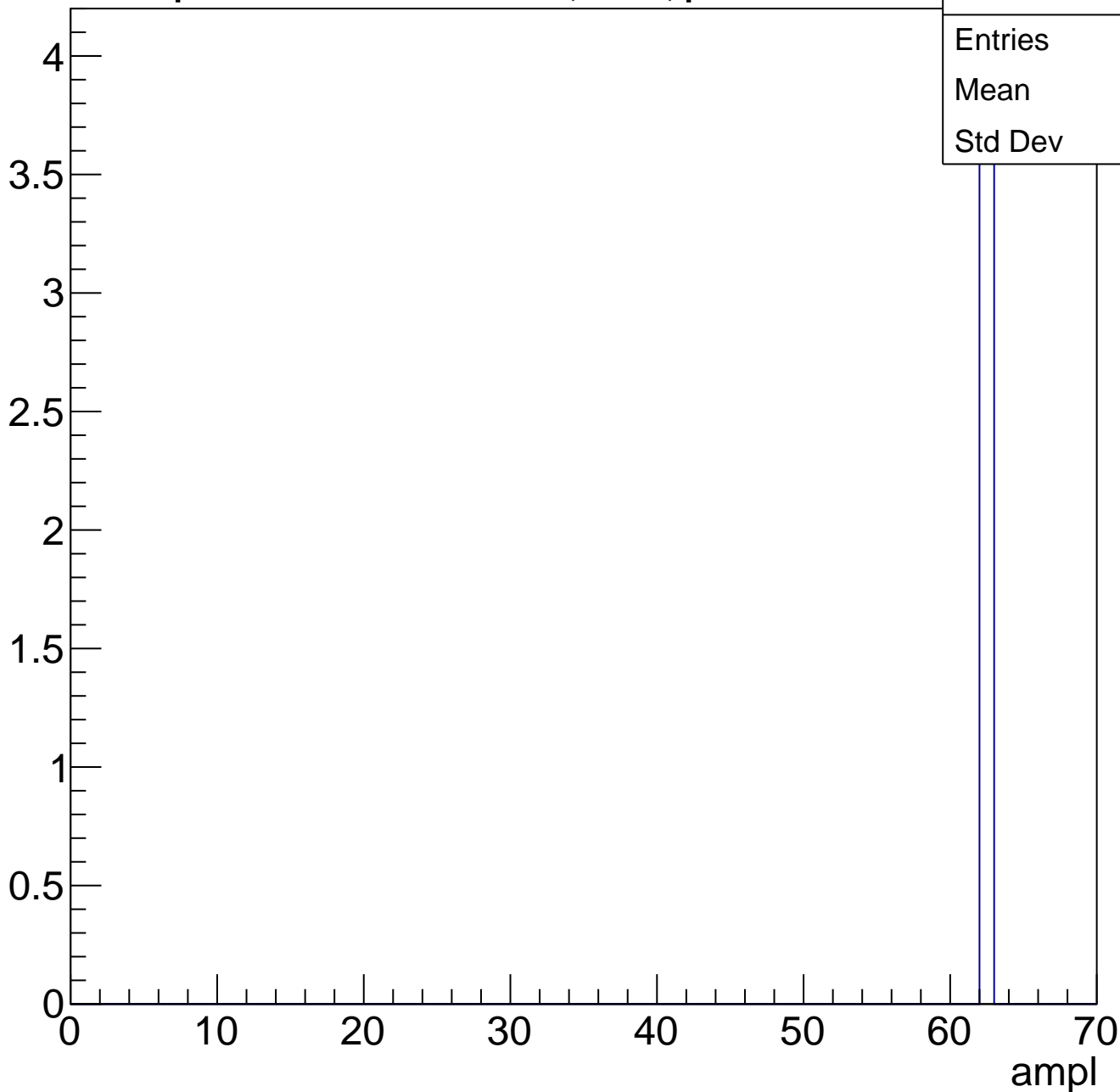
6

7

B1L100S, U7-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch86, adc0

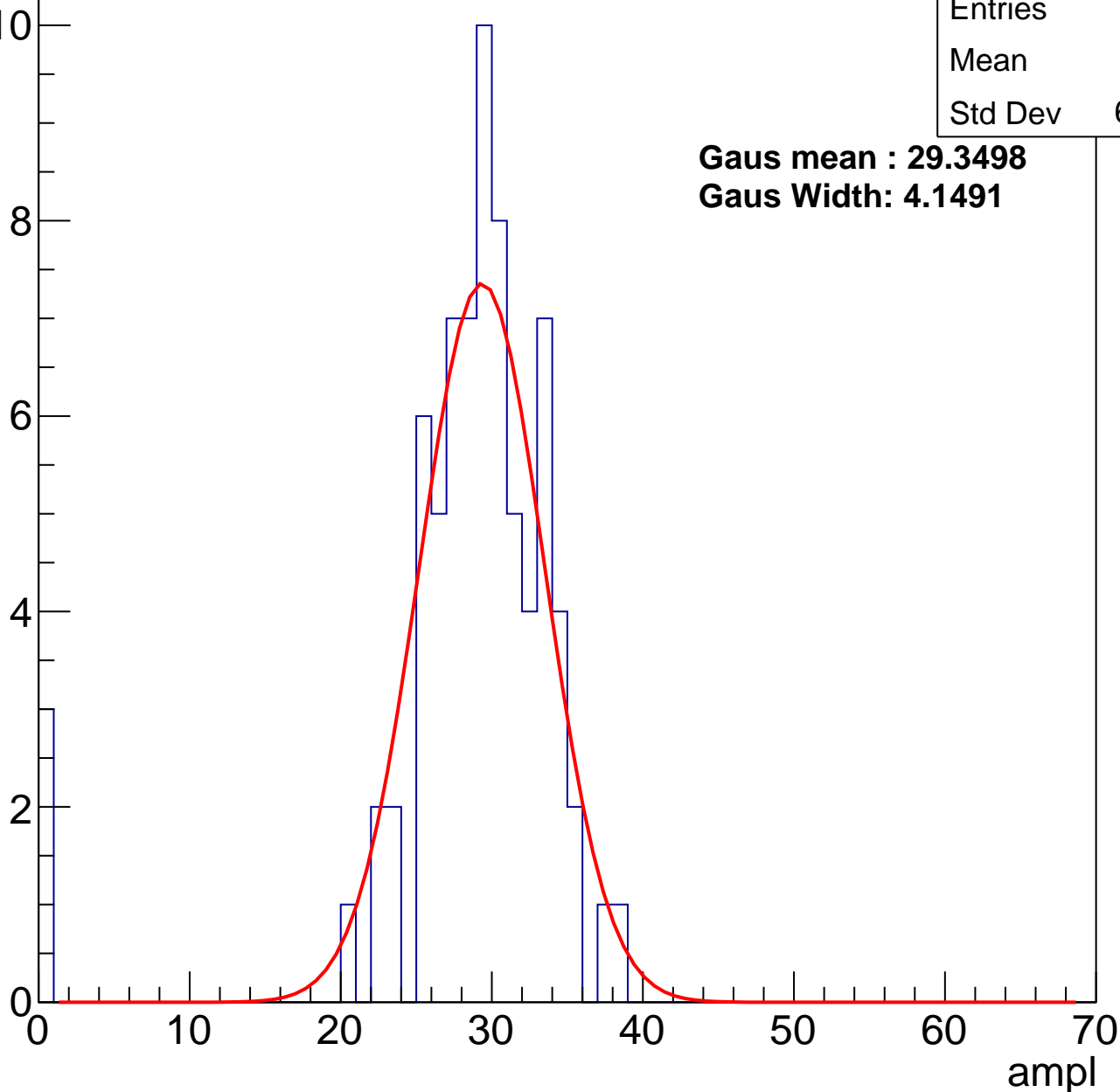
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 28 |
| Std Dev | 6.701 |

Gaus mean : 29.3498

Gaus Width: 4.1491



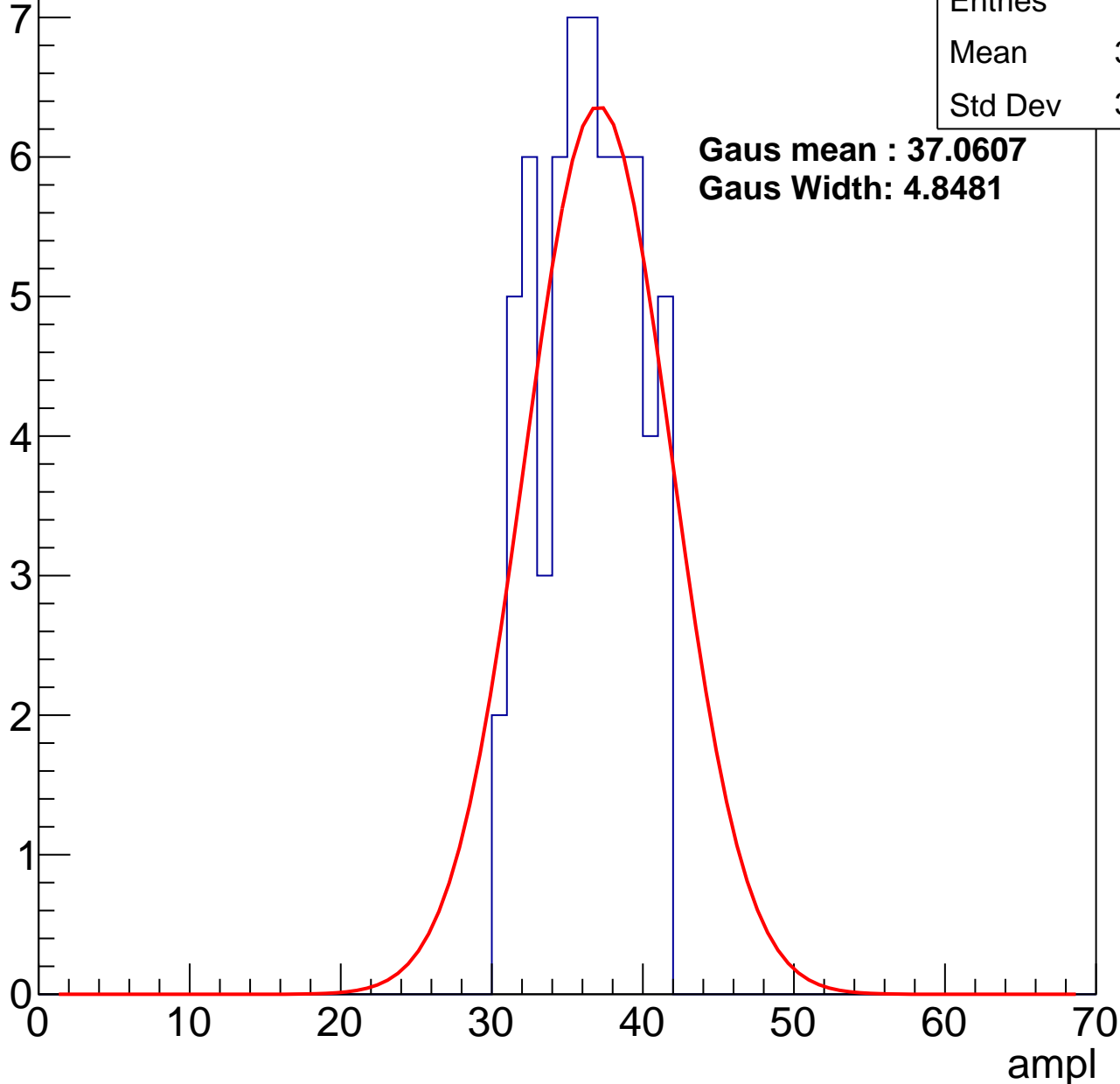
B1L100S, U7-ch86, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.81 |
| Std Dev | 3.141 |

Gaus mean : 37.0607
Gaus Width: 4.8481



B1L100S, U7-ch86, adc2

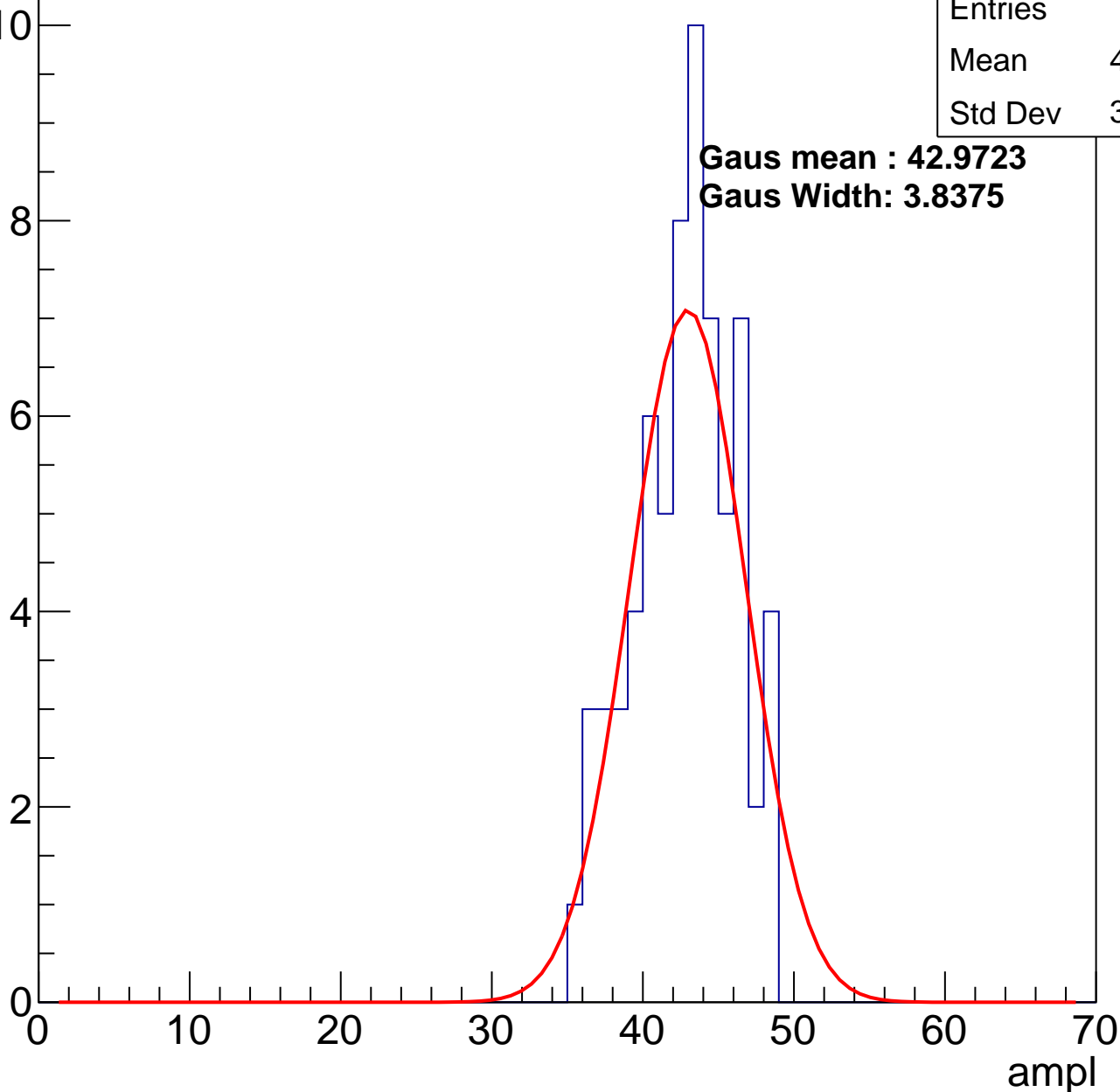
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.29 |
| Std Dev | 3.272 |

Gaus mean : 42.9723

Gaus Width: 3.8375

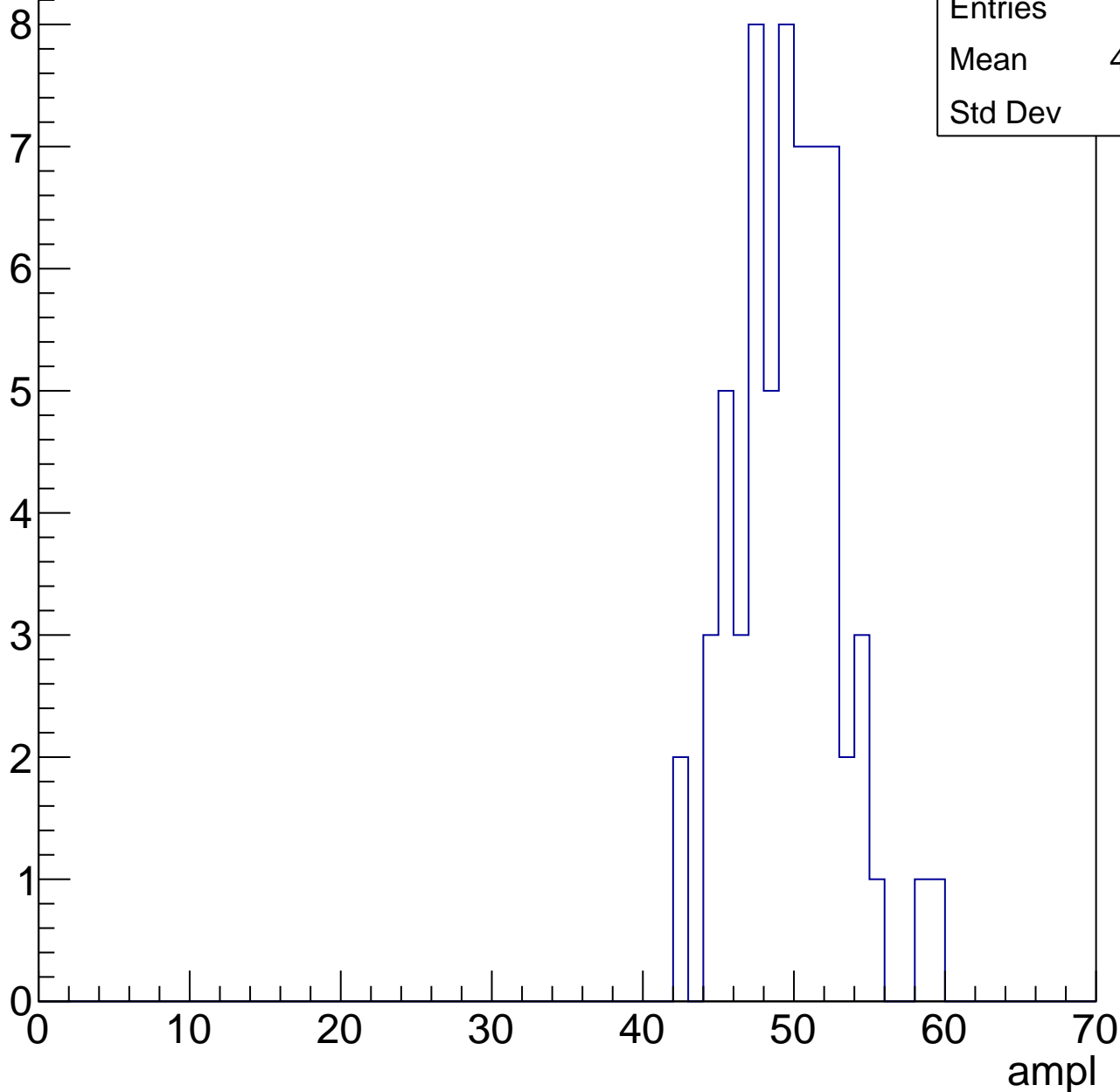


B1L100S, U7-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

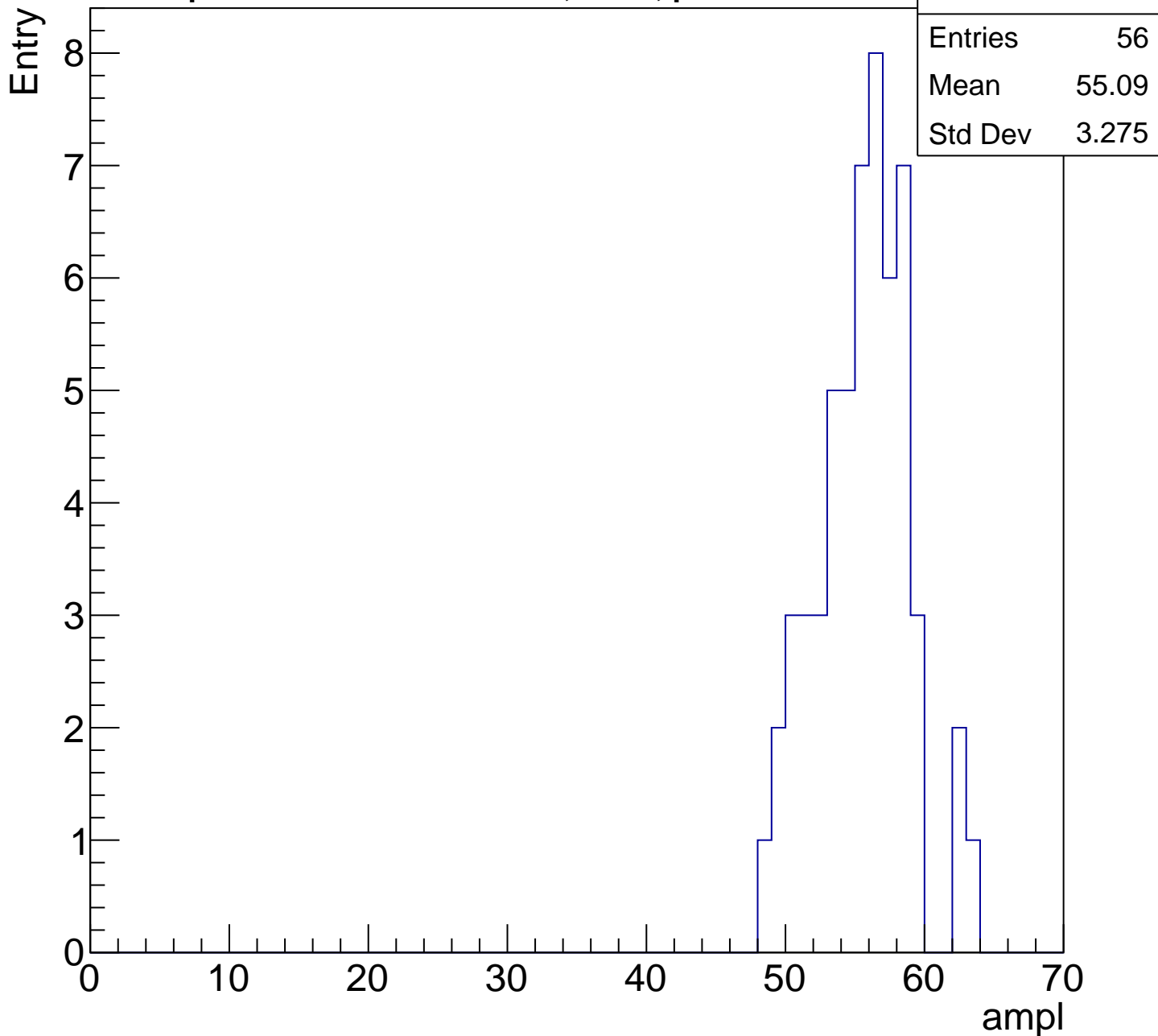
Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 49.17 |
| Std Dev | 3.42 |



B1L100S, U7-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

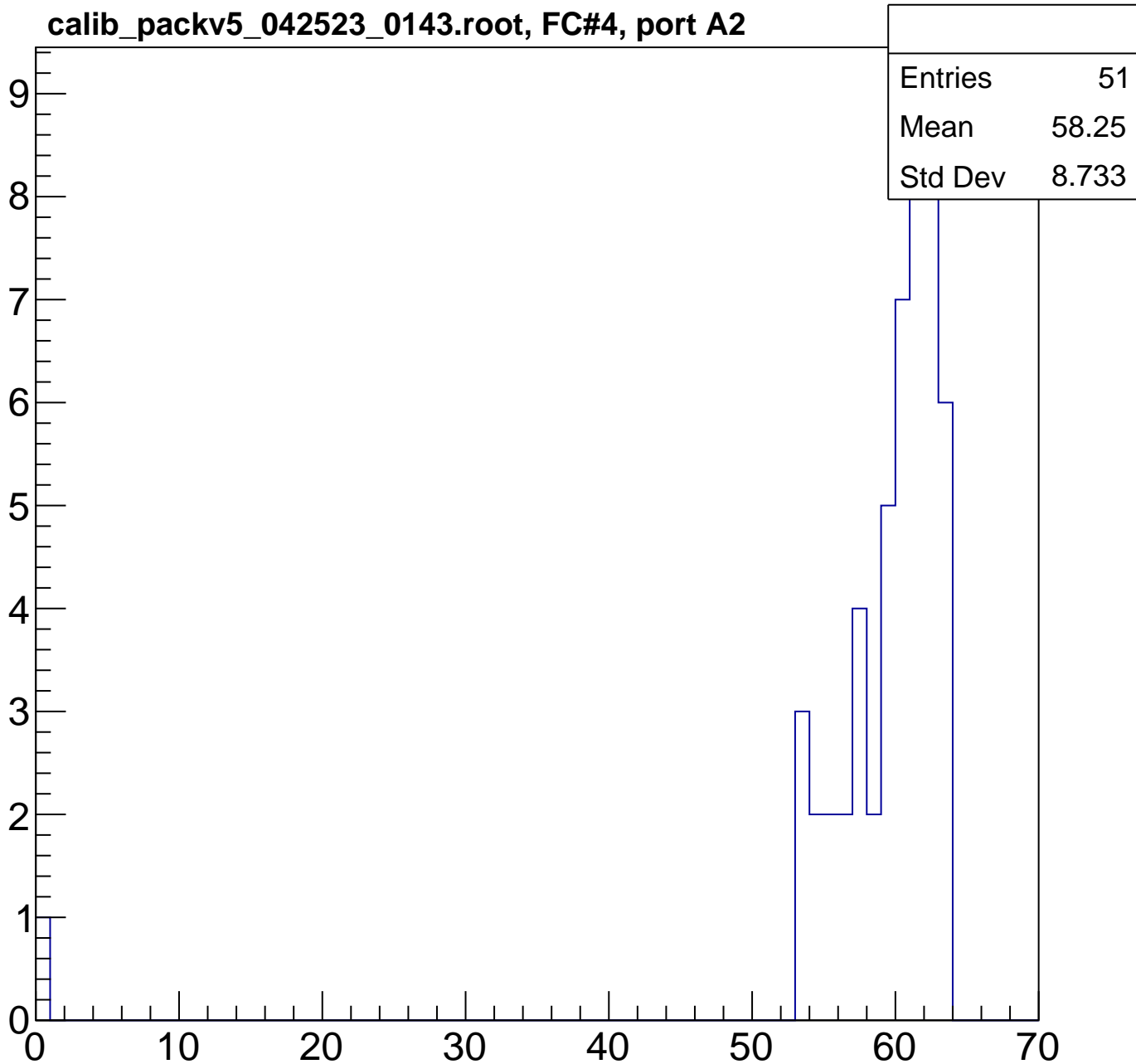
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.25 |
| Std Dev | 8.733 |

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

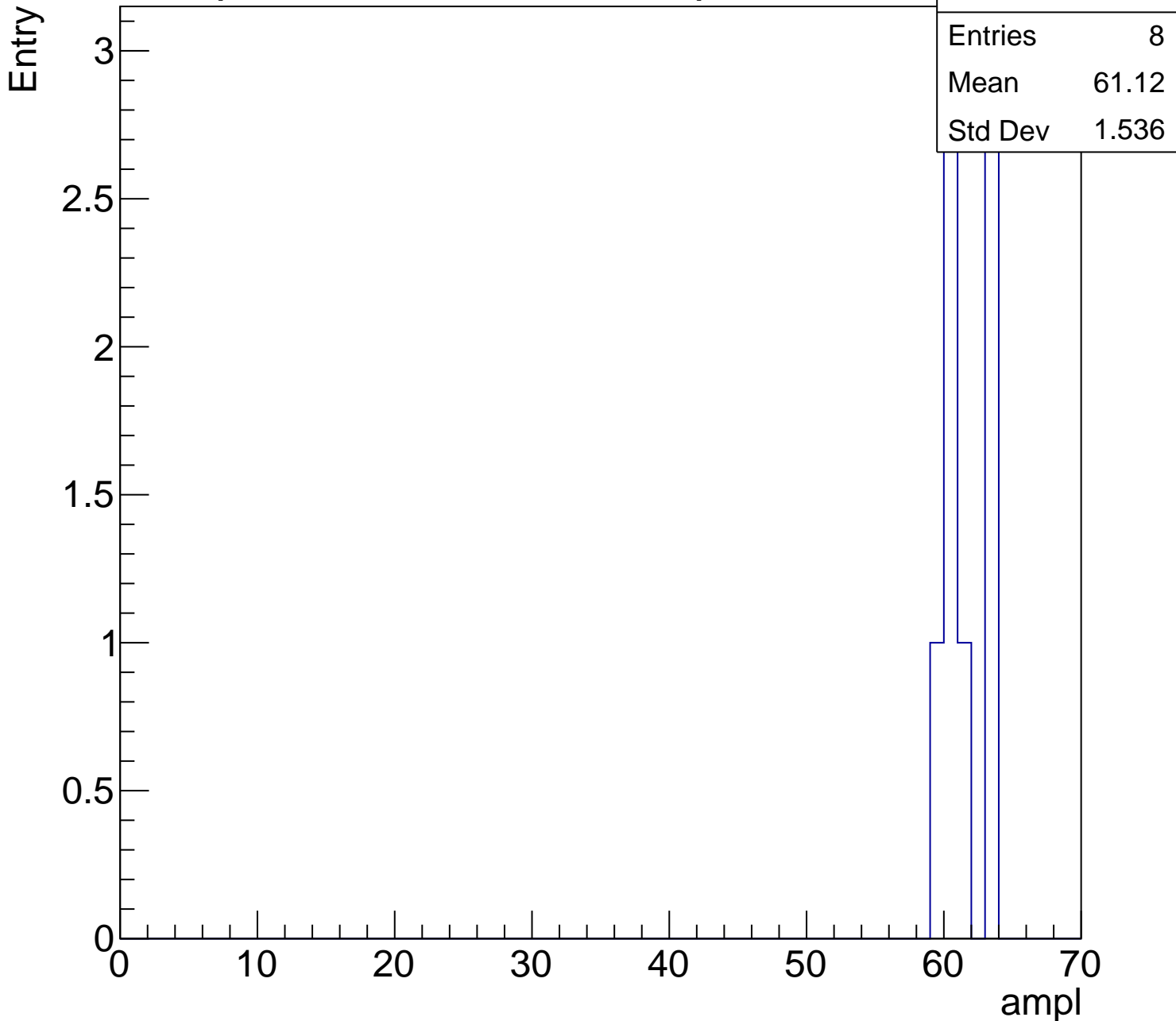
8

Mean

61.12

Std Dev

1.536



B1L100S, U7-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch87, adc0

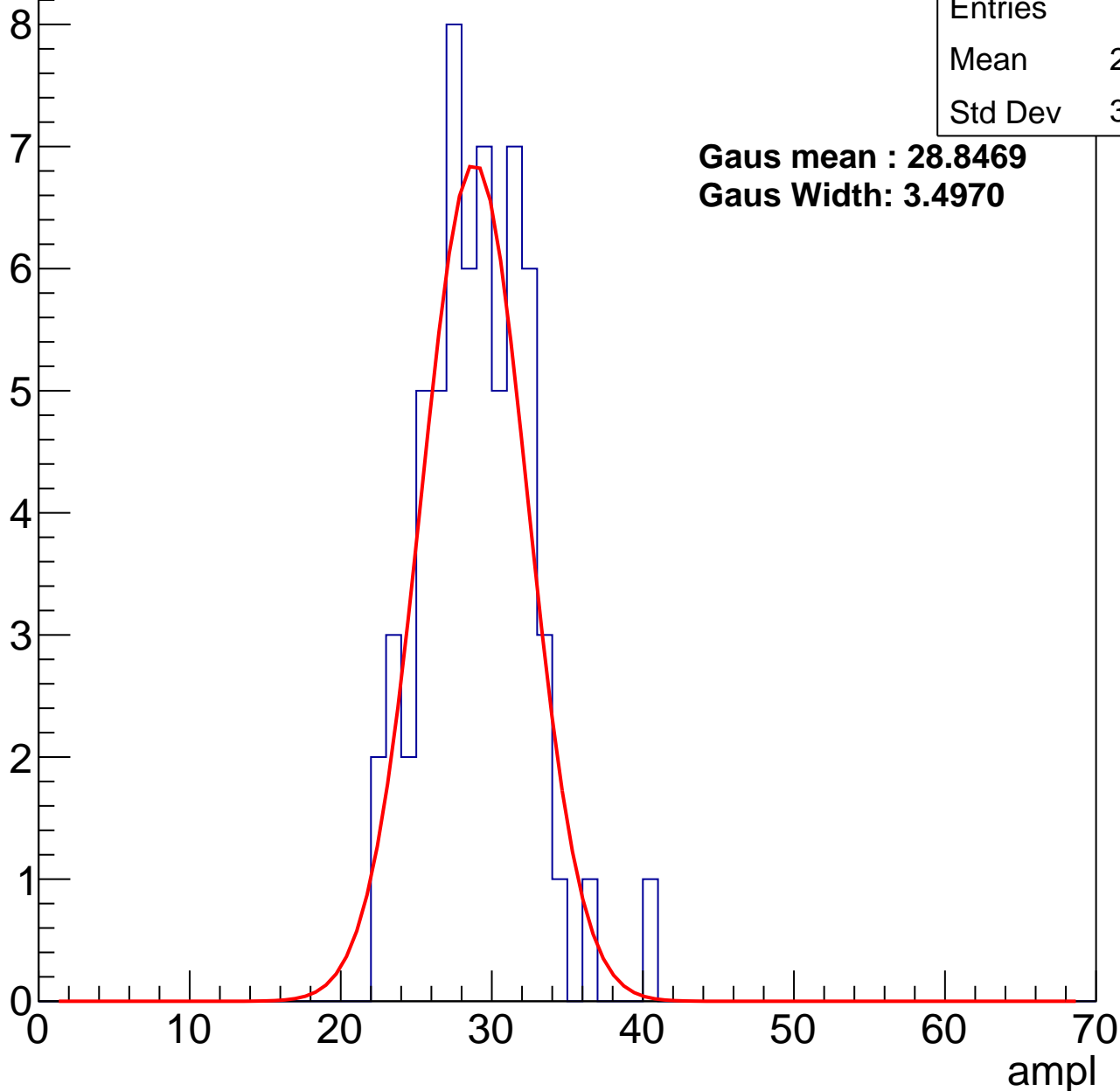
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.56 |
| Std Dev | 3.448 |

Gaus mean : 28.8469

Gaus Width: 3.4970



B1L100S, U7-ch87, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.21 |
| Std Dev | 3.85 |

Gaus mean : 35.8543

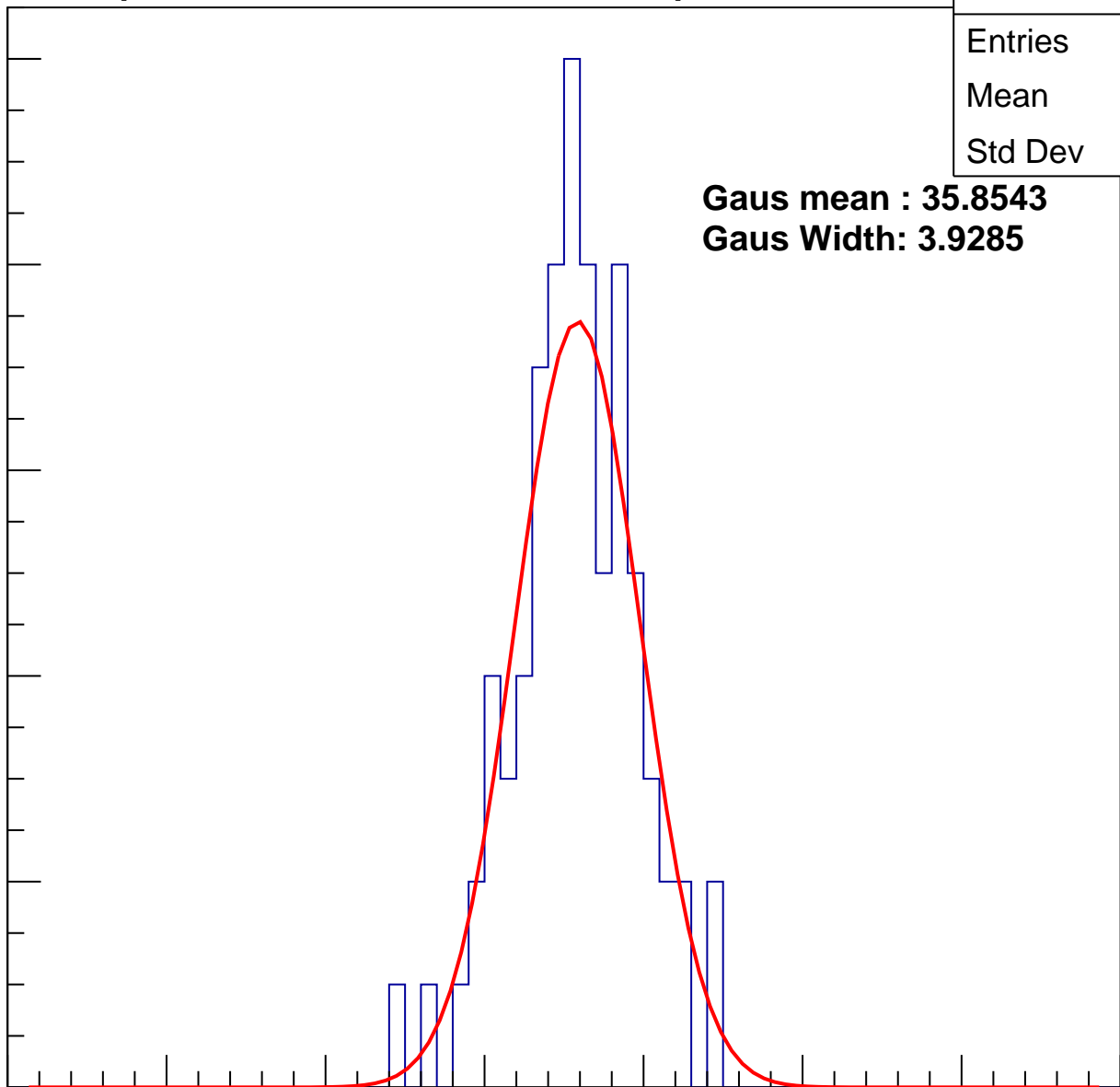
Gaus Width: 3.9285

Entry

10
8
6
4
2
0

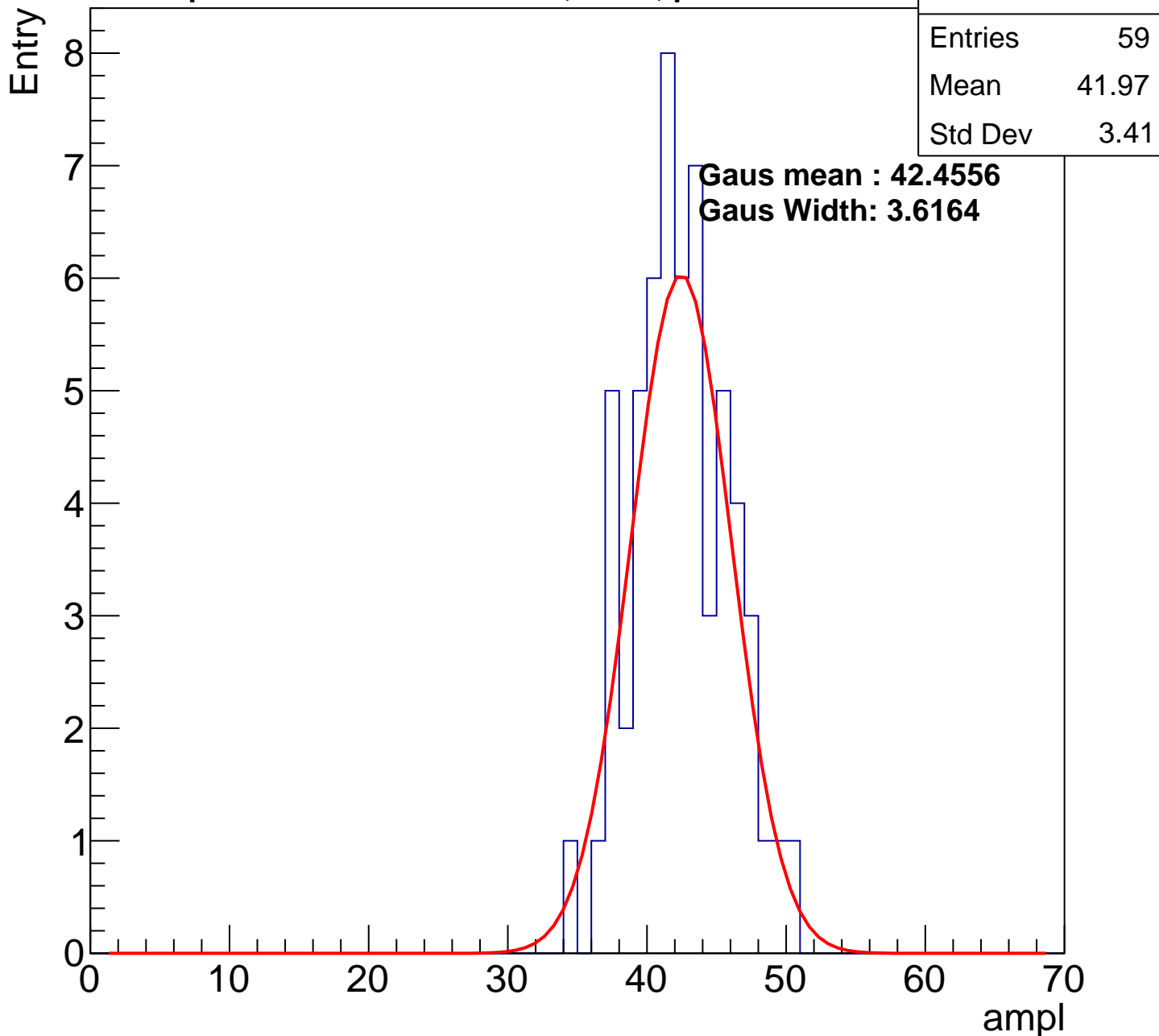
ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch87, adc2

calib_packv5_042523_0143.root, FC#4, port A2

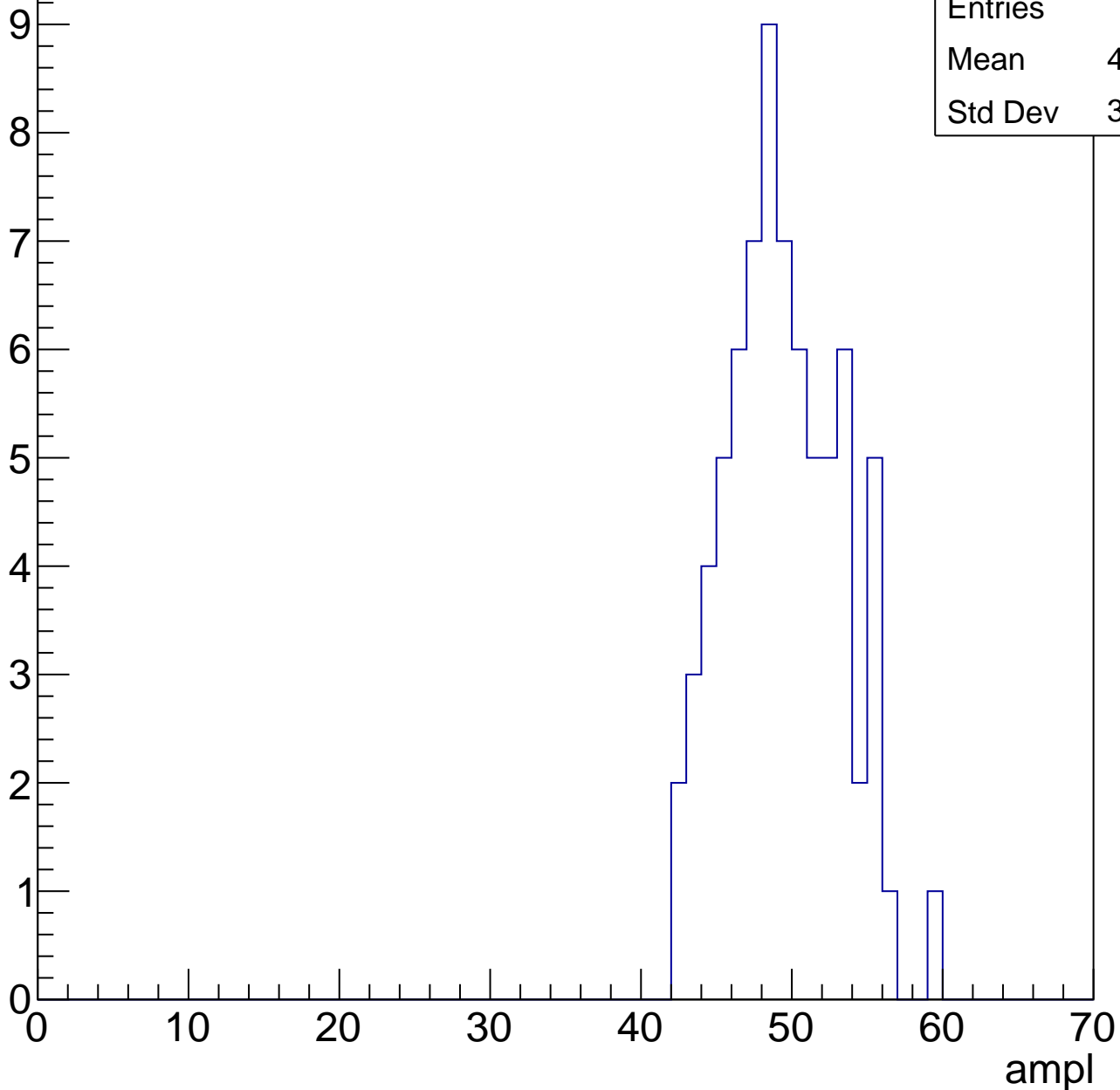


B1L100S, U7-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

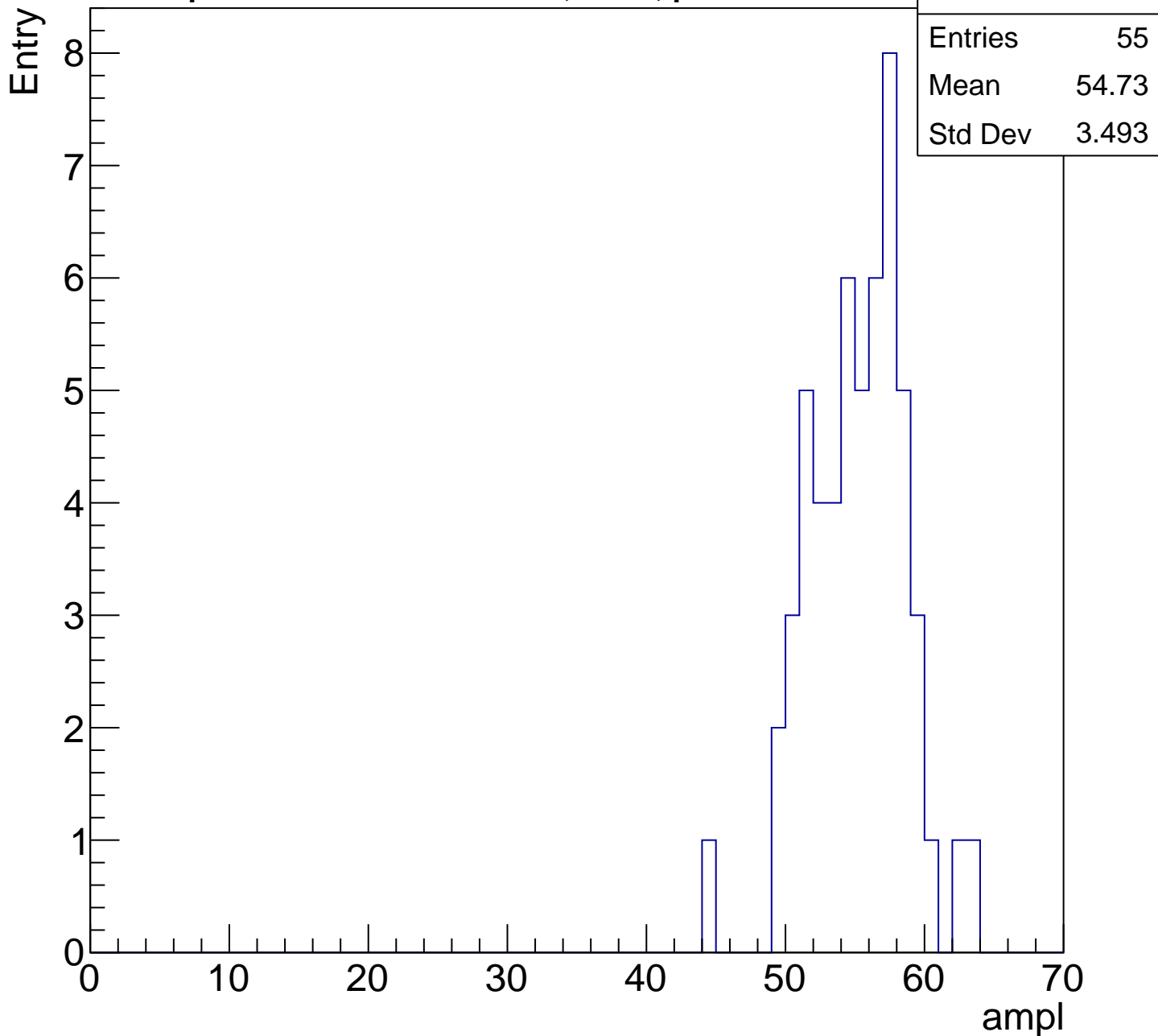
Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 48.99 |
| Std Dev | 3.718 |



B1L100S, U7-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

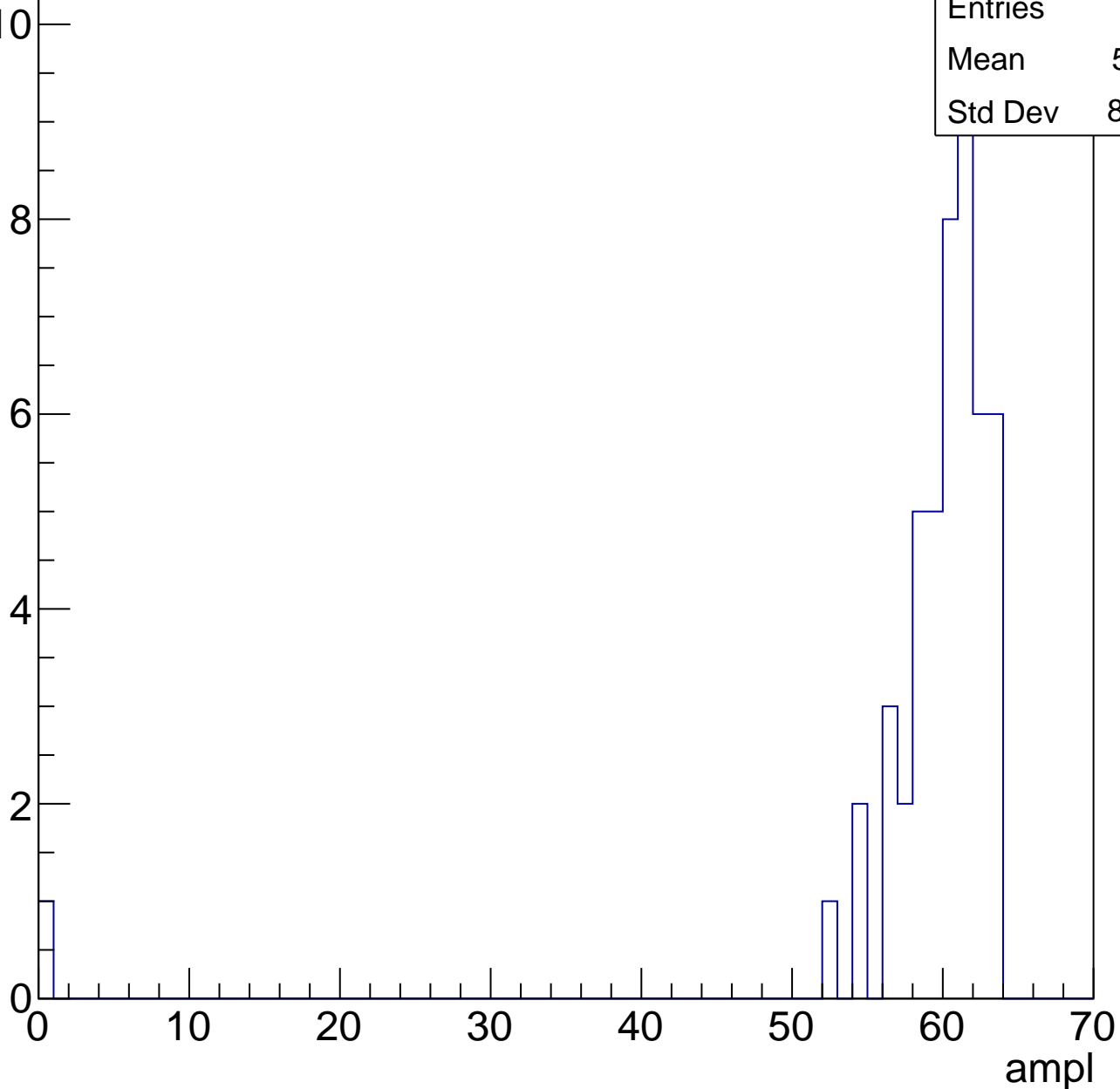


B1L100S, U7-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

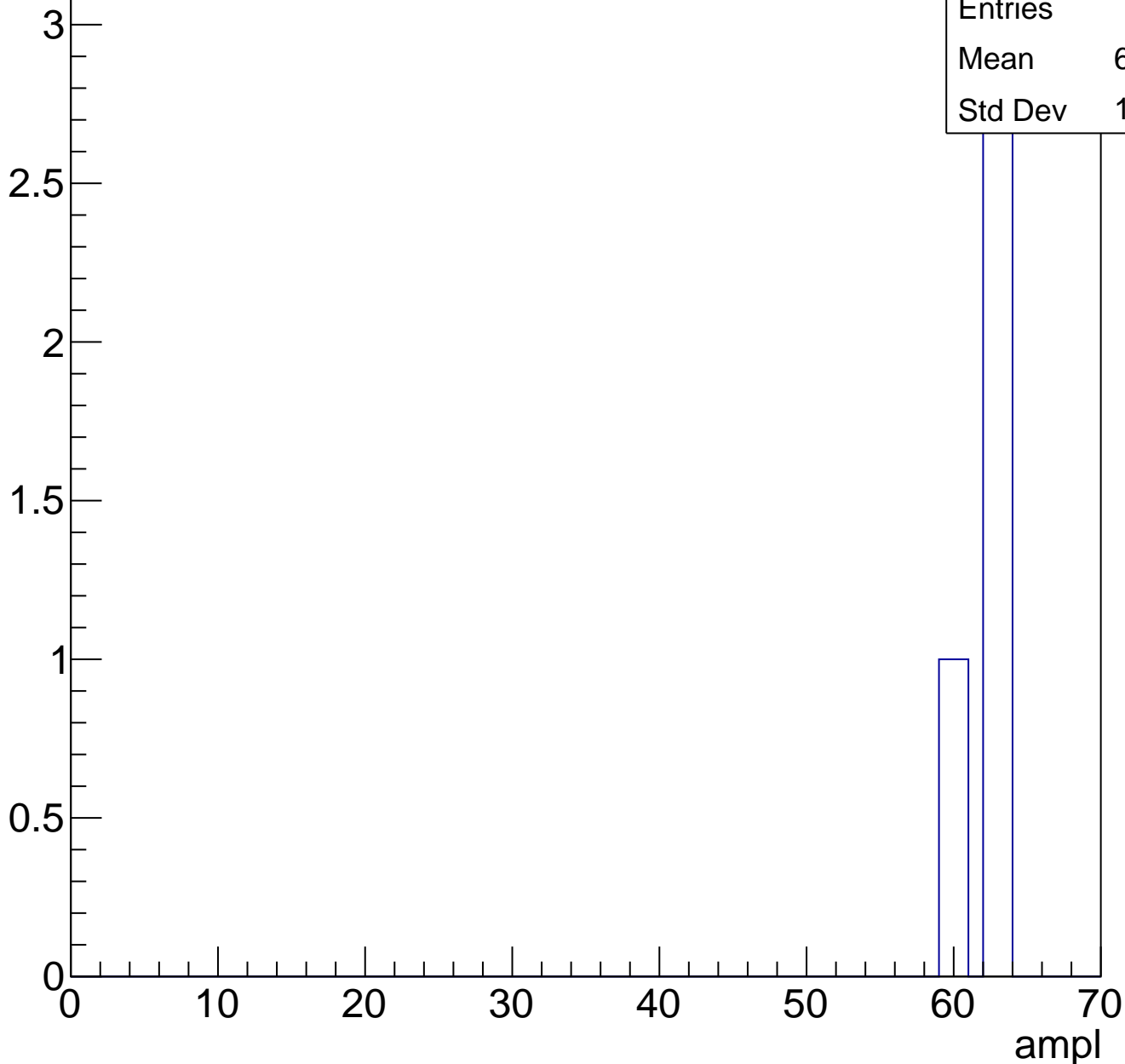
| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.51 |
| Std Dev | 8.814 |



B1L100S, U7-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch88, adc0

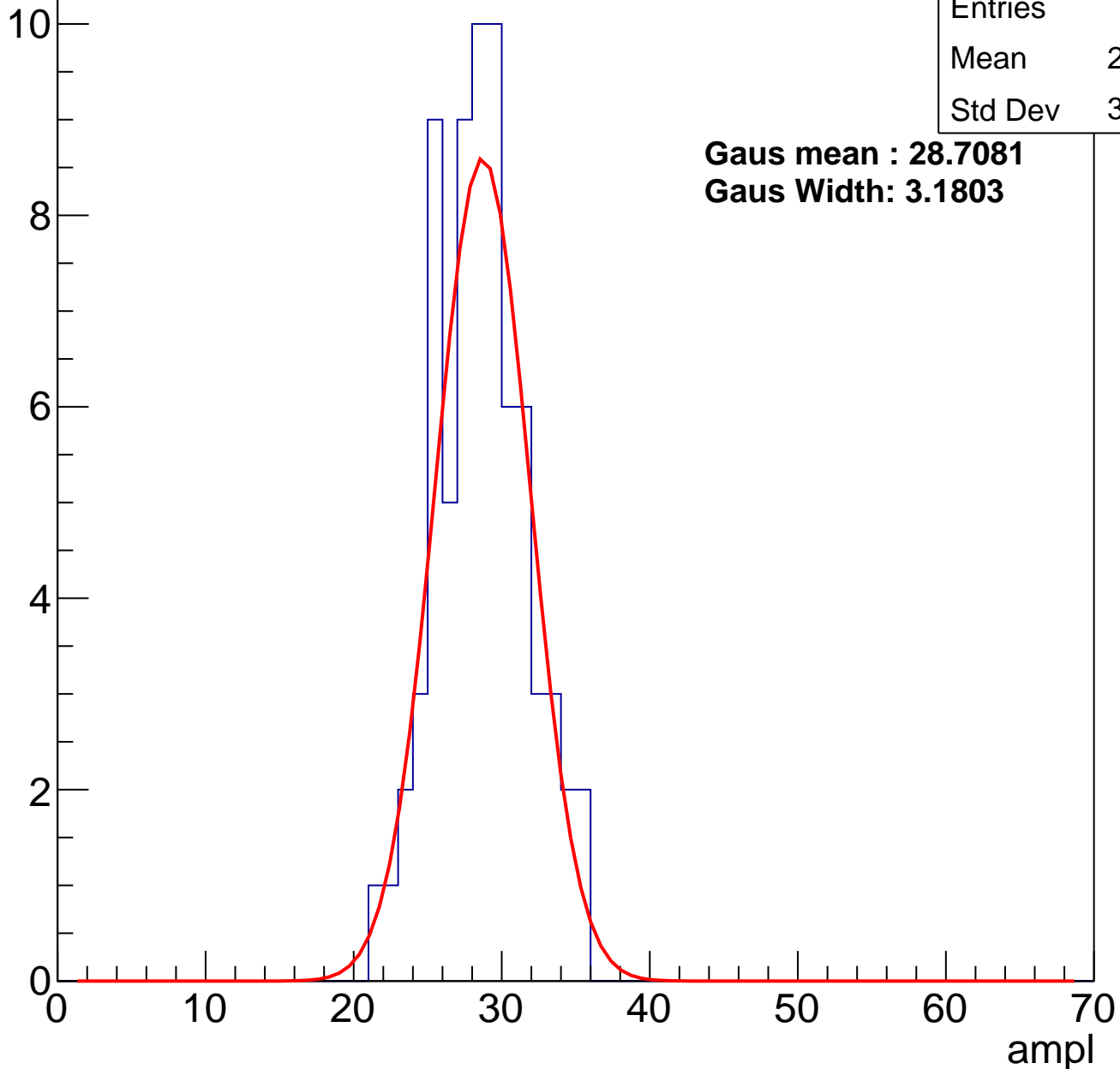
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 28.17 |
| Std Dev | 3.055 |

Gaus mean : 28.7081

Gaus Width: 3.1803

Entry



B1L100S, U7-ch88, adc1

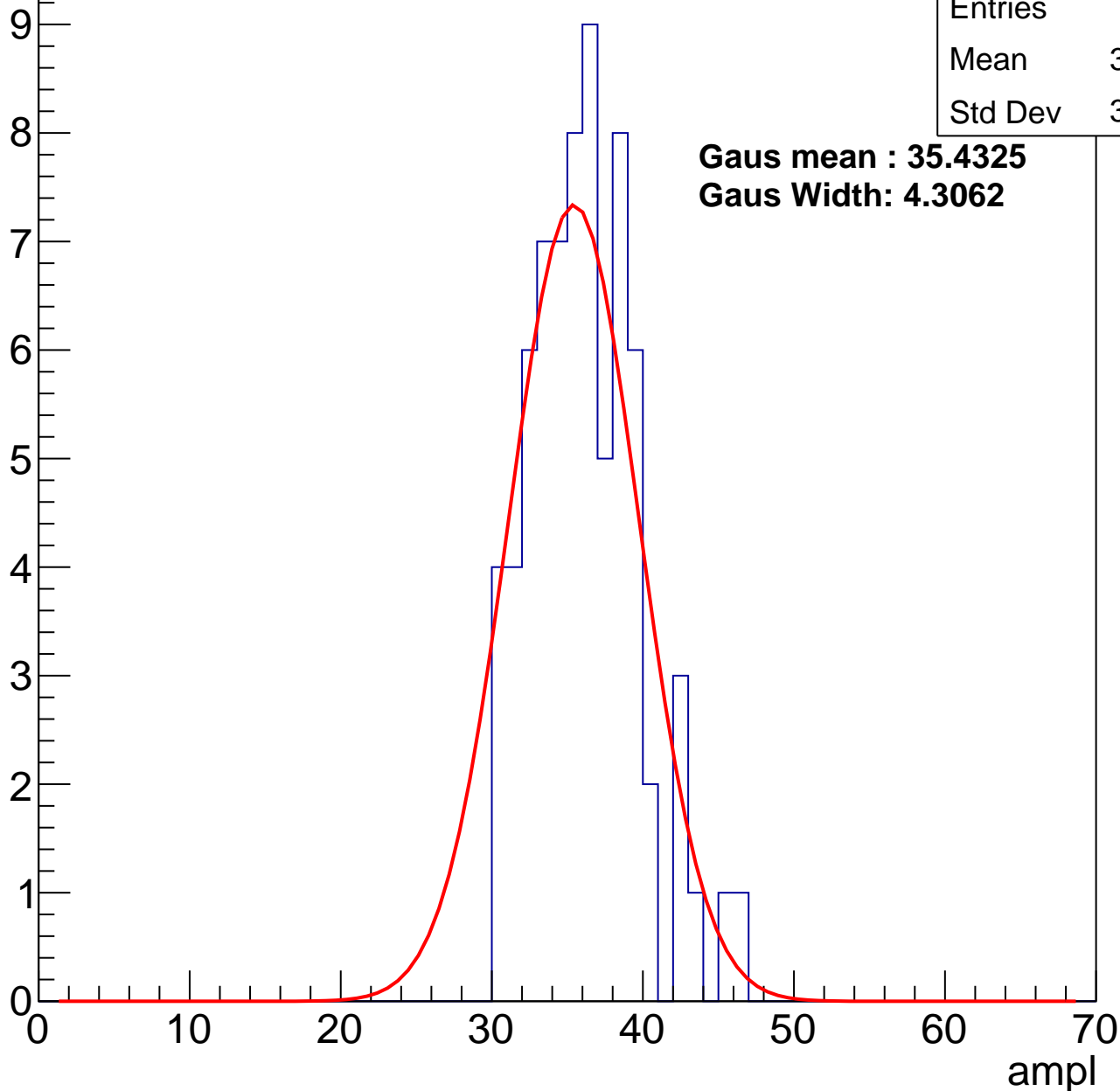
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.72 |
| Std Dev | 3.525 |

Gaus mean : 35.4325

Gaus Width: 4.3062



B1L100S, U7-ch88, adc2

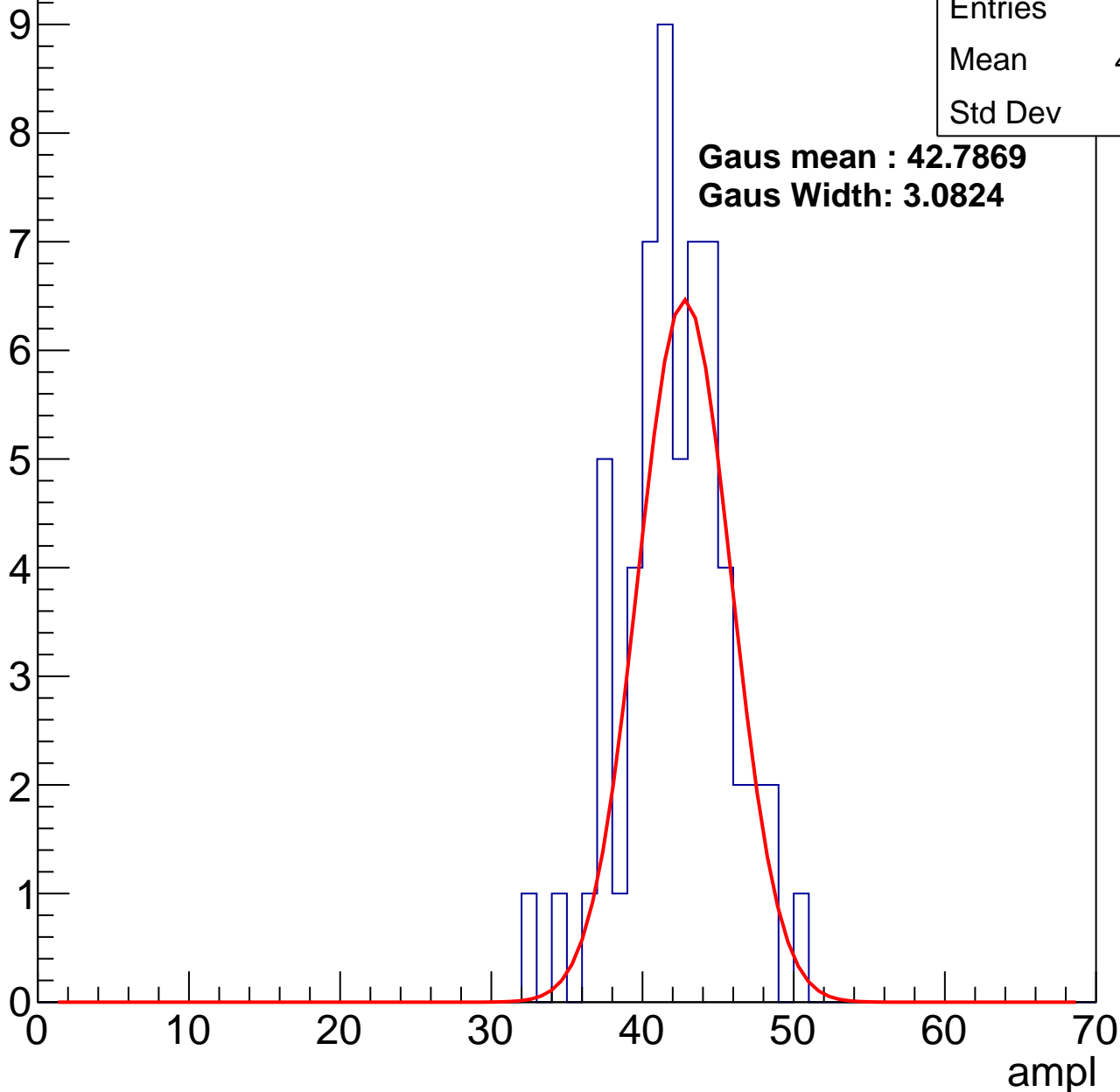
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 41.71 |
| Std Dev | 3.44 |

Gaus mean : 42.7869

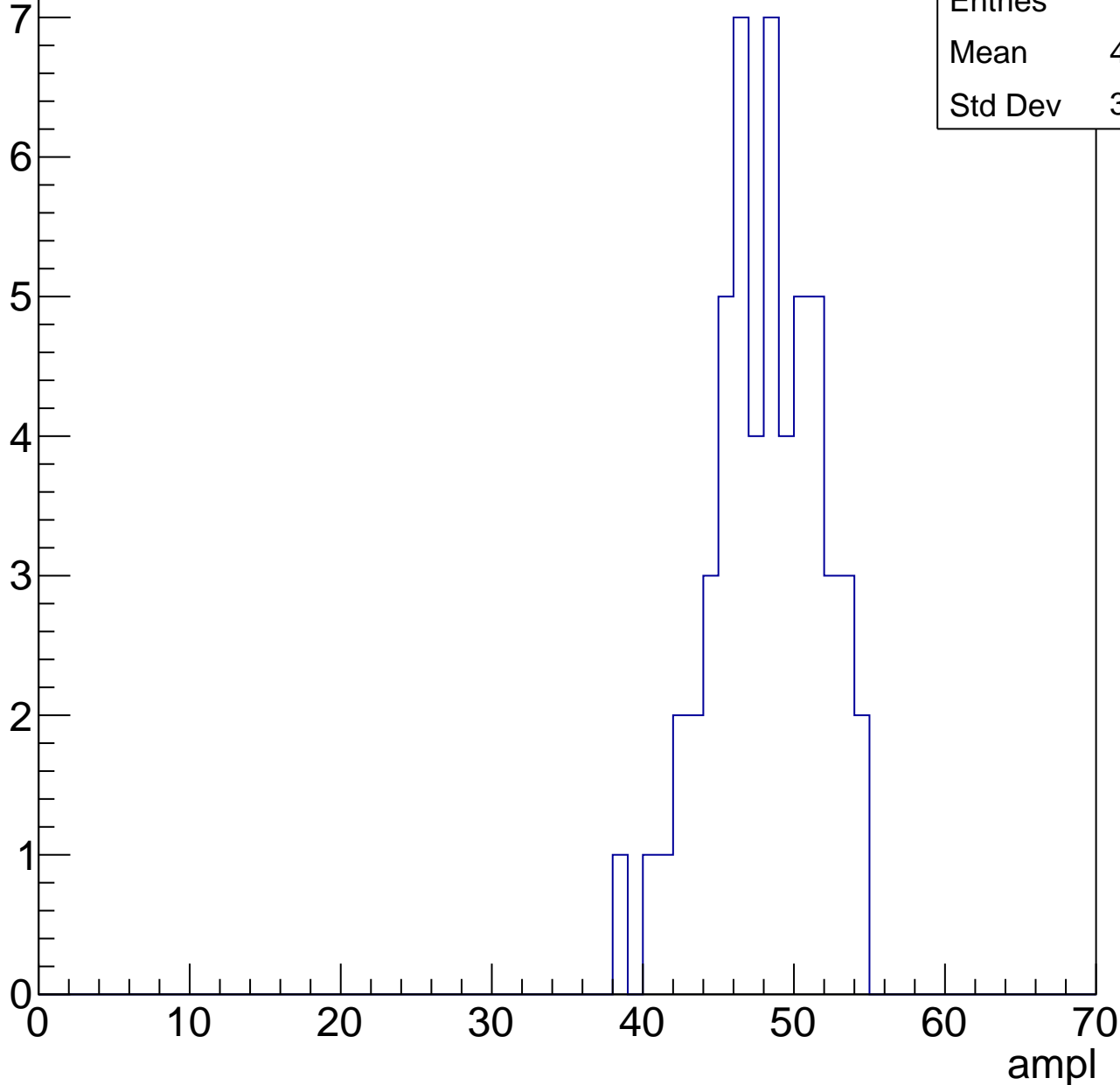
Gaus Width: 3.0824



B1L100S, U7-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

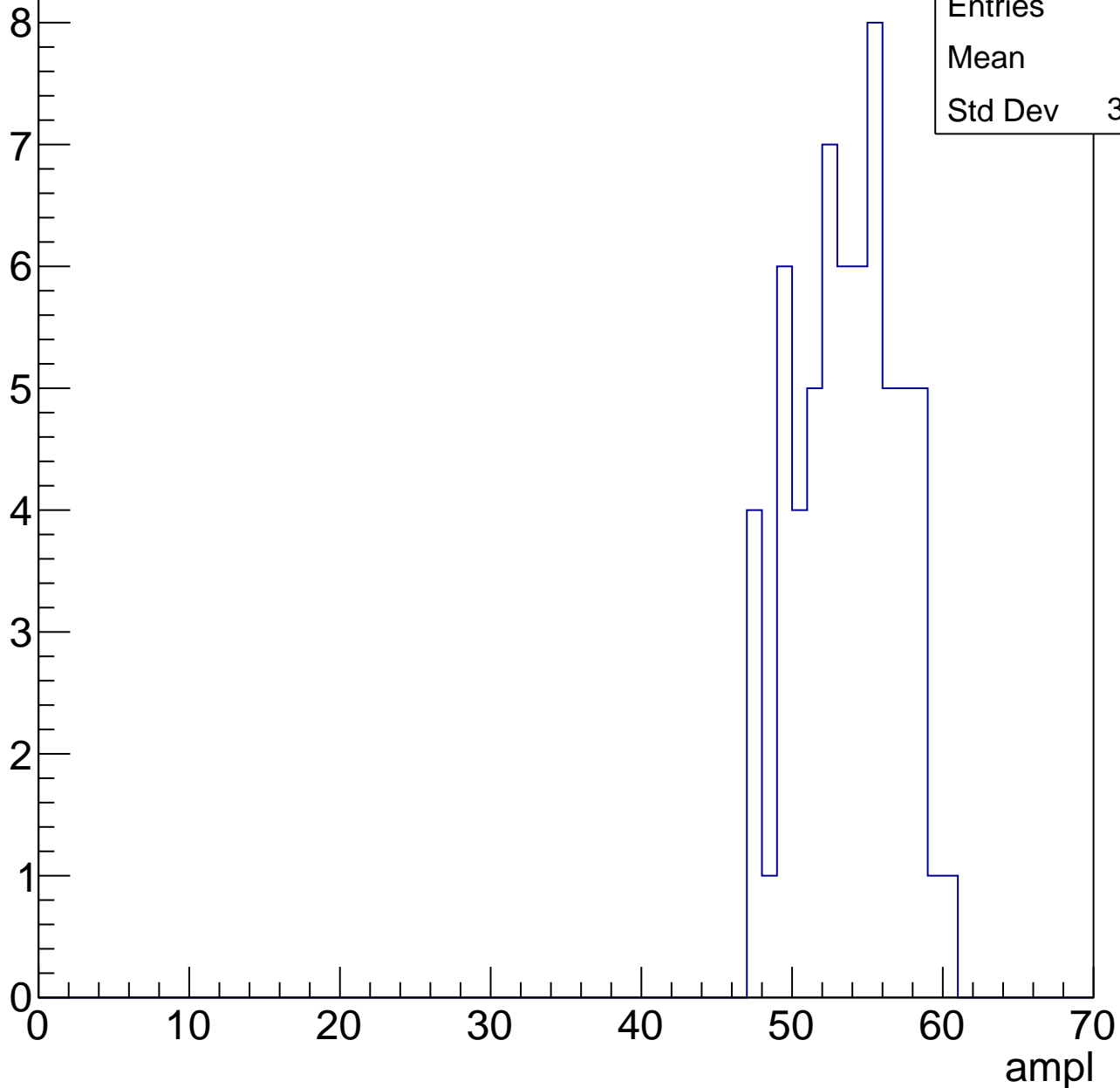


| | |
|---------|-------|
| Entries | 55 |
| Mean | 47.56 |
| Std Dev | 3.602 |

B1L100S, U7-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

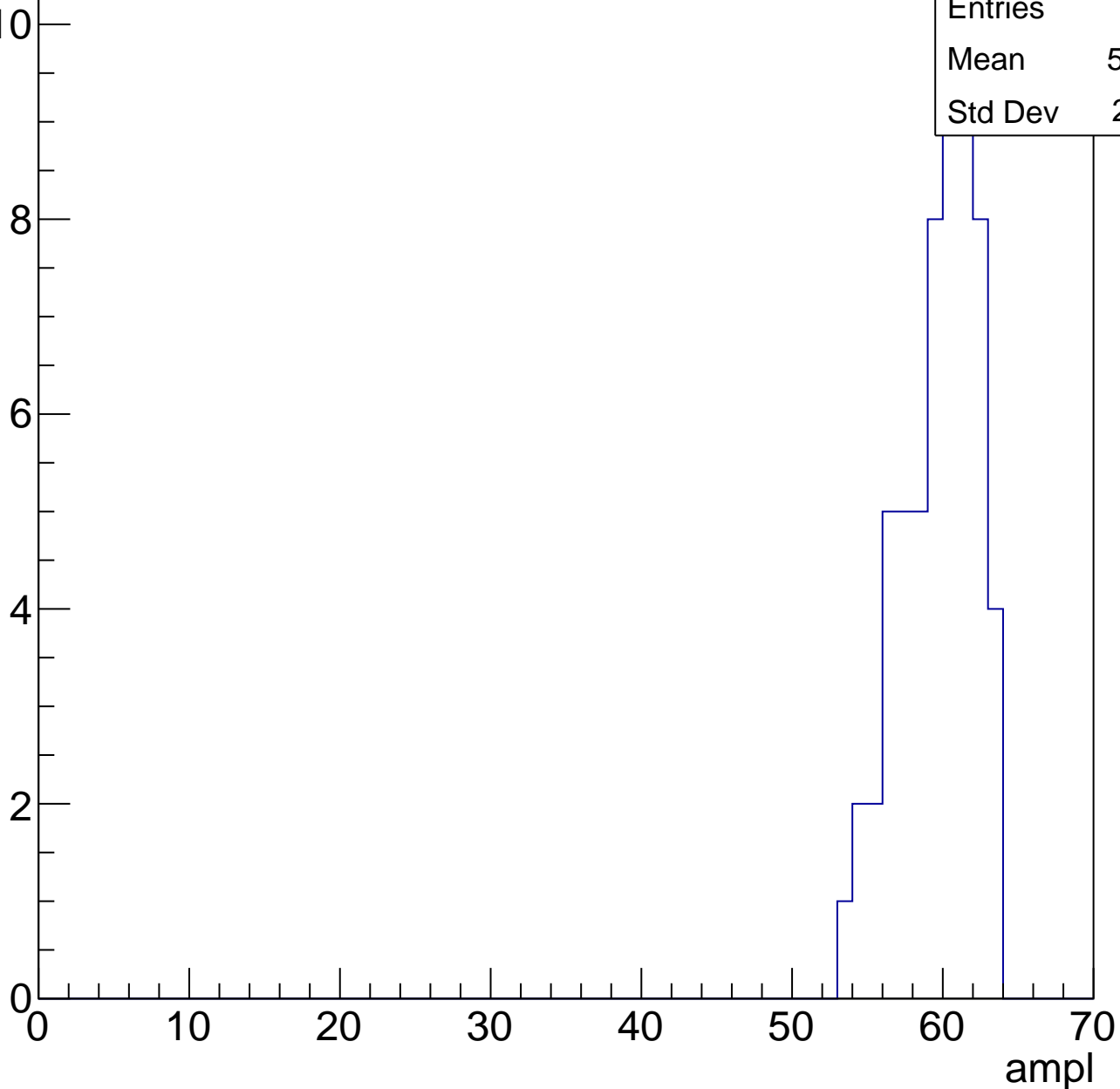


B1L100S, U7-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 59.25 |
| Std Dev | 2.501 |

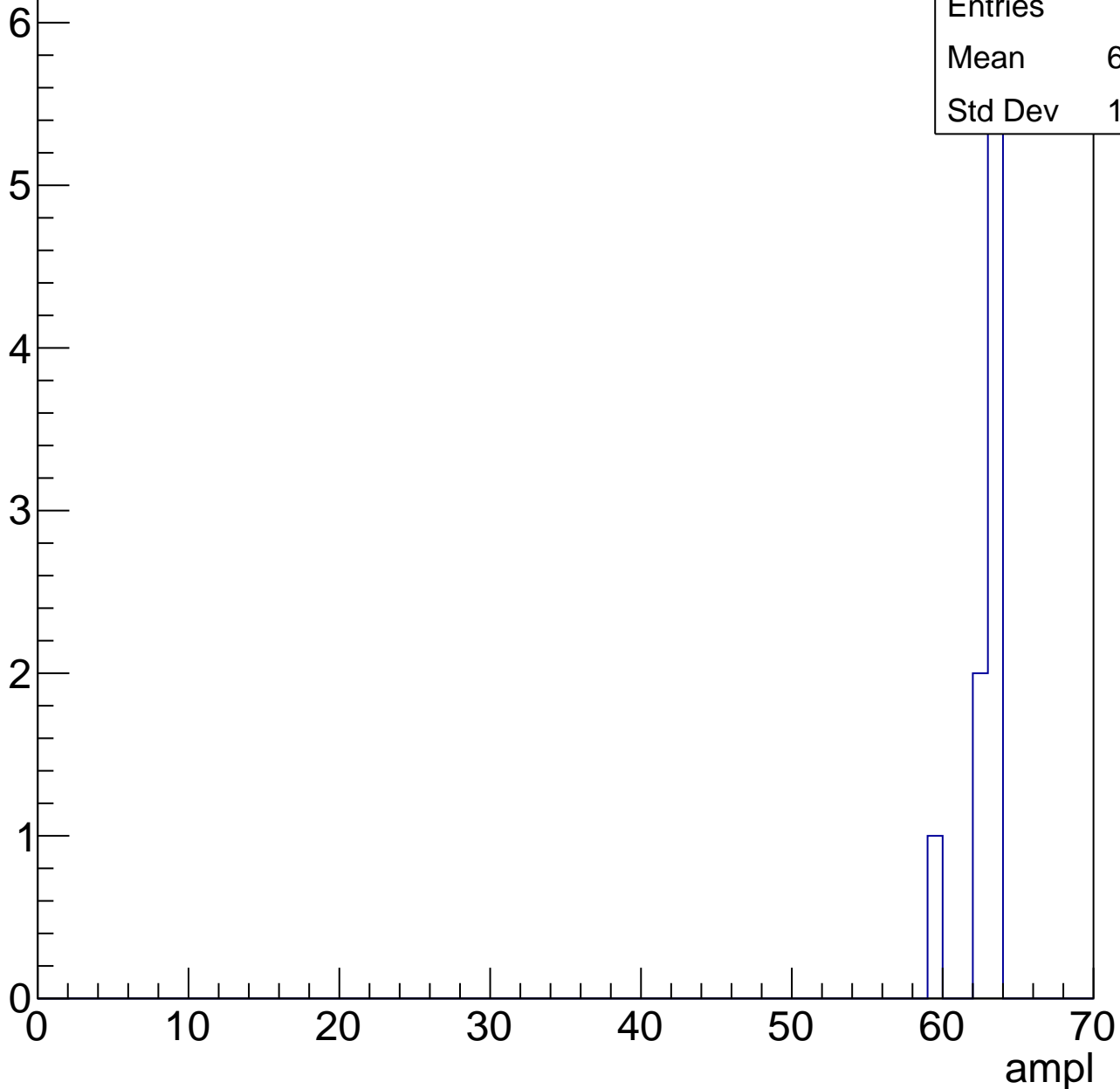


B1L100S, U7-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 62.33 |
| Std Dev | 1.247 |



B1L100S, U7-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch89, adc0

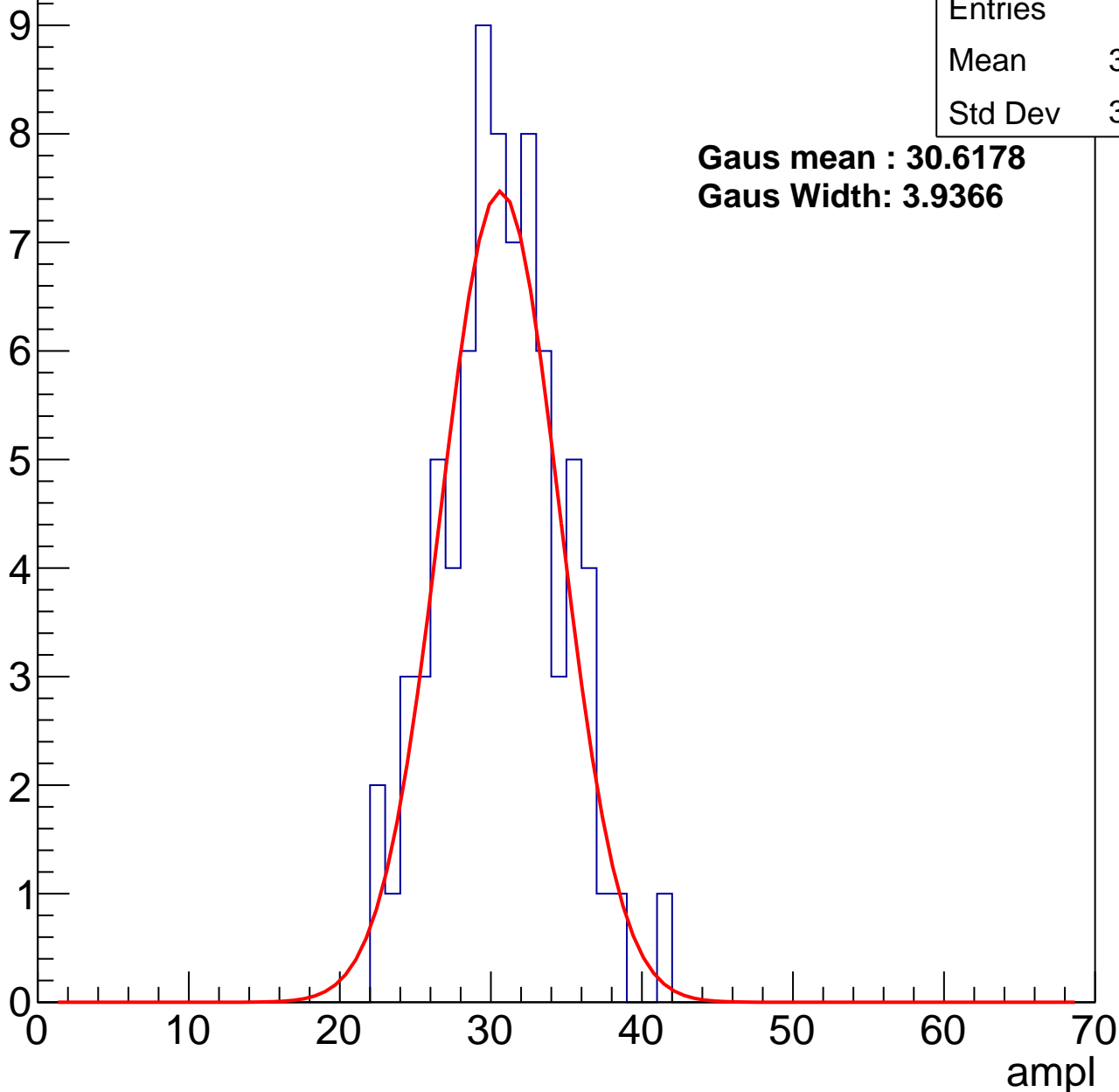
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 30.25 |
| Std Dev | 3.862 |

Gaus mean : 30.6178

Gaus Width: 3.9366



B1L100S, U7-ch89, adc1

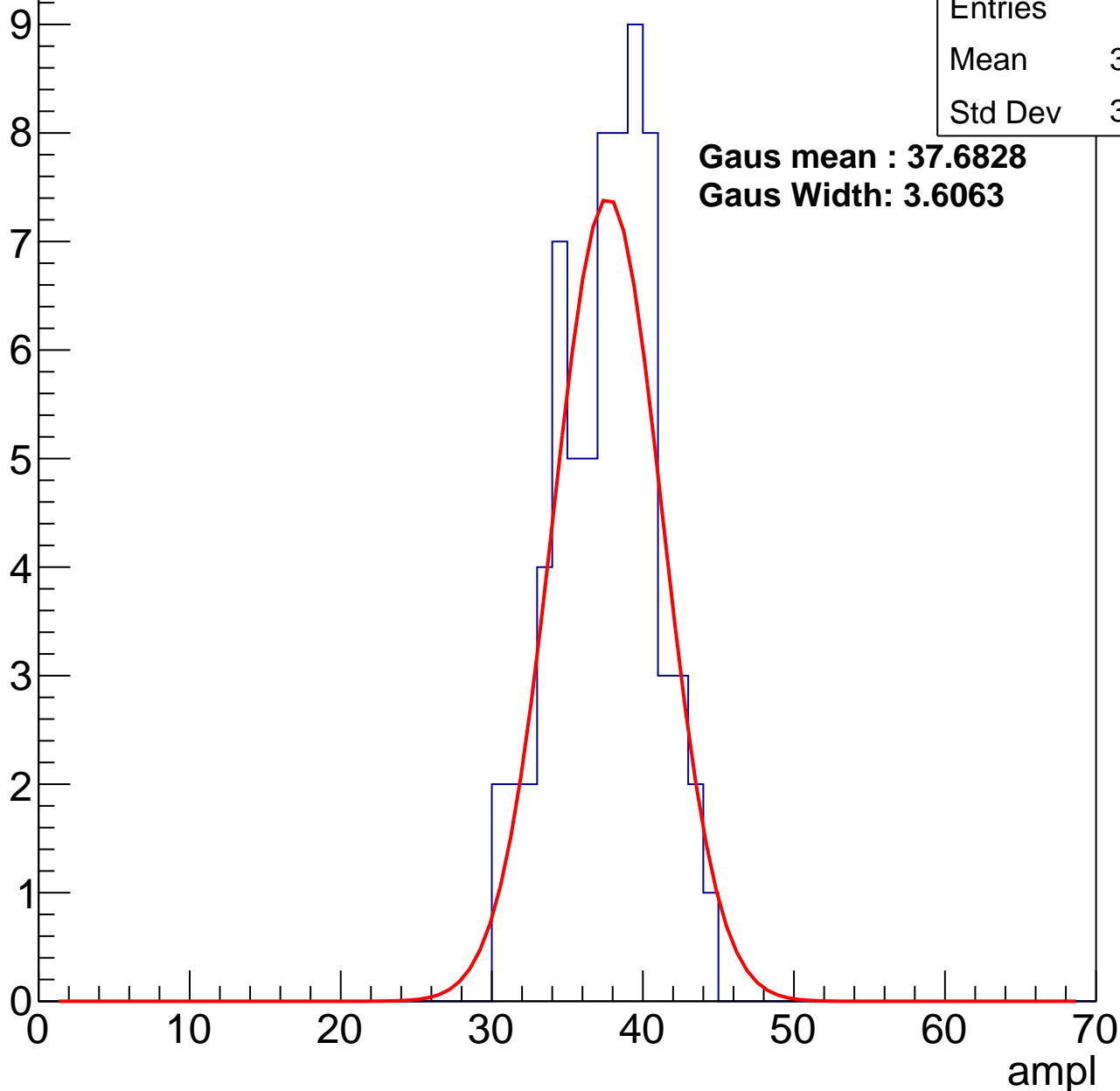
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 37.12 |
| Std Dev | 3.255 |

Gaus mean : 37.6828

Gaus Width: 3.6063



B1L100S, U7-ch89, adc2

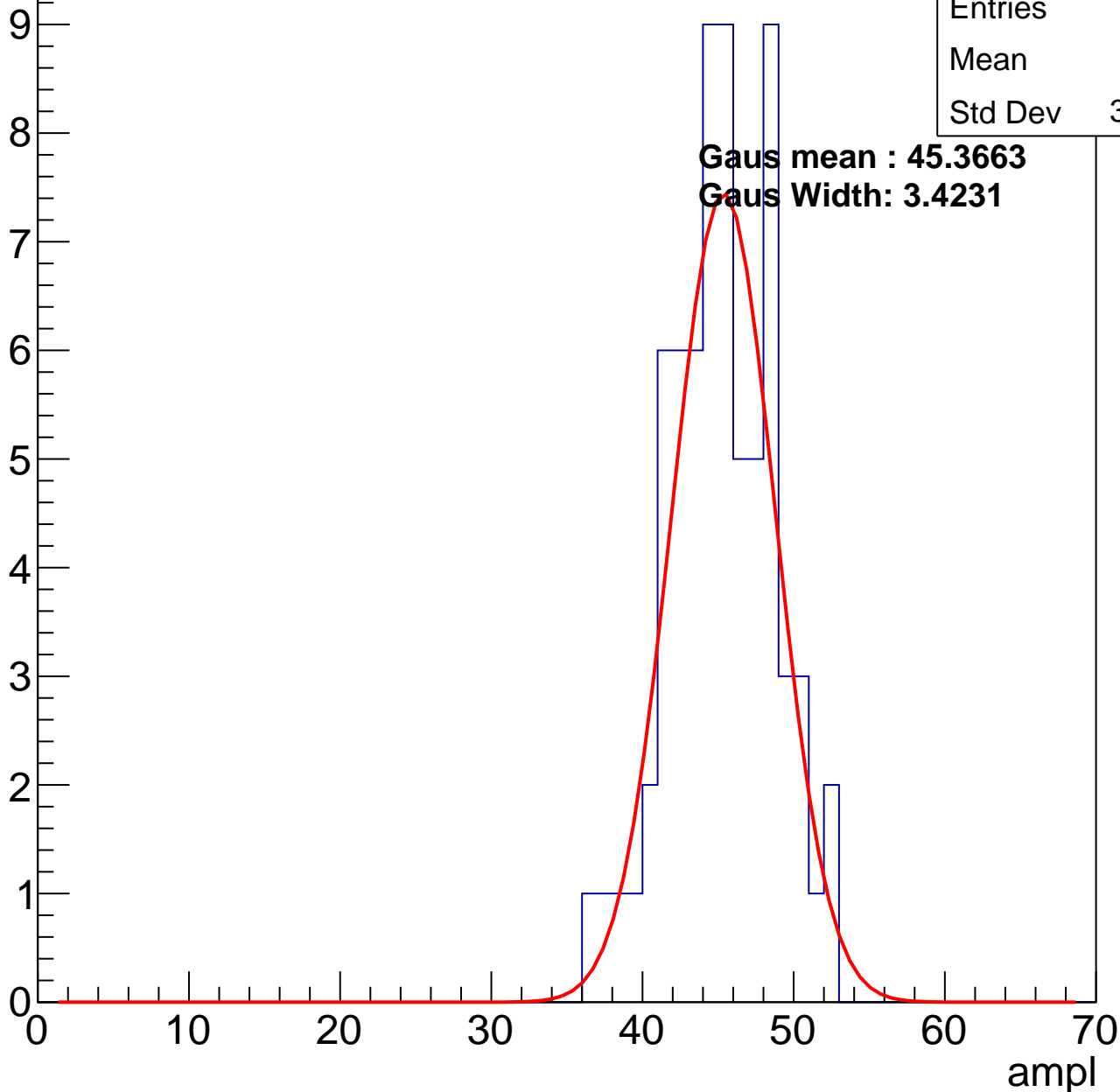
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.8 |
| Std Dev | 3.429 |

Gaus mean : 45.3663

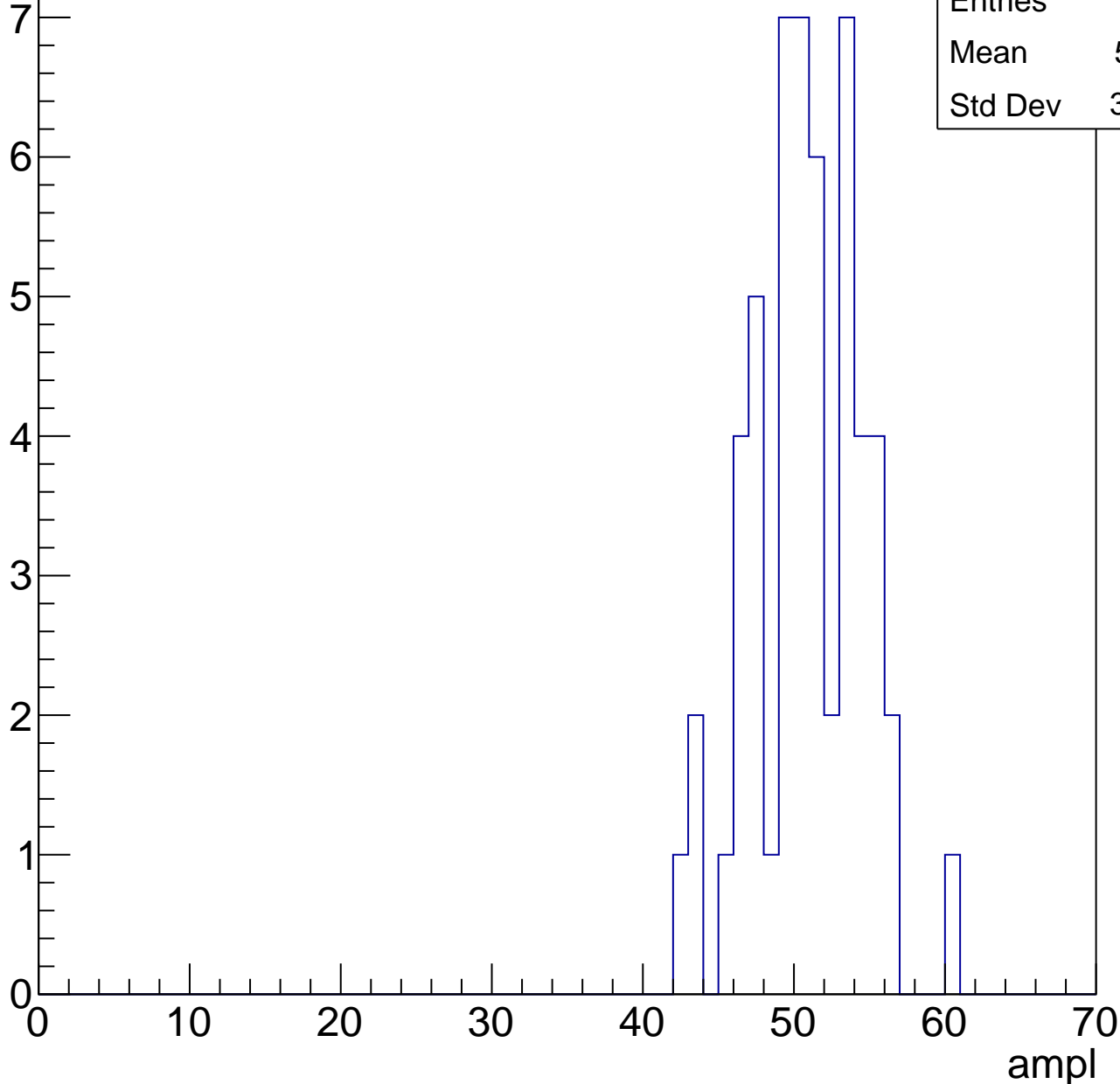
Gaus Width: 3.4231



B1L100S, U7-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

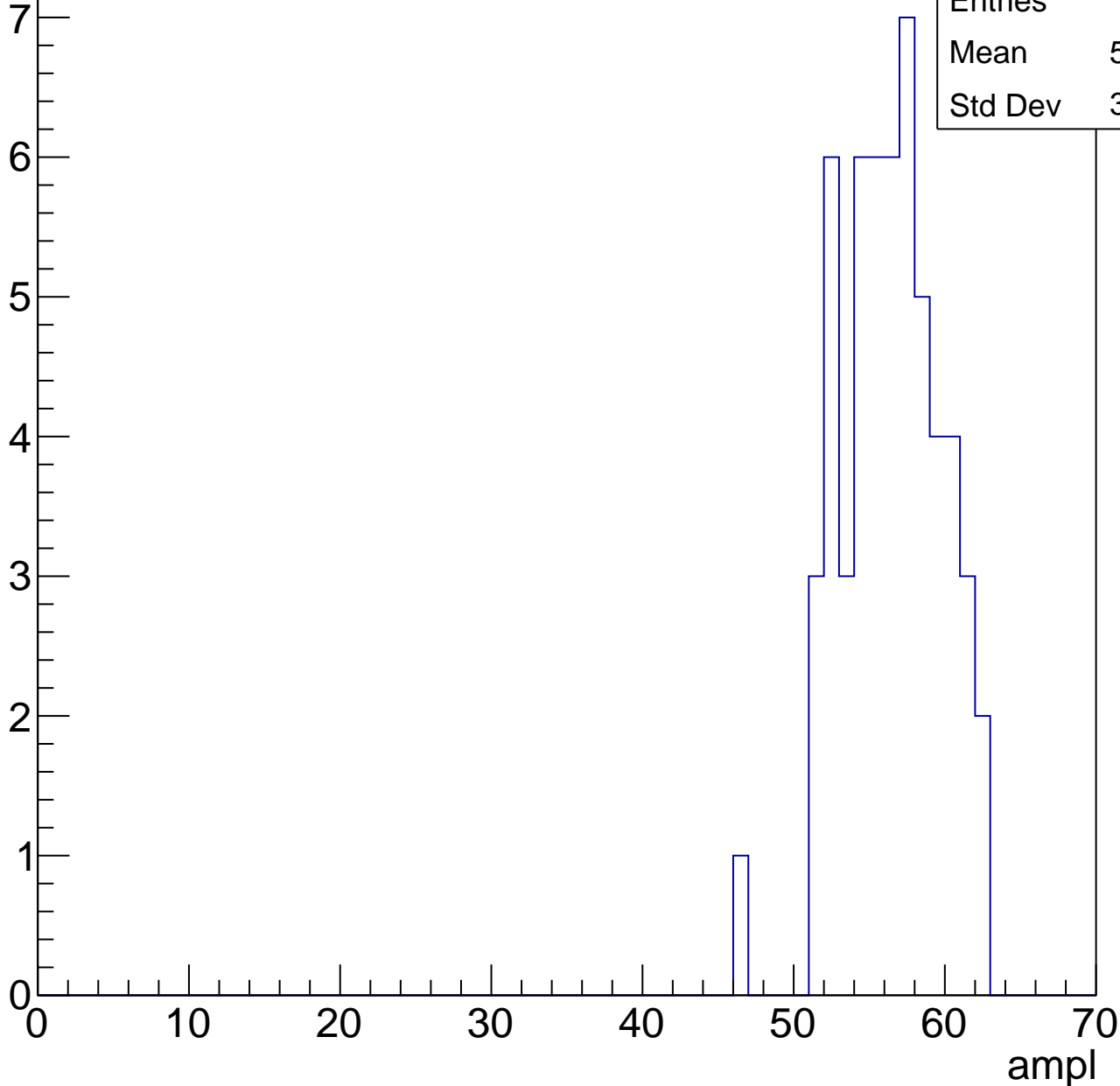


B1L100S, U7-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.93 |
| Std Dev | 3.278 |

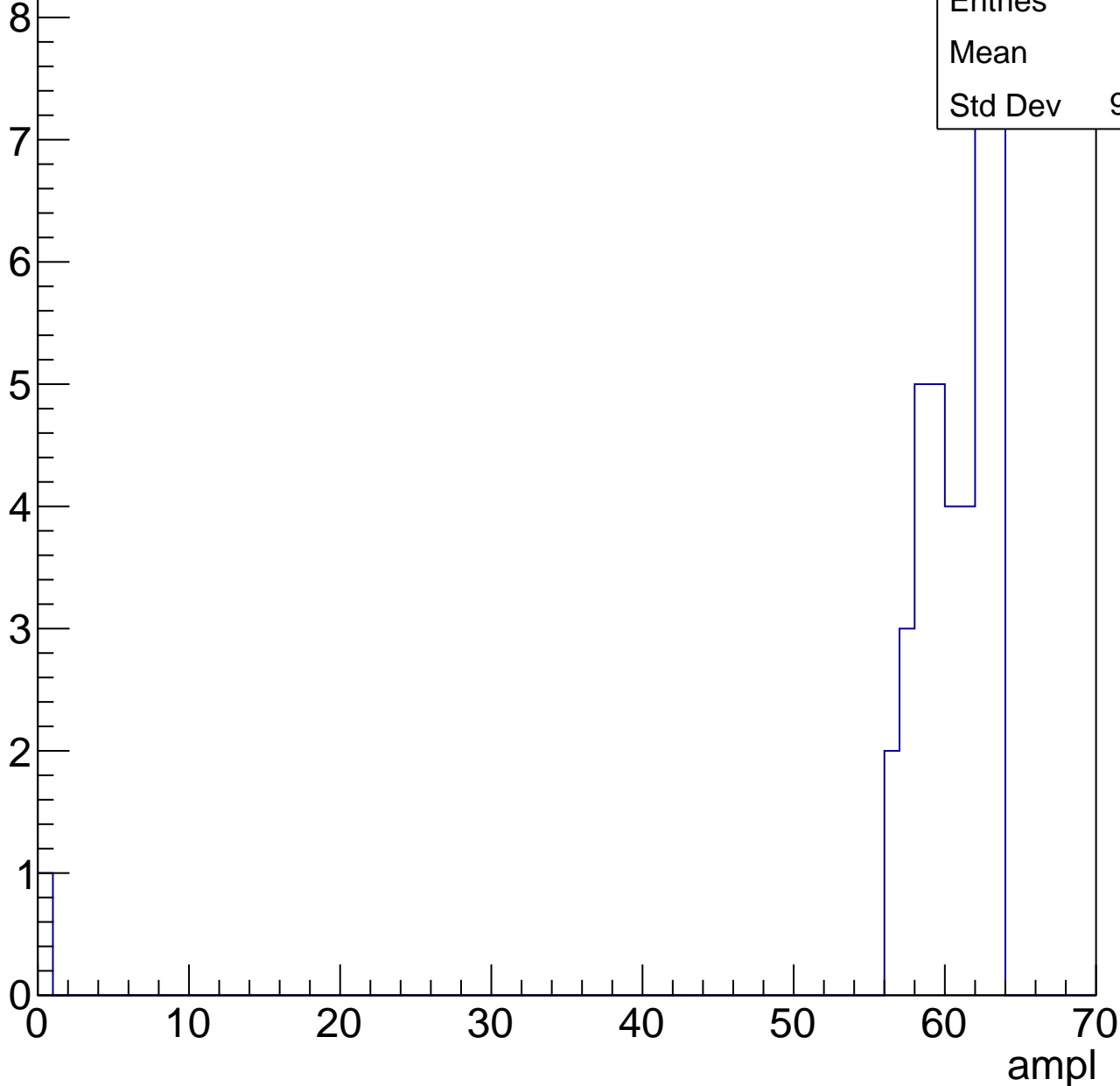


B1L100S, U7-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

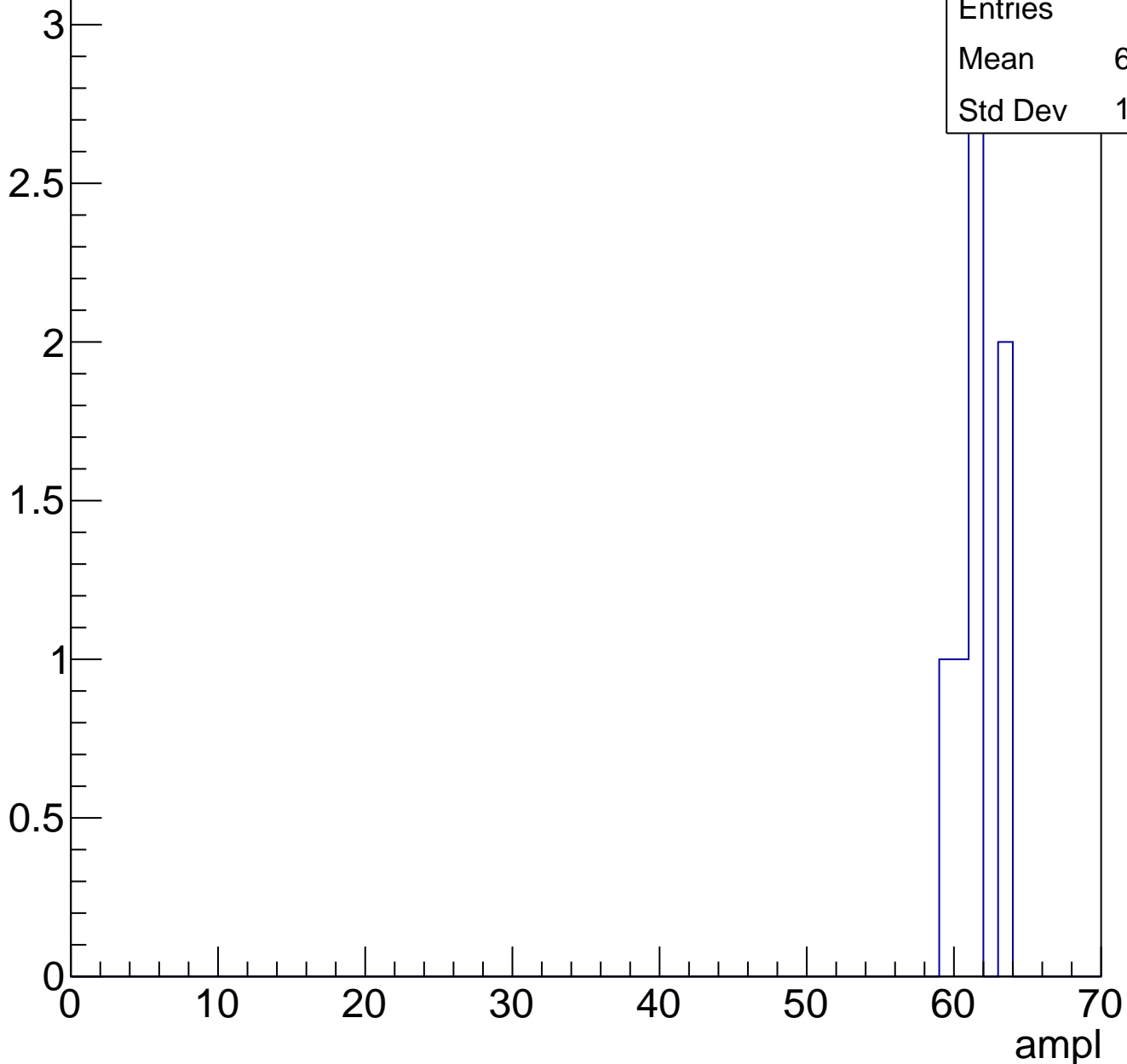
| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.8 |
| Std Dev | 9.662 |



B1L100S, U7-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch90, adc0

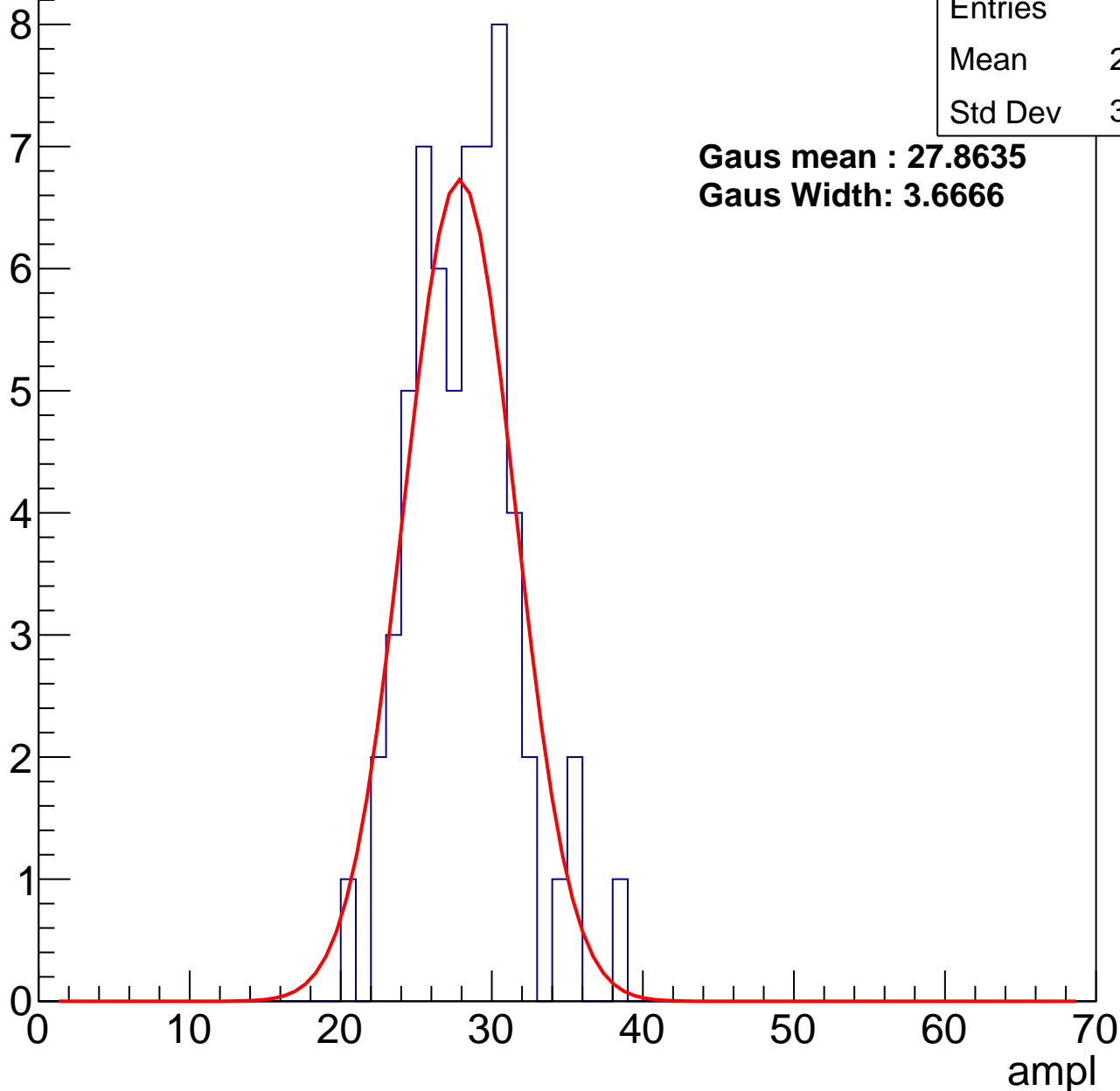
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 27.67 |
| Std Dev | 3.429 |

Gaus mean : 27.8635

Gaus Width: 3.6666



B1L100S, U7-ch90, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 34.79 |
| Std Dev | 3.568 |

Gaus mean : 35.4137

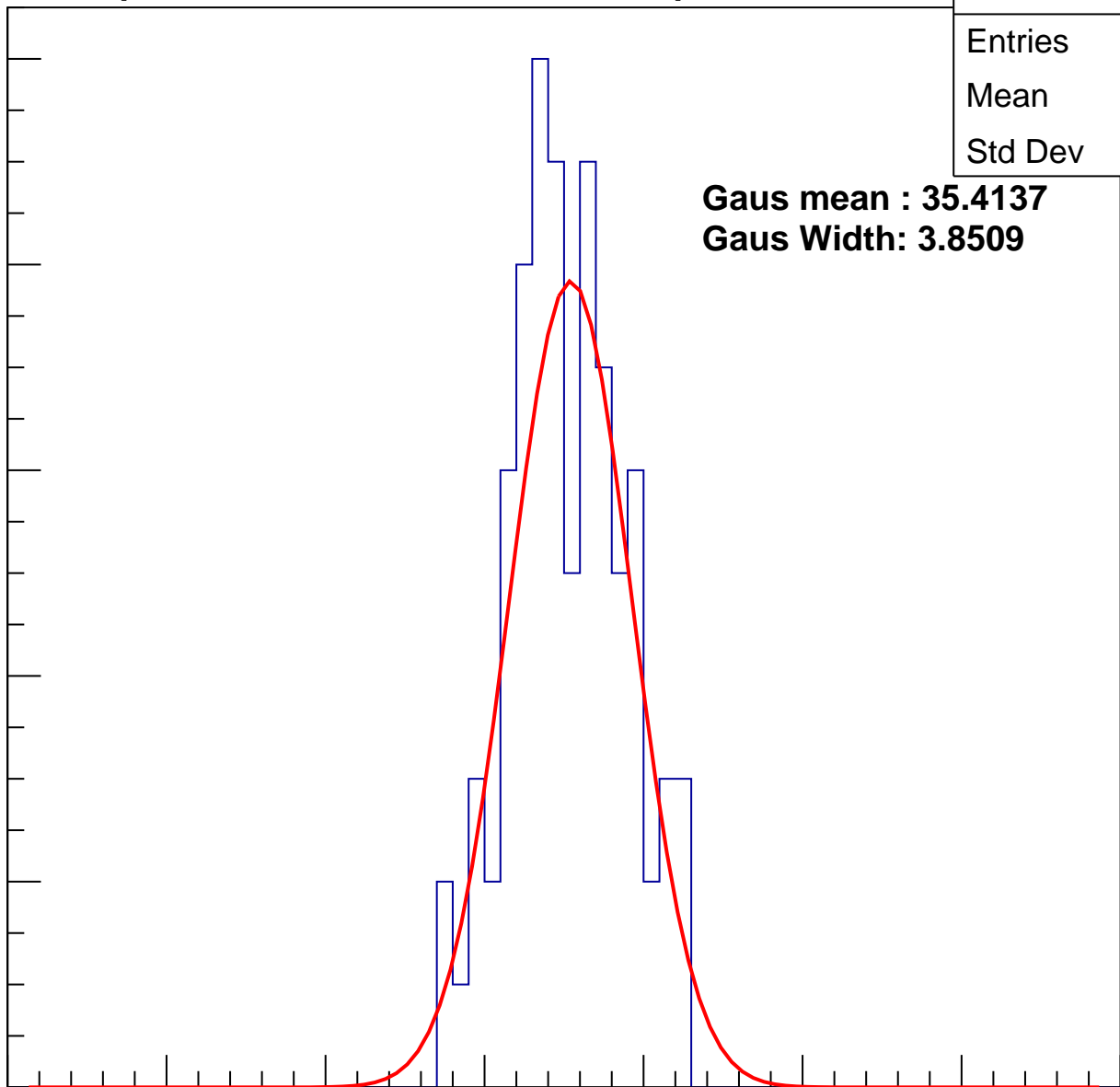
Gaus Width: 3.8509

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch90, adc2

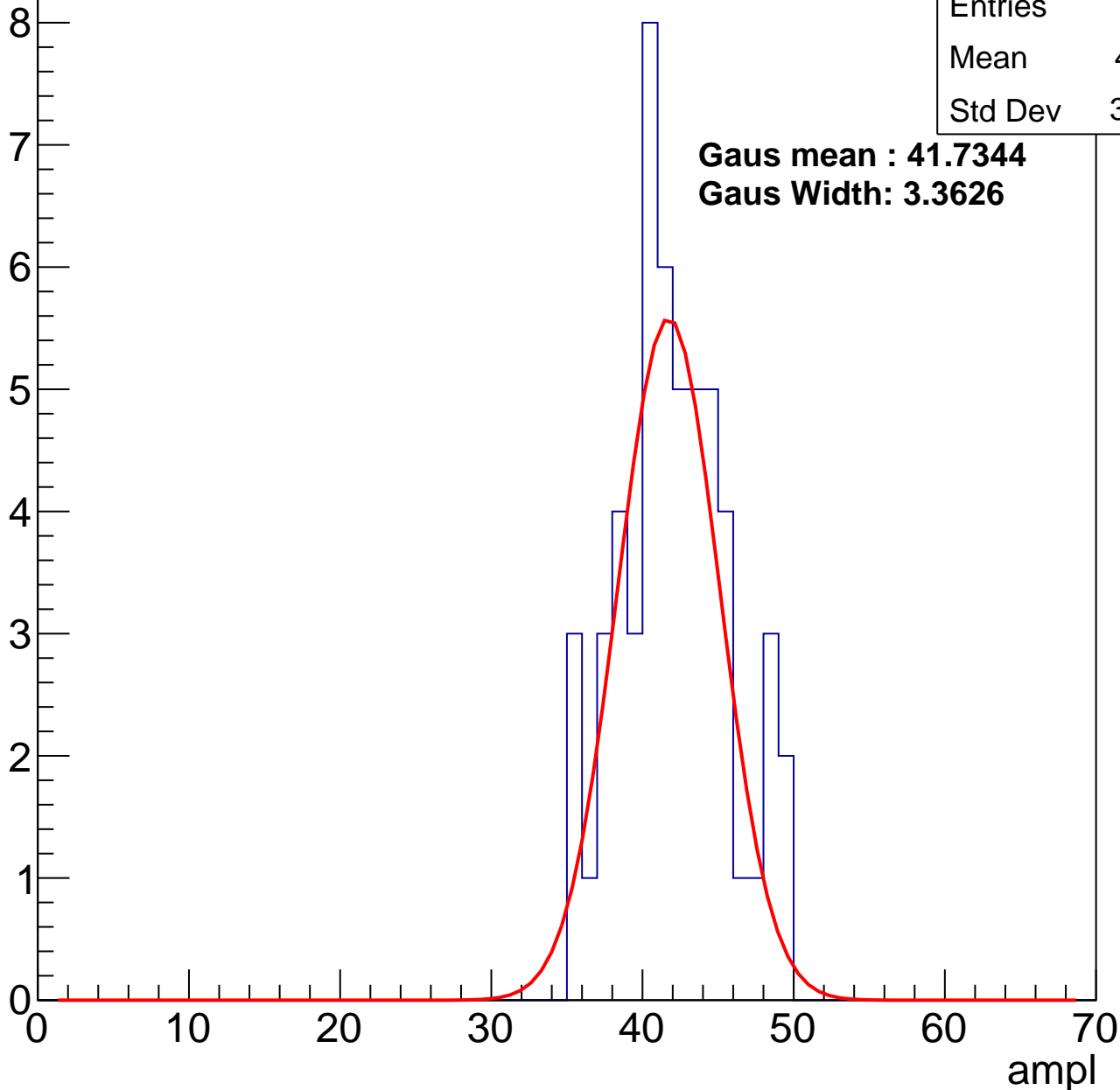
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 41.61 |
| Std Dev | 3.566 |

Gaus mean : 41.7344

Gaus Width: 3.3626

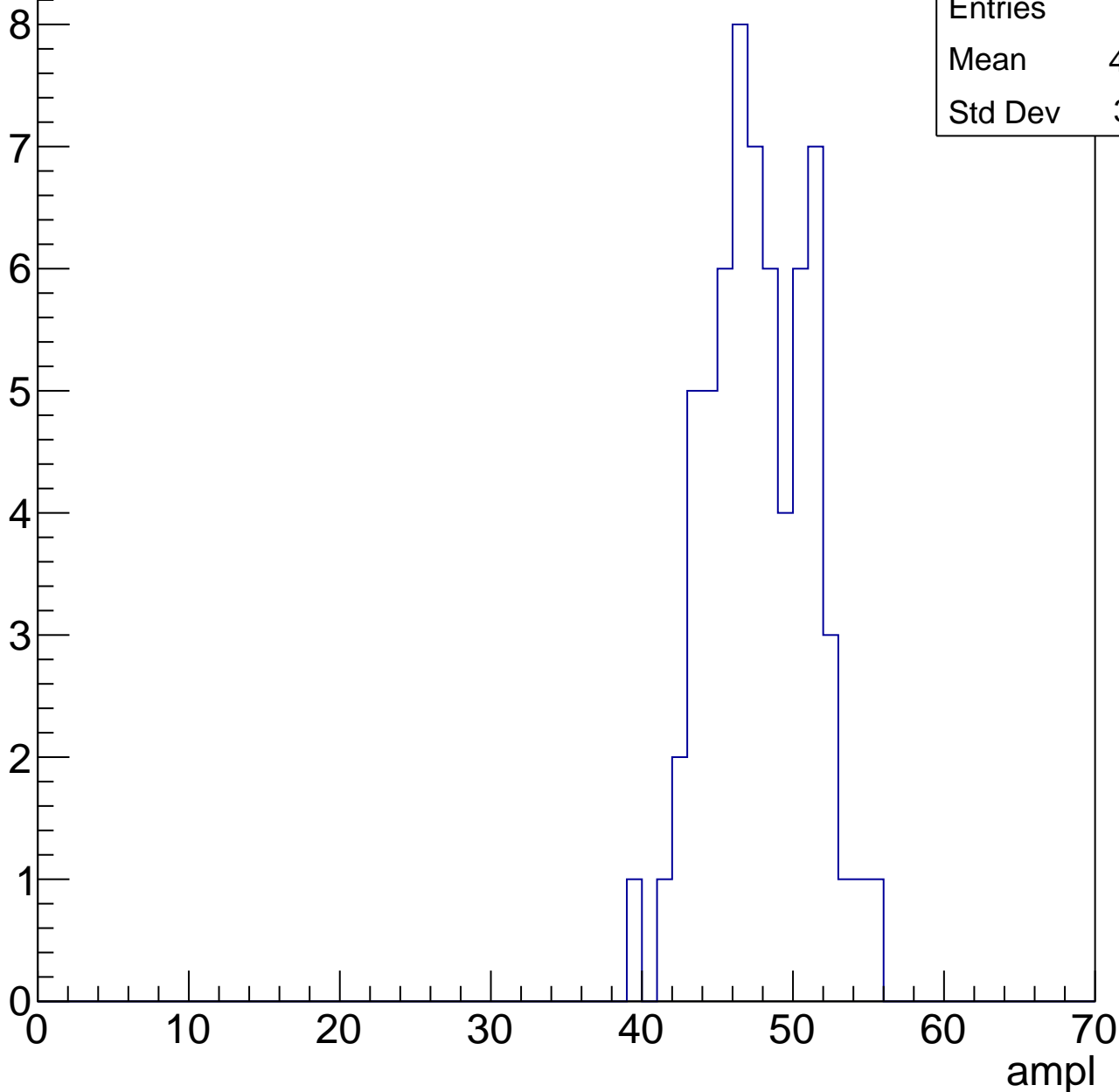


B1L100S, U7-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 47.27 |
| Std Dev | 3.341 |

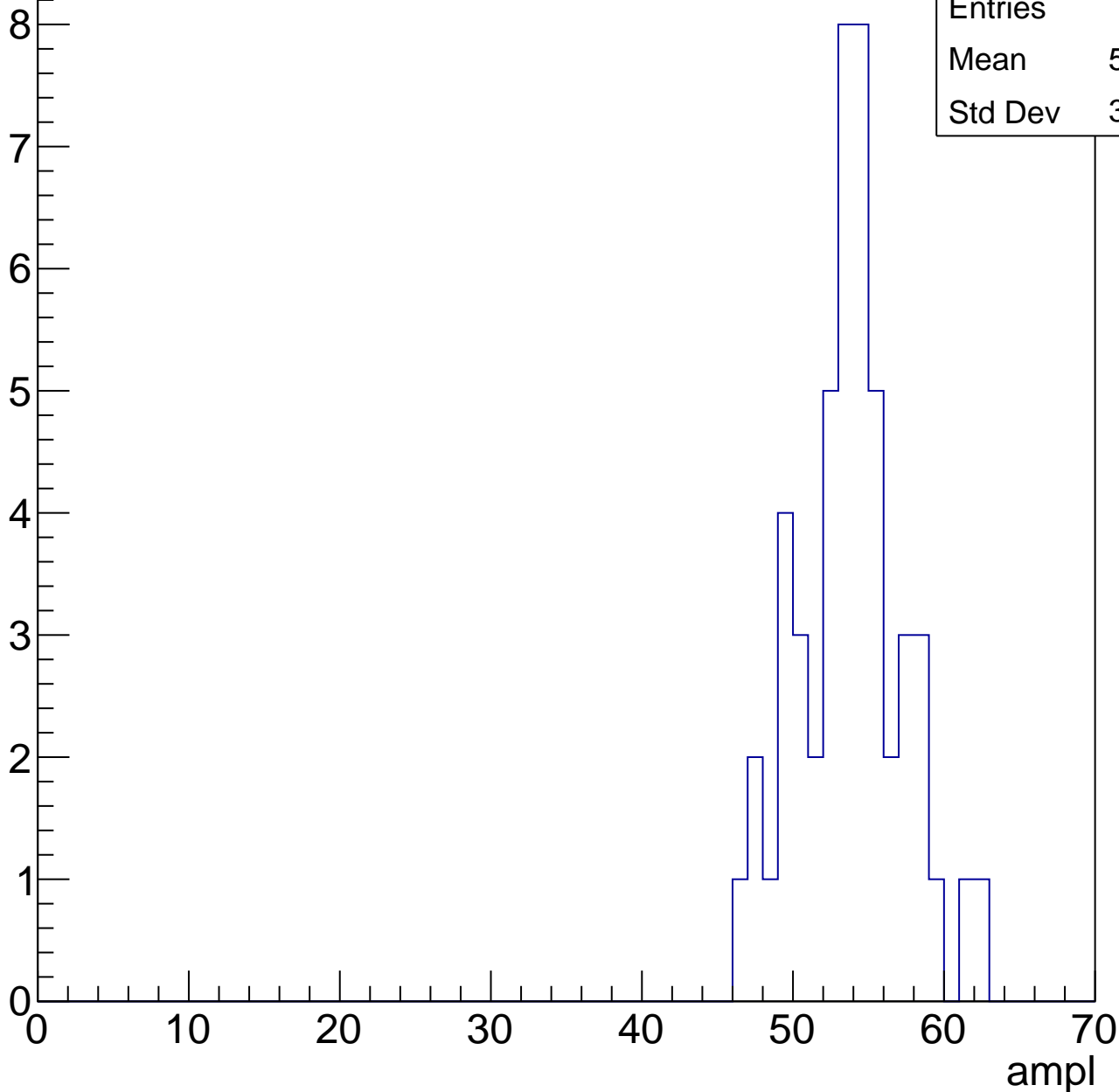


B1L100S, U7-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

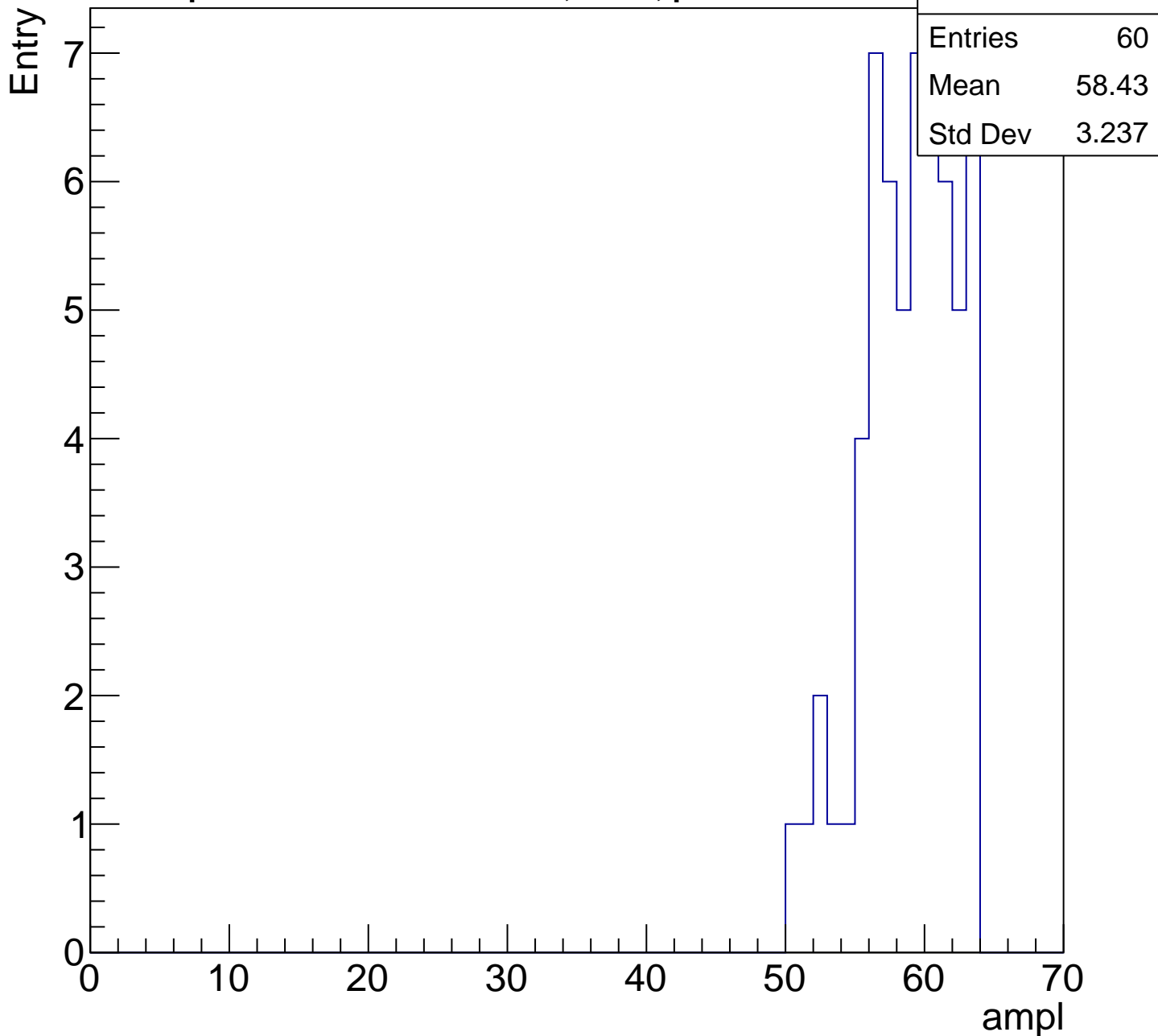
Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 53.32 |
| Std Dev | 3.449 |



B1L100S, U7-ch90, adc5

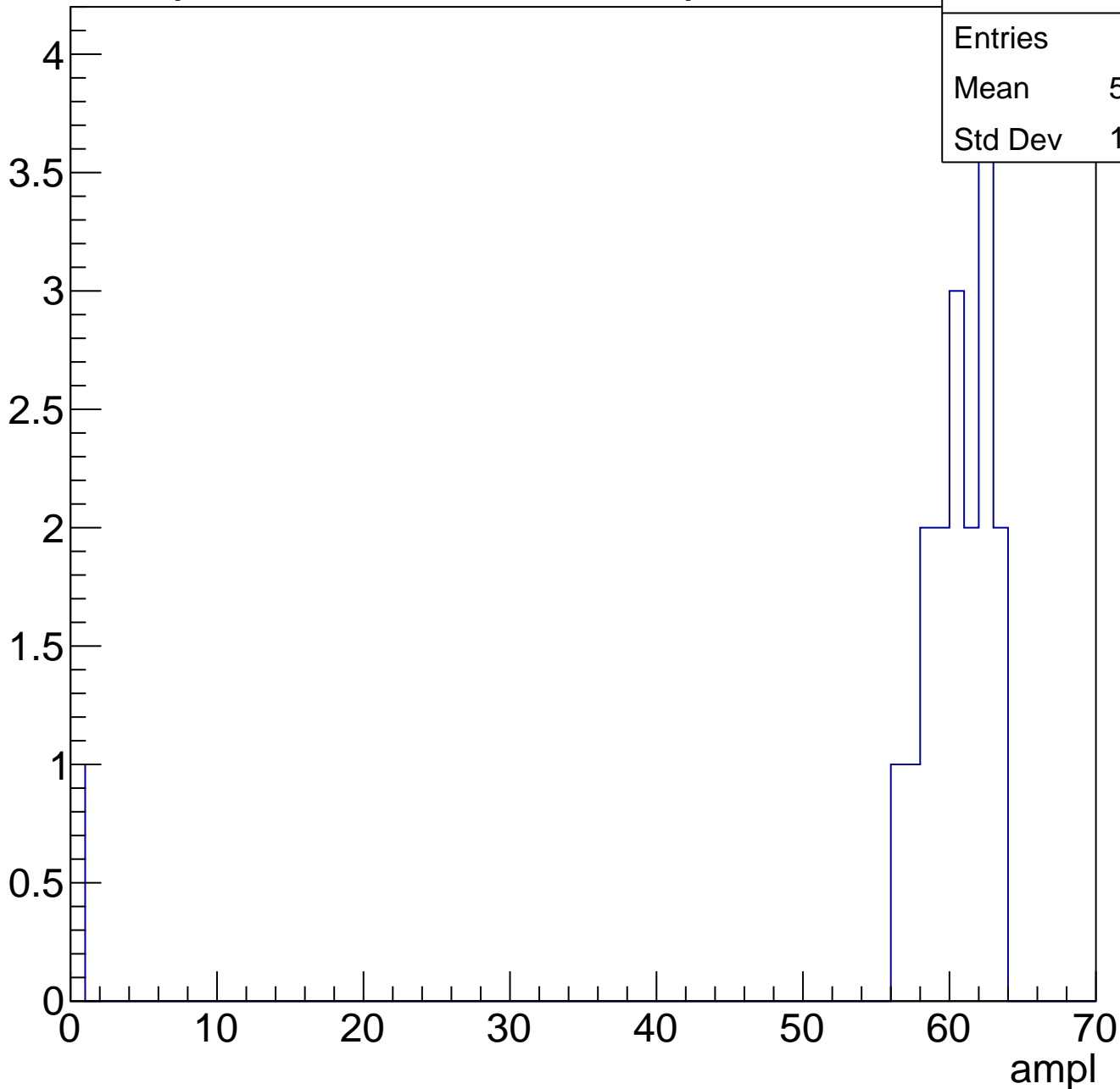
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

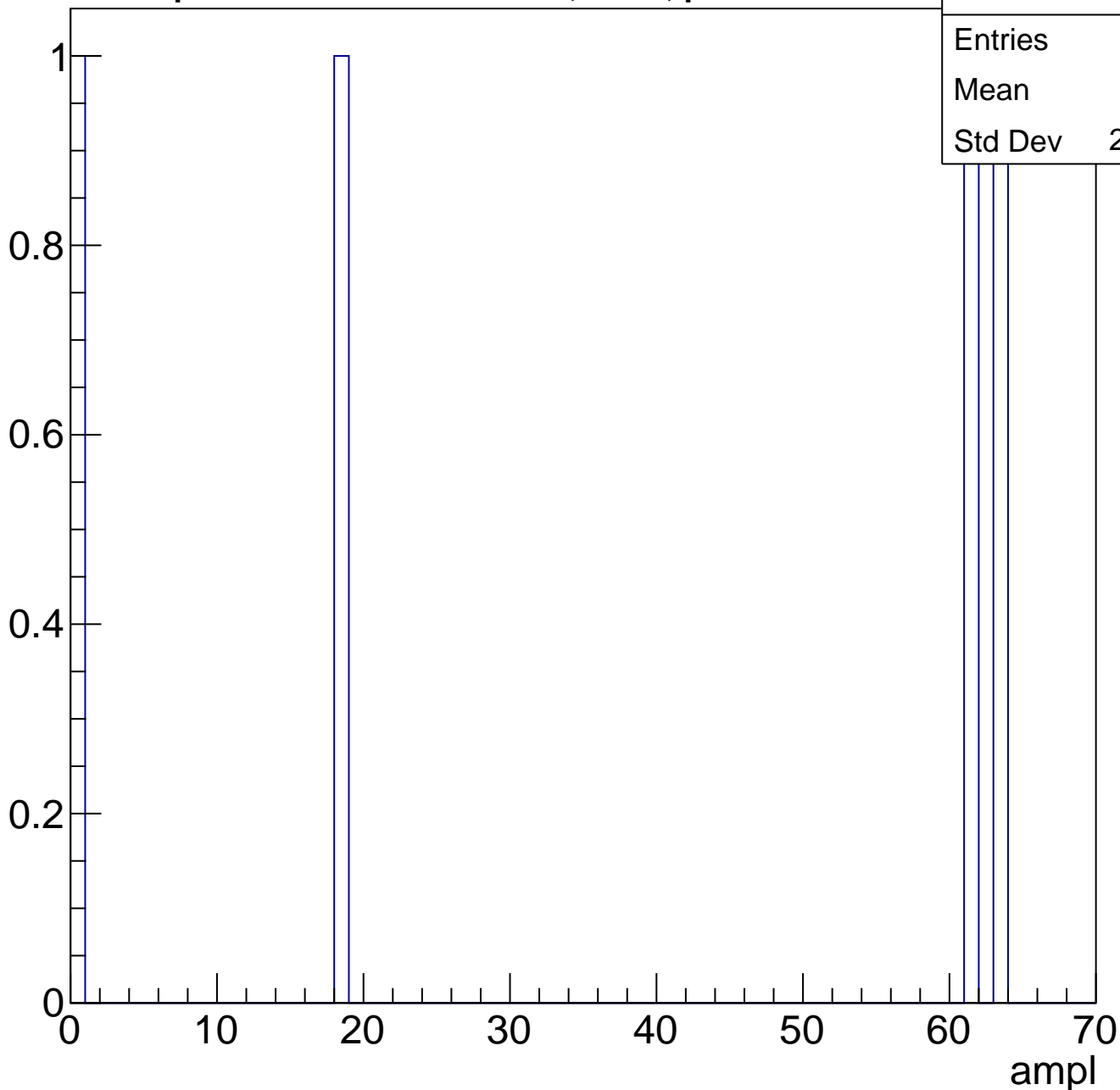
Entry



B1L100S, U7-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 35.5 |
| Std Dev | 27.26 |

B1L100S, U7-ch91, adc0

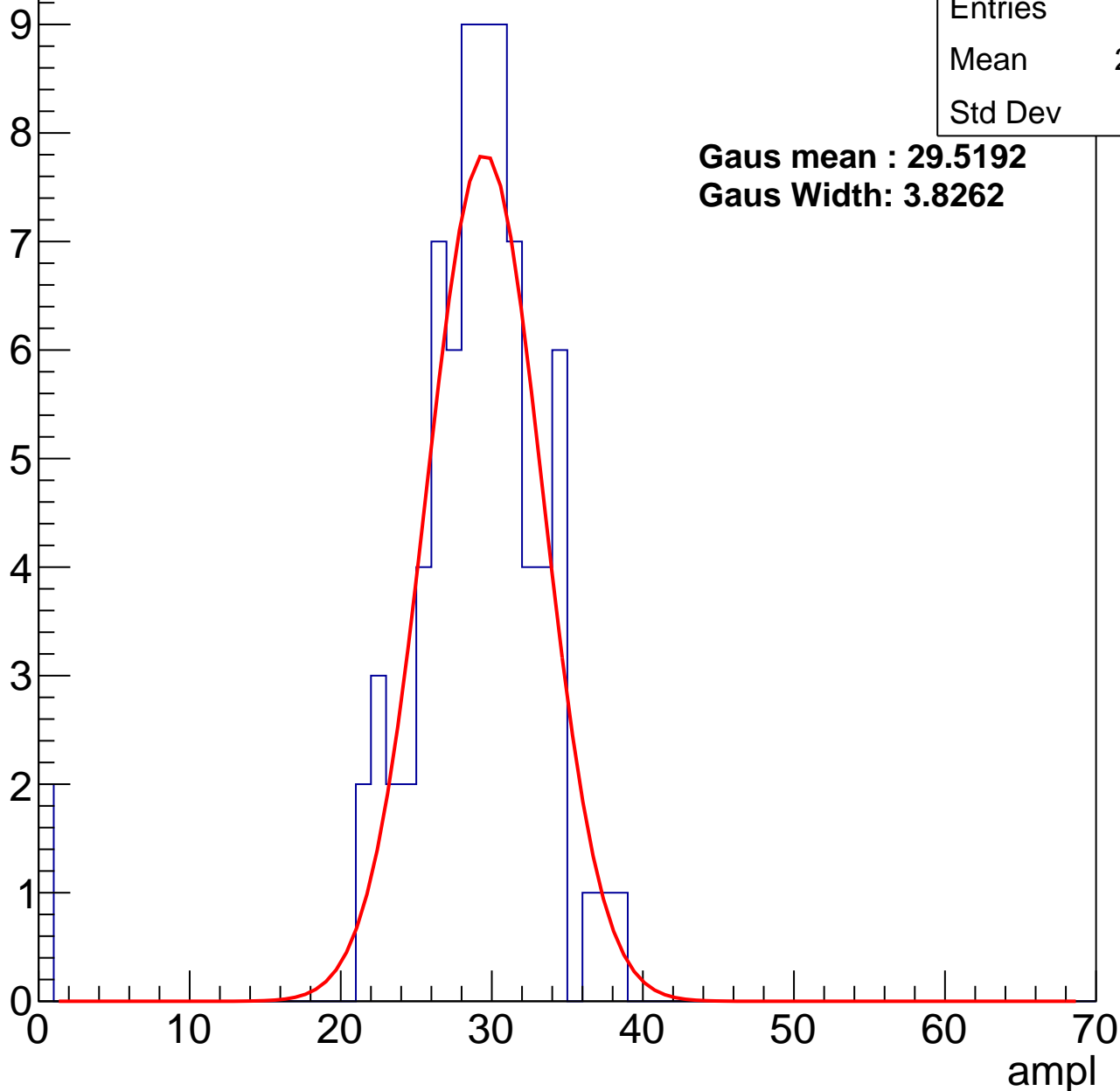
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.11 |
| Std Dev | 5.8 |

Gaus mean : 29.5192

Gaus Width: 3.8262



B1L100S, U7-ch91, adc1

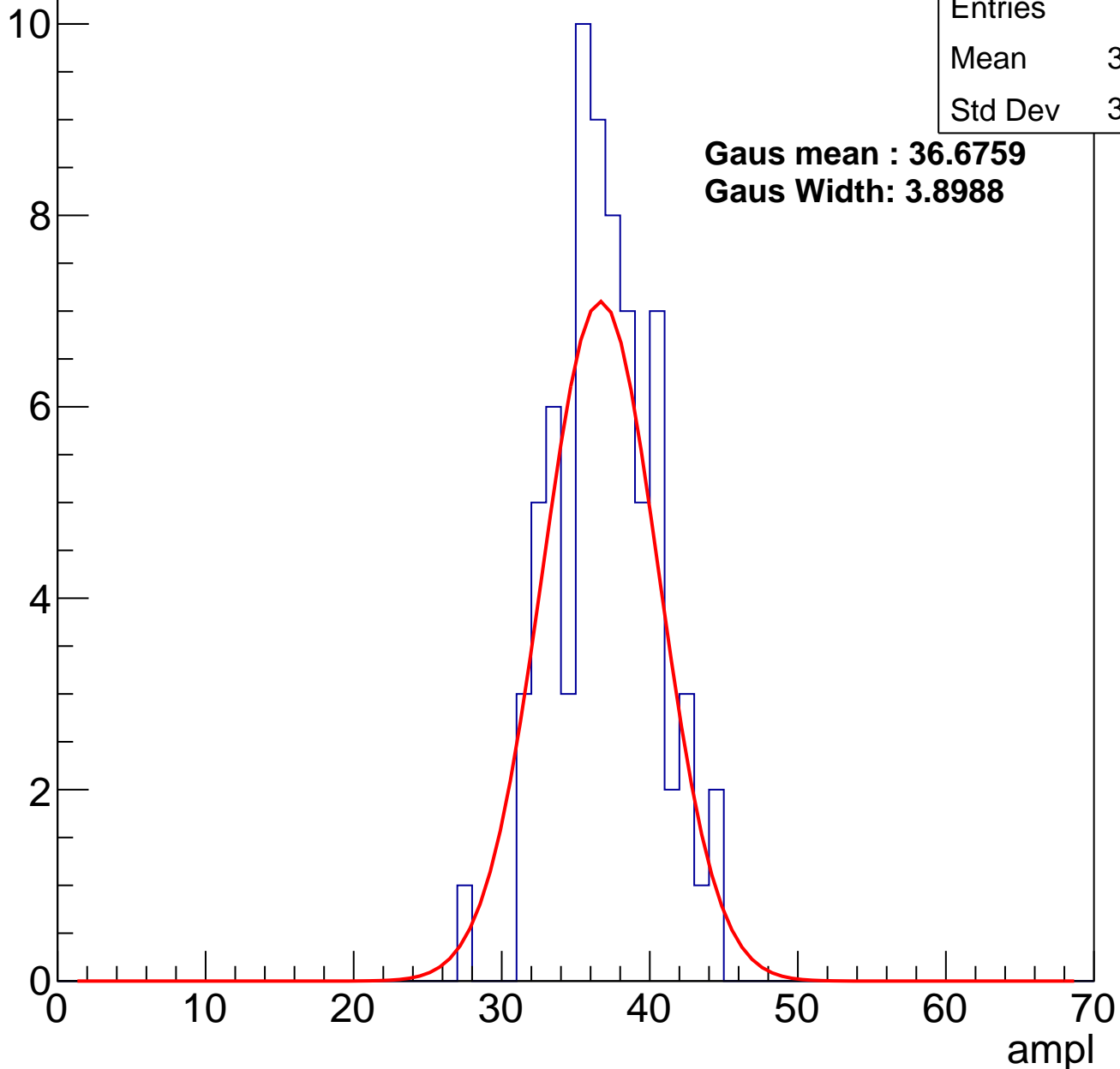
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.53 |
| Std Dev | 3.375 |

Gaus mean : 36.6759

Gaus Width: 3.8988

Entry



B1L100S, U7-ch91, adc2

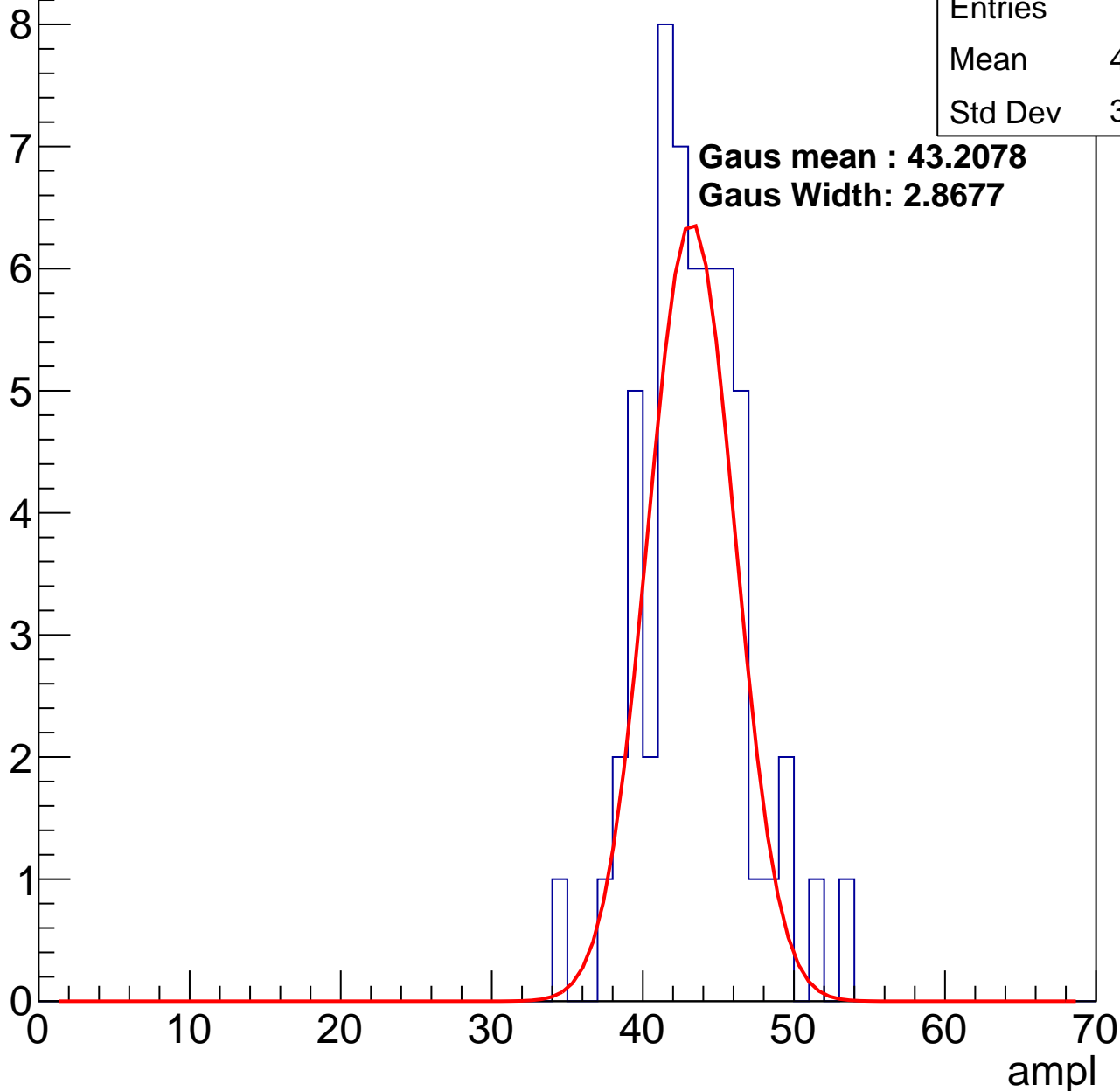
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 42.96 |
| Std Dev | 3.453 |

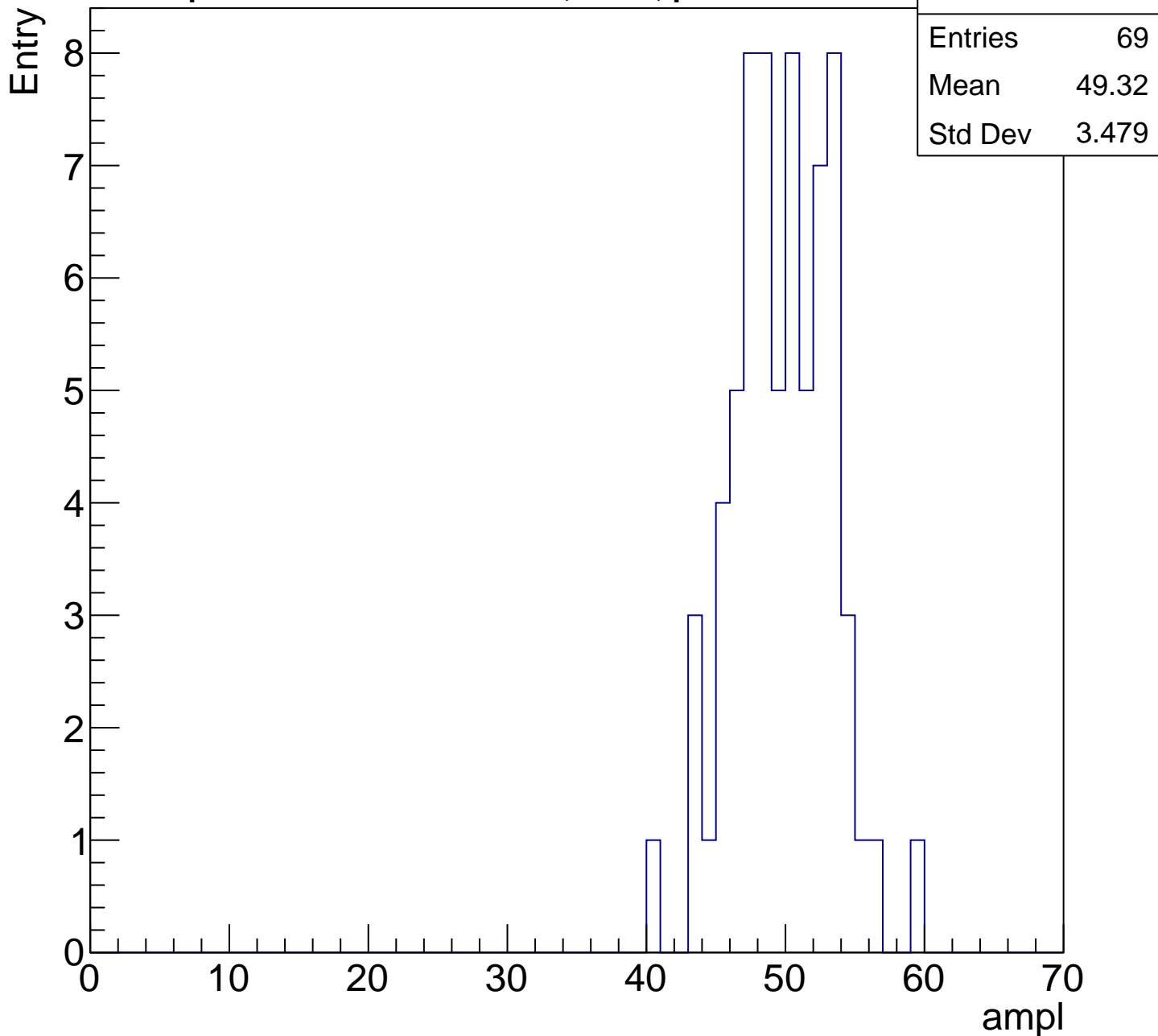
Gaus mean : 43.2078

Gaus Width: 2.8677



B1L100S, U7-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

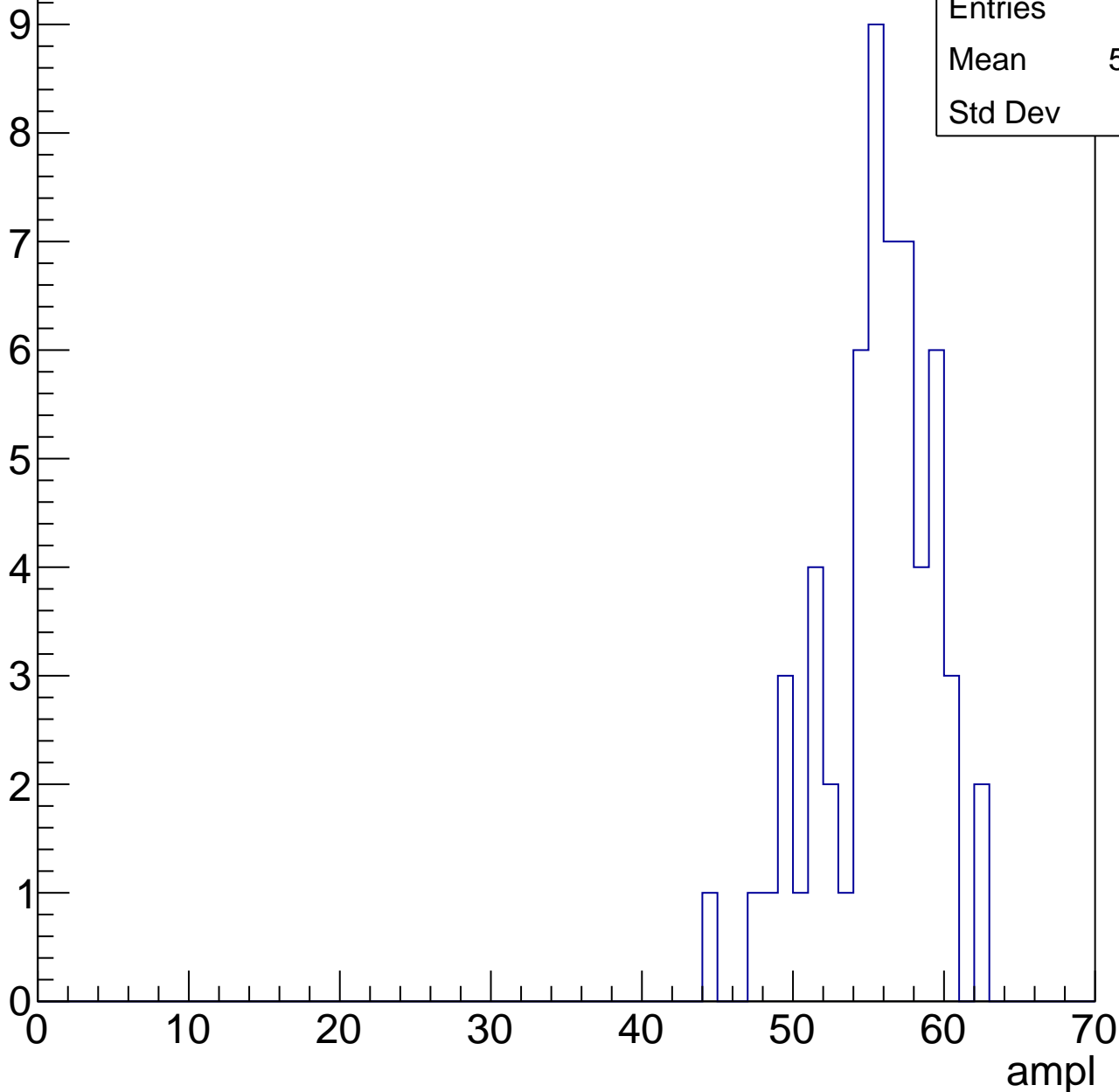


B1L100S, U7-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.12 |
| Std Dev | 3.71 |

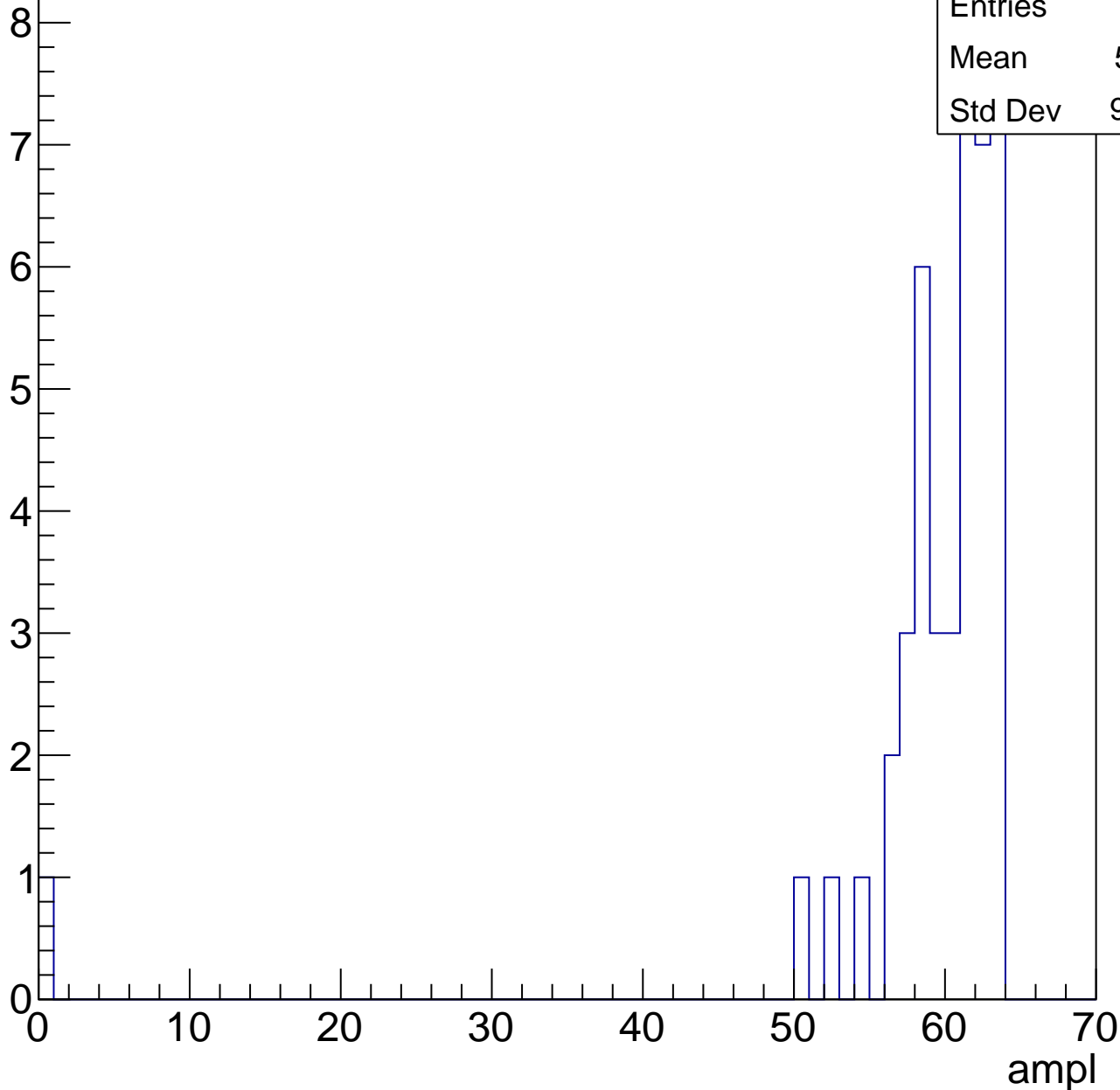


B1L100S, U7-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

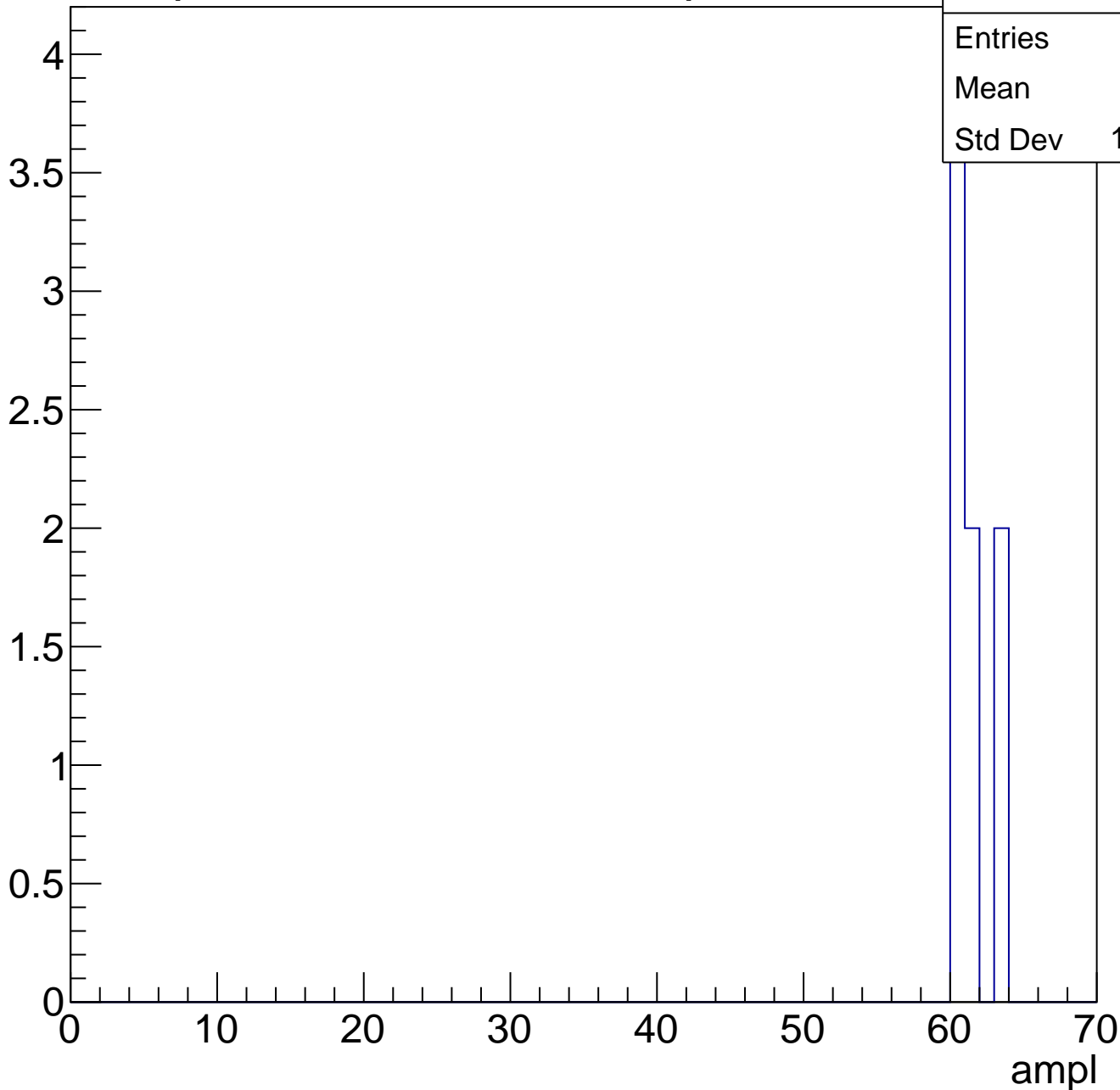
| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.41 |
| Std Dev | 9.394 |



B1L100S, U7-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

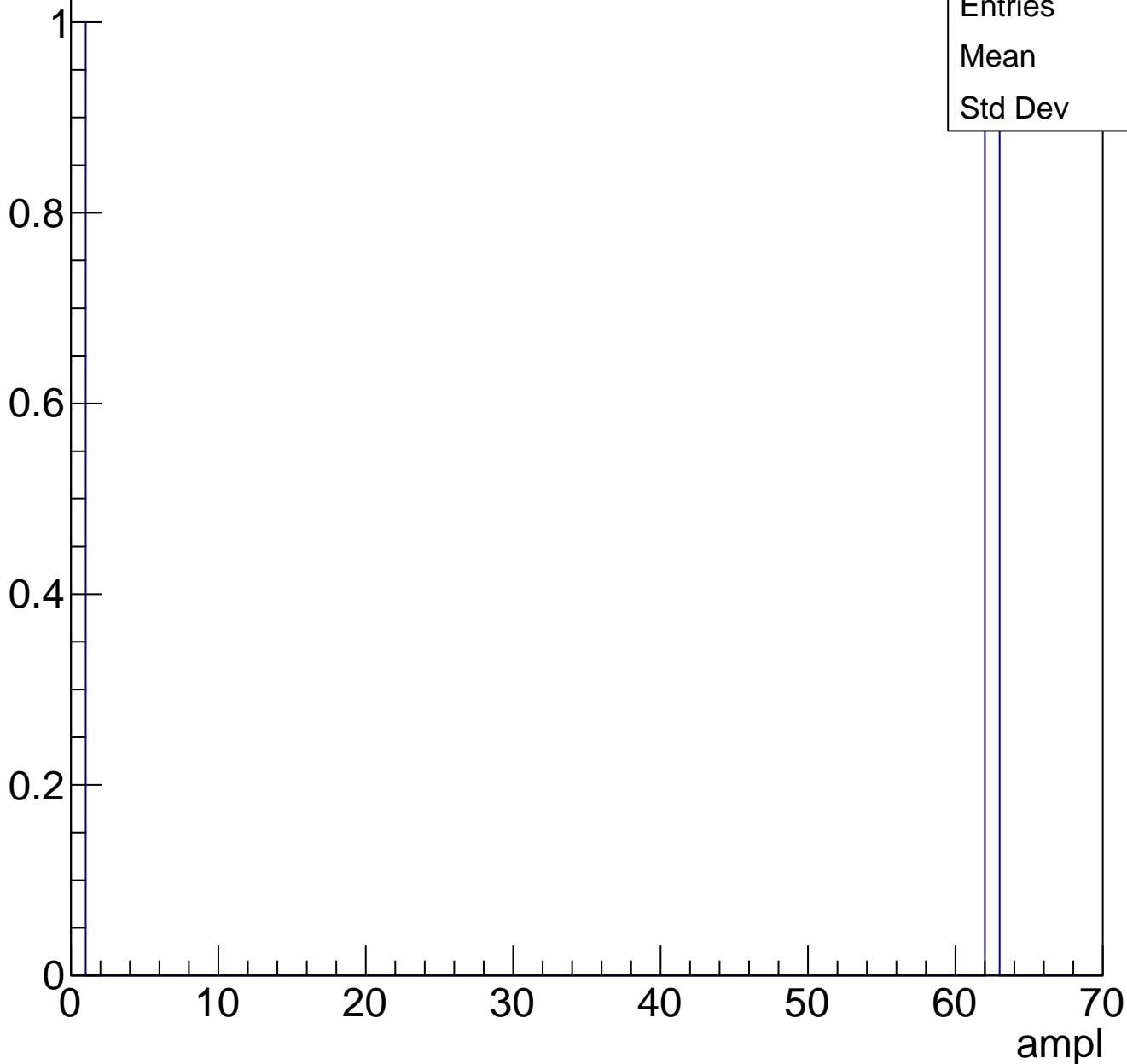
Entry



B1L100S, U7-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch92, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 64 |
| Mean | 27.03 |
| Std Dev | 3.167 |

Gaus mean : 27.5078

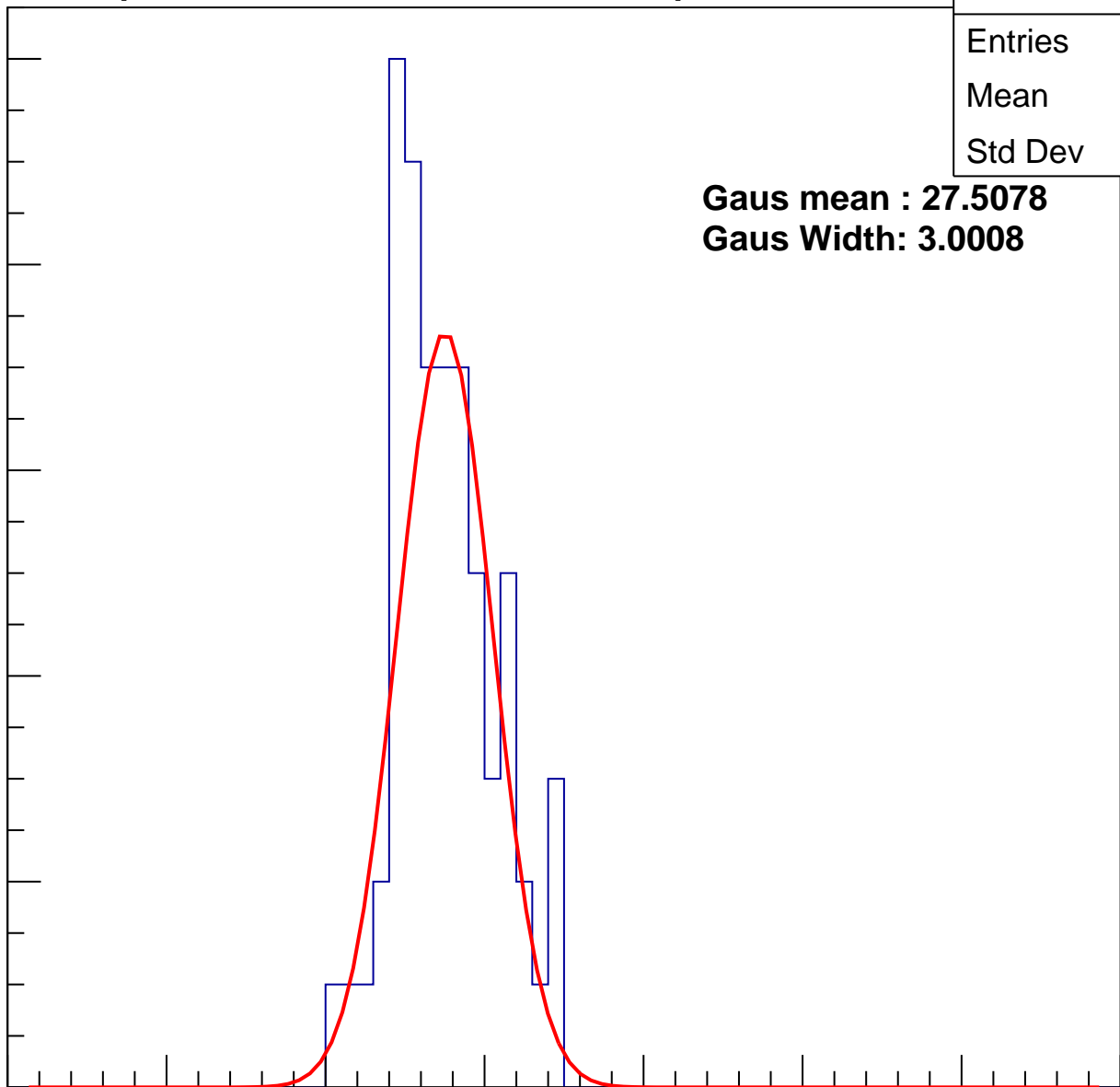
Gaus Width: 3.0008

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch92, adc1

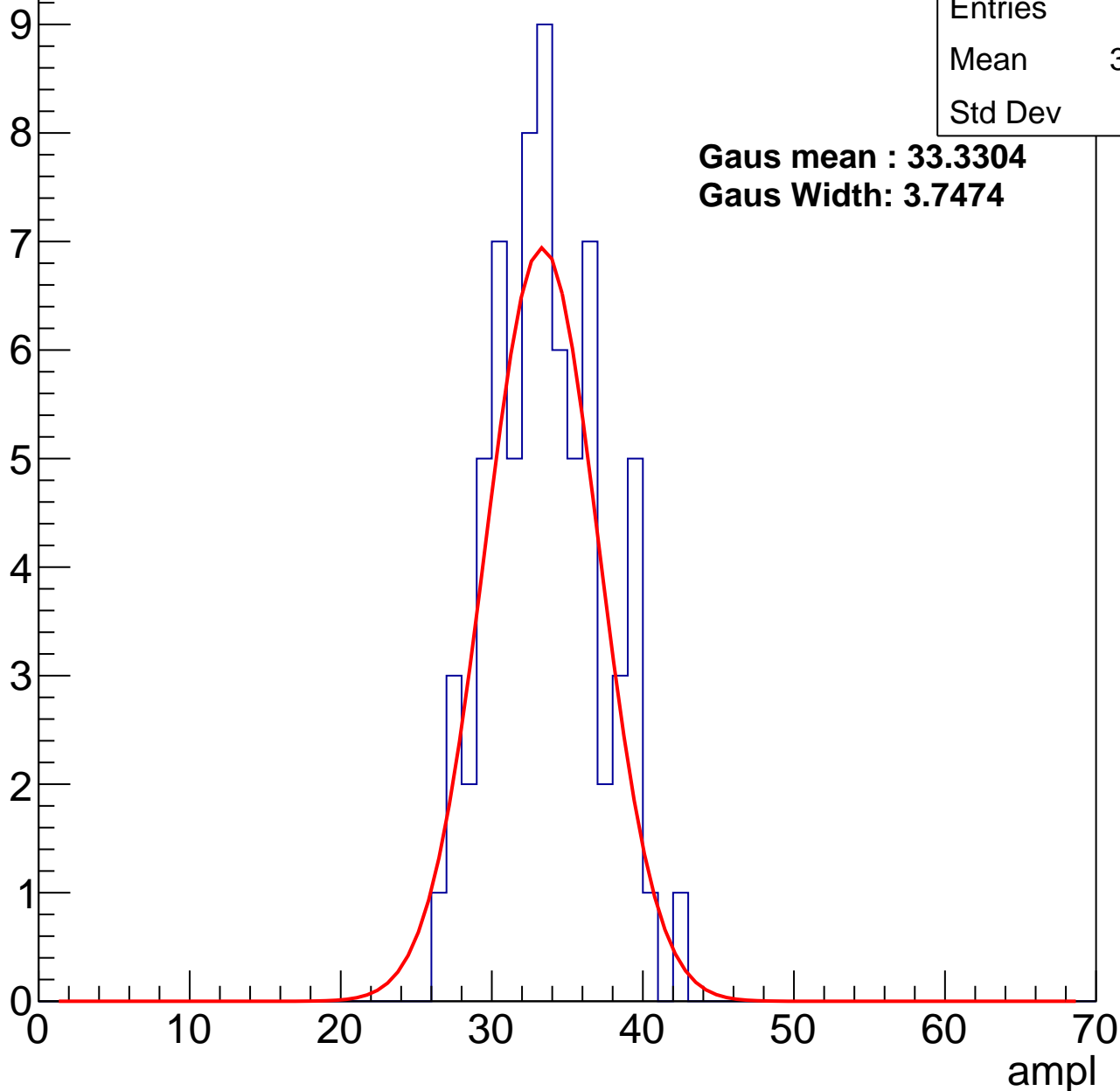
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 33.17 |
| Std Dev | 3.55 |

Gaus mean : 33.3304

Gaus Width: 3.7474



B1L100S, U7-ch92, adc2

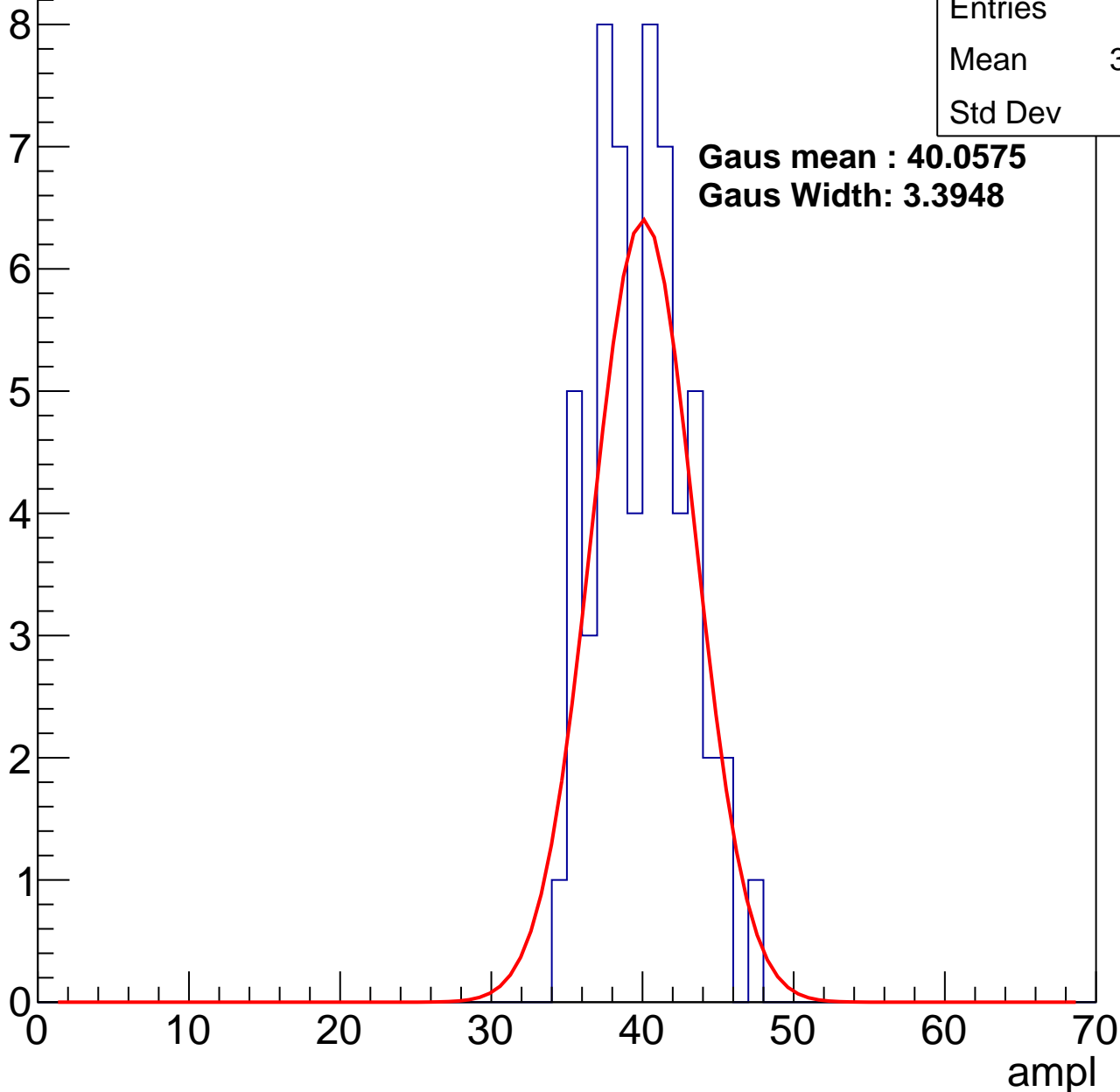
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 39.47 |
| Std Dev | 2.95 |

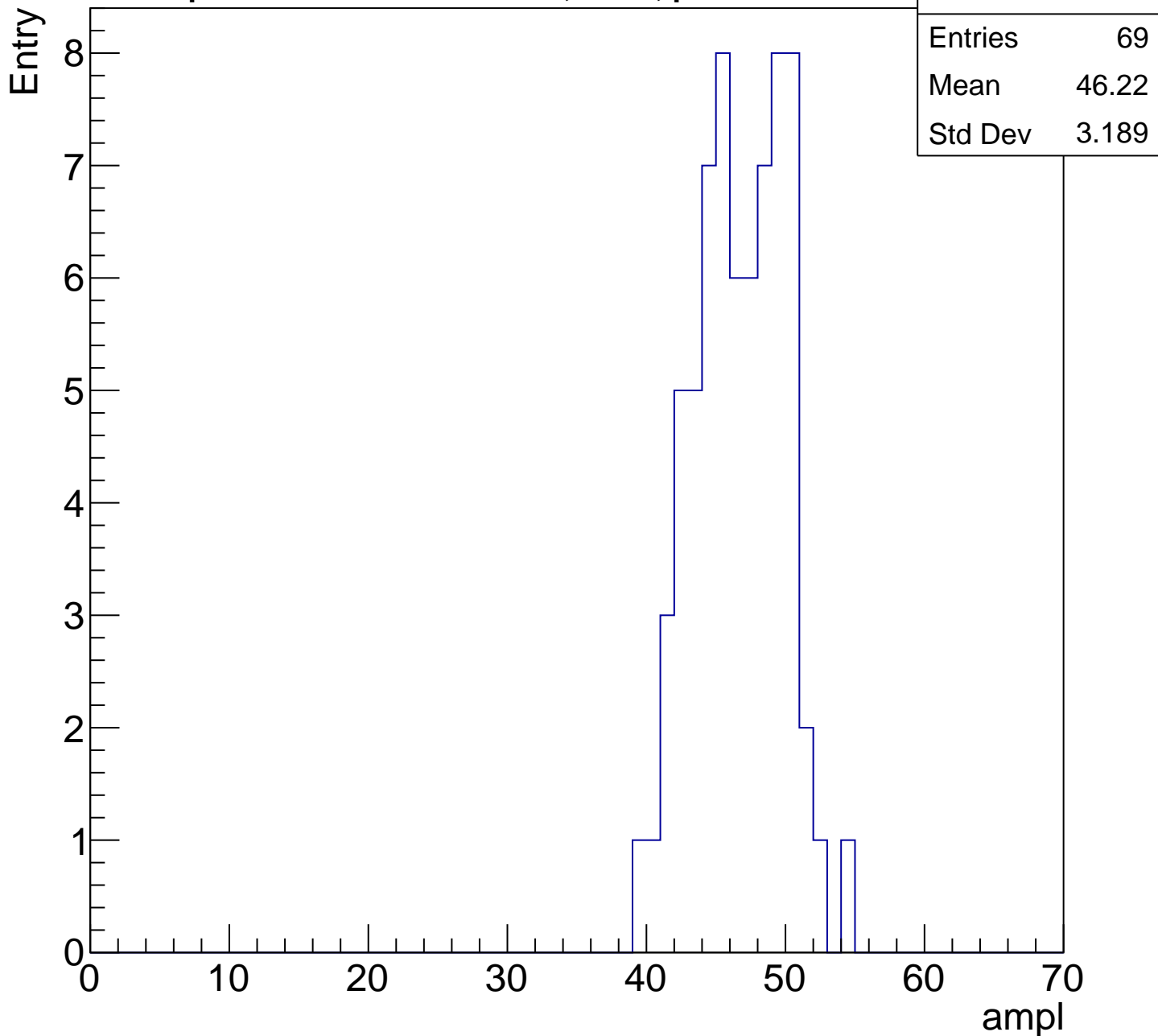
Gaus mean : 40.0575

Gaus Width: 3.3948



B1L100S, U7-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

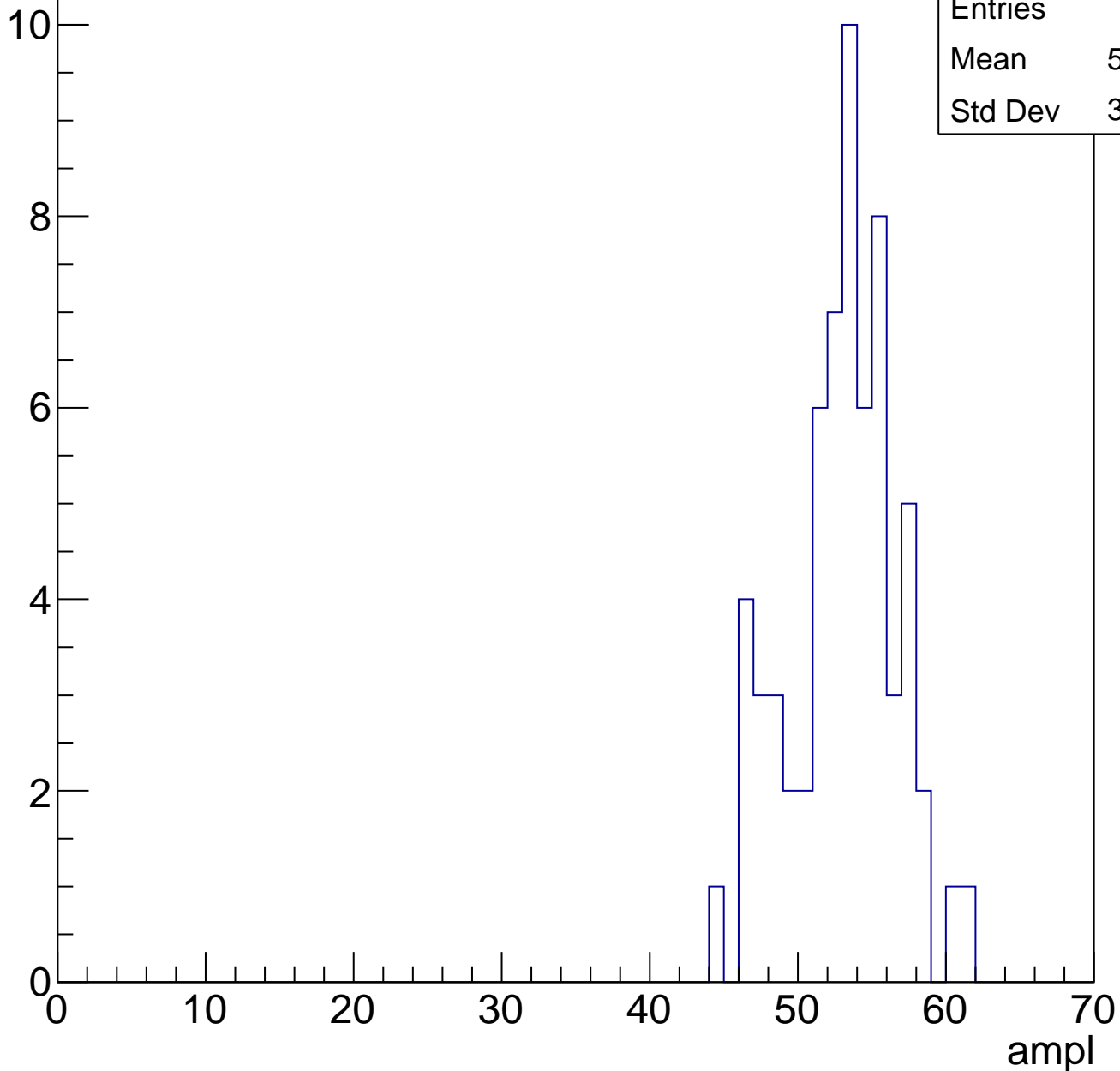


B1L100S, U7-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 52.58 |
| Std Dev | 3.618 |

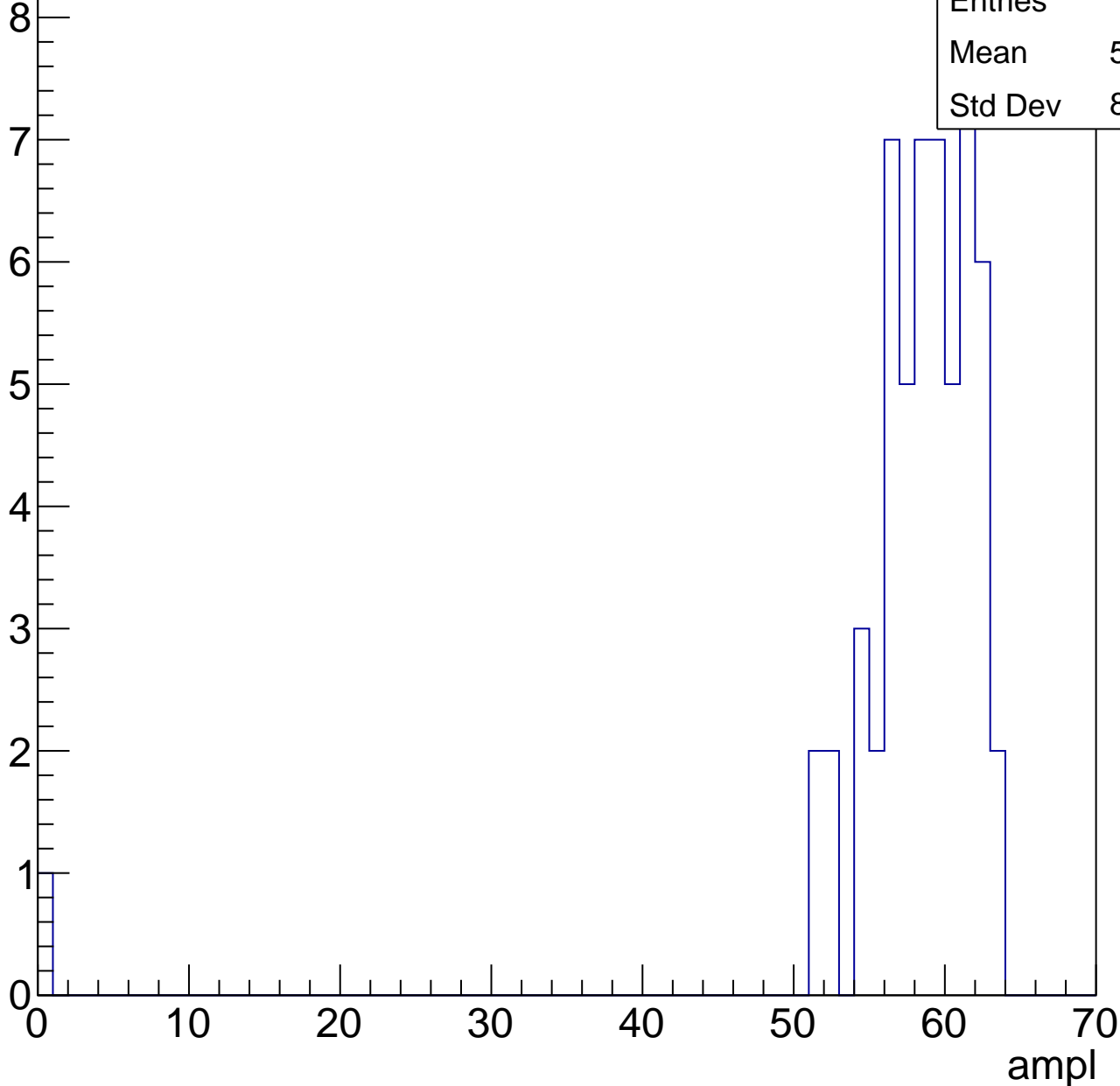


B1L100S, U7-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

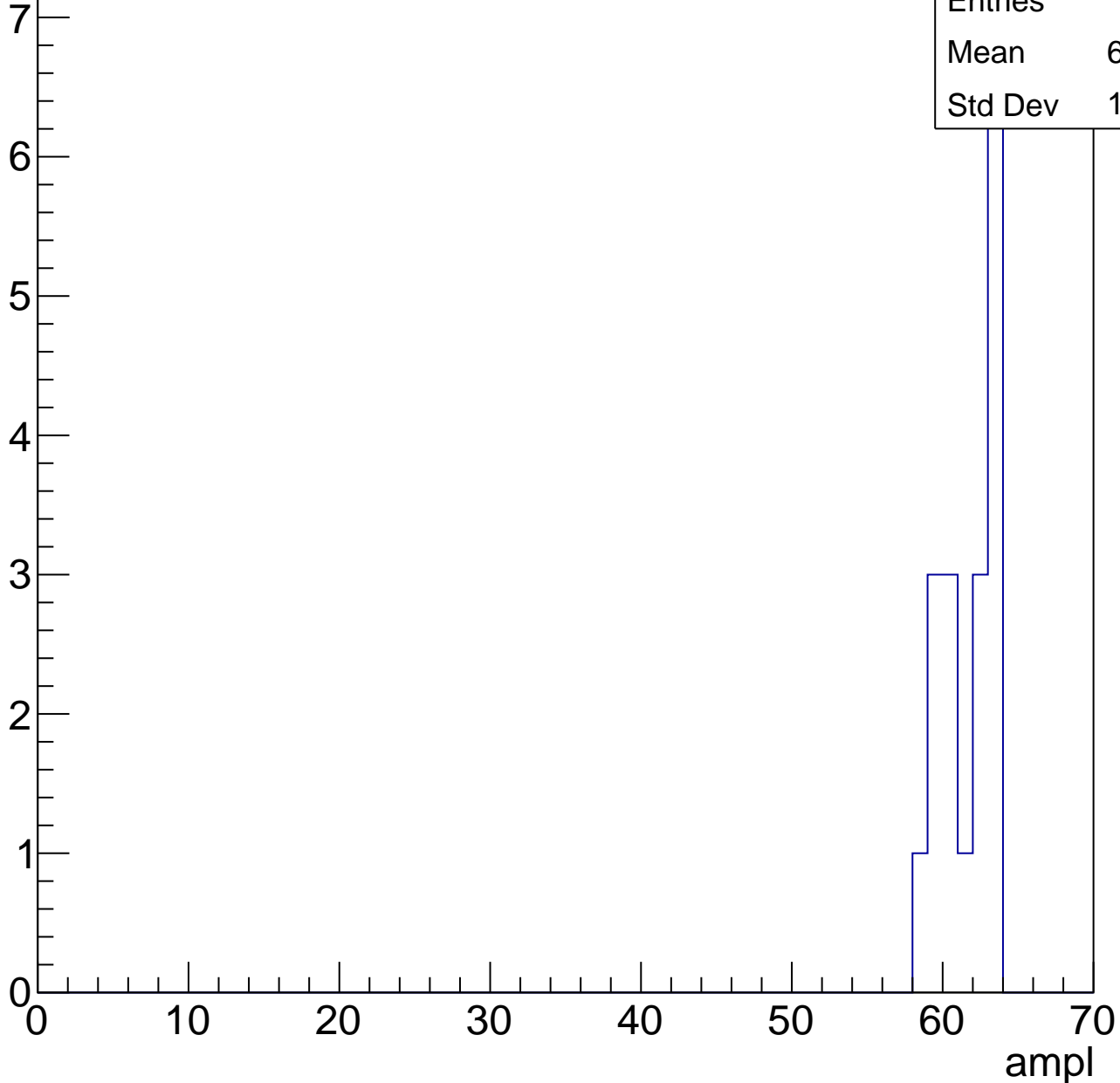
| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.19 |
| Std Dev | 8.209 |



B1L100S, U7-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

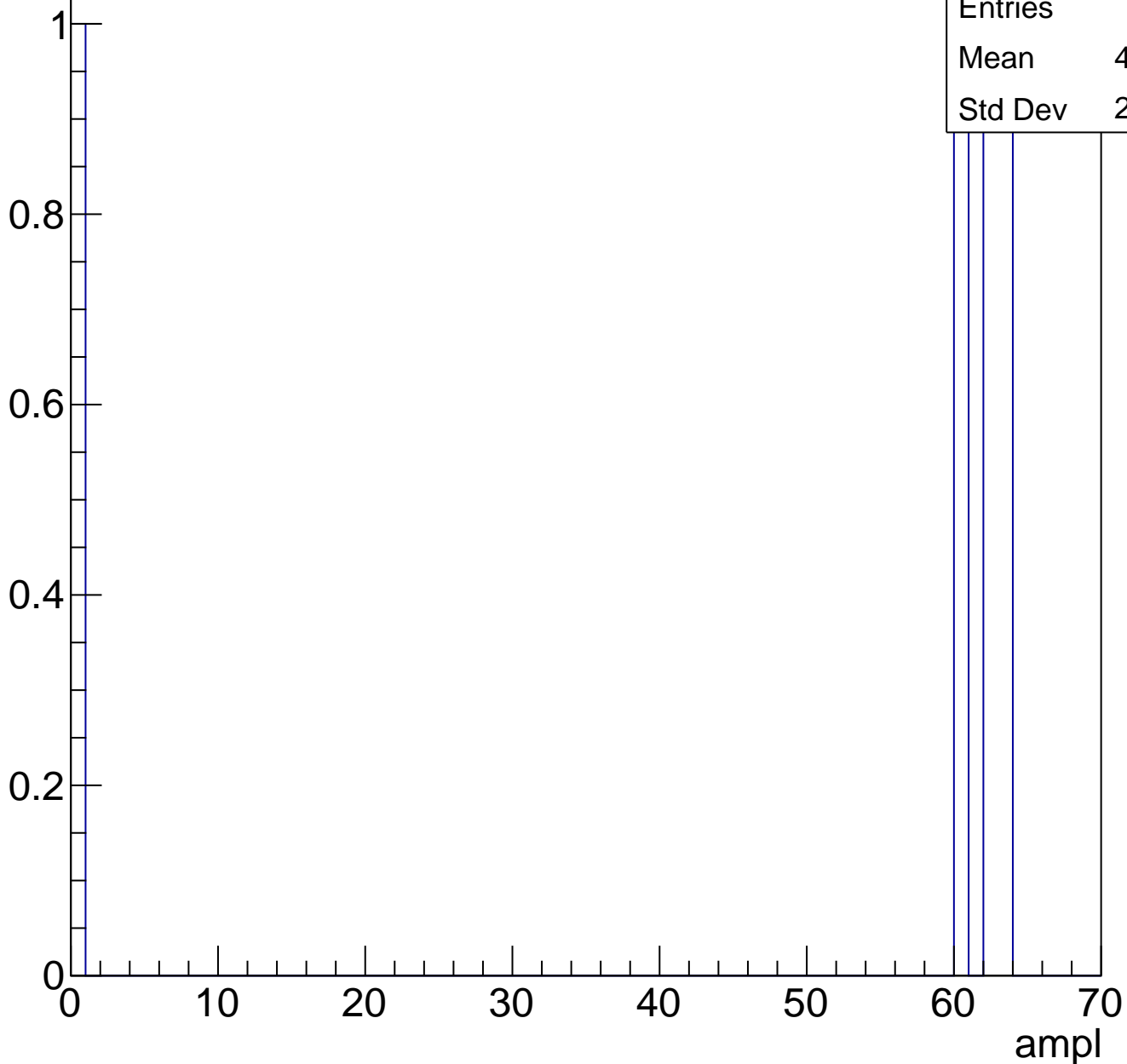


| | |
|---------|-------|
| Entries | 18 |
| Mean | 61.28 |
| Std Dev | 1.726 |

B1L100S, U7-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 28.76 |
| Std Dev | 3.632 |

Gaus mean : 29.2635

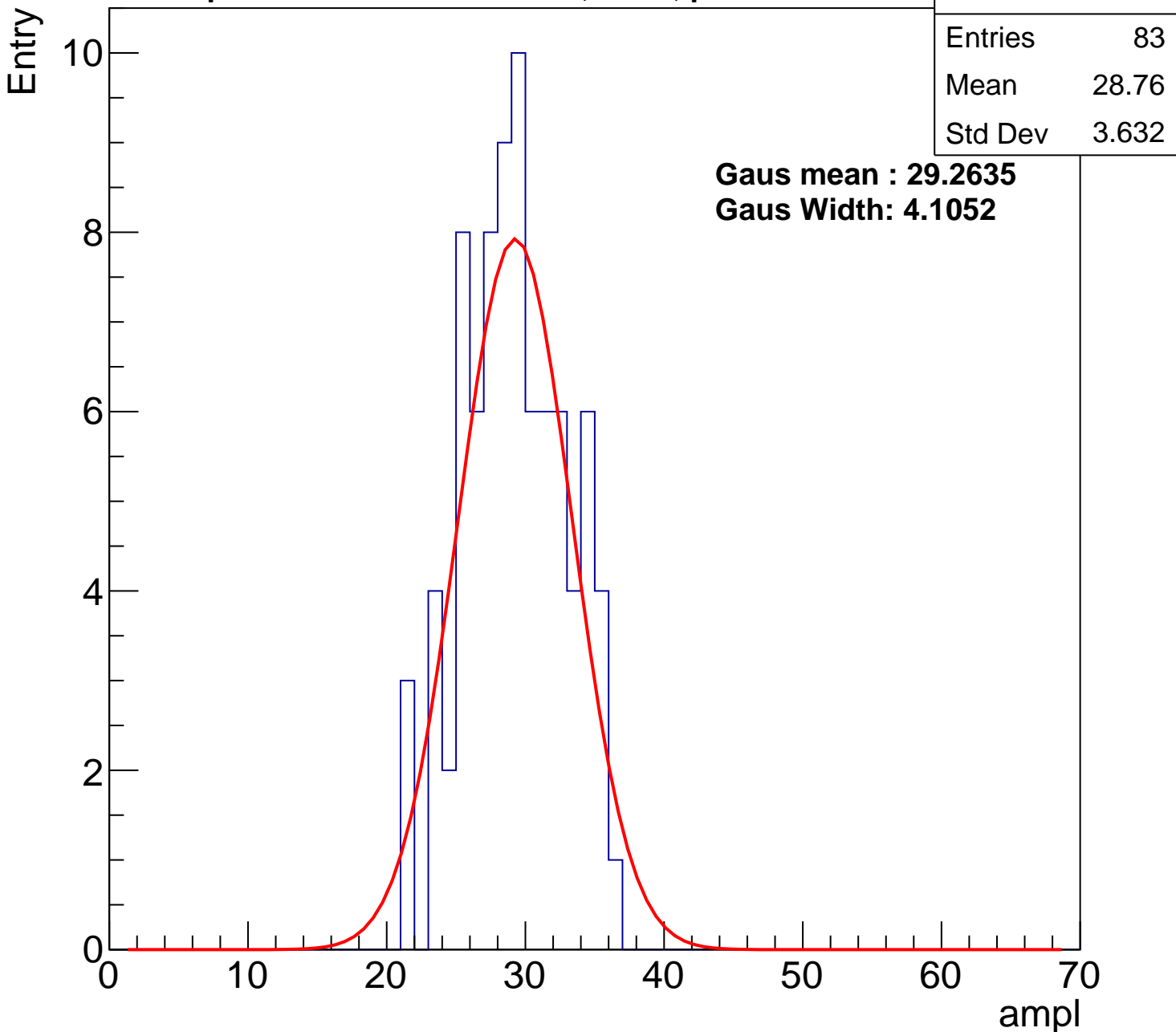
Gaus Width: 4.1052

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



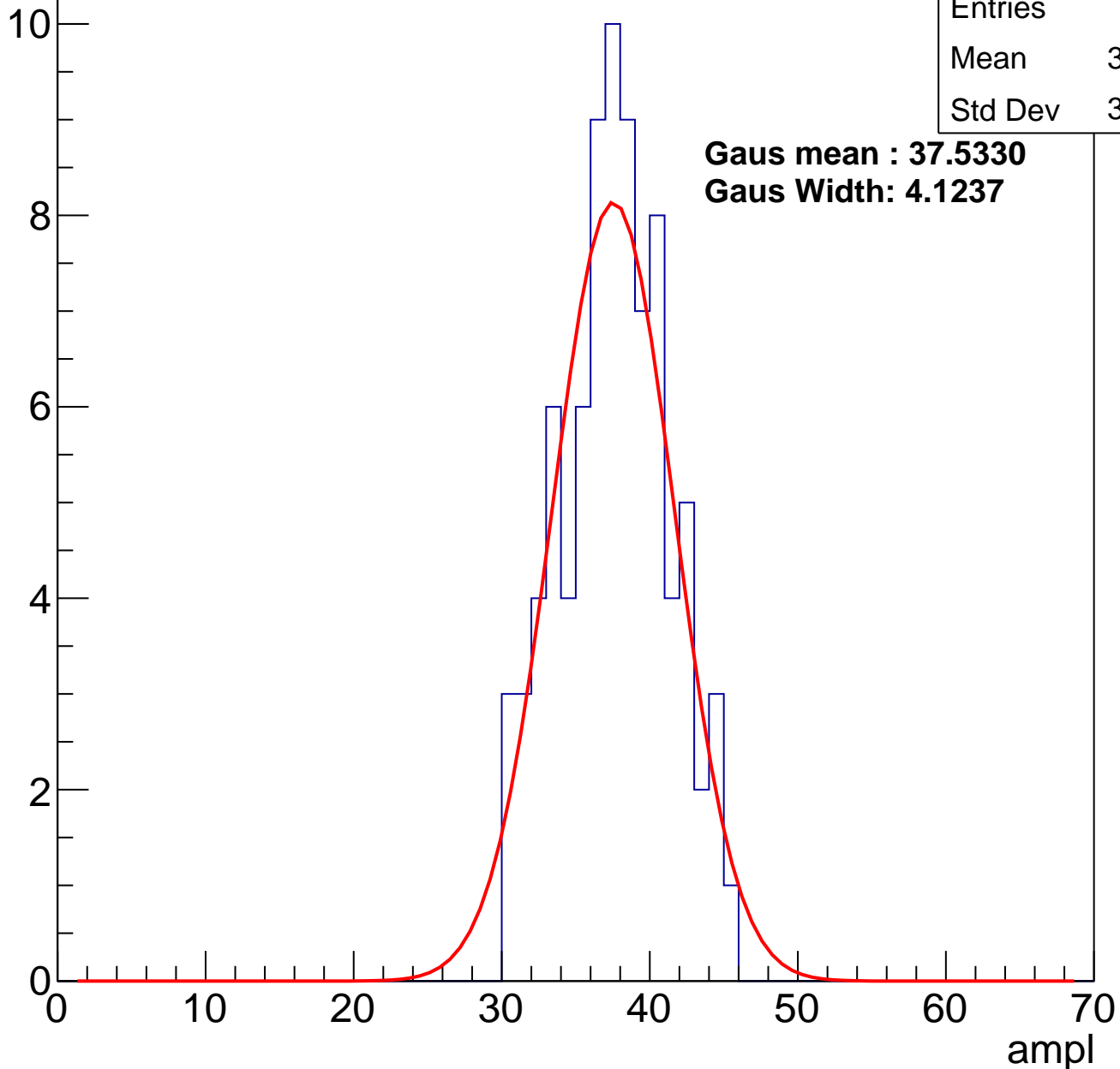
B1L100S, U7-ch93, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 84 |
| Mean | 37.15 |
| Std Dev | 3.617 |

Gaus mean : 37.5330
Gaus Width: 4.1237

Entry



B1L100S, U7-ch93, adc2

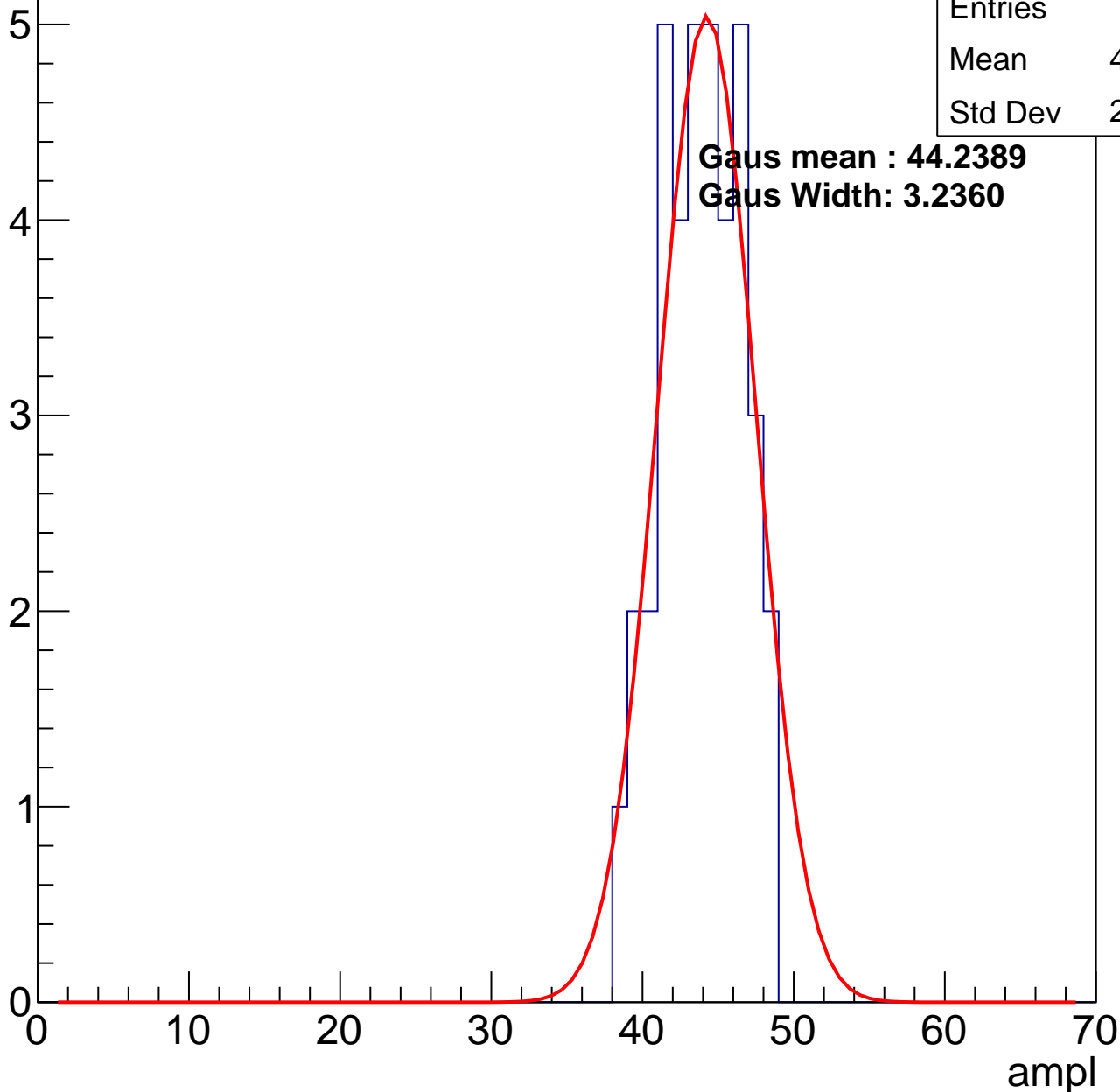
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 43.45 |
| Std Dev | 2.592 |

Gaus mean : 44.2389

Gaus Width: 3.2360

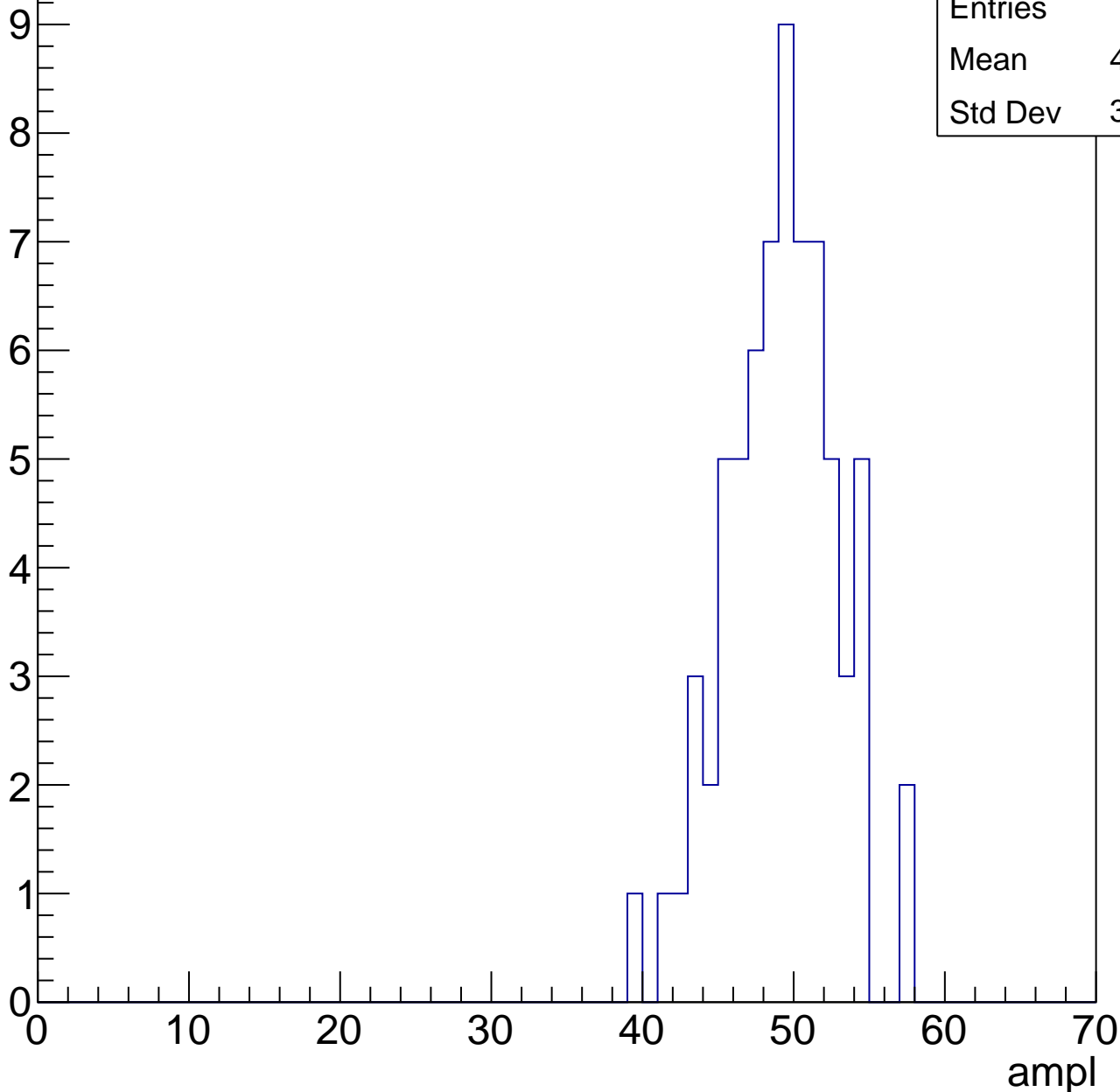


B1L100S, U7-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.74 |
| Std Dev | 3.618 |

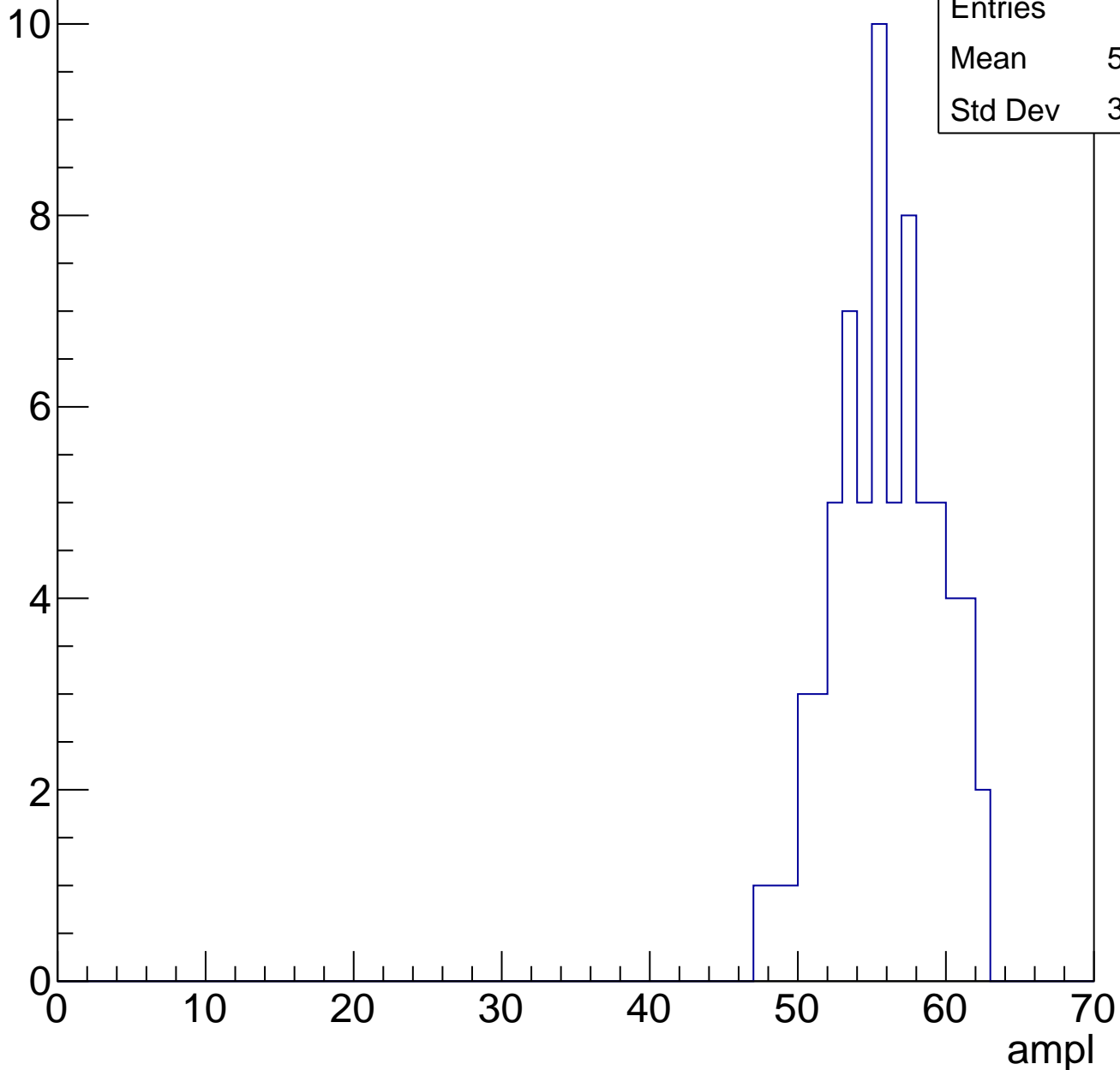


B1L100S, U7-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 55.46 |
| Std Dev | 3.487 |

Entry

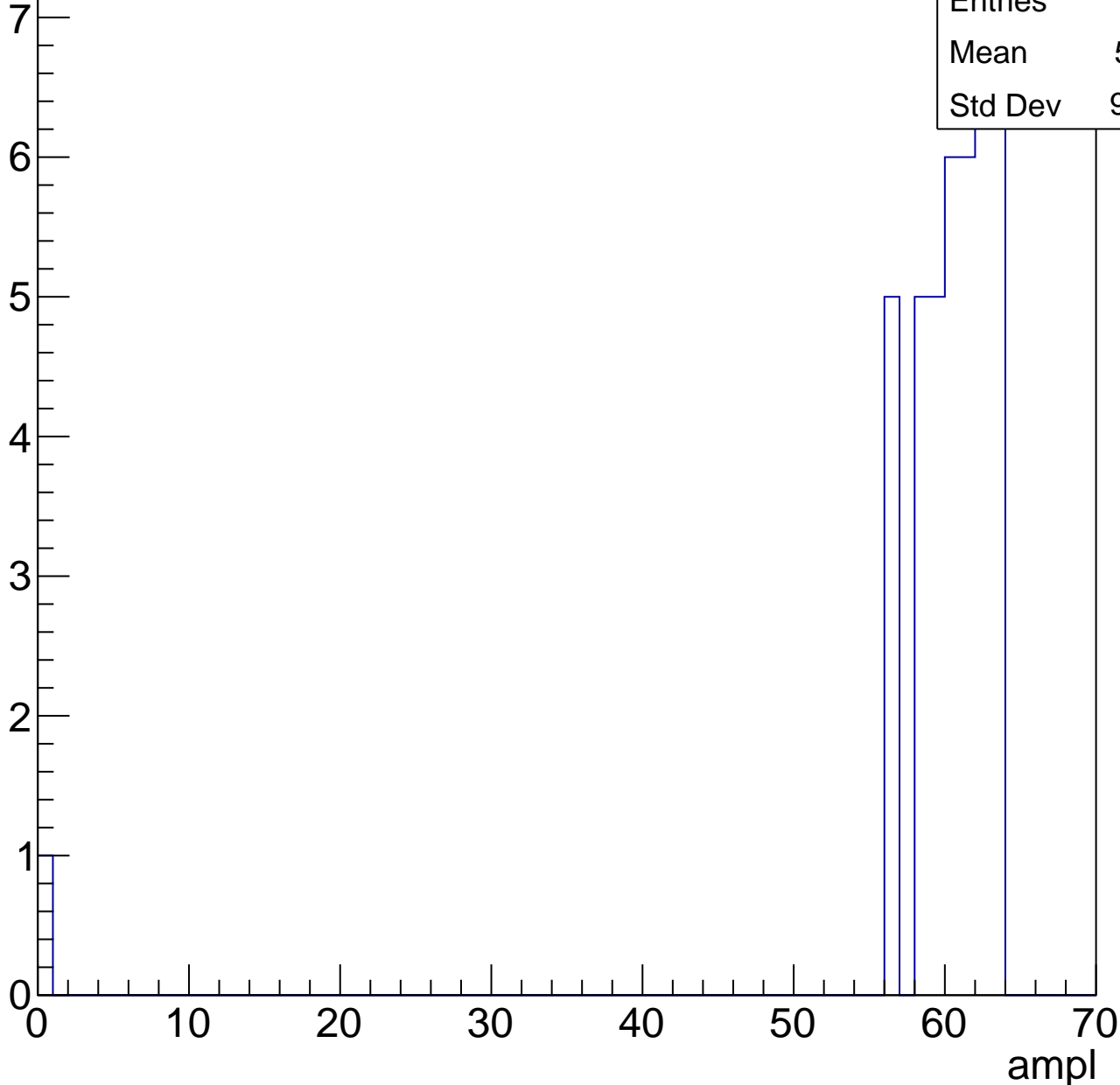


B1L100S, U7-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.71 |
| Std Dev | 9.427 |



B1L100S, U7-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch94, adc0

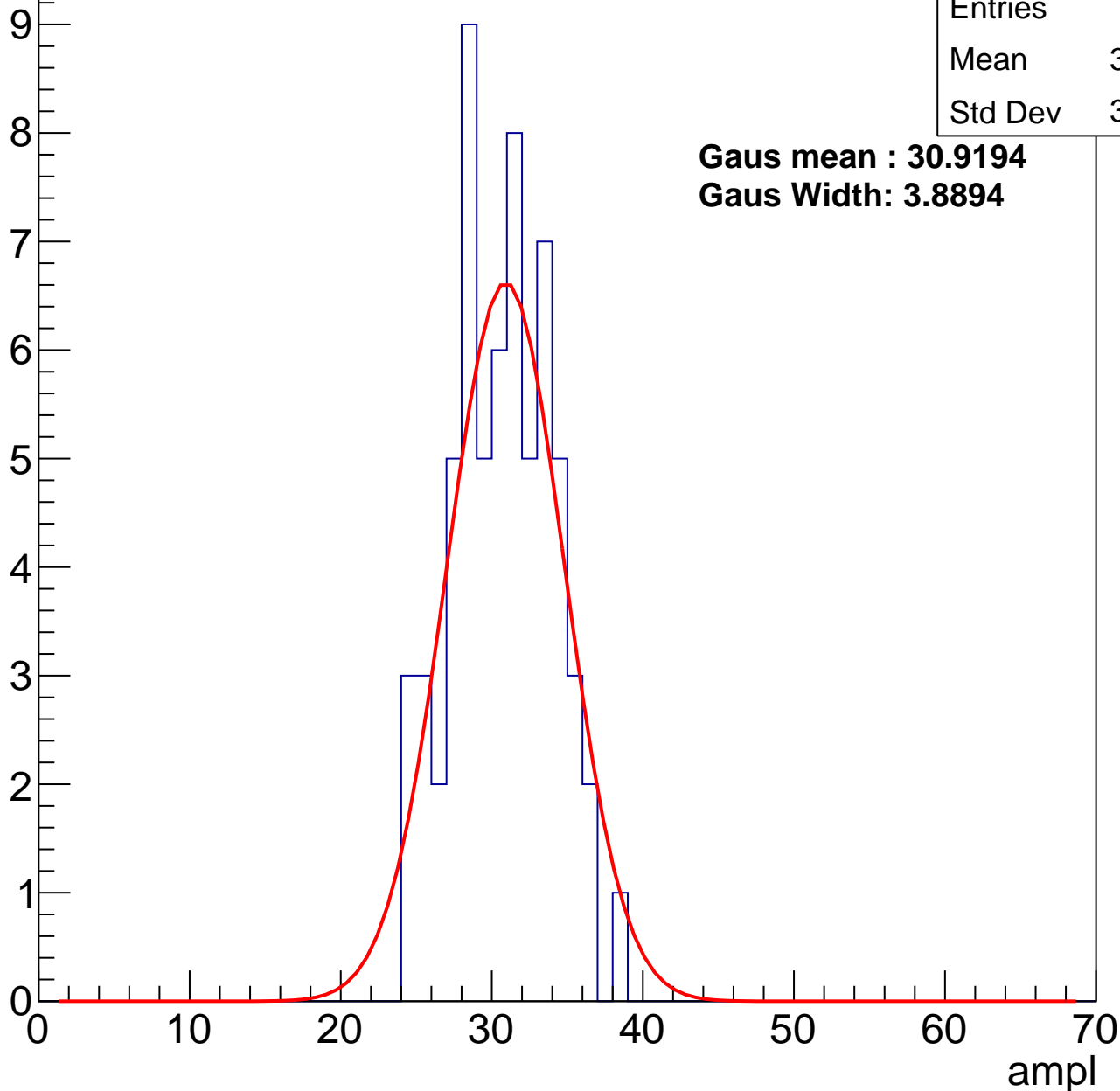
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.23 |
| Std Dev | 3.258 |

Gaus mean : 30.9194

Gaus Width: 3.8894



B1L100S, U7-ch94, adc1

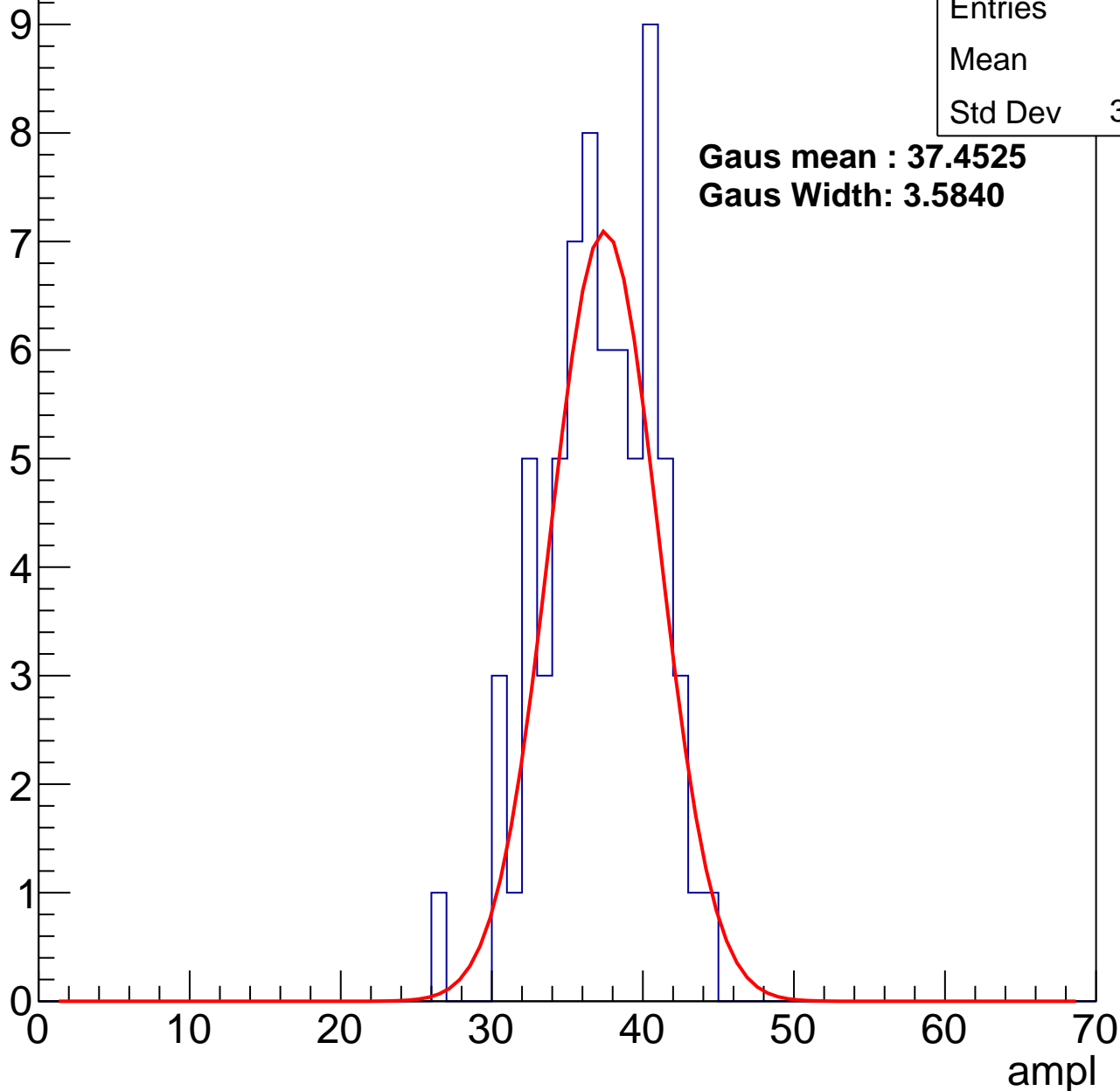
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.7 |
| Std Dev | 3.609 |

Gaus mean : 37.4525

Gaus Width: 3.5840



B1L100S, U7-ch94, adc2

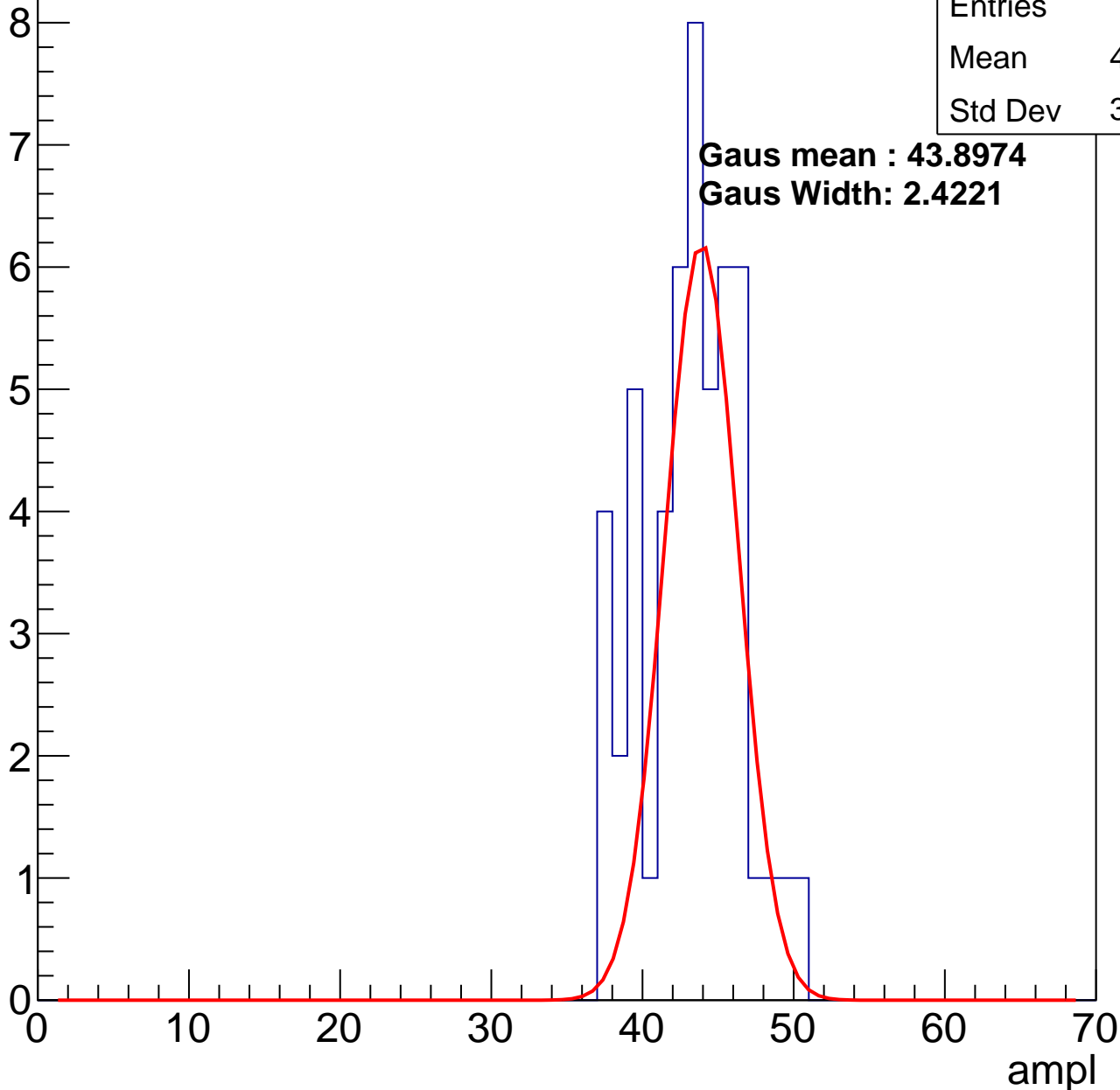
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 42.73 |
| Std Dev | 3.163 |

Gaus mean : 43.8974

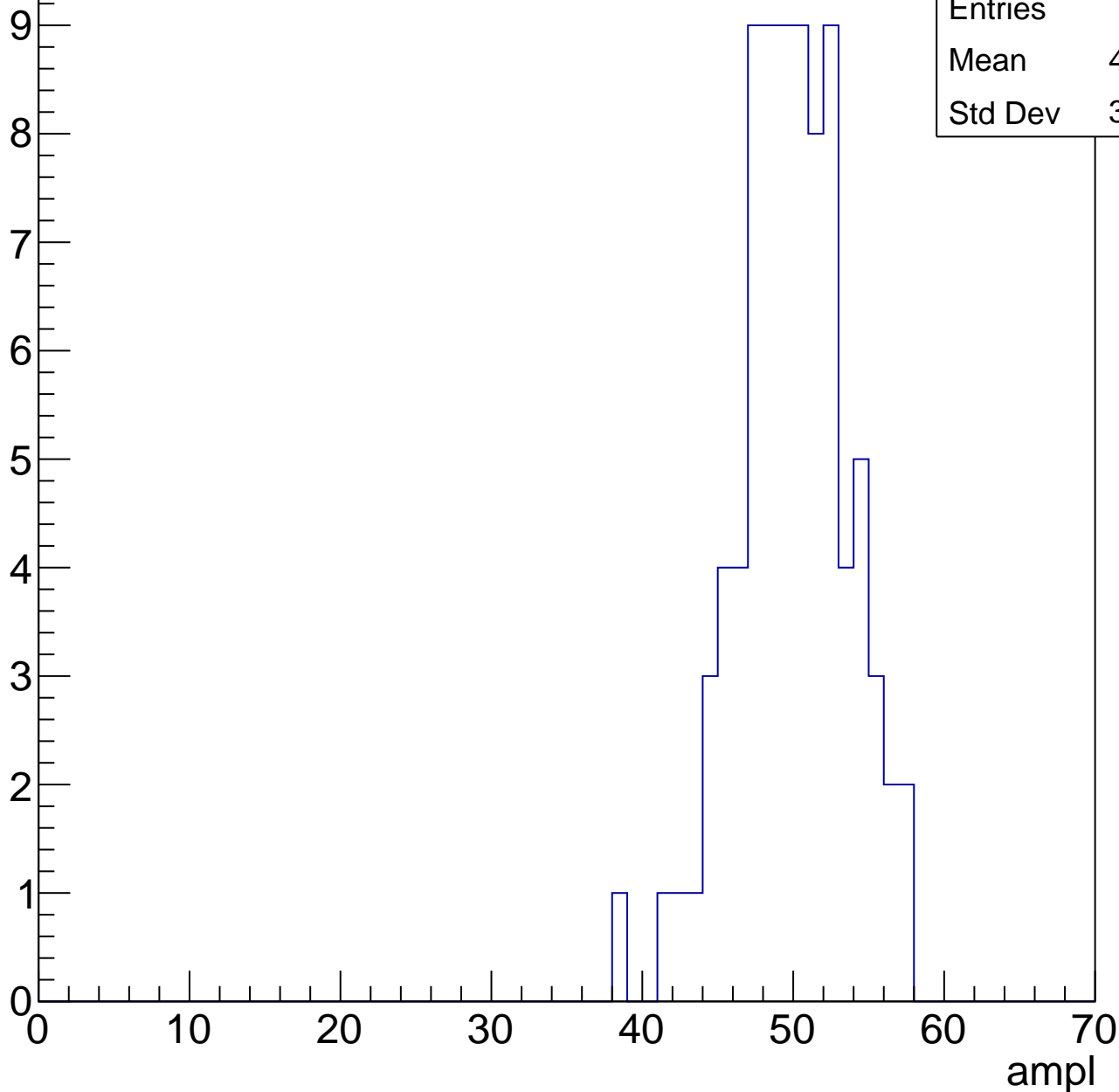
Gaus Width: 2.4221



B1L100S, U7-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

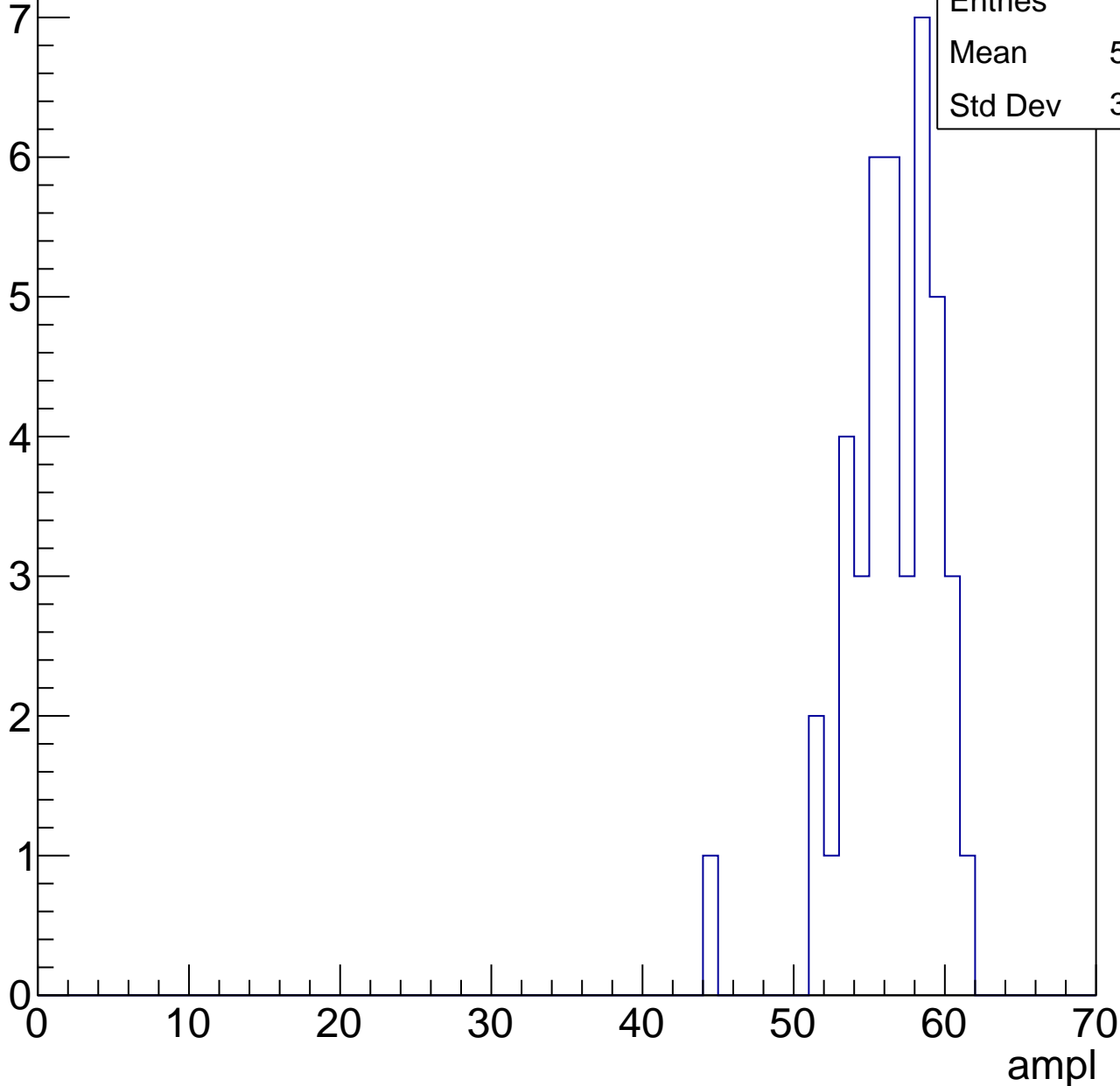


B1L100S, U7-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 55.98 |
| Std Dev | 3.136 |

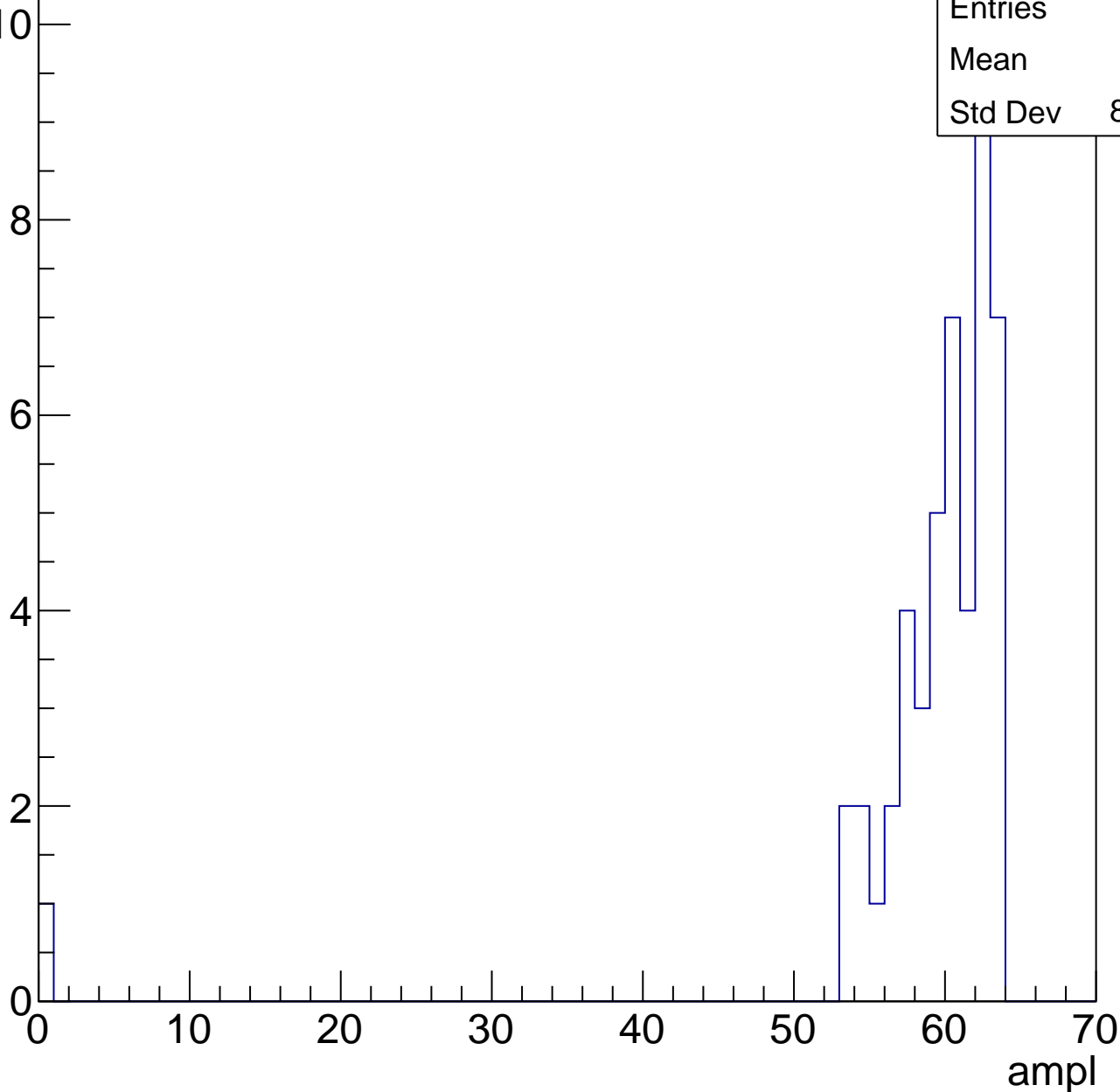


B1L100S, U7-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.4 |
| Std Dev | 8.972 |

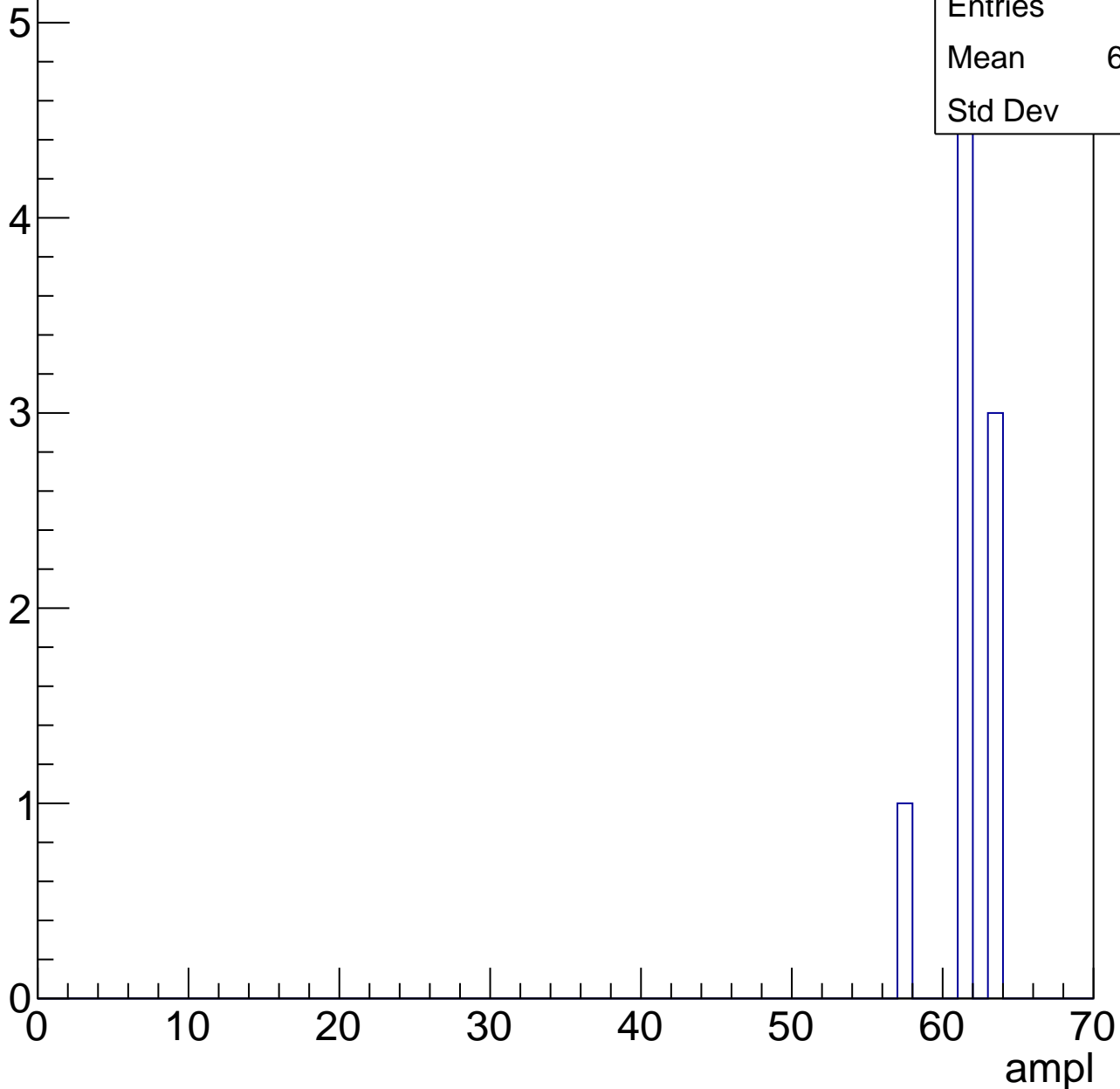


B1L100S, U7-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

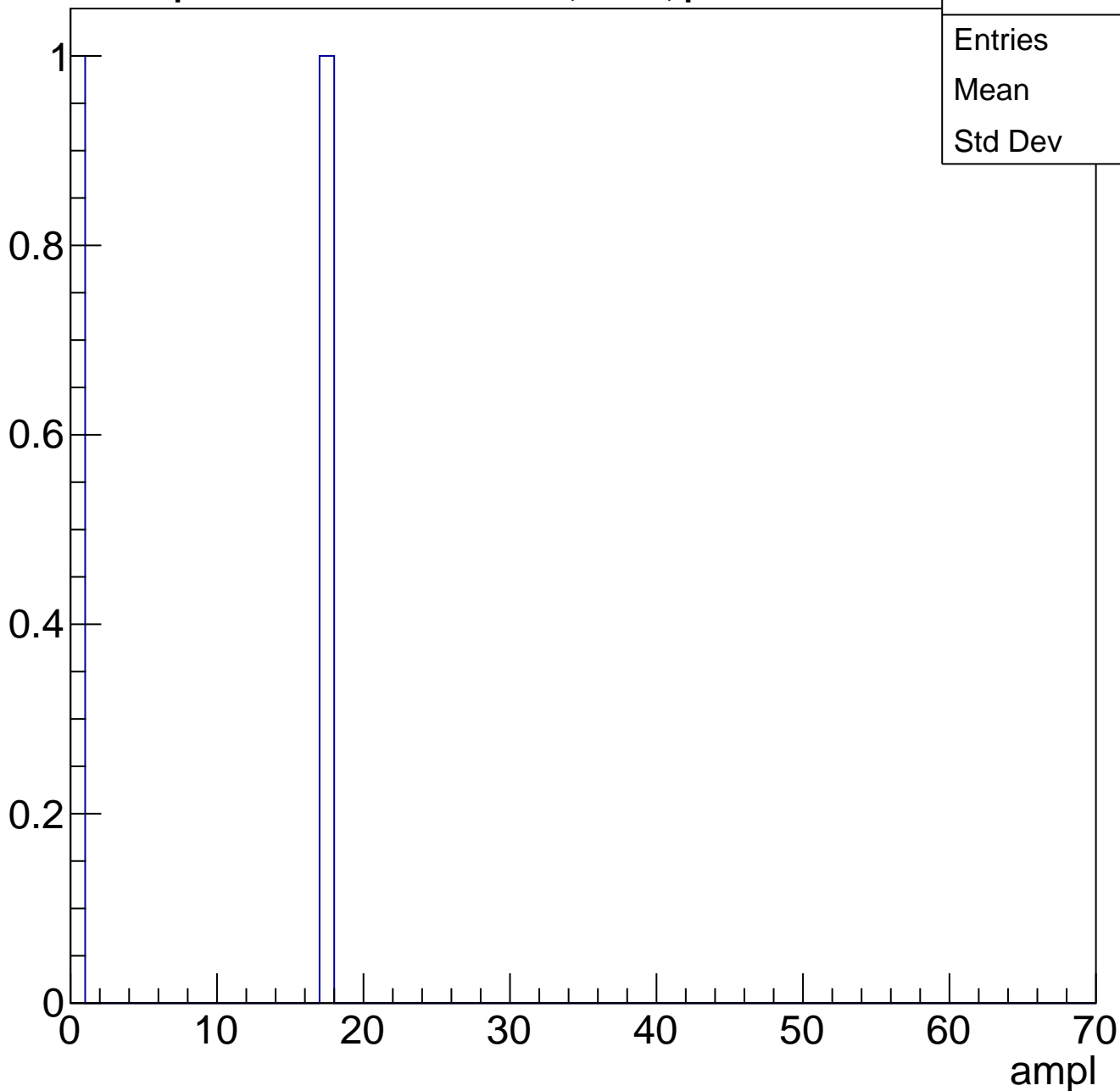
| | |
|---------|-------|
| Entries | 9 |
| Mean | 61.22 |
| Std Dev | 1.75 |



B1L100S, U7-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

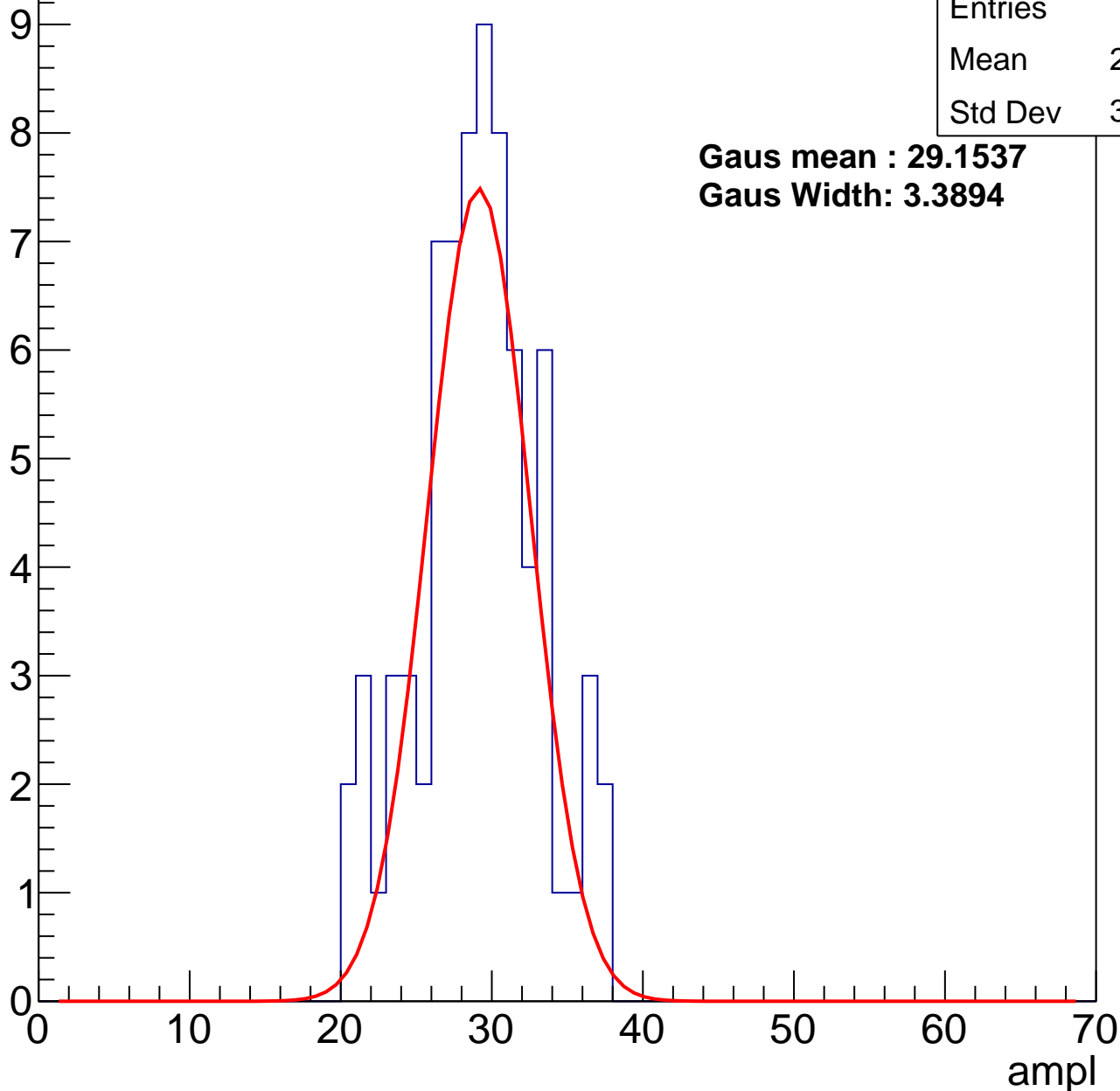


| | |
|---------|-----|
| Entries | 2 |
| Mean | 8.5 |
| Std Dev | 8.5 |

B1L100S, U7-ch95, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch95, adc1

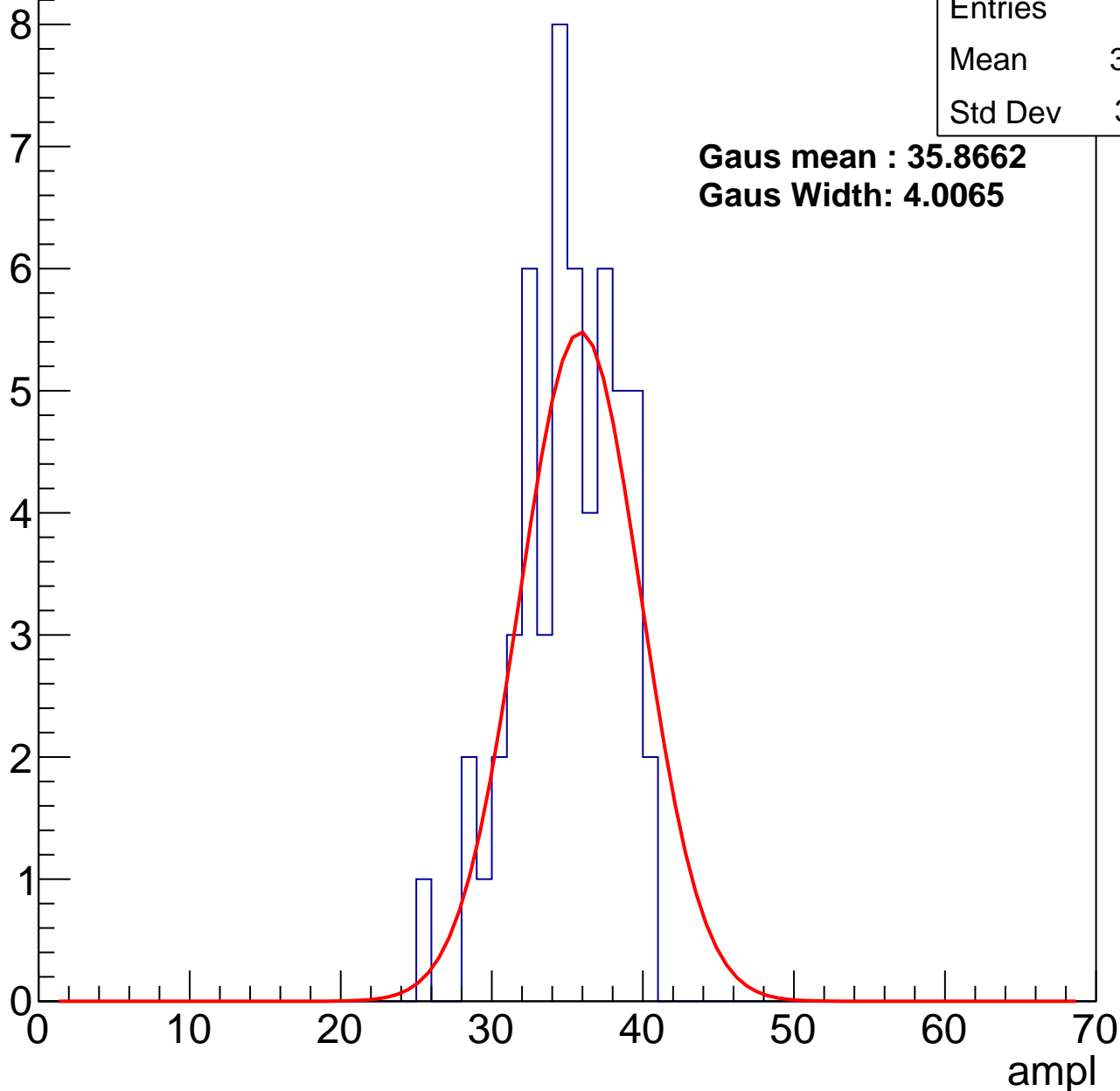
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 34.57 |
| Std Dev | 3.331 |

Gaus mean : 35.8662

Gaus Width: 4.0065



B1L100S, U7-ch95, adc2

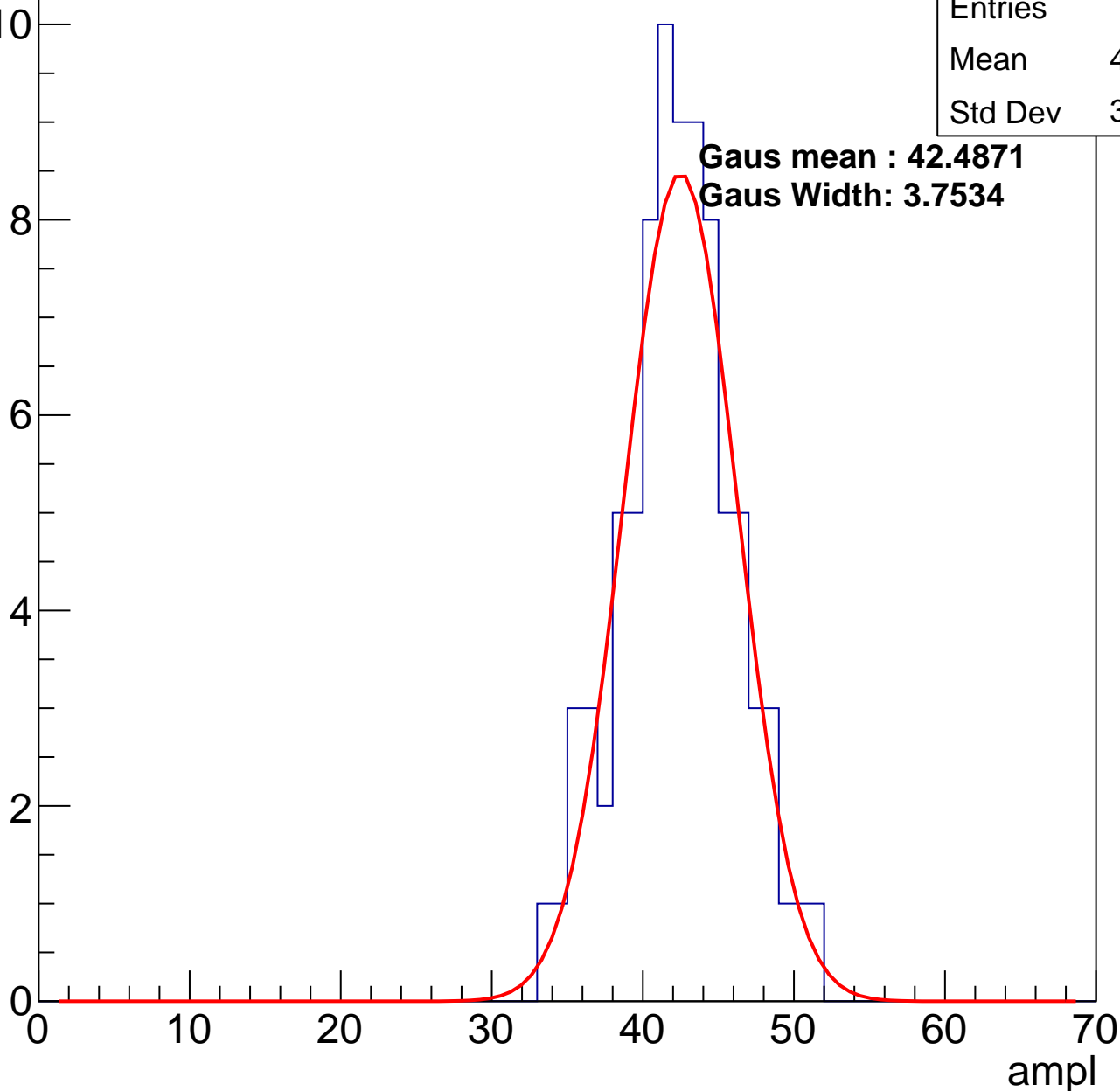
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 41.88 |
| Std Dev | 3.736 |

Gaus mean : 42.4871

Gaus Width: 3.7534

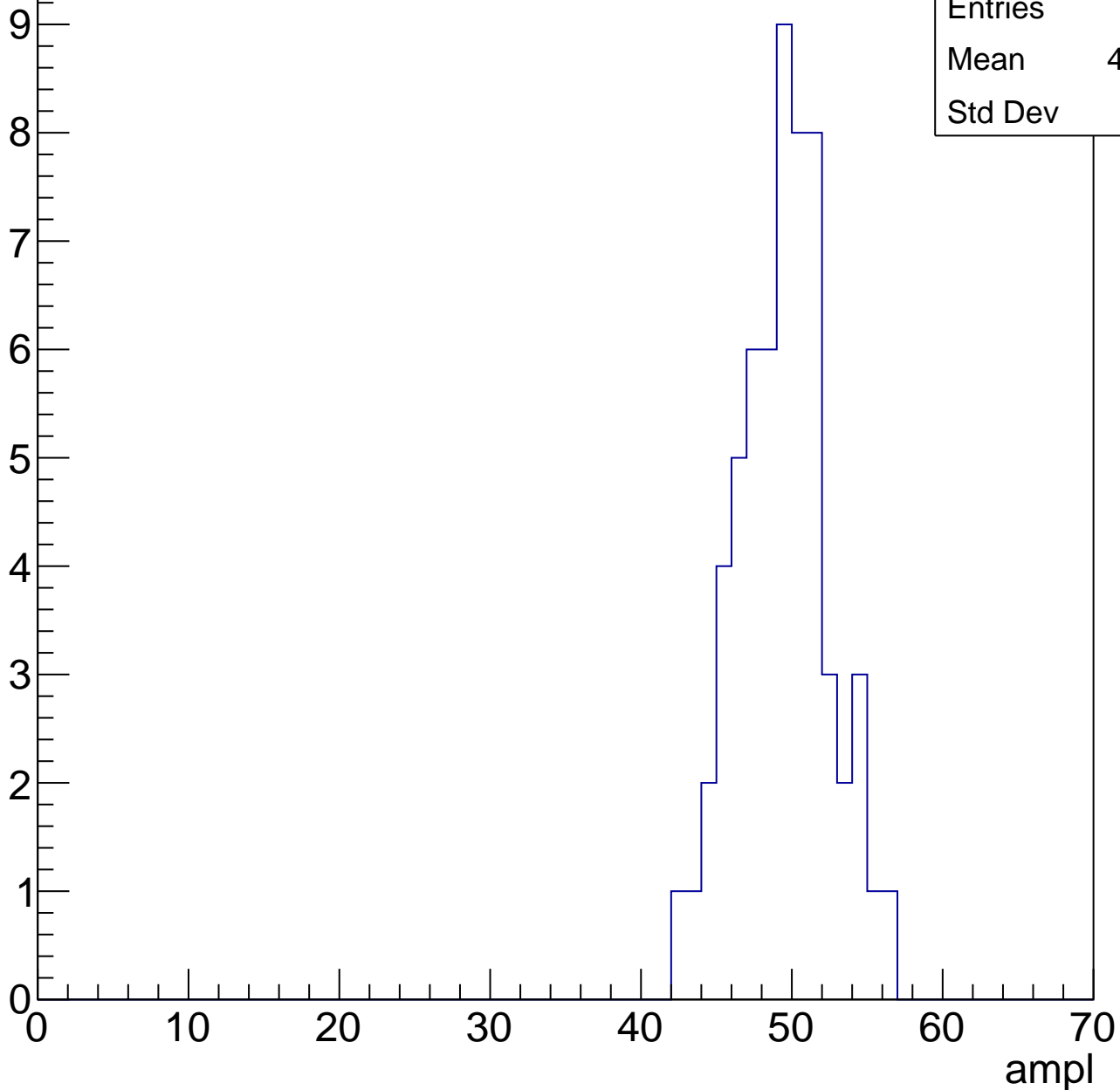


B1L100S, U7-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

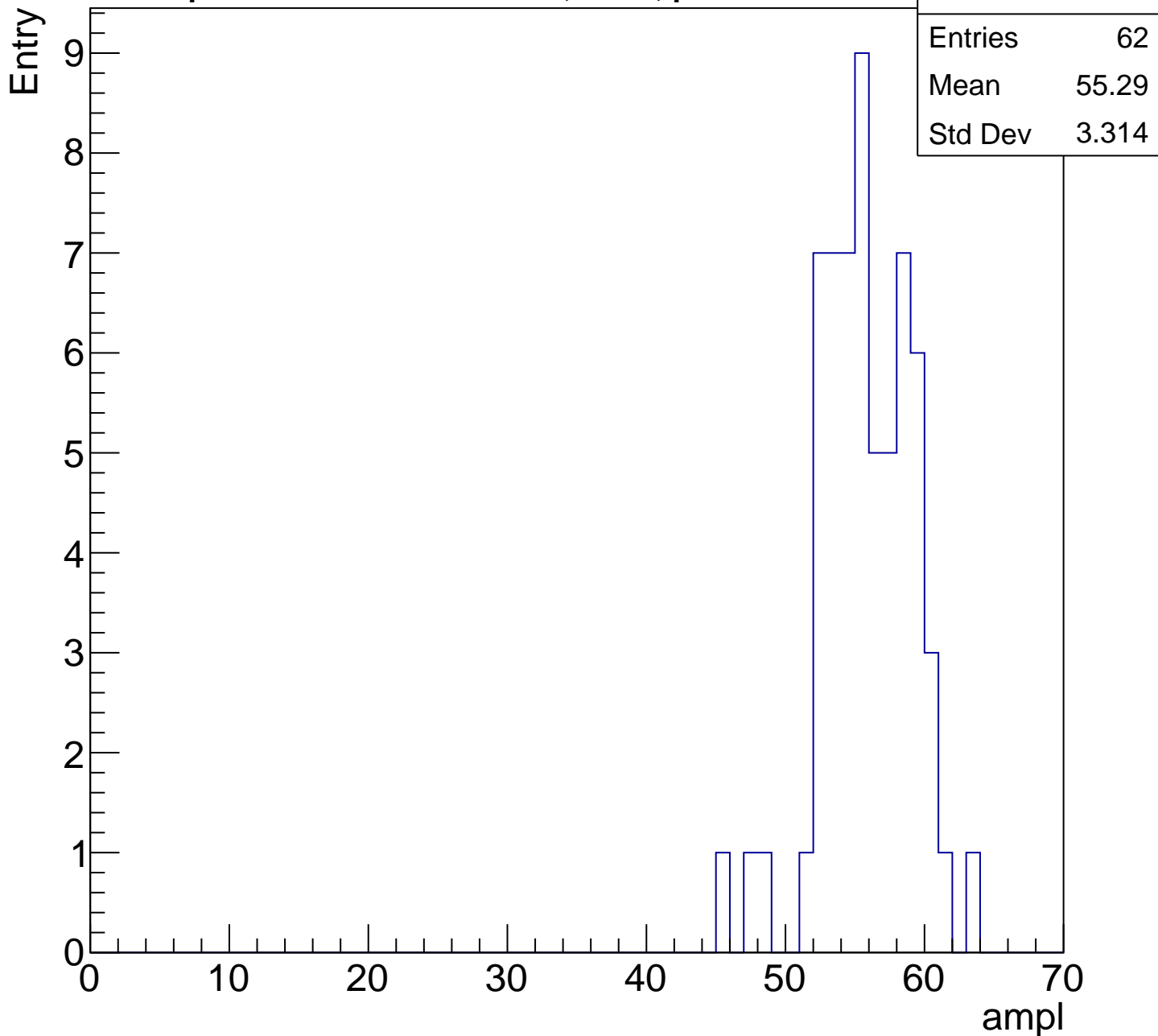
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.95 |
| Std Dev | 2.98 |



B1L100S, U7-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

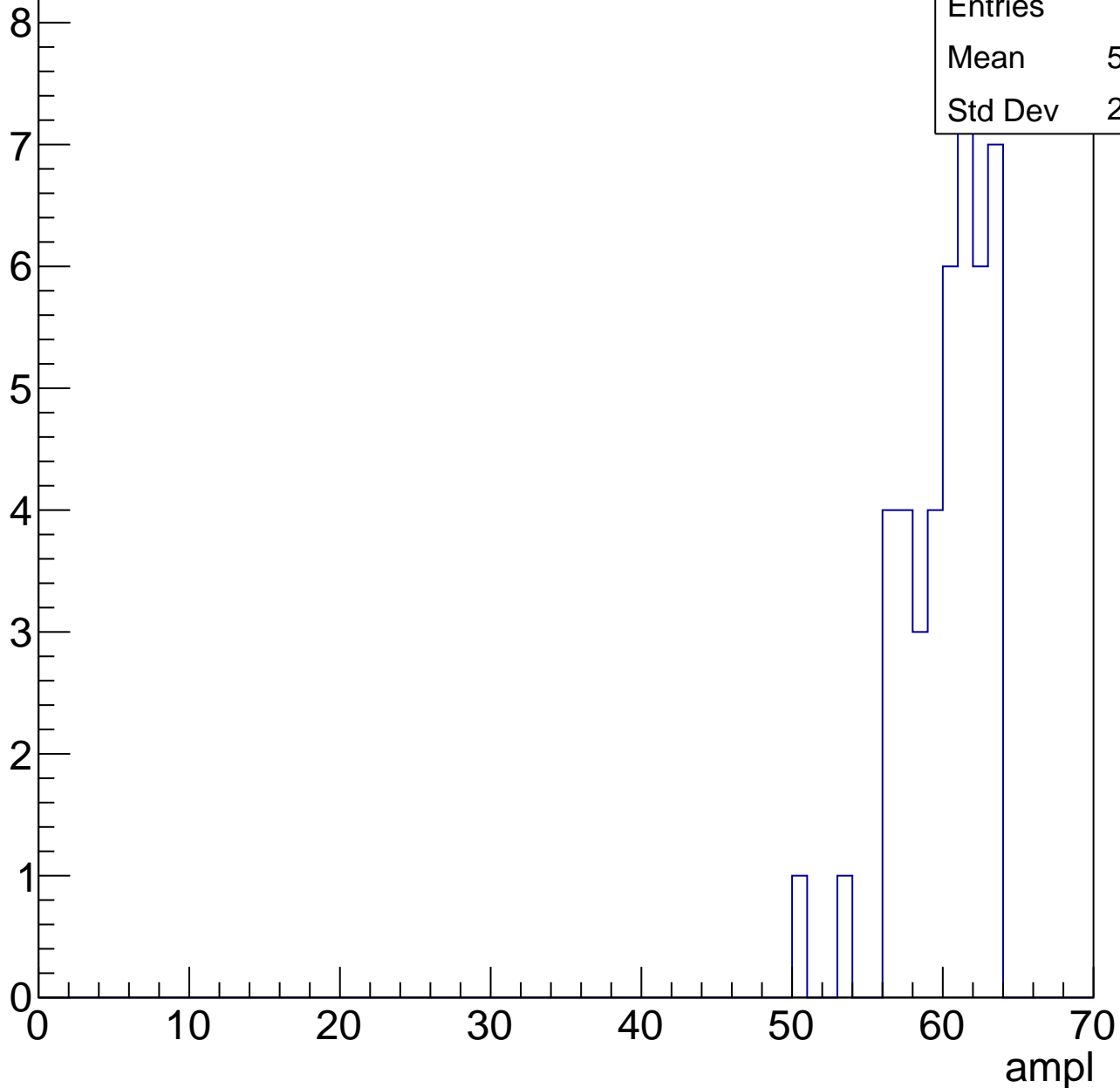


B1L100S, U7-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

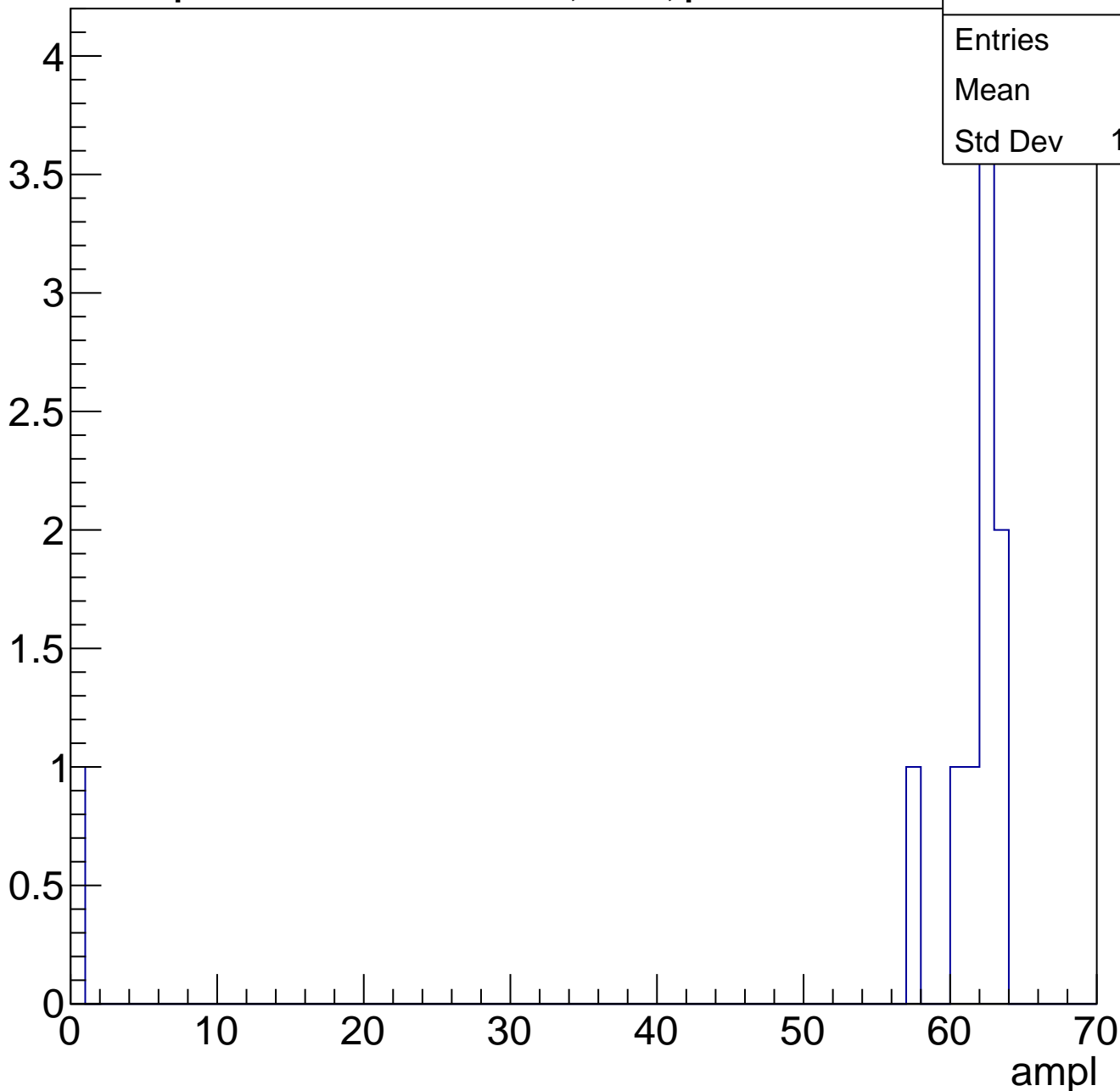
| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.68 |
| Std Dev | 2.843 |



B1L100S, U7-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch96, adc0

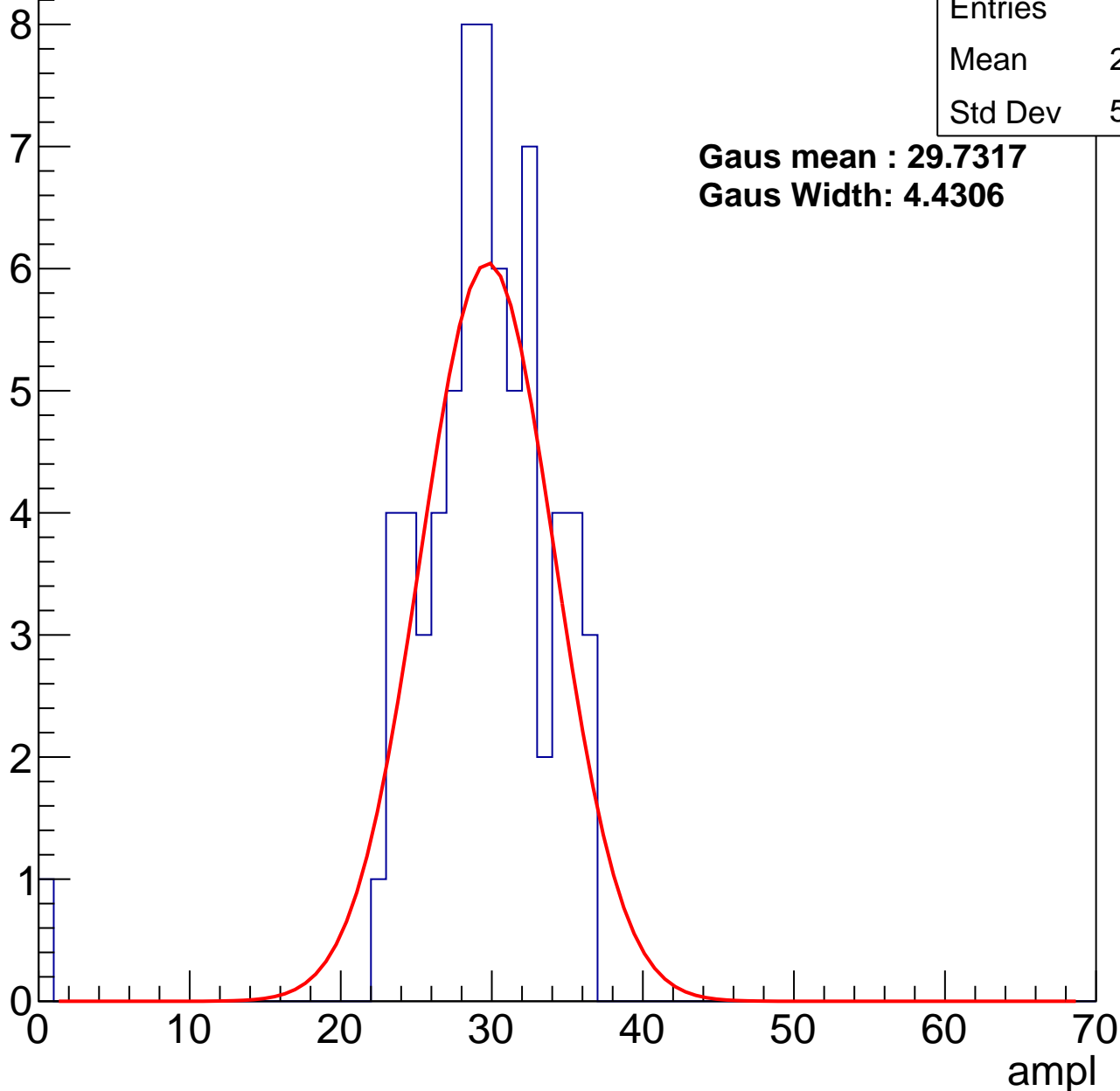
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.83 |
| Std Dev | 5.045 |

Gaus mean : 29.7317

Gaus Width: 4.4306



B1L100S, U7-ch96, adc1

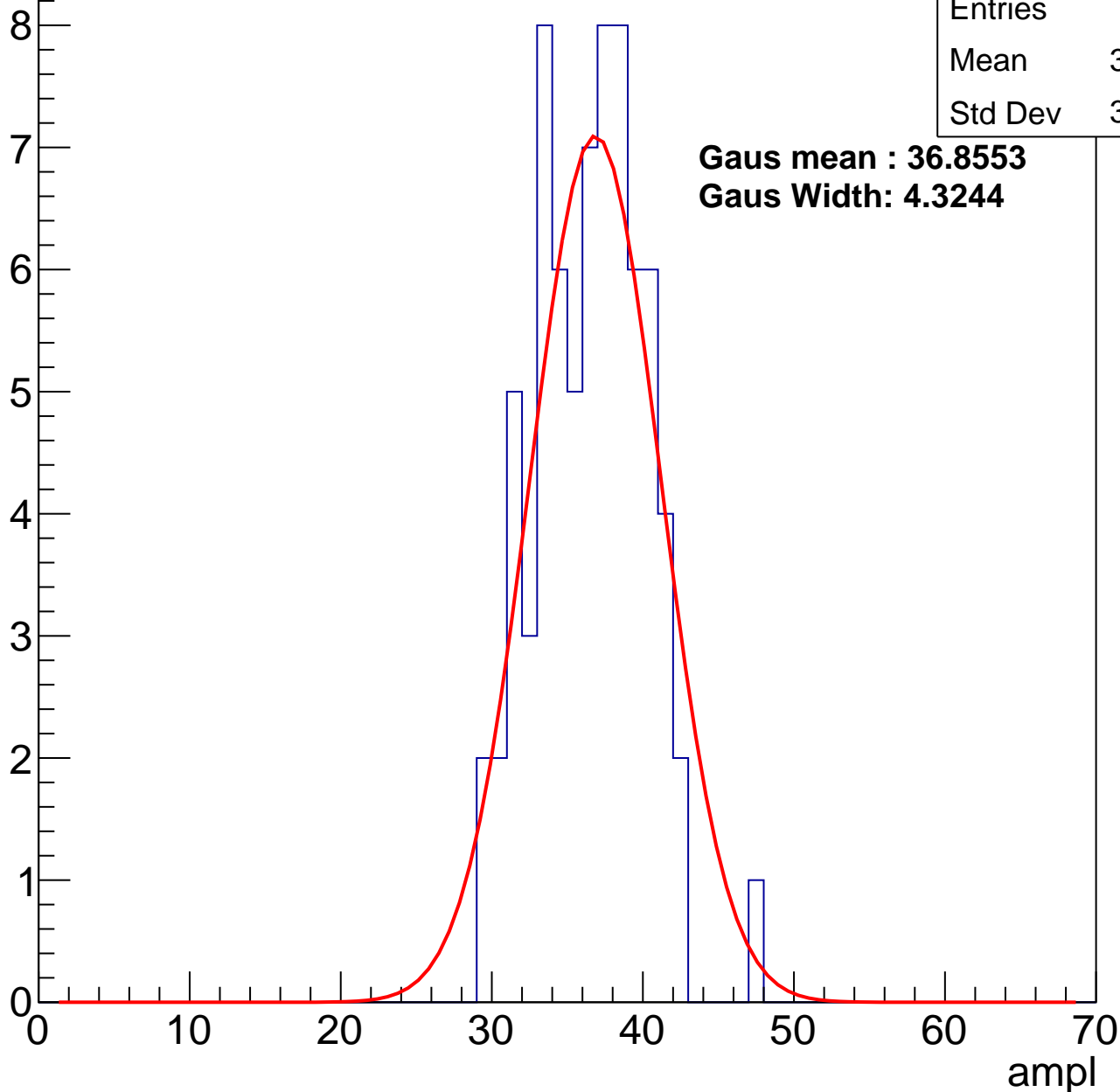
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 36.07 |
| Std Dev | 3.563 |

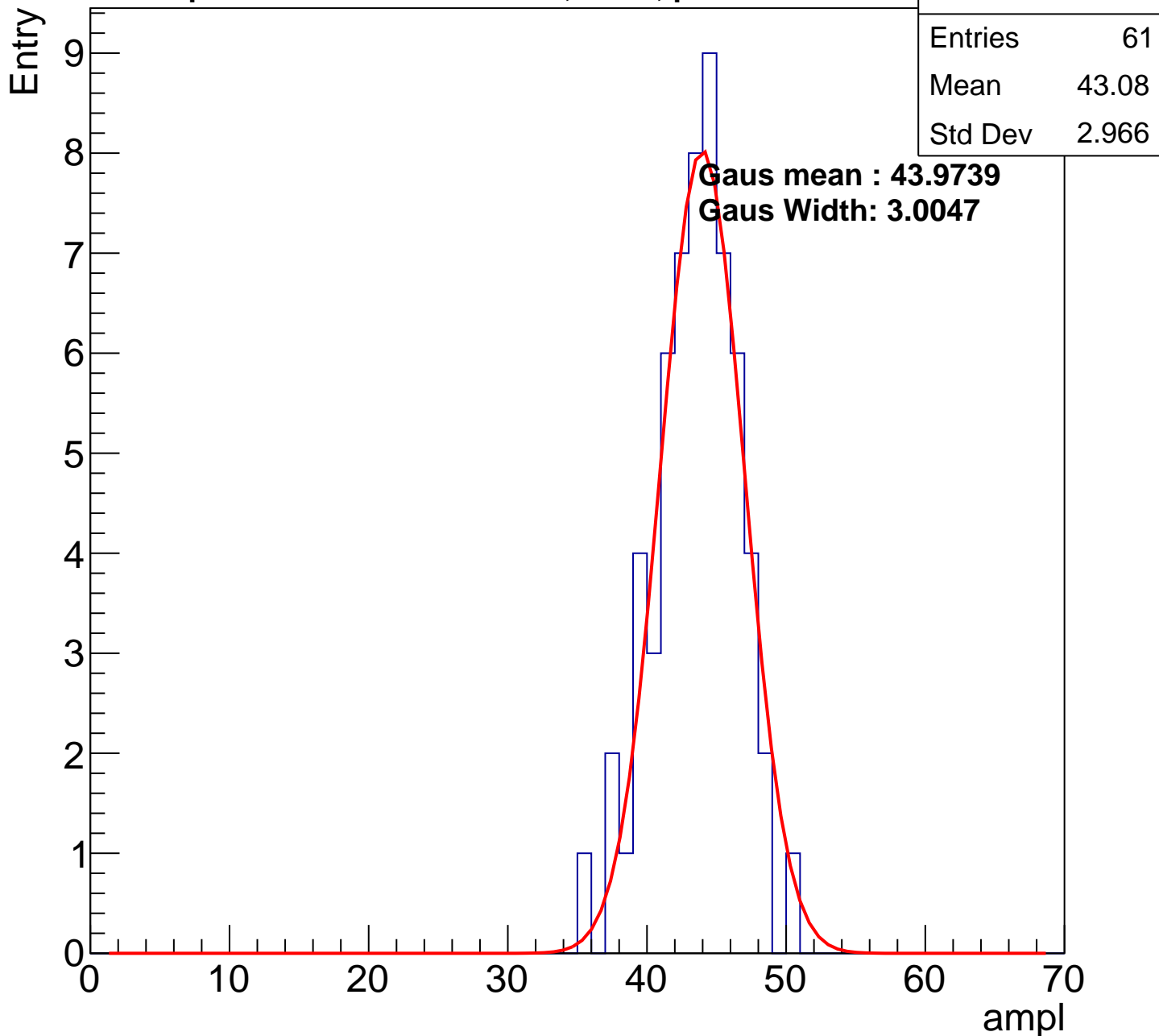
Gaus mean : 36.8553

Gaus Width: 4.3244



B1L100S, U7-ch96, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

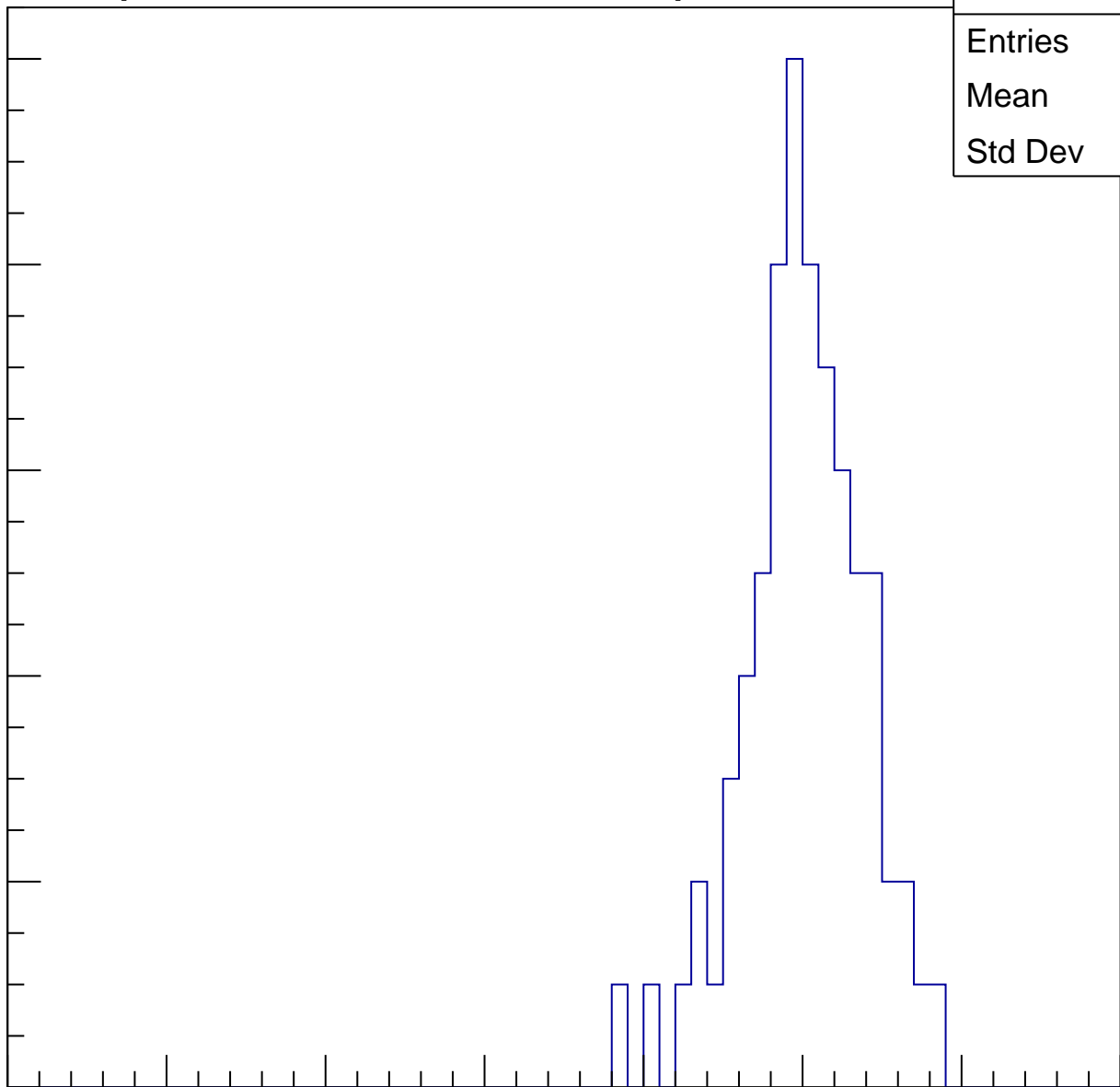
| | |
|---------|-------|
| Entries | 73 |
| Mean | 49.58 |
| Std Dev | 3.785 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

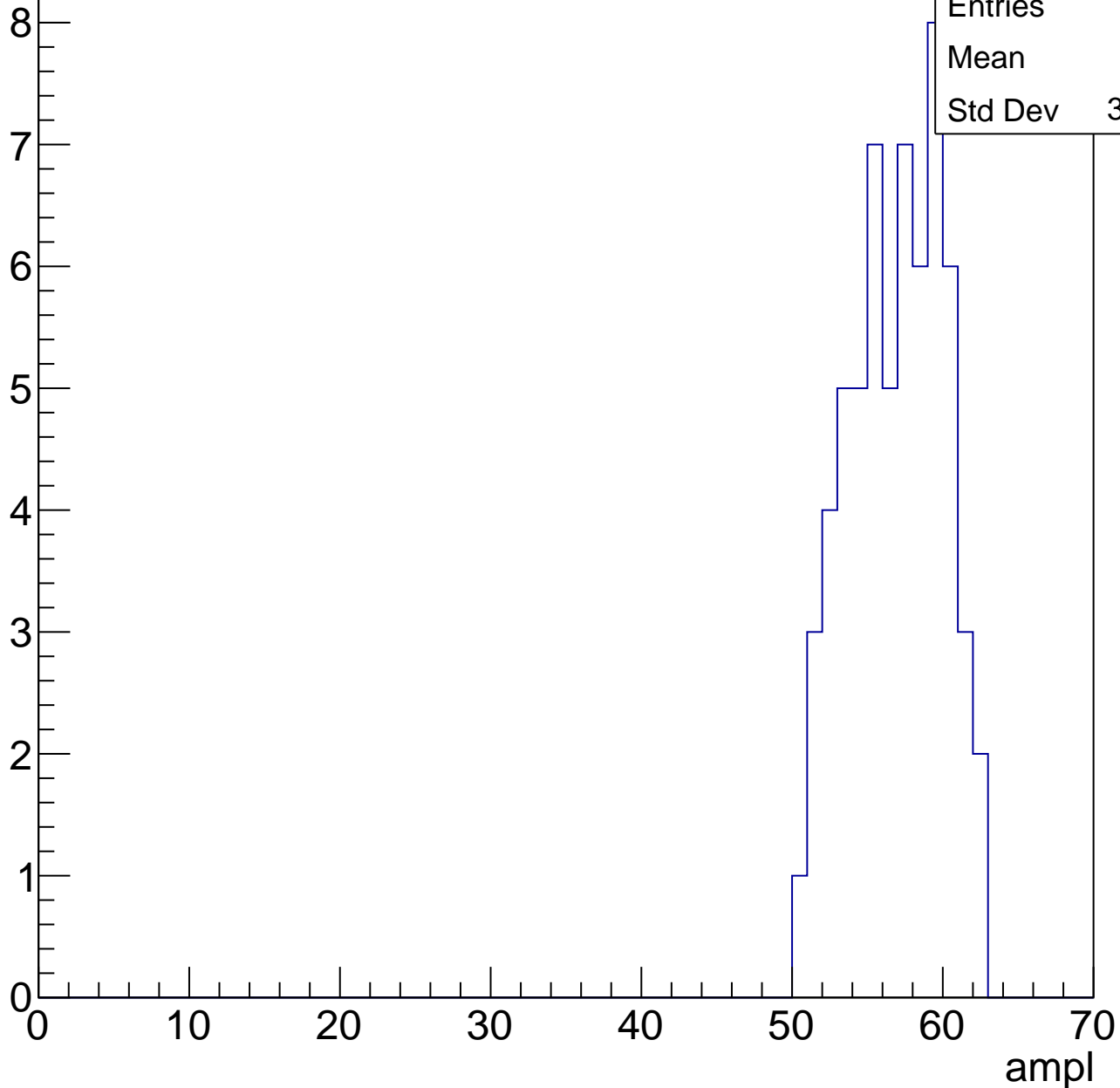
ampl



B1L100S, U7-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



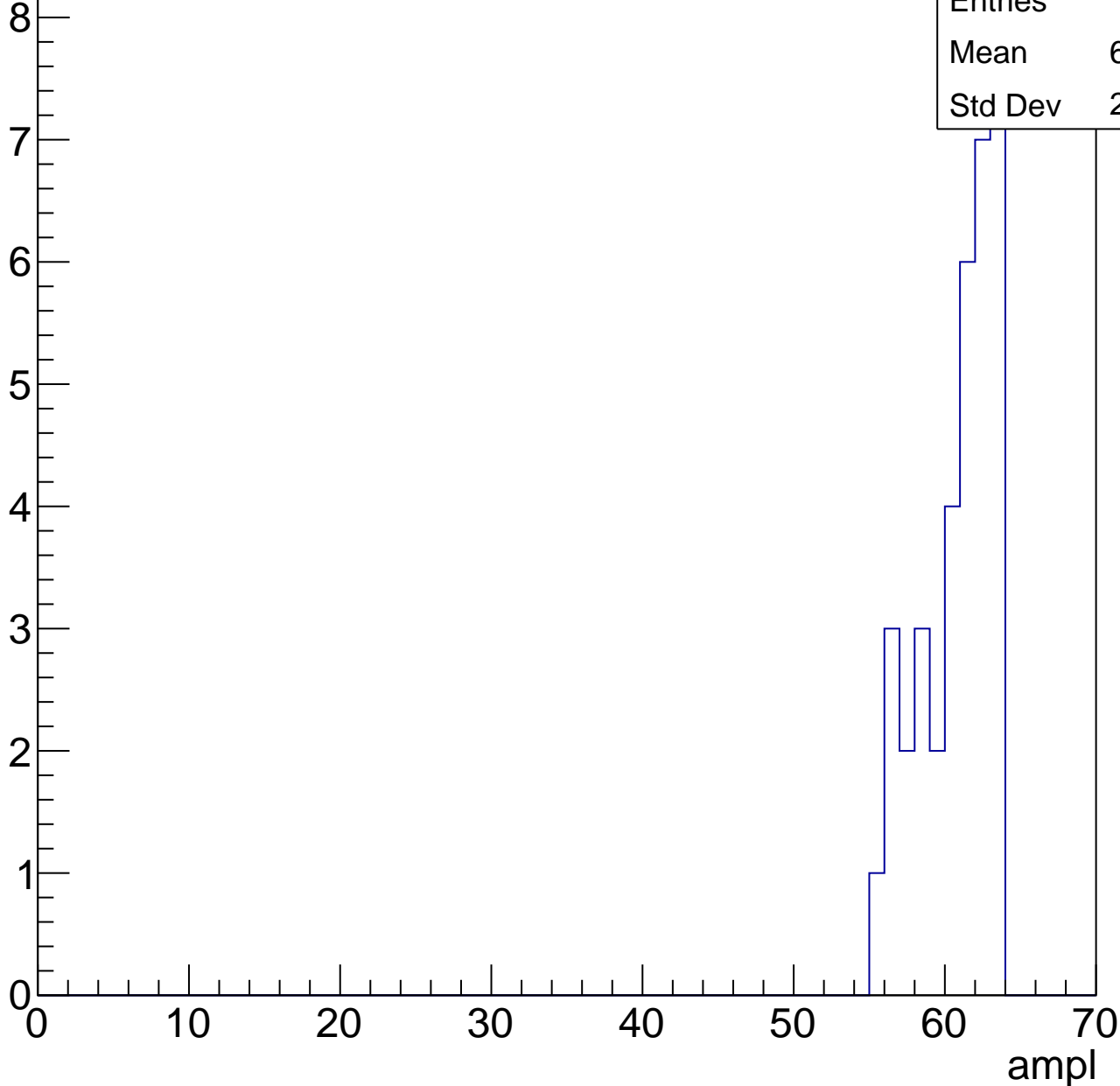
| | |
|---------|-------|
| Entries | 62 |
| Mean | 56.4 |
| Std Dev | 3.066 |

B1L100S, U7-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

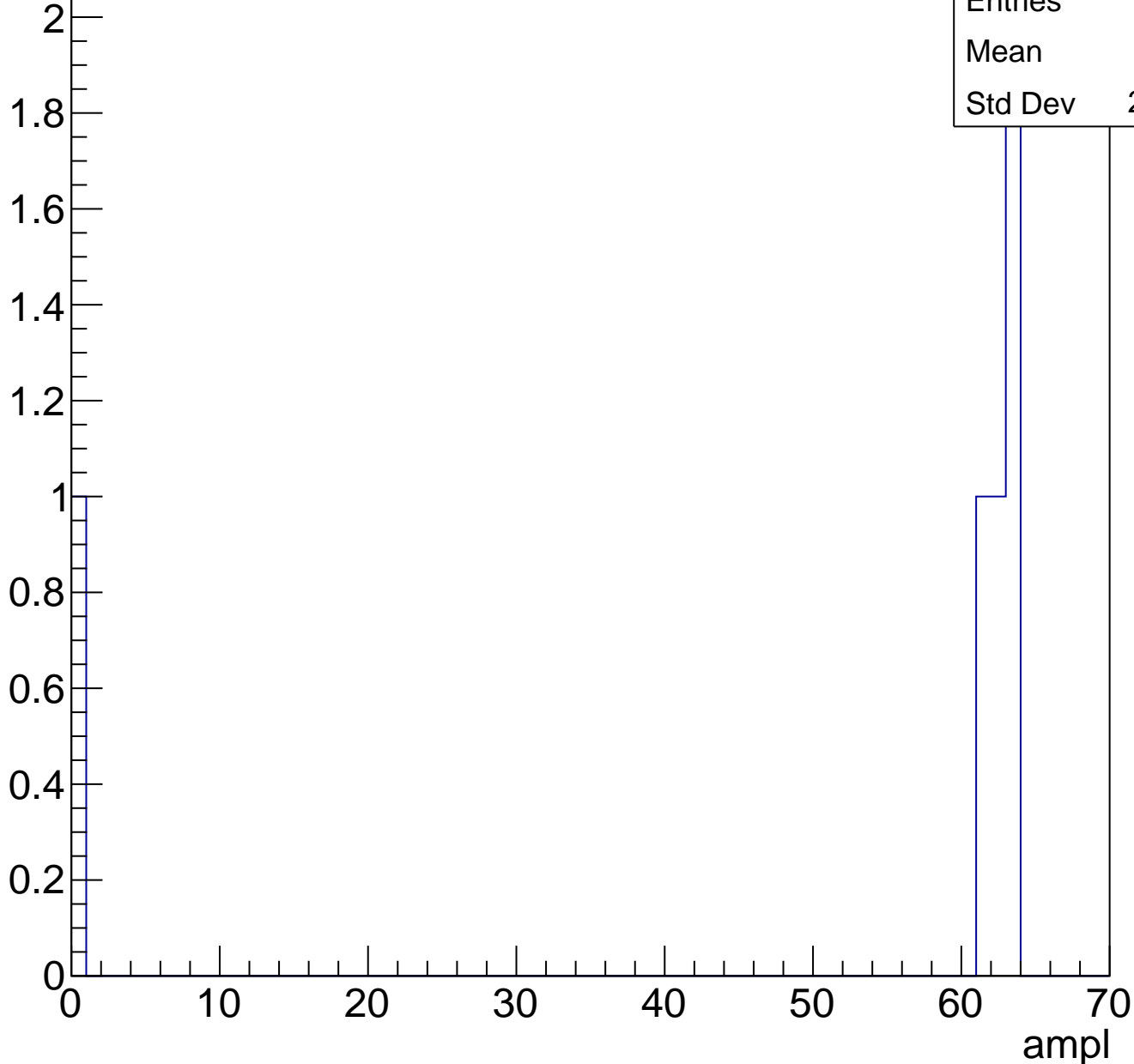
| | |
|---------|-------|
| Entries | 36 |
| Mean | 60.36 |
| Std Dev | 2.394 |



B1L100S, U7-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.333 |
| Std Dev | 10.37 |

B1L100S, U7-ch97, adc0

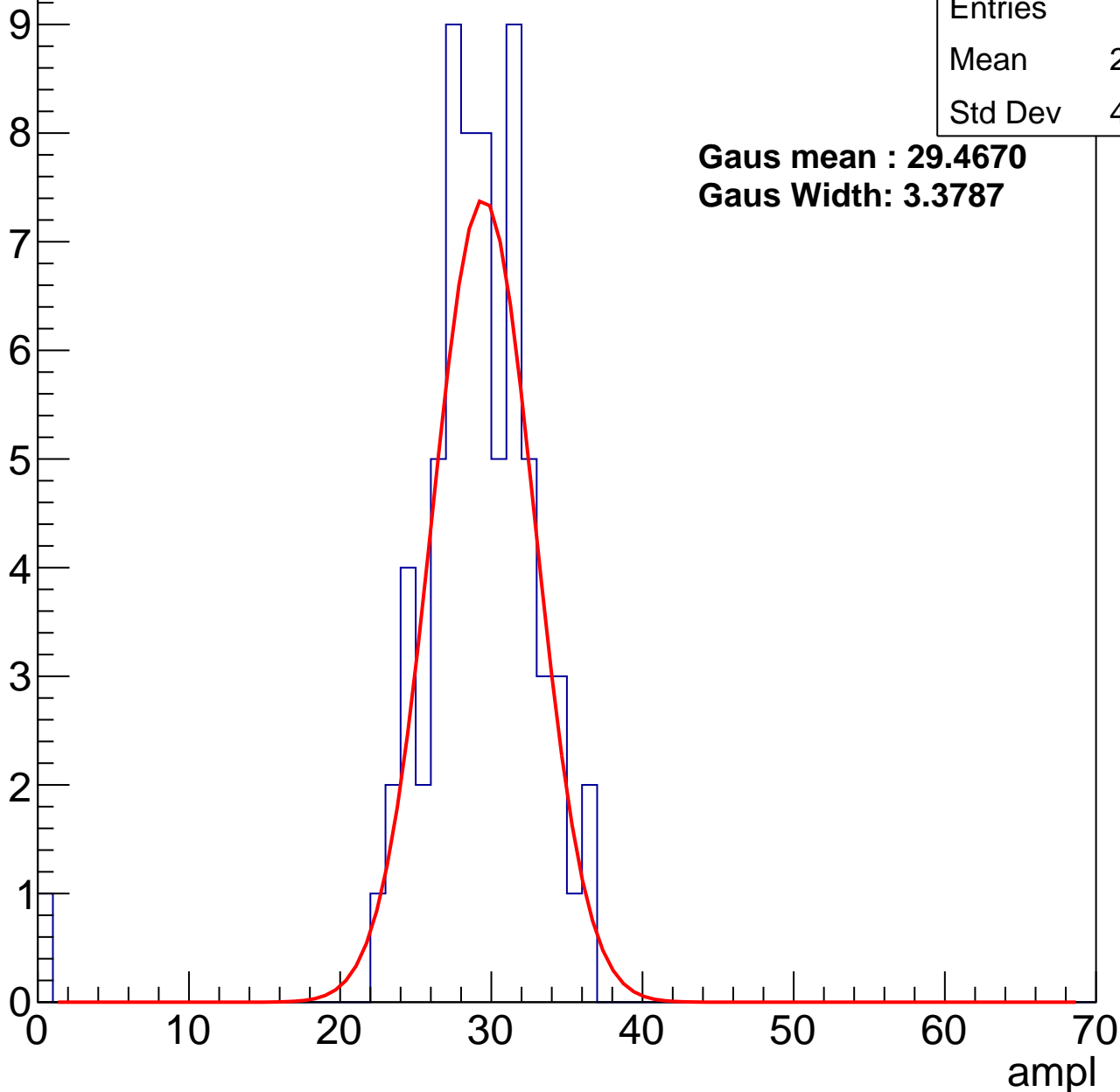
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.53 |
| Std Dev | 4.714 |

Gaus mean : 29.4670

Gaus Width: 3.3787



B1L100S, U7-ch97, adc1

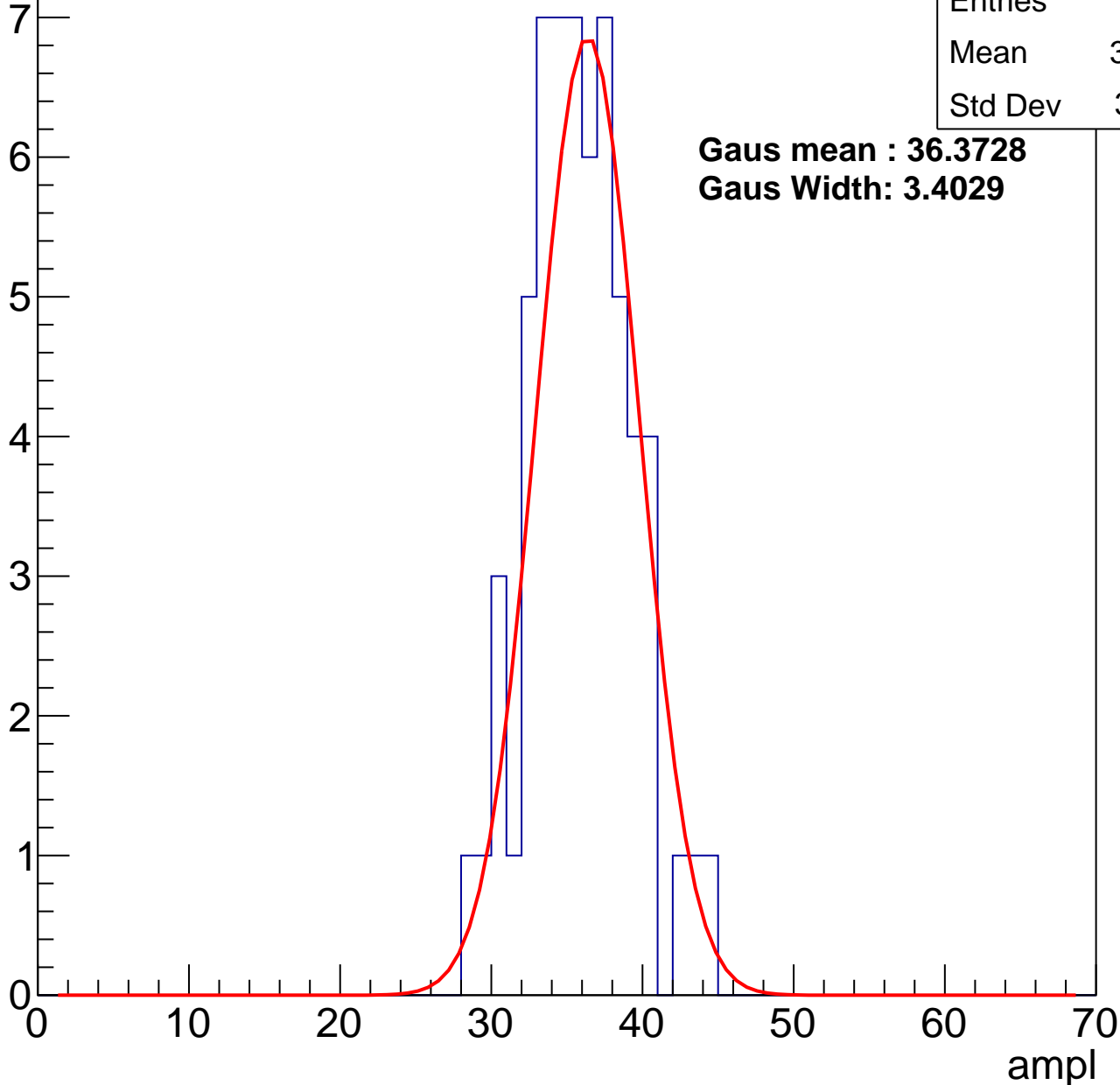
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.44 |
| Std Dev | 3.341 |

Gaus mean : 36.3728

Gaus Width: 3.4029



B1L100S, U7-ch97, adc2

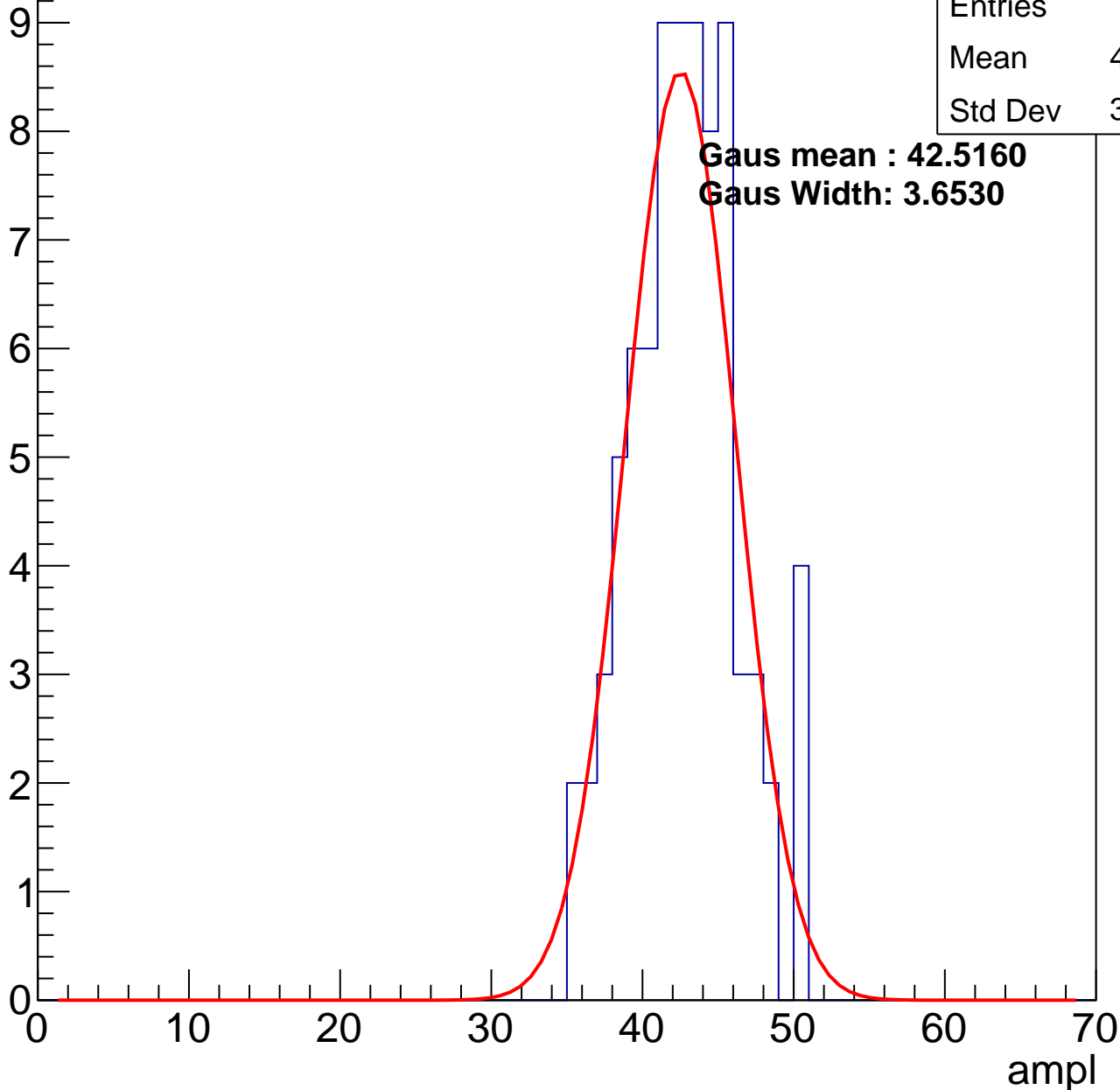
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 42.29 |
| Std Dev | 3.497 |

Gaus mean : 42.5160

Gaus Width: 3.6530

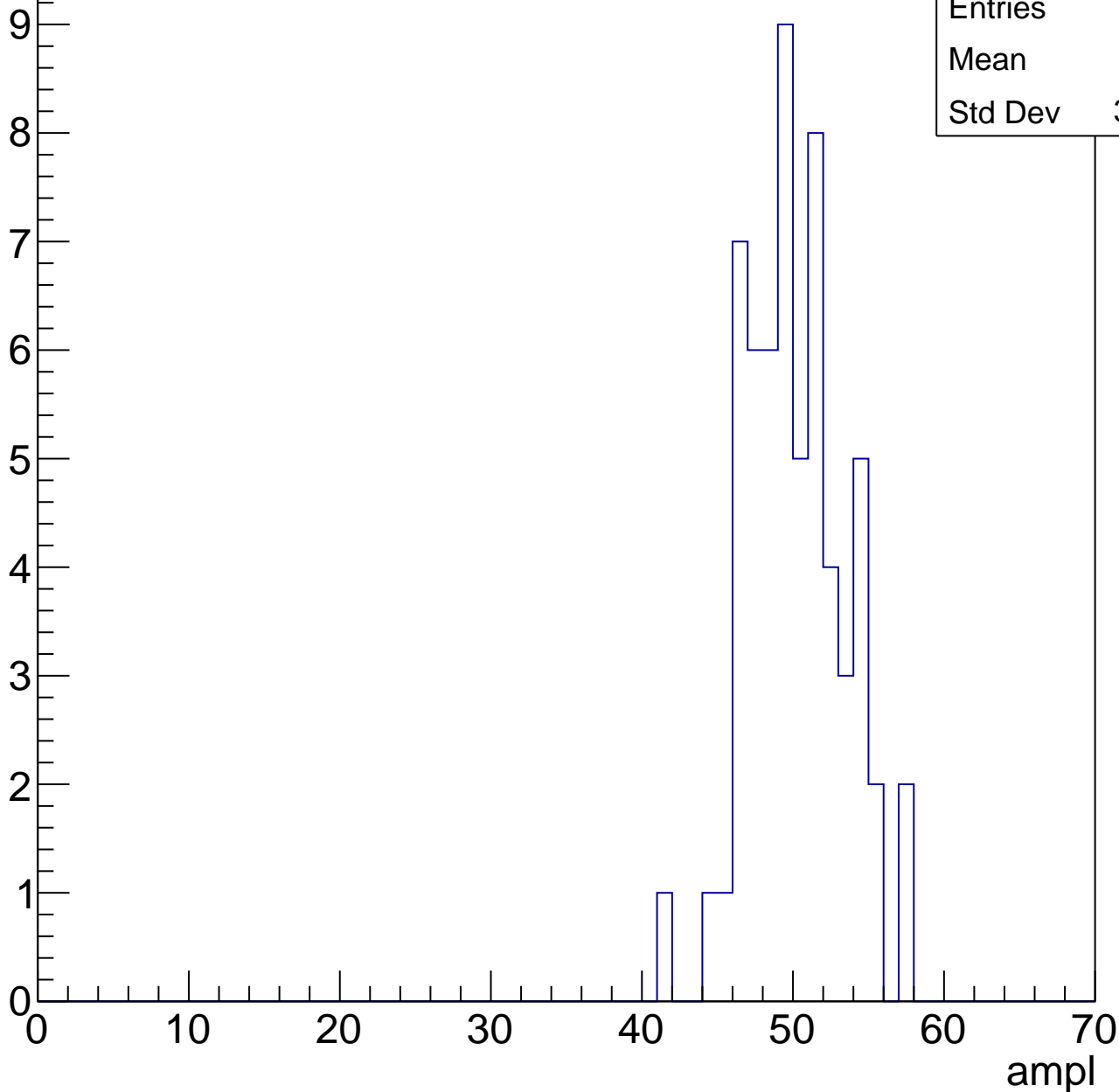


B1L100S, U7-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

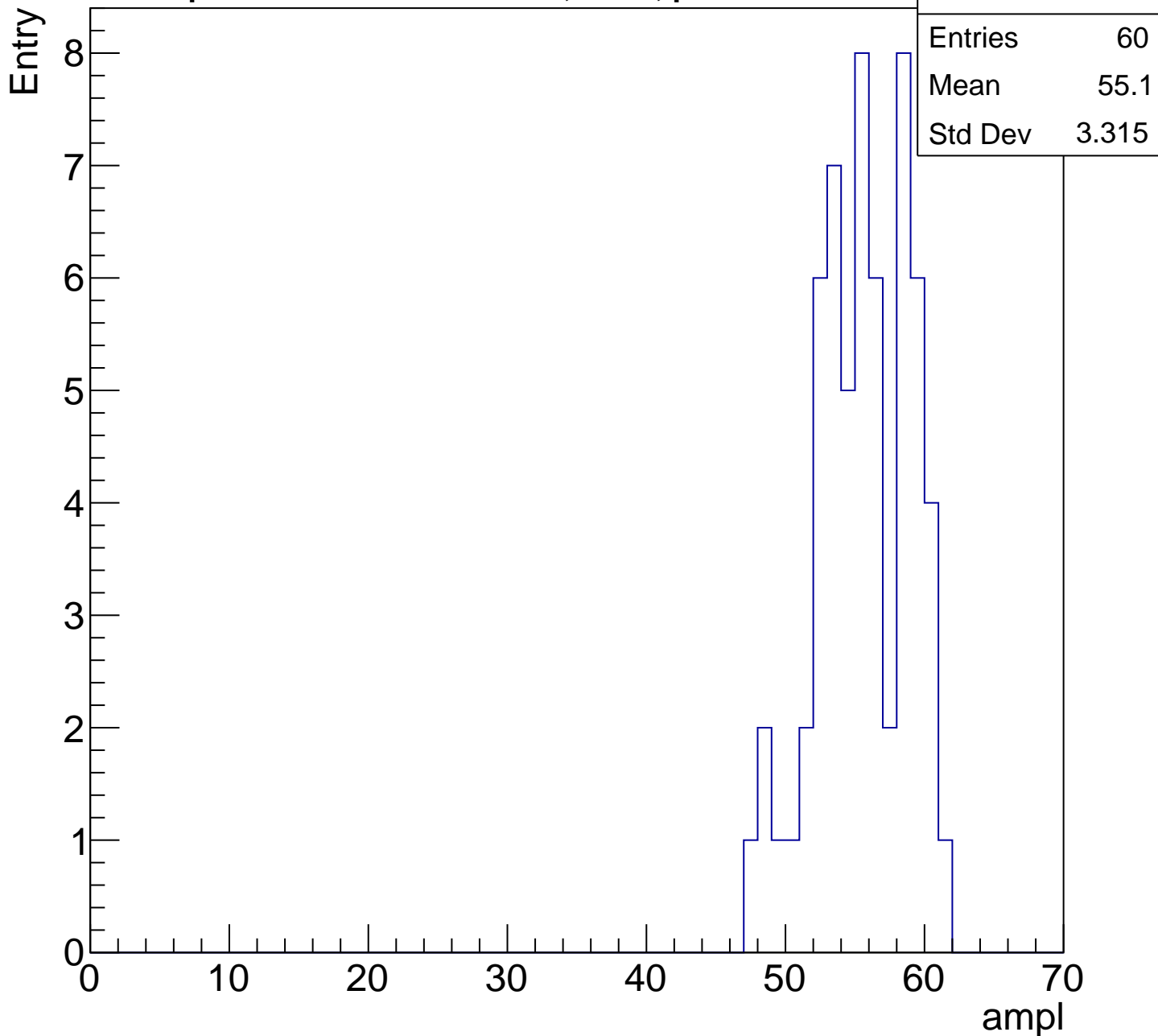
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.7 |
| Std Dev | 3.201 |



B1L100S, U7-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch97, adc5

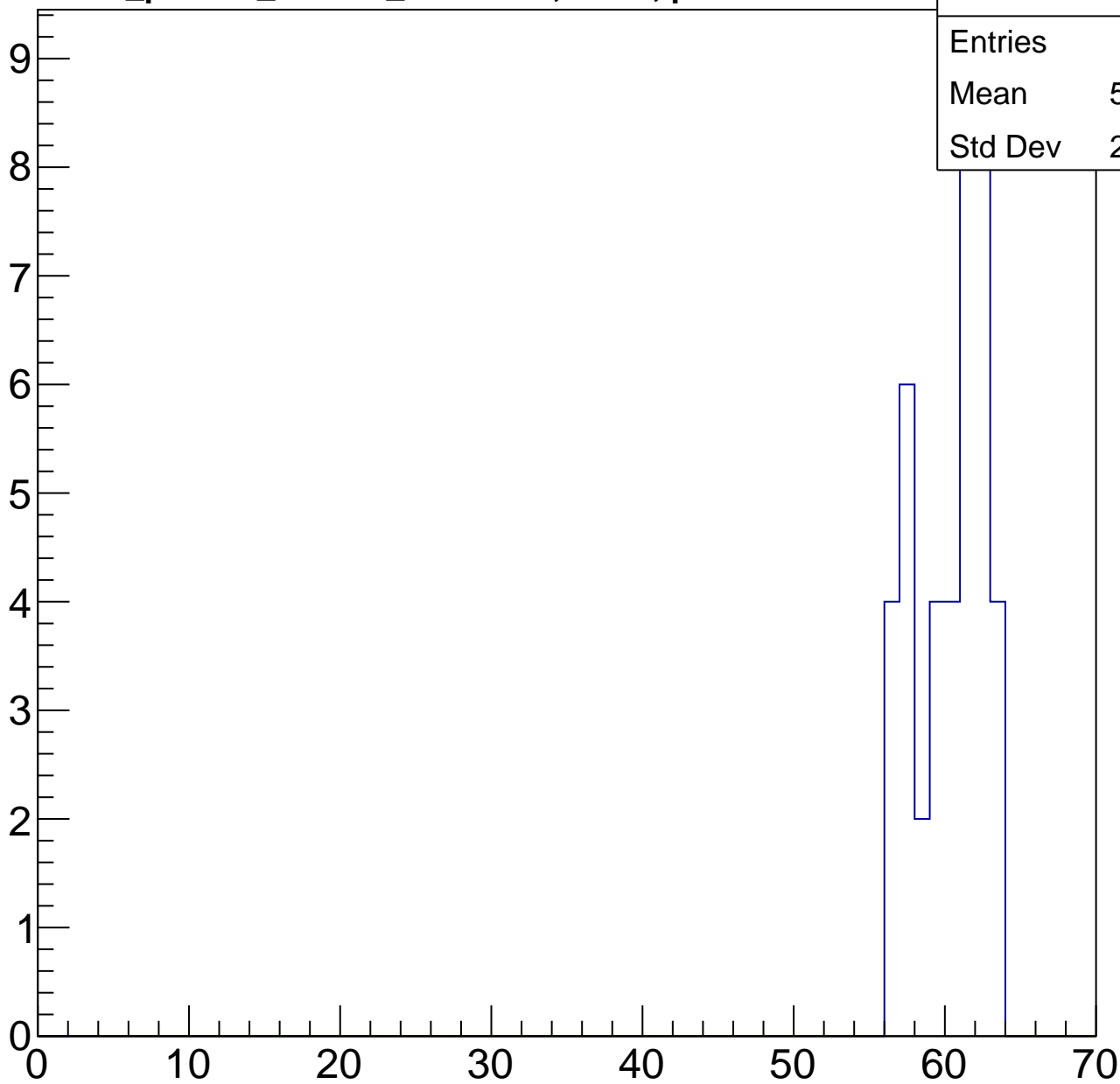
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 41 |
| Mean | 59.88 |
| Std Dev | 2.244 |

ampl

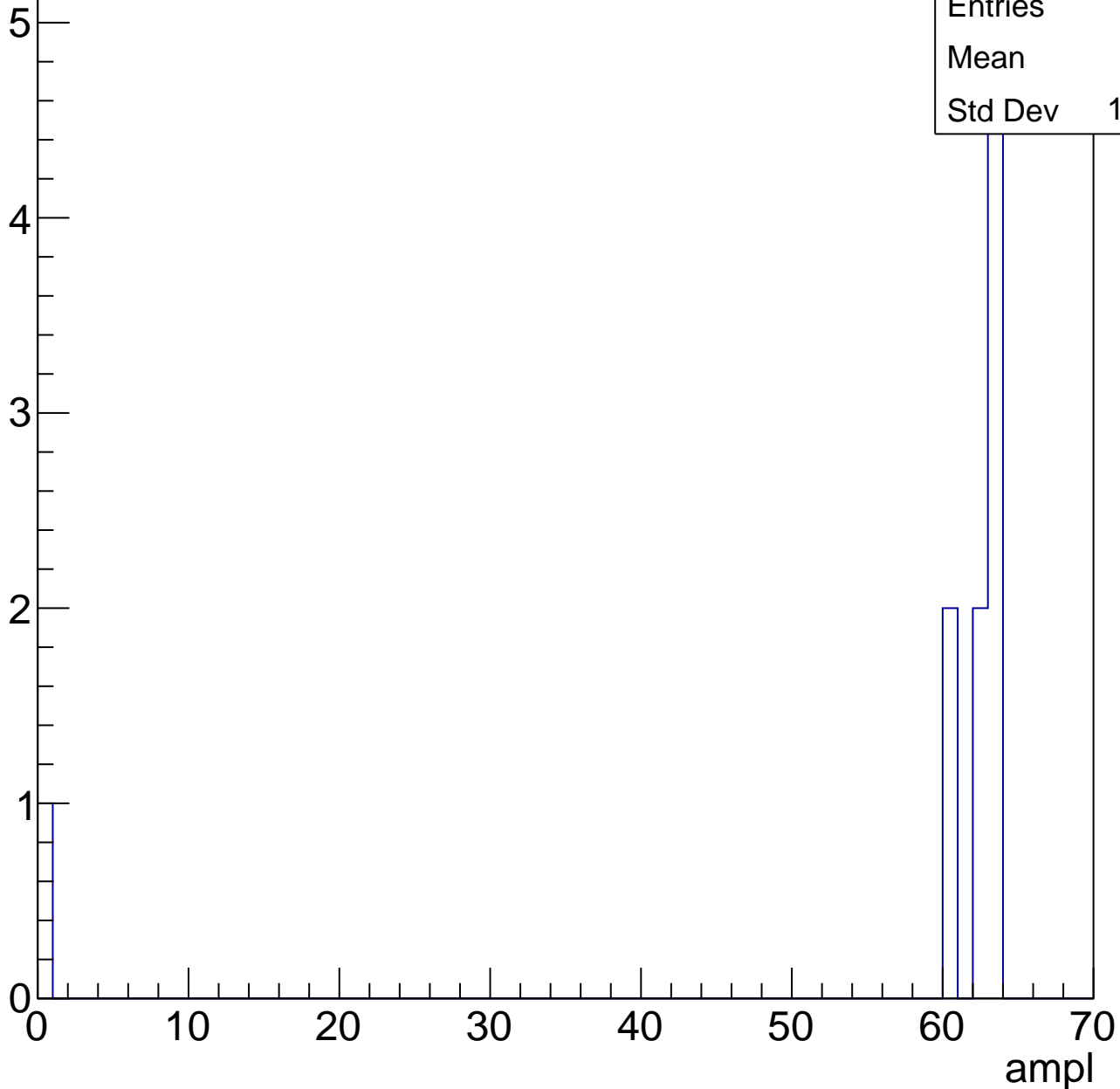


B1L100S, U7-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

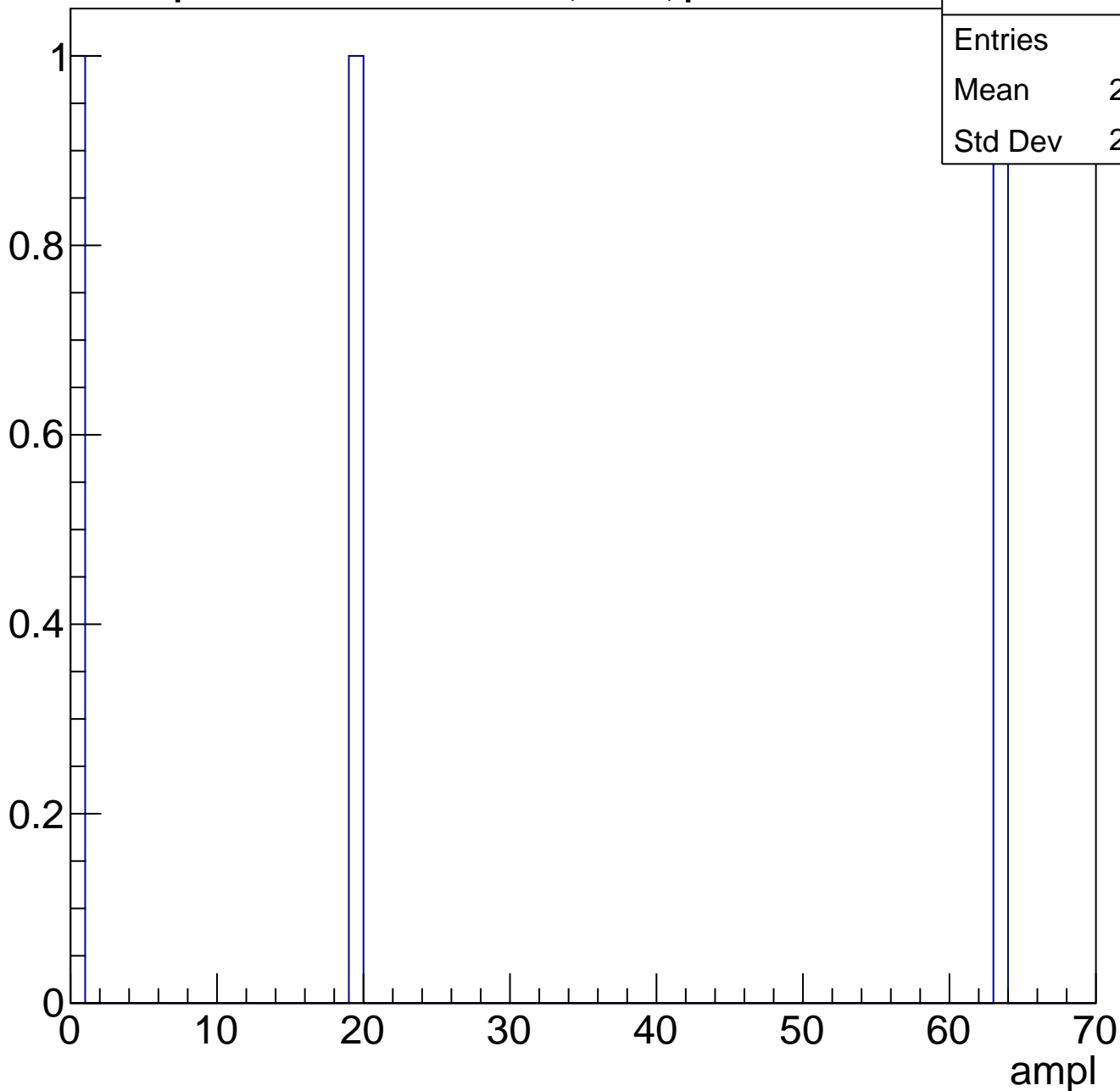
| | |
|---------|-------|
| Entries | 10 |
| Mean | 55.9 |
| Std Dev | 18.67 |



B1L100S, U7-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch98, adc0

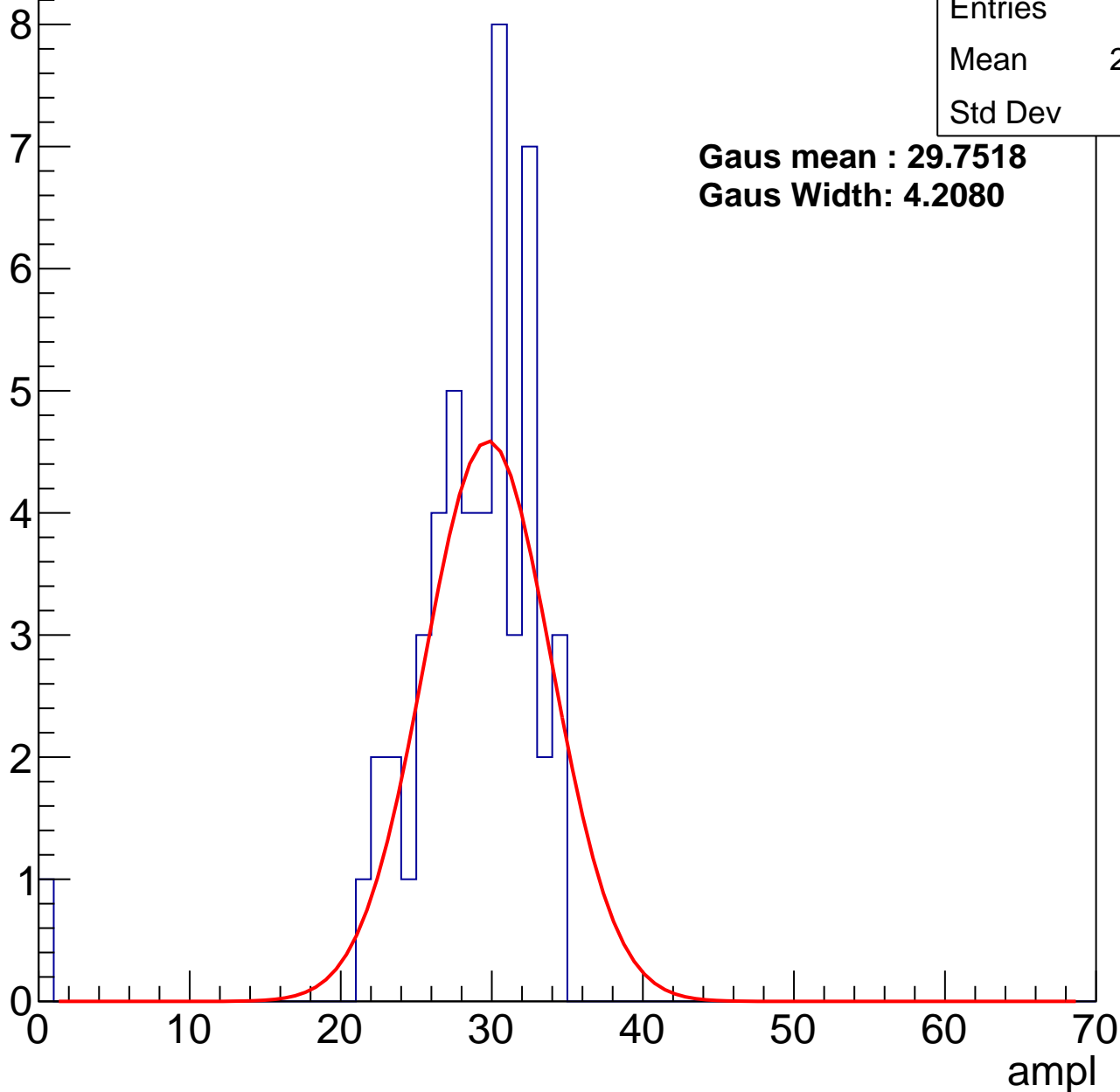
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 28.04 |
| Std Dev | 5.2 |

Gaus mean : 29.7518

Gaus Width: 4.2080



B1L100S, U7-ch98, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 34.84 |
| Std Dev | 3.849 |

Gaus mean : 35.9418

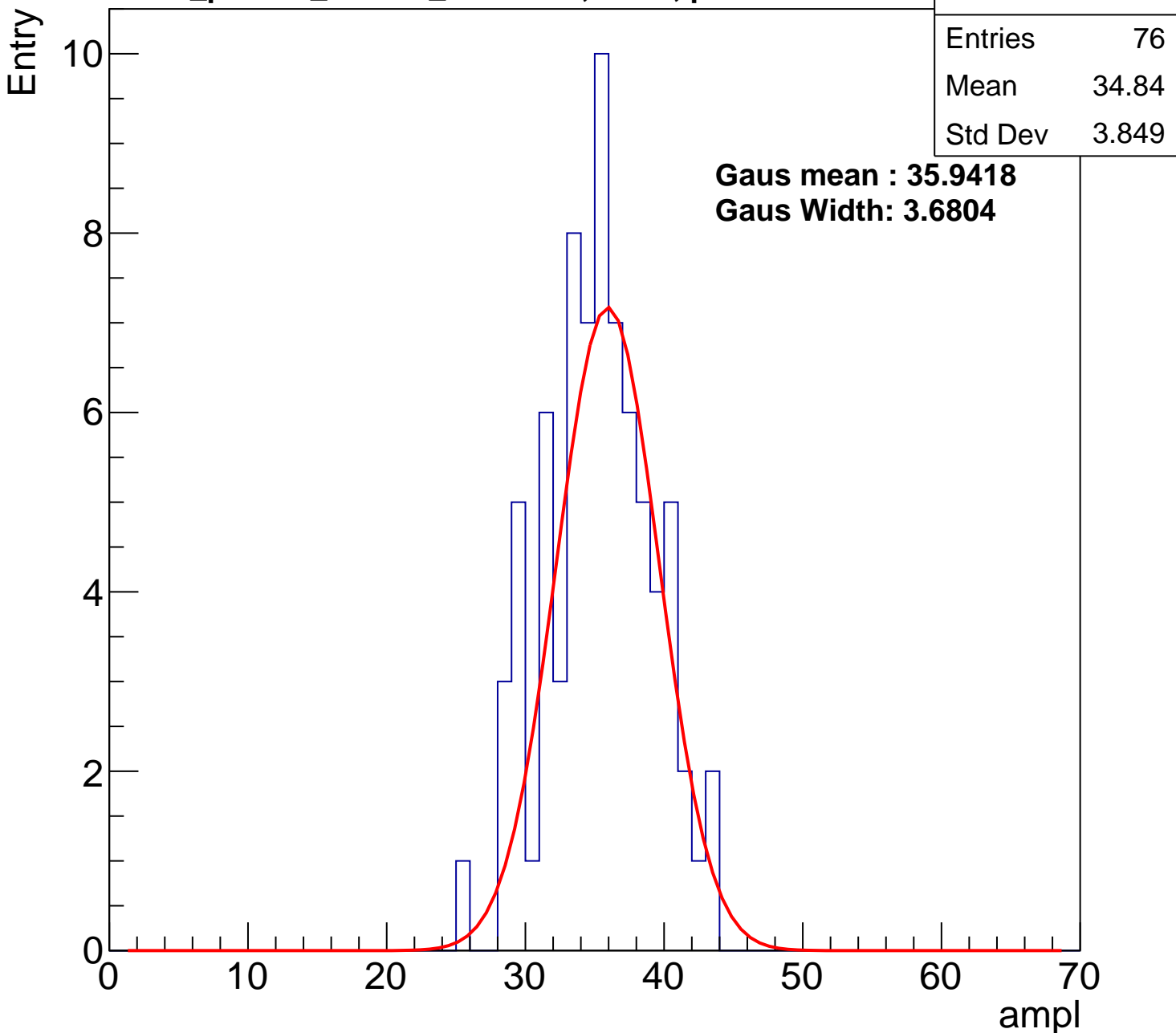
Gaus Width: 3.6804

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch98, adc2

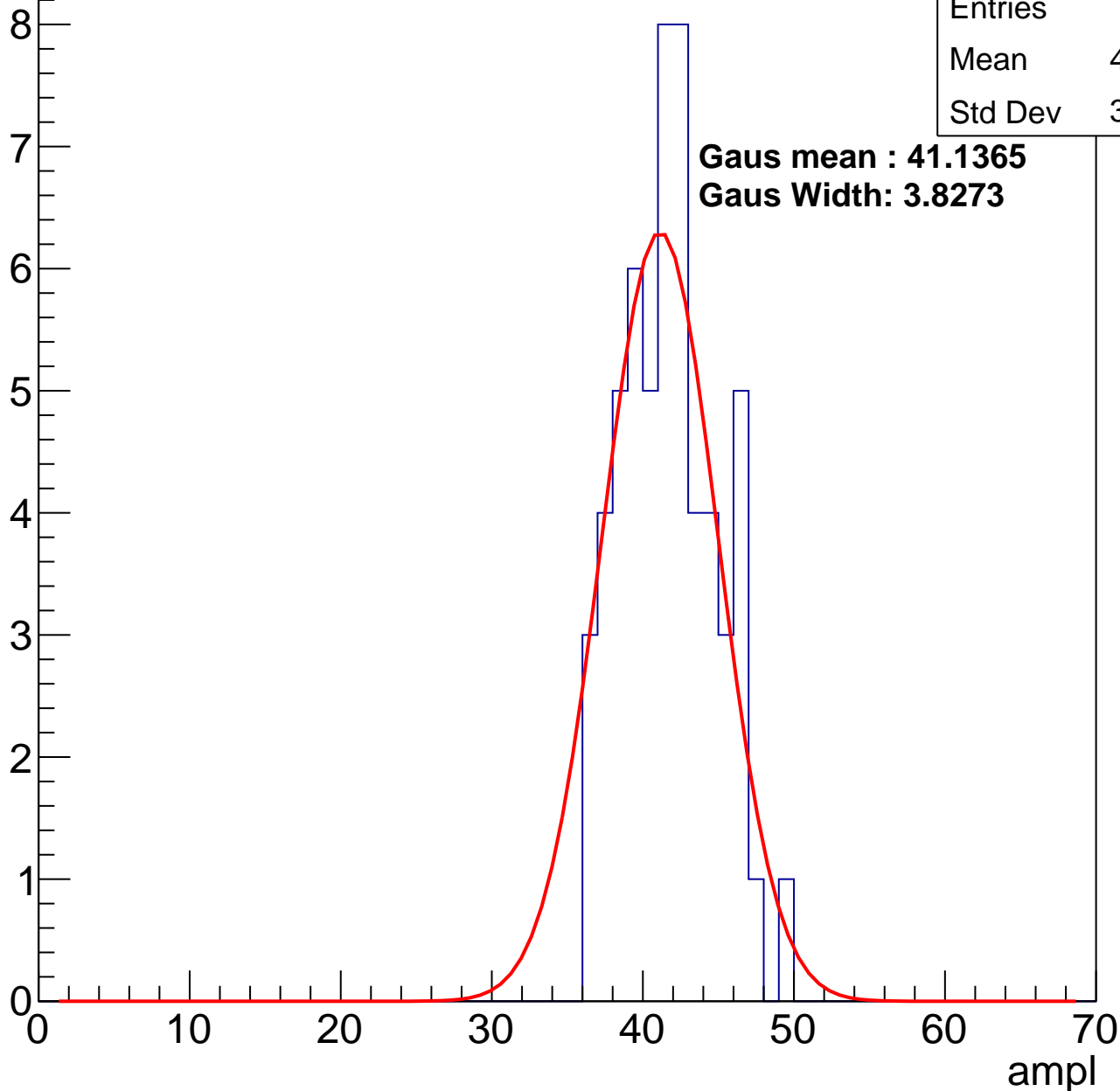
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 41.28 |
| Std Dev | 3.082 |

Gaus mean : 41.1365

Gaus Width: 3.8273

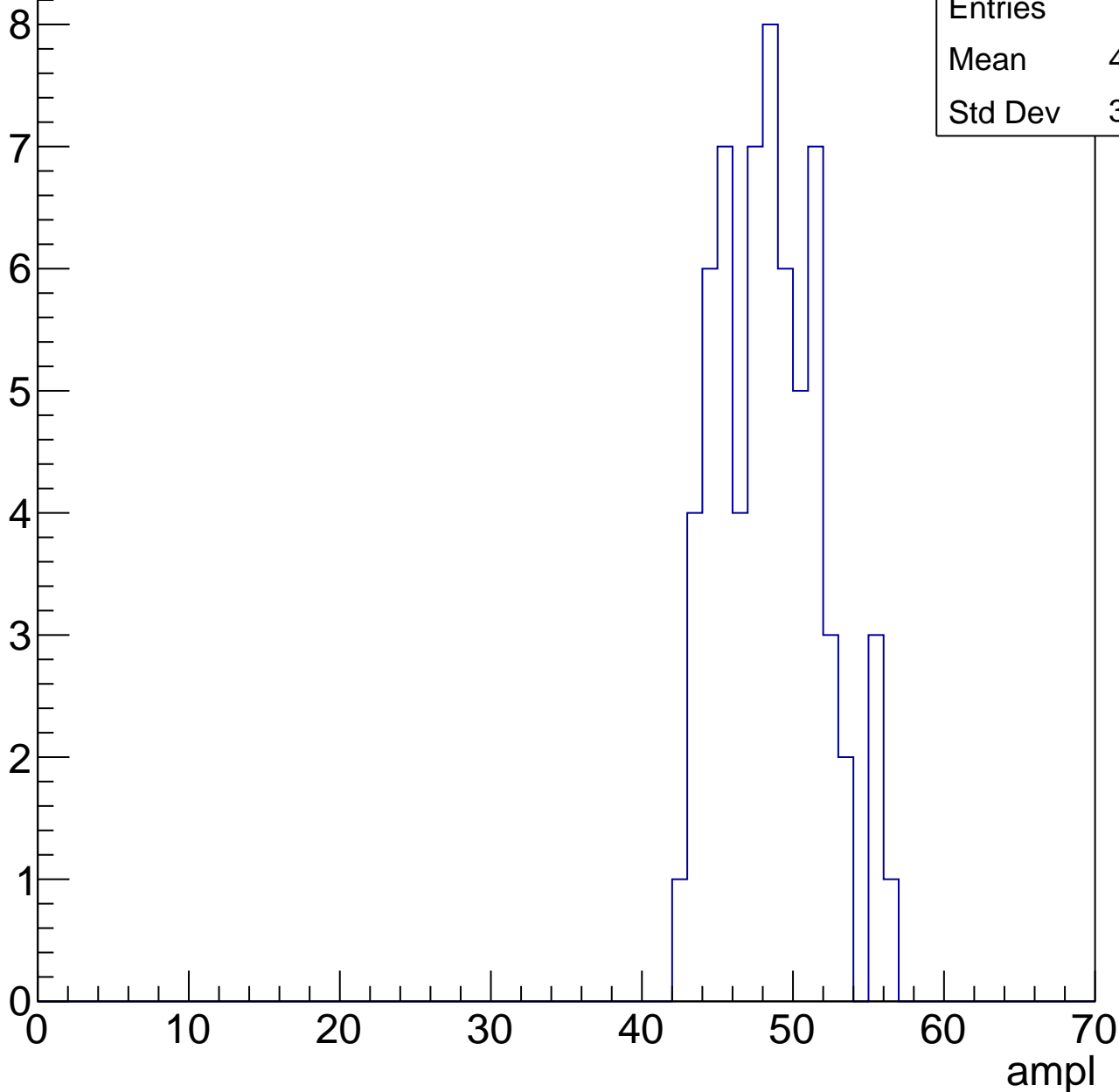


B1L100S, U7-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.03 |
| Std Dev | 3.345 |

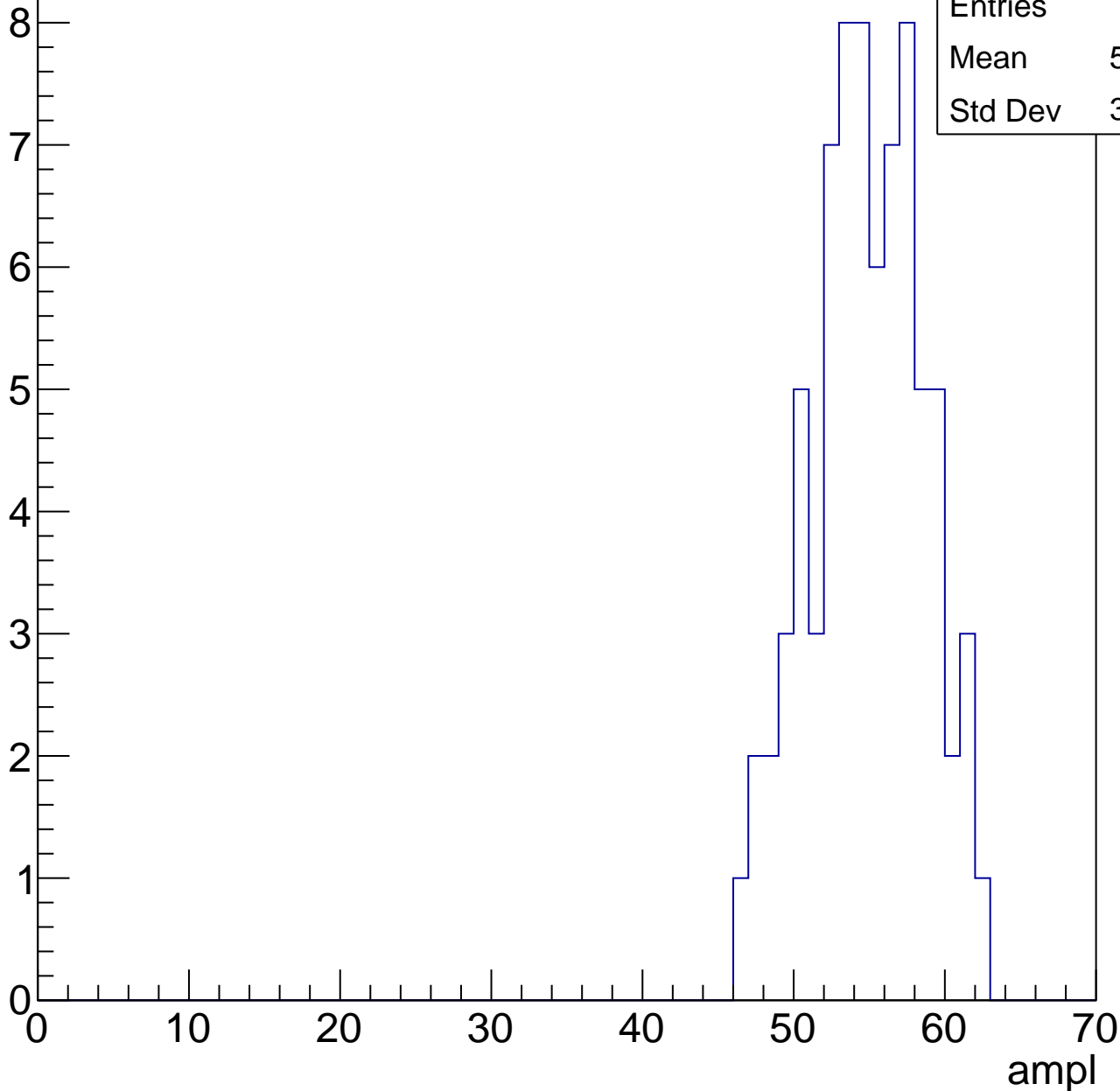


B1L100S, U7-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

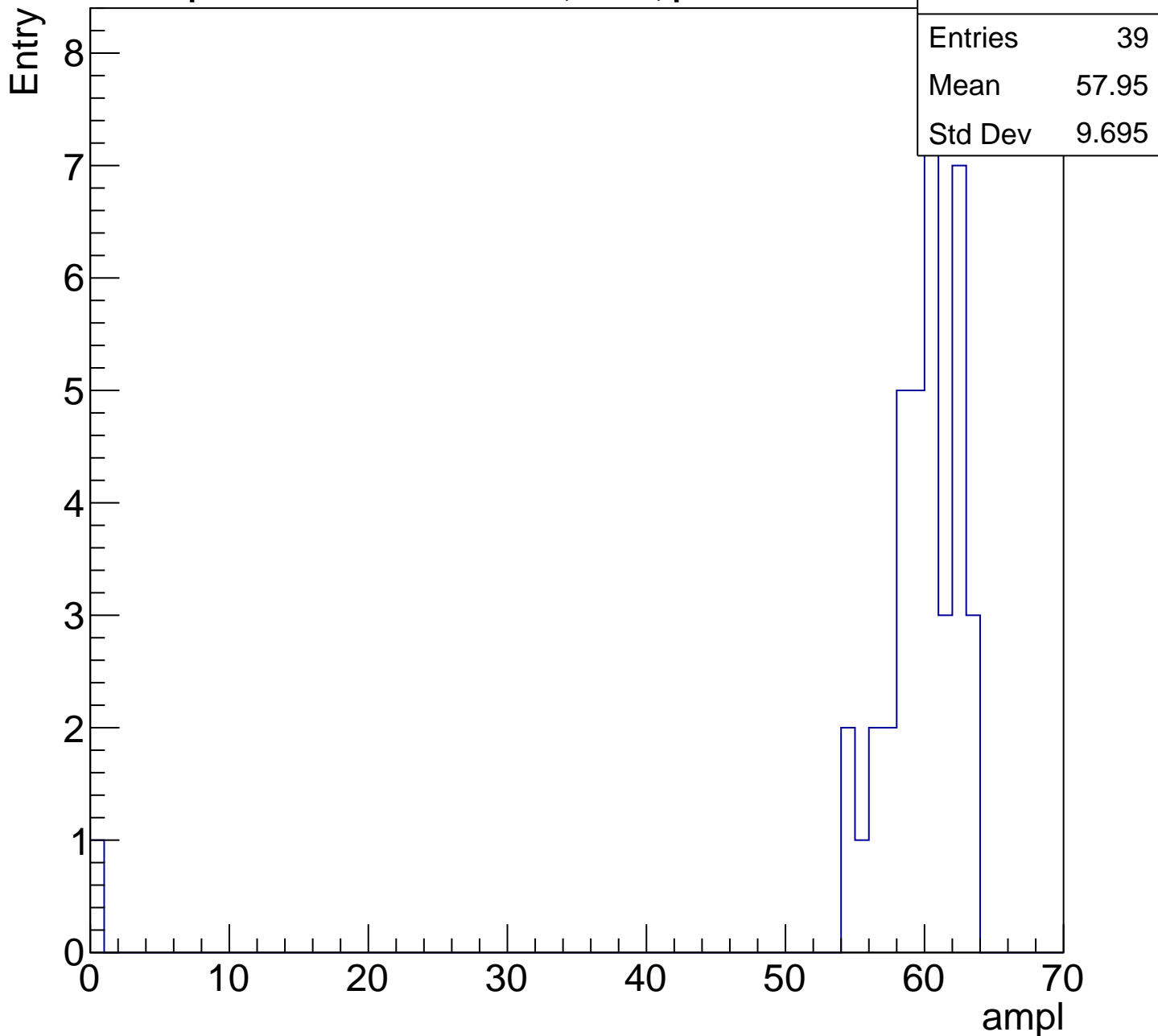
Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 54.39 |
| Std Dev | 3.689 |



B1L100S, U7-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

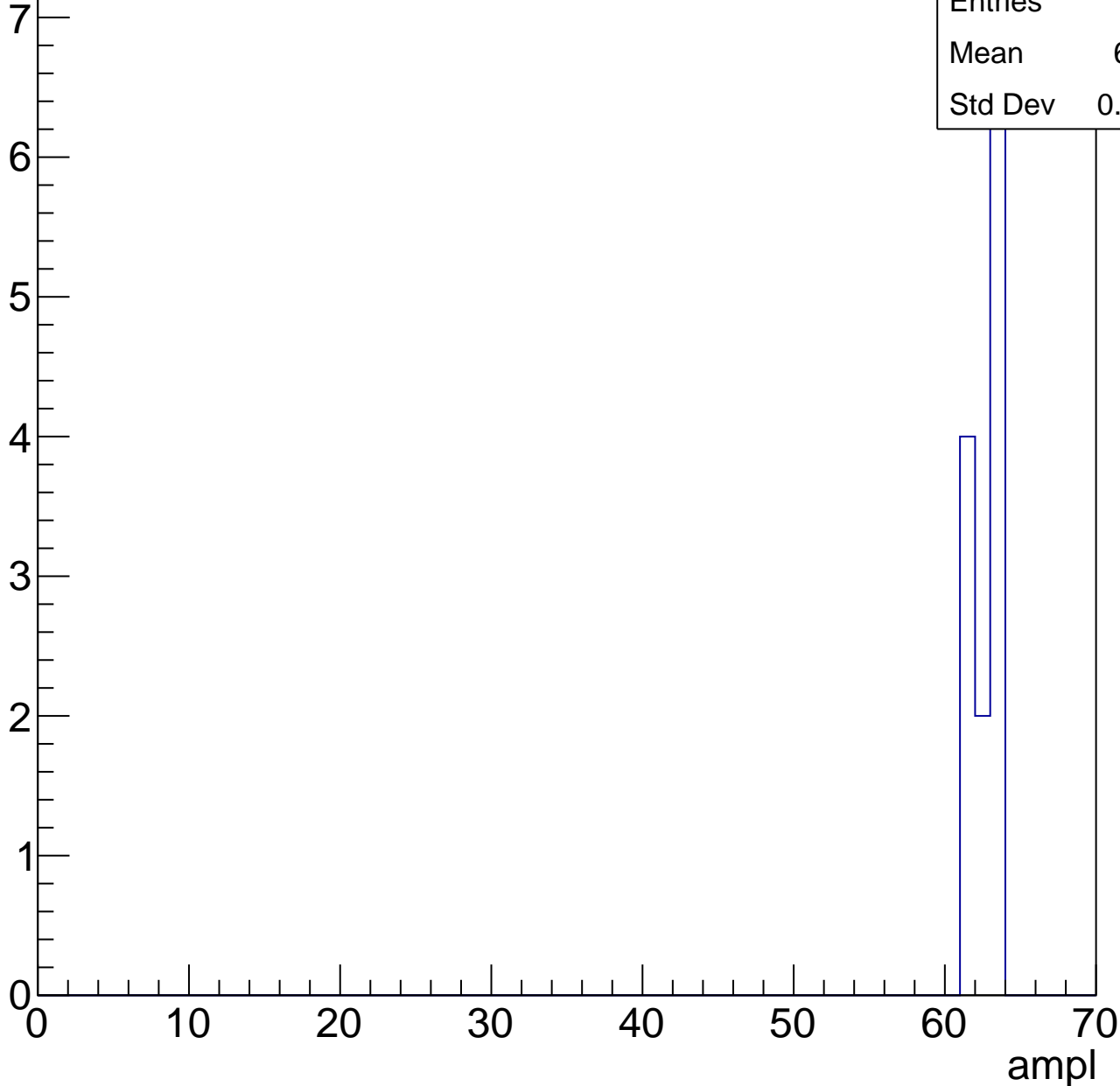


B1L100S, U7-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|--------|
| Entries | 13 |
| Mean | 62.23 |
| Std Dev | 0.8904 |



B1L100S, U7-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch99, adc0

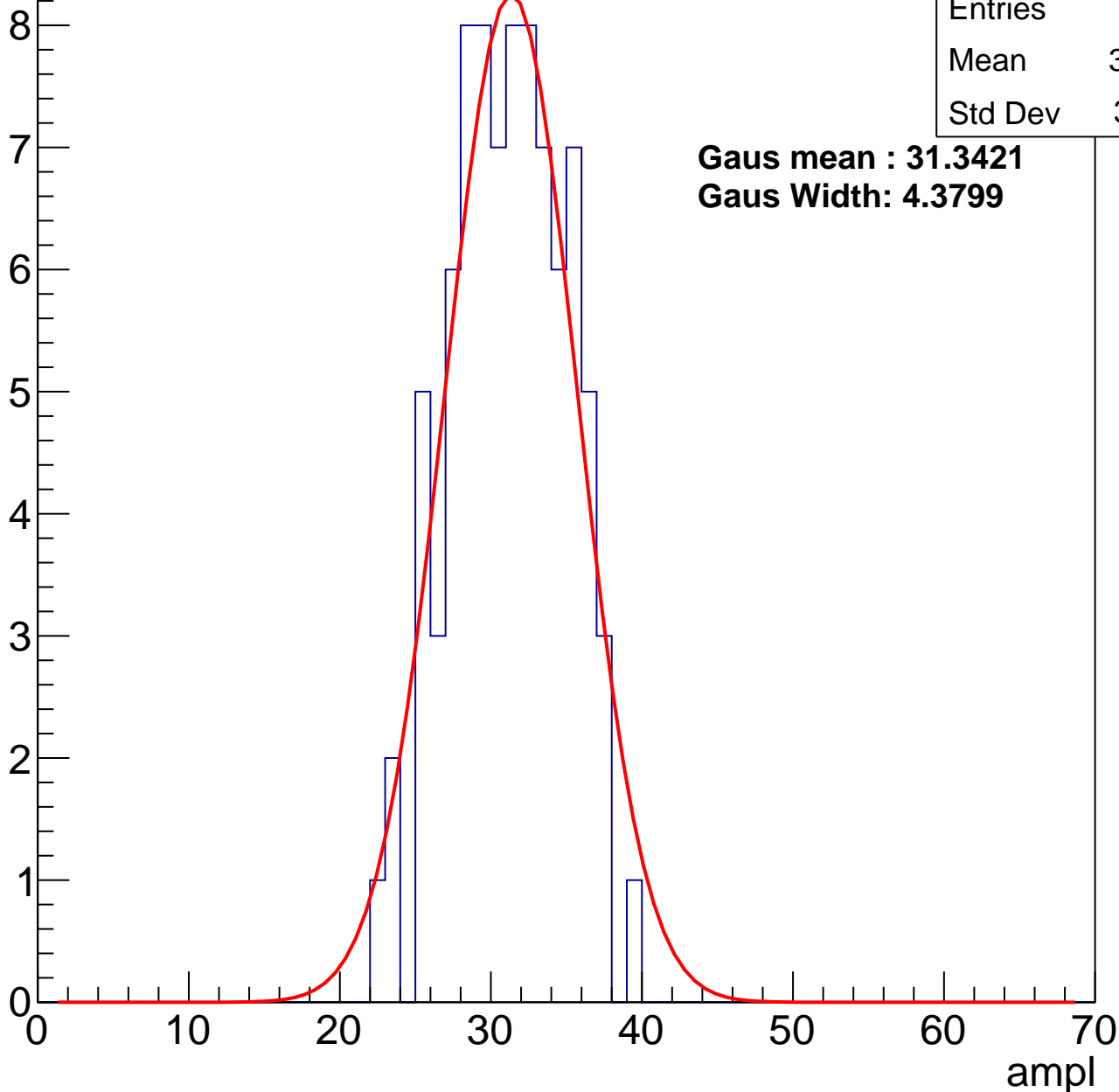
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 30.74 |
| Std Dev | 3.701 |

Gaus mean : 31.3421

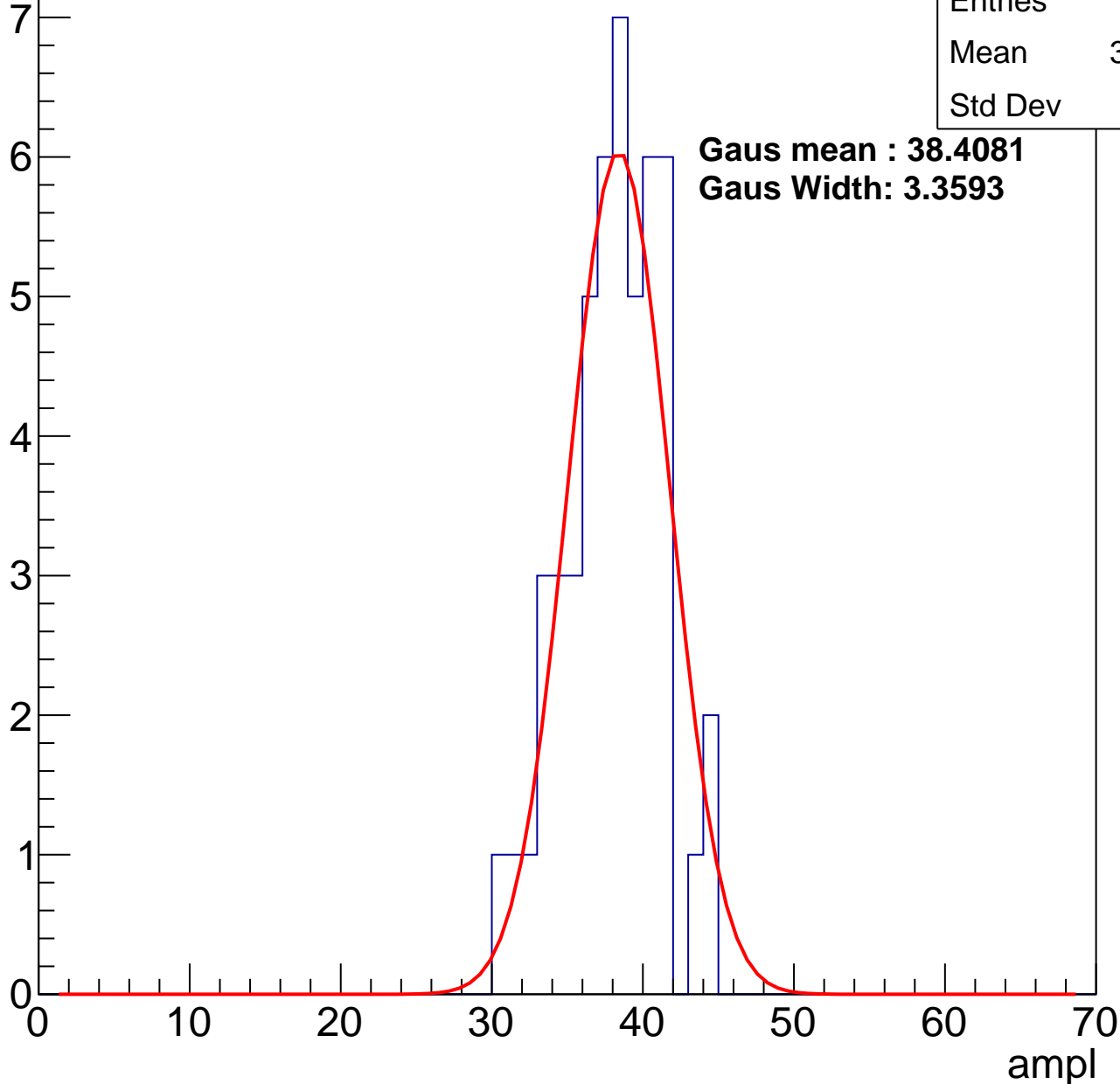
Gaus Width: 4.3799



B1L100S, U7-ch99, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch99, adc2

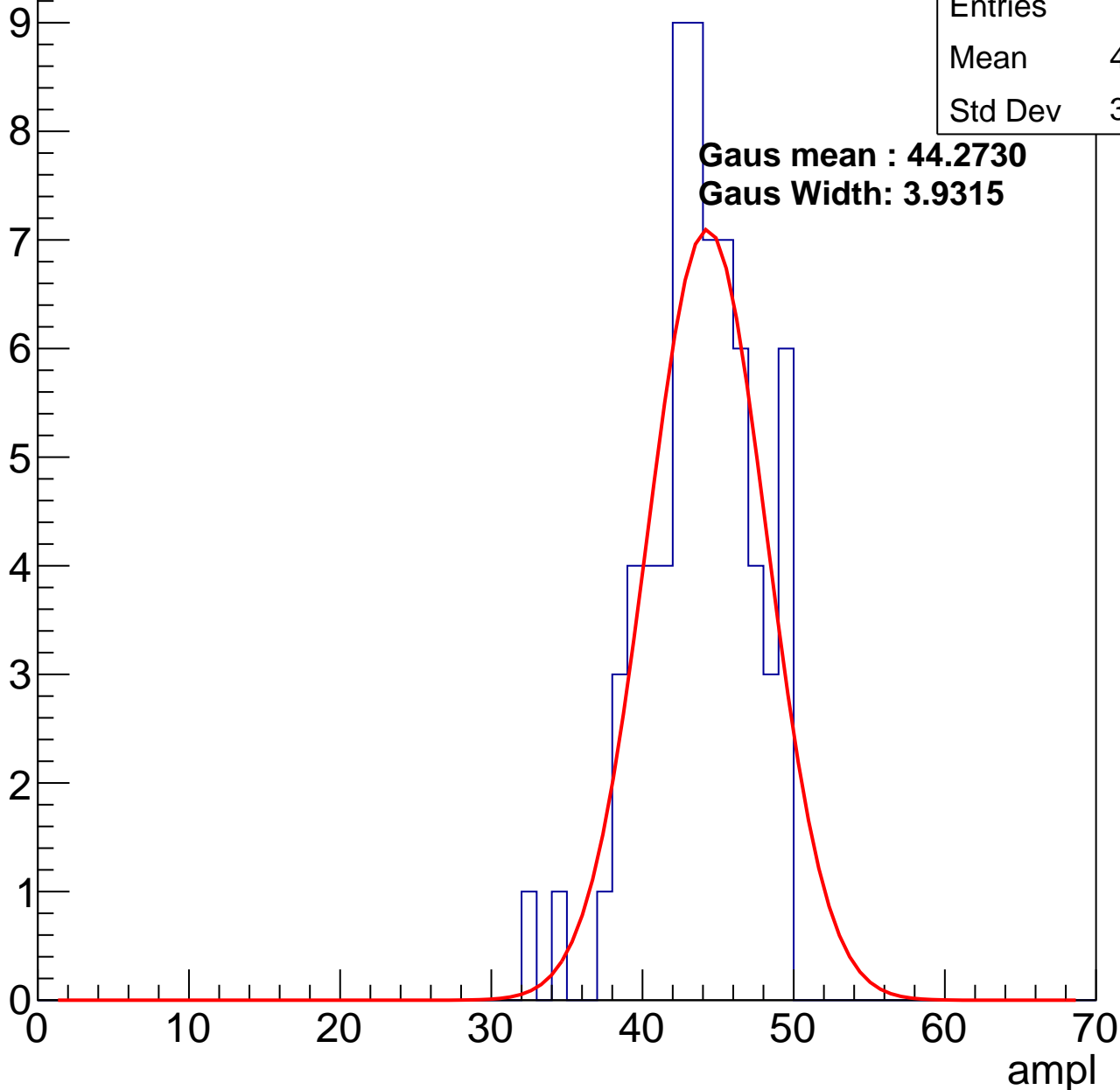
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.29 |
| Std Dev | 3.564 |

Gaus mean : 44.2730

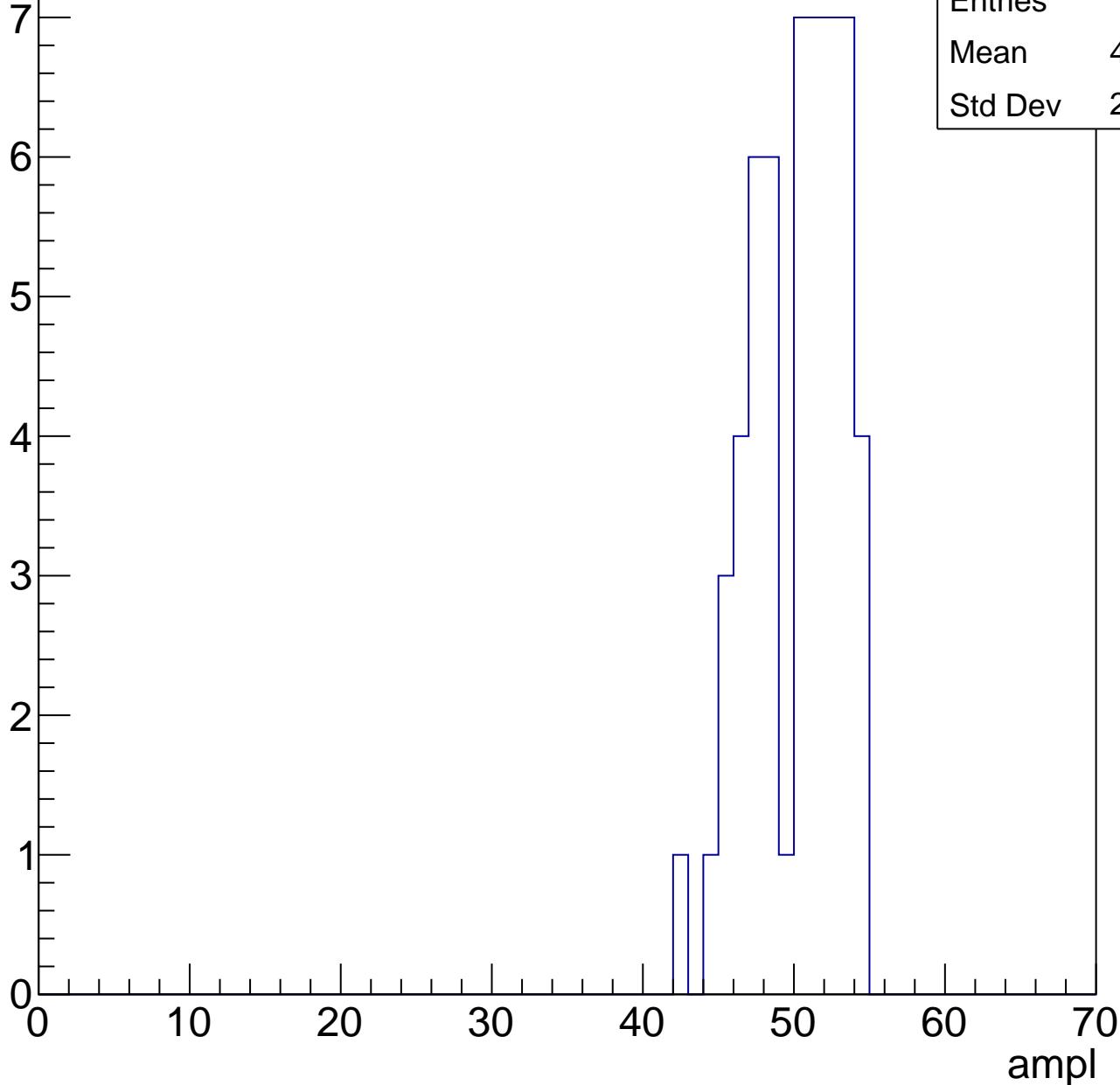
Gaus Width: 3.9315



B1L100S, U7-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

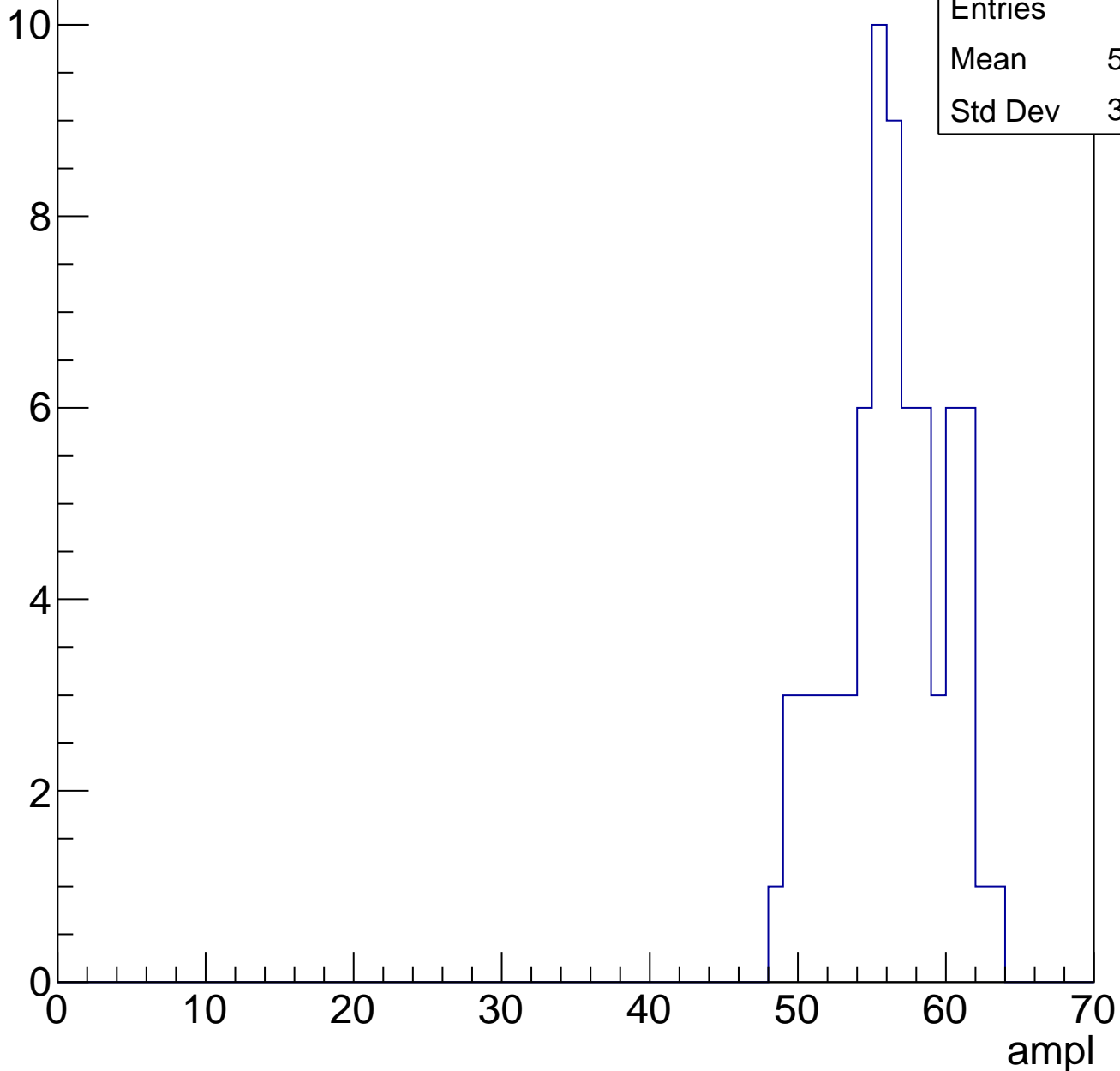


B1L100S, U7-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 70 |
| Mean | 55.84 |
| Std Dev | 3.564 |

Entry



B1L100S, U7-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.93 |
| Std Dev | 9.435 |

ampl

0

10

20

30

40

50

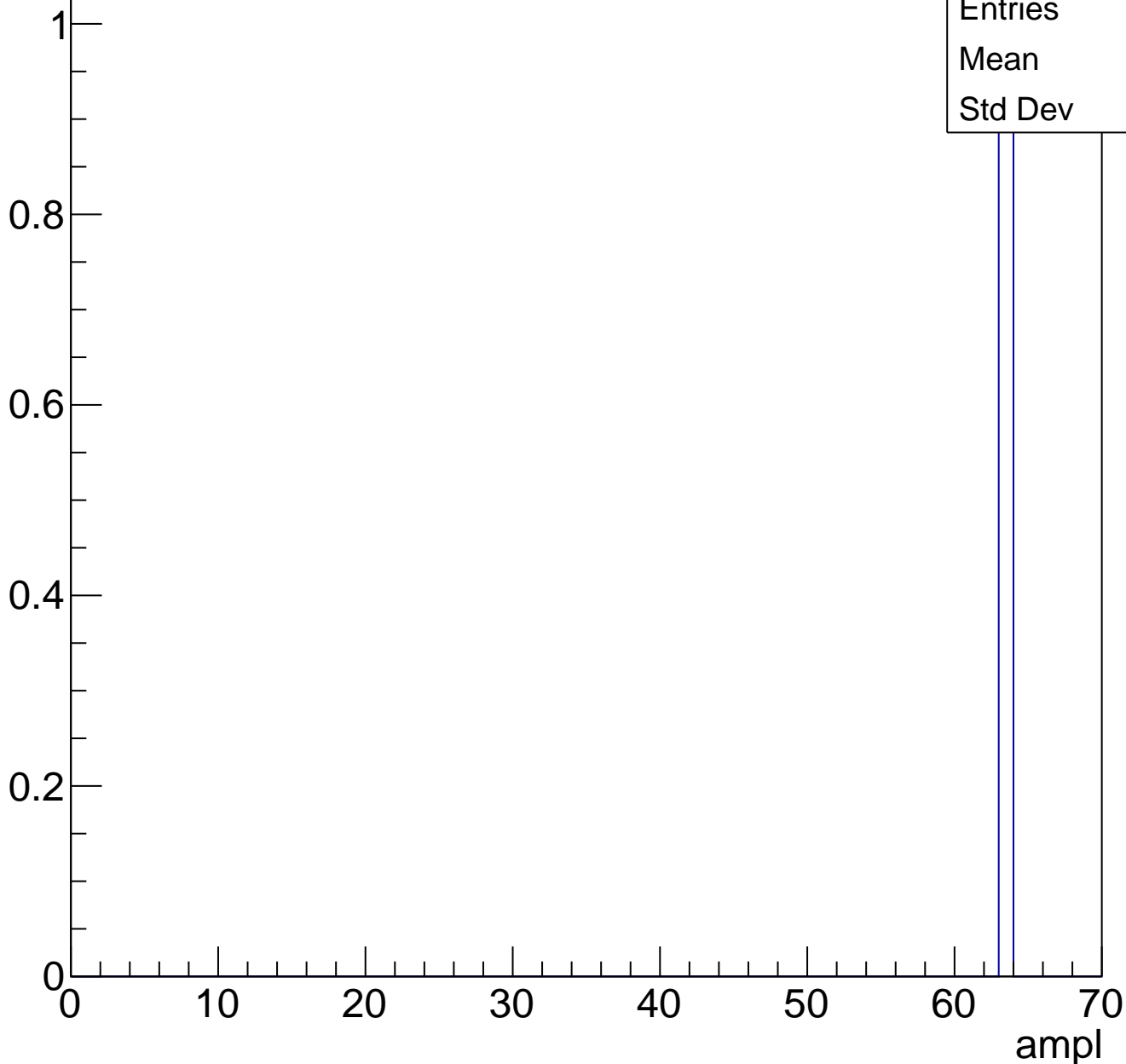
60

70

B1L100S, U7-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch100, adc0

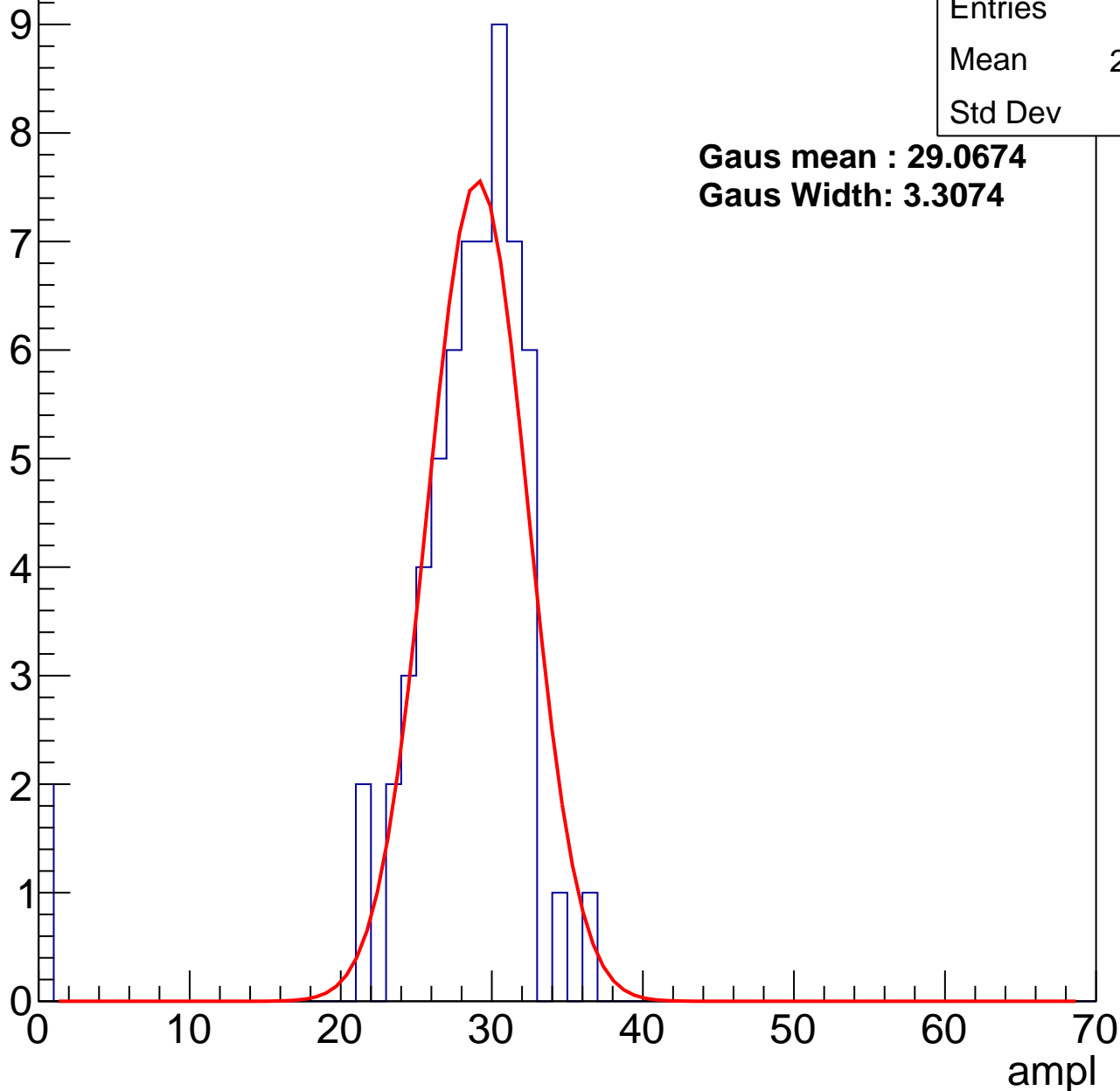
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 27.42 |
| Std Dev | 5.83 |

Gaus mean : 29.0674

Gaus Width: 3.3074

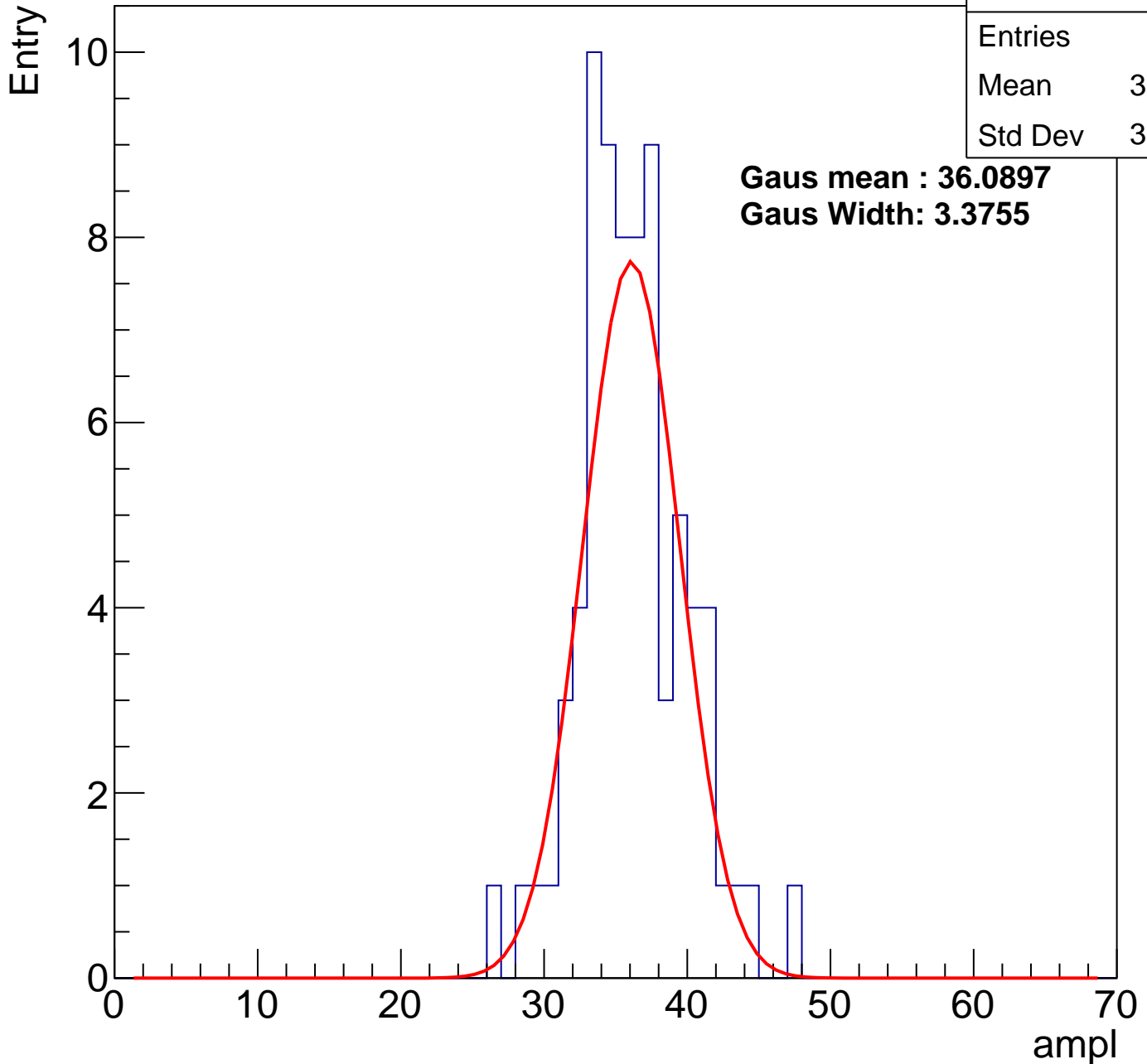


B1L100S, U7-ch100, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 35.73 |
| Std Dev | 3.682 |

Gaus mean : 36.0897
Gaus Width: 3.3755



B1L100S, U7-ch100, adc2

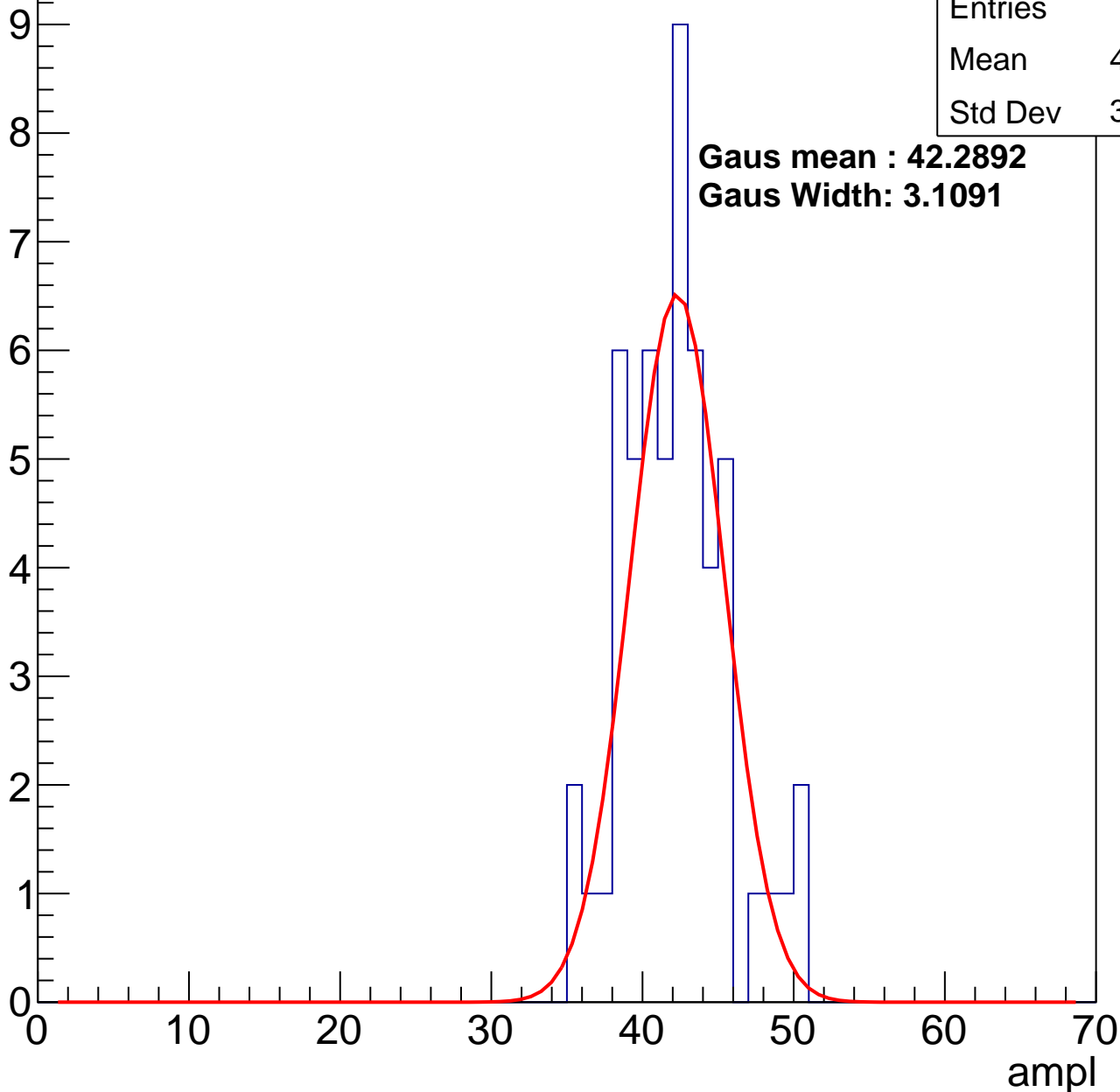
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 41.67 |
| Std Dev | 3.379 |

Gaus mean : 42.2892

Gaus Width: 3.1091

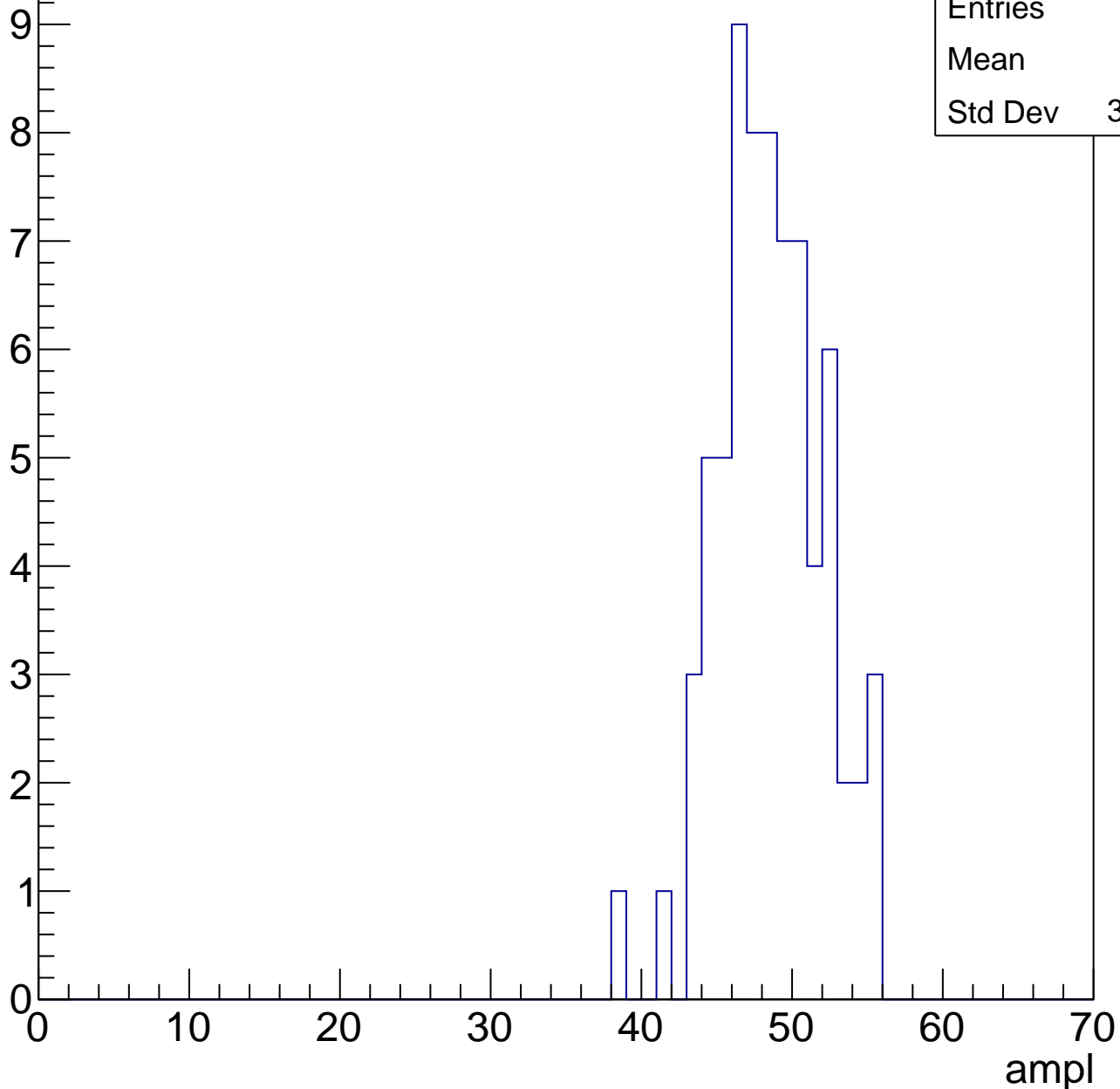


B1L100S, U7-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 48.1 |
| Std Dev | 3.416 |

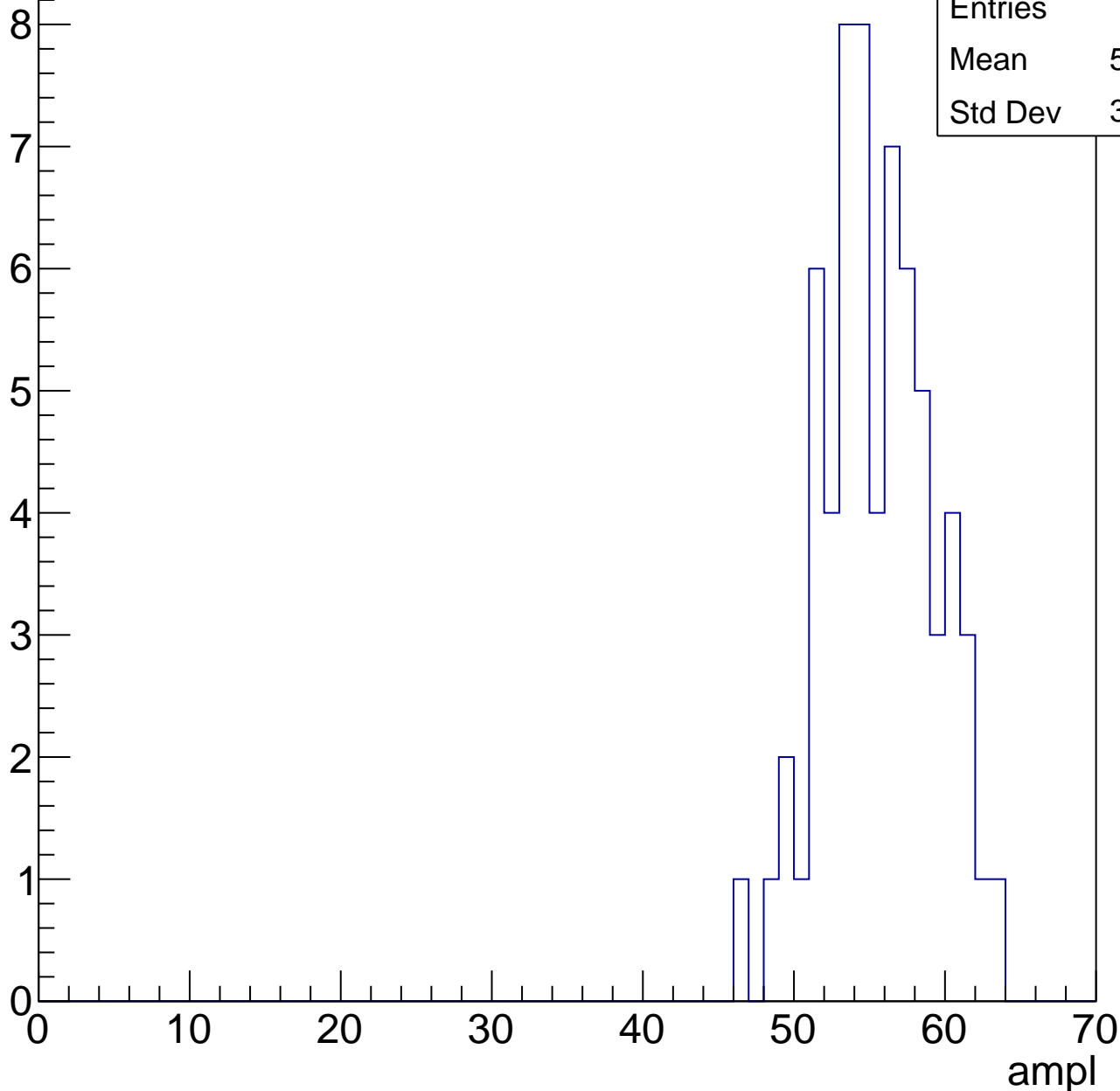


B1L100S, U7-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 55.09 |
| Std Dev | 3.602 |

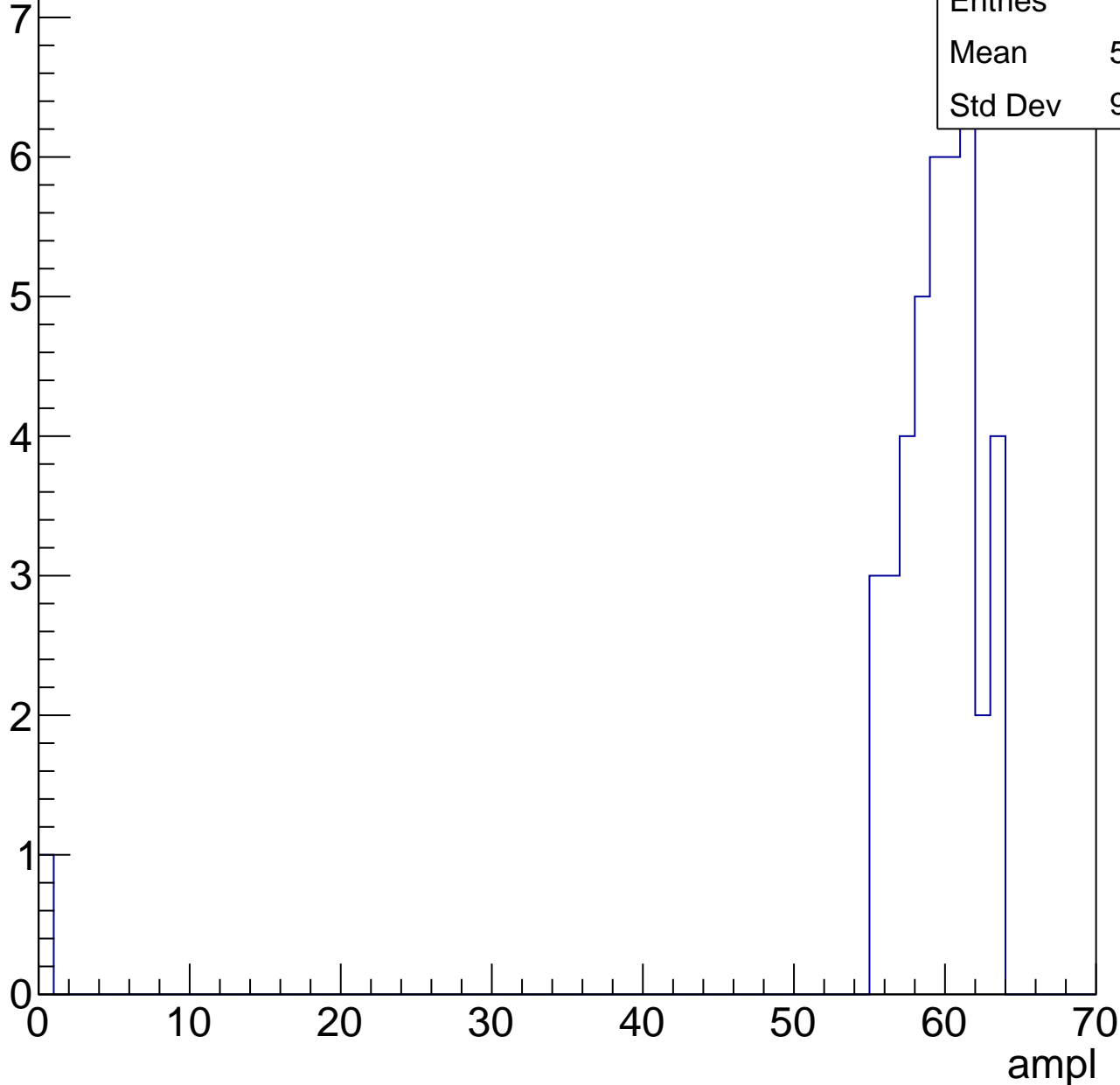


B1L100S, U7-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 57.76 |
| Std Dev | 9.409 |

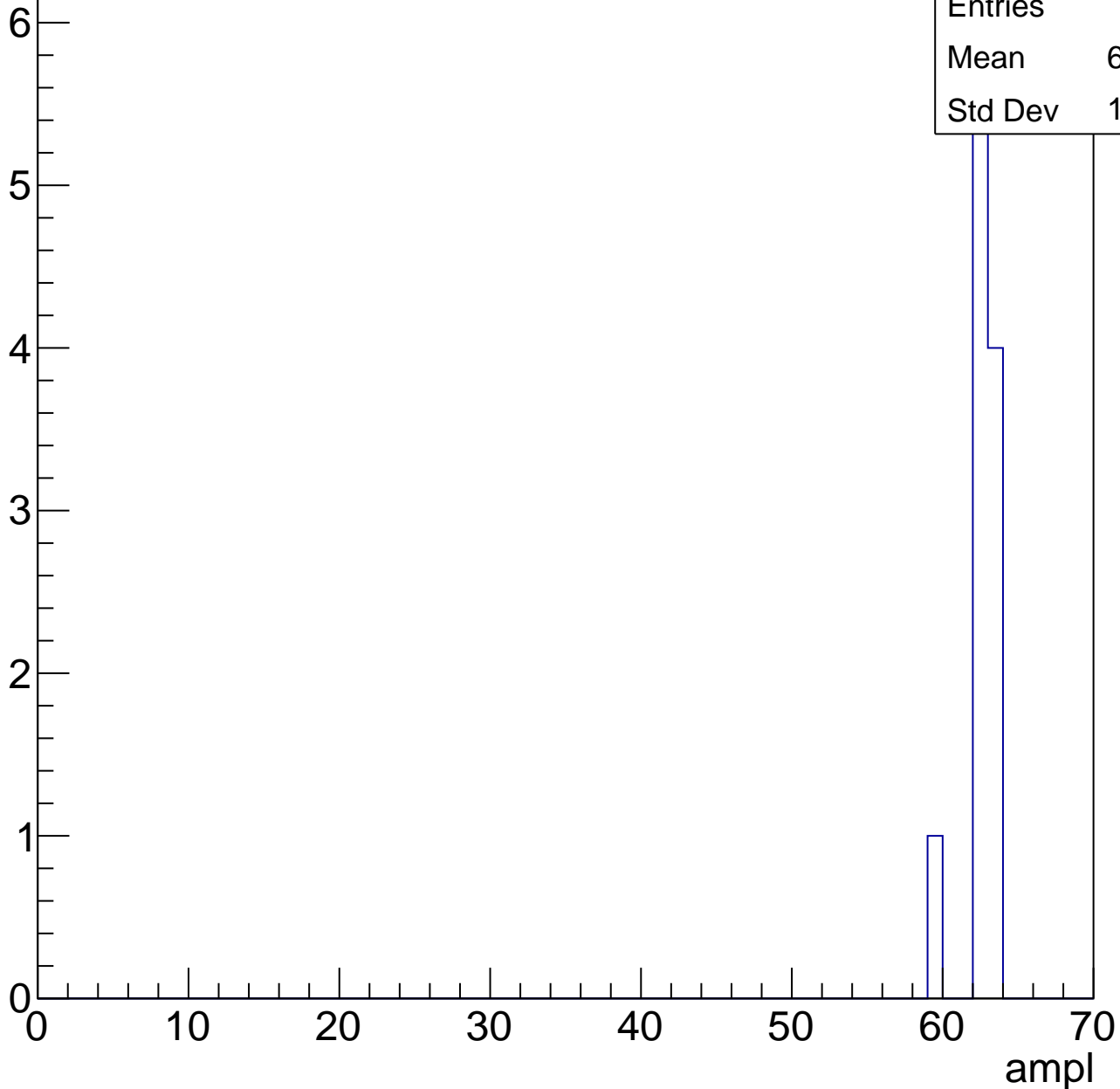


B1L100S, U7-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

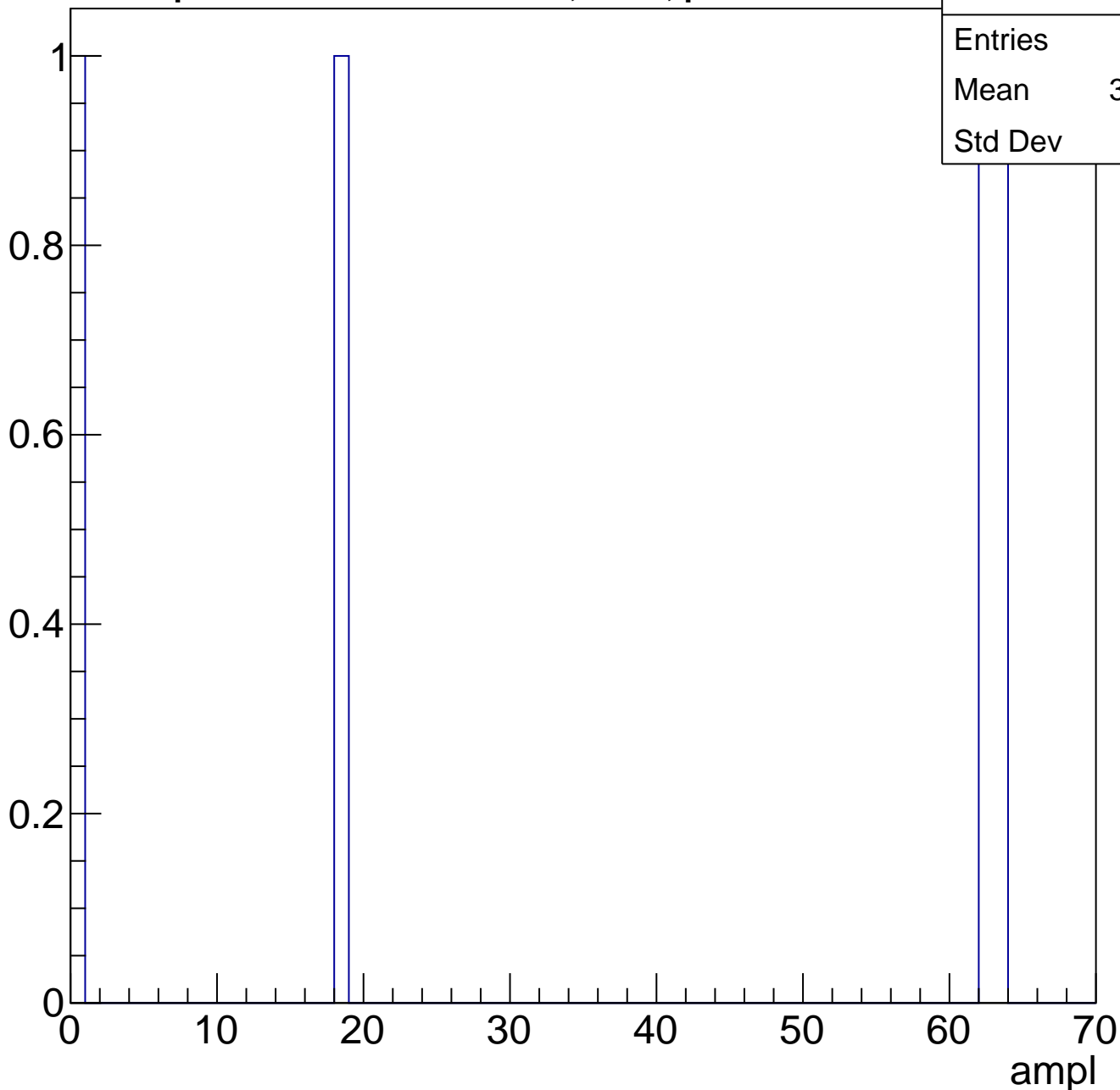
| | |
|---------|-------|
| Entries | 11 |
| Mean | 62.09 |
| Std Dev | 1.083 |



B1L100S, U7-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch101, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 30.03 |
| Std Dev | 3.388 |

Gaus mean : 30.3695

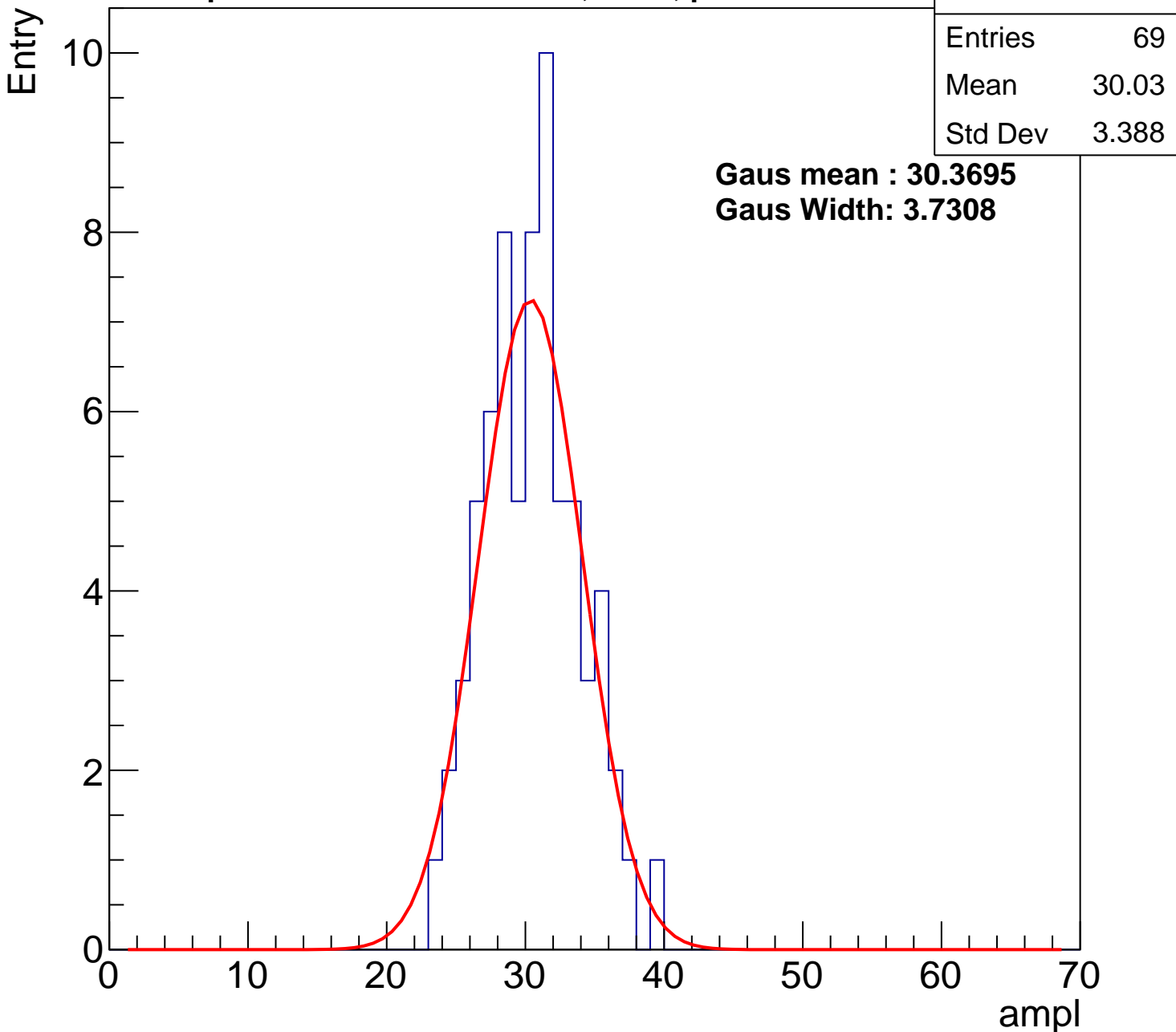
Gaus Width: 3.7308

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch101, adc1

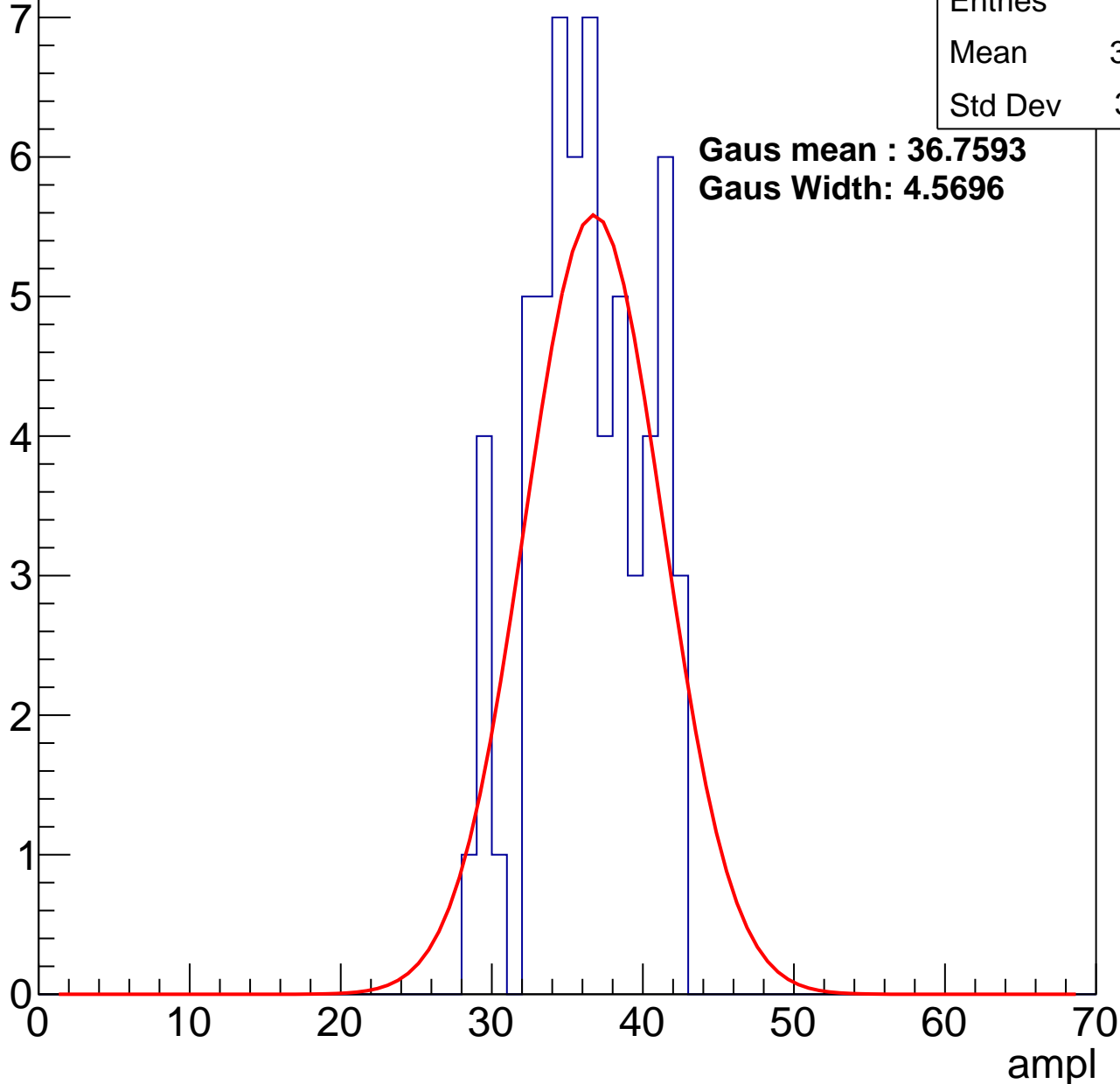
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.84 |
| Std Dev | 3.681 |

Gaus mean : 36.7593

Gaus Width: 4.5696



B1L100S, U7-ch101, adc2

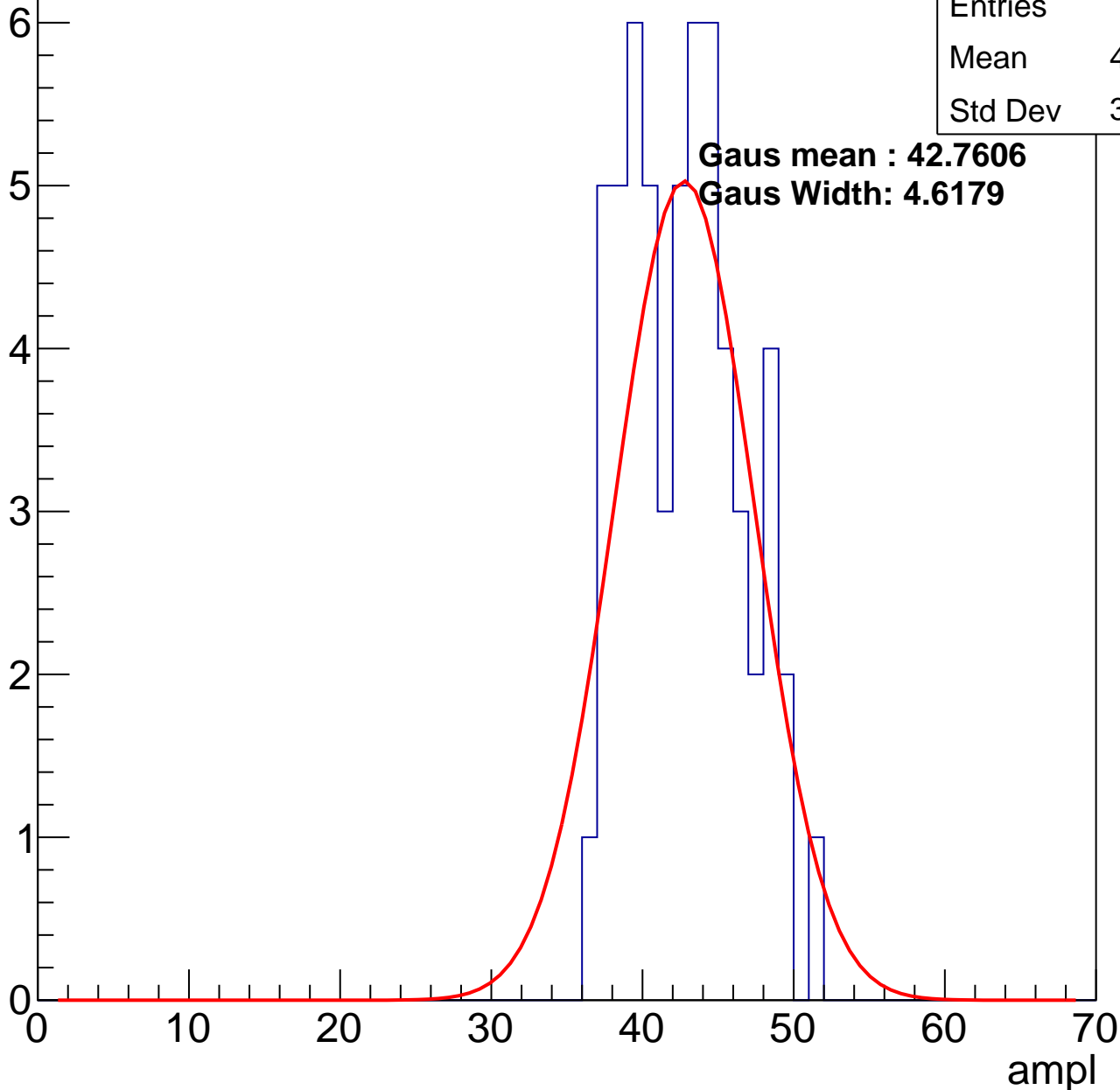
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 42.29 |
| Std Dev | 3.714 |

Gaus mean : 42.7606

Gaus Width: 4.6179

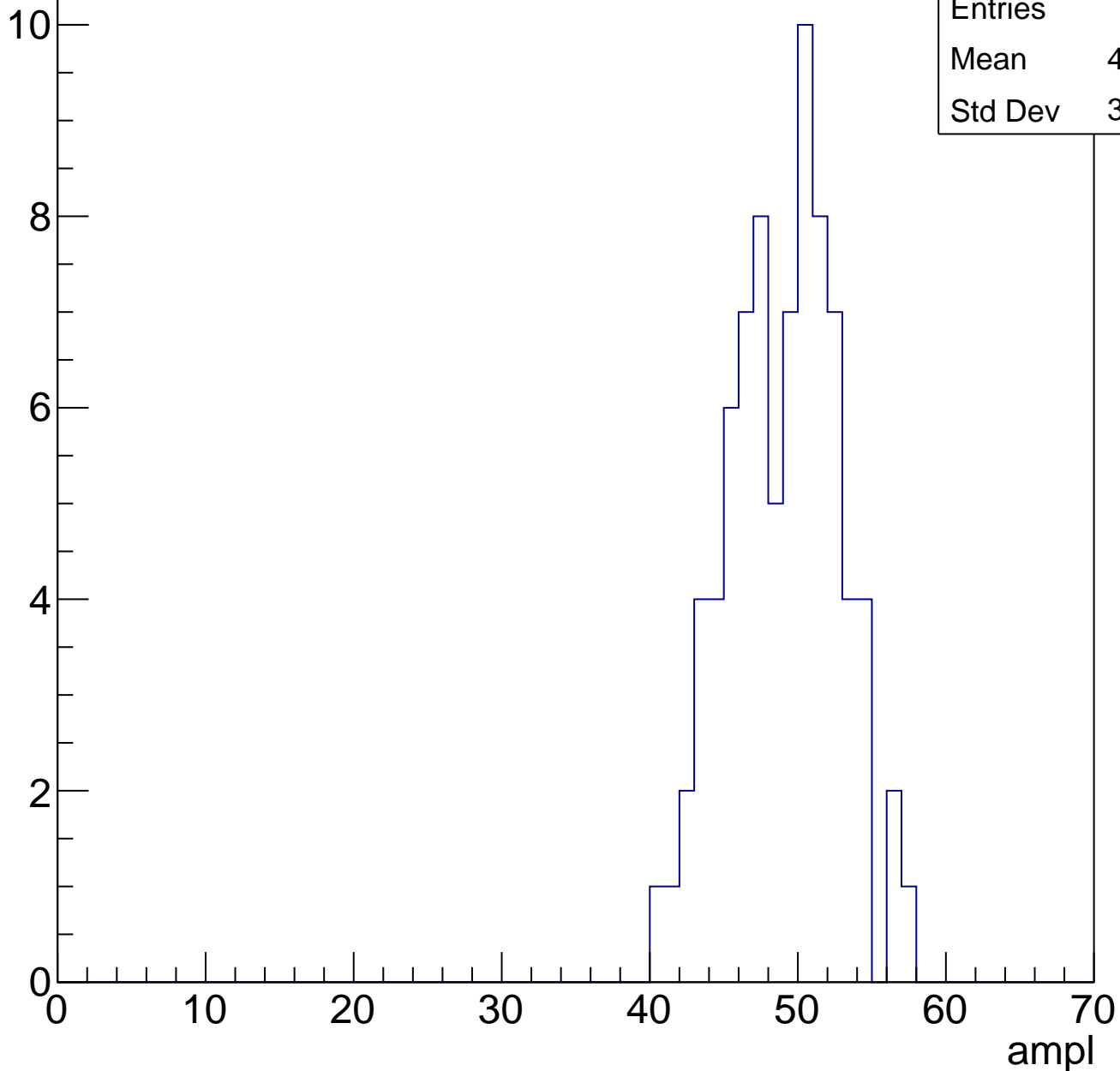


B1L100S, U7-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 48.56 |
| Std Dev | 3.682 |

Entry

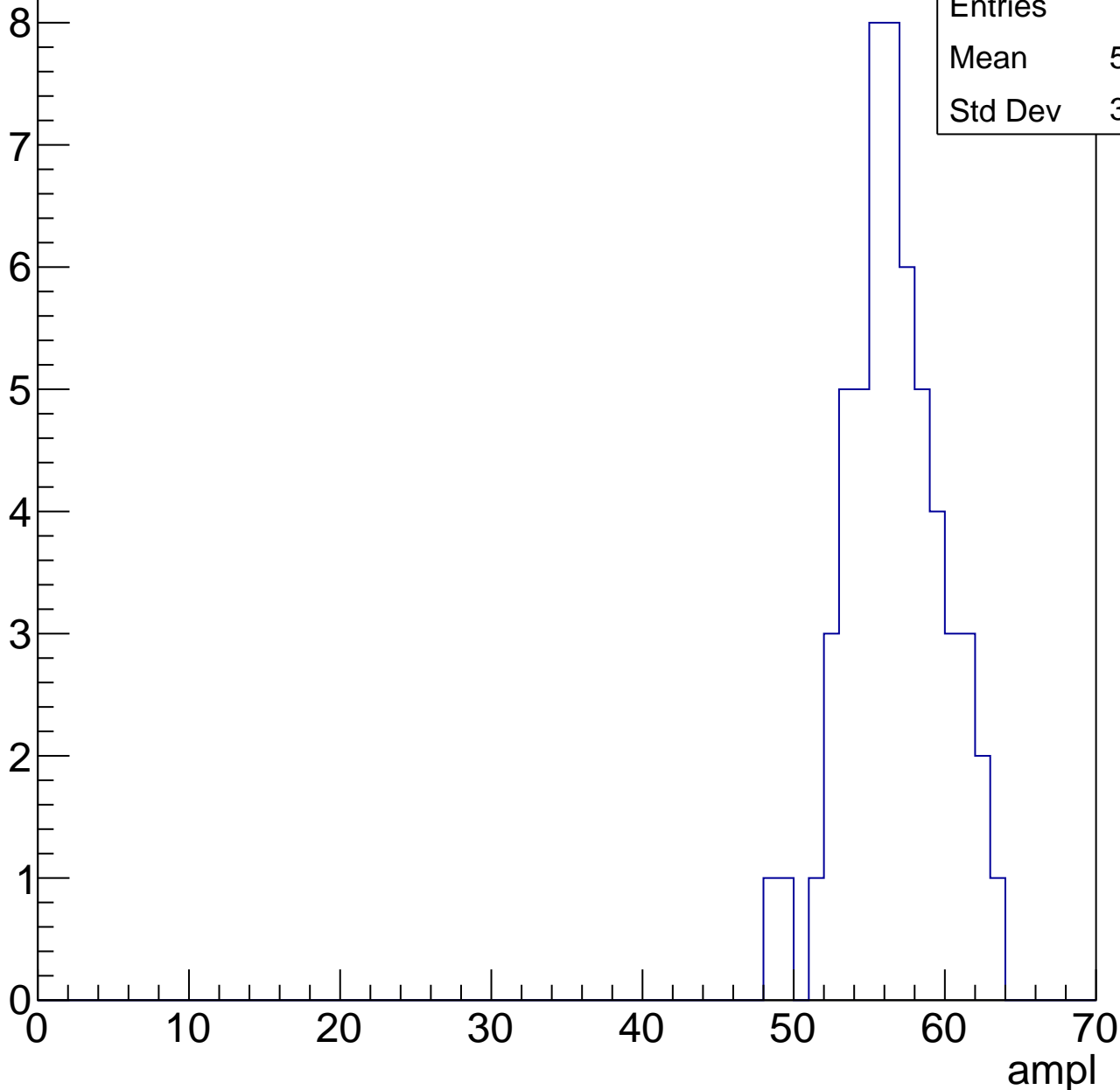


B1L100S, U7-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

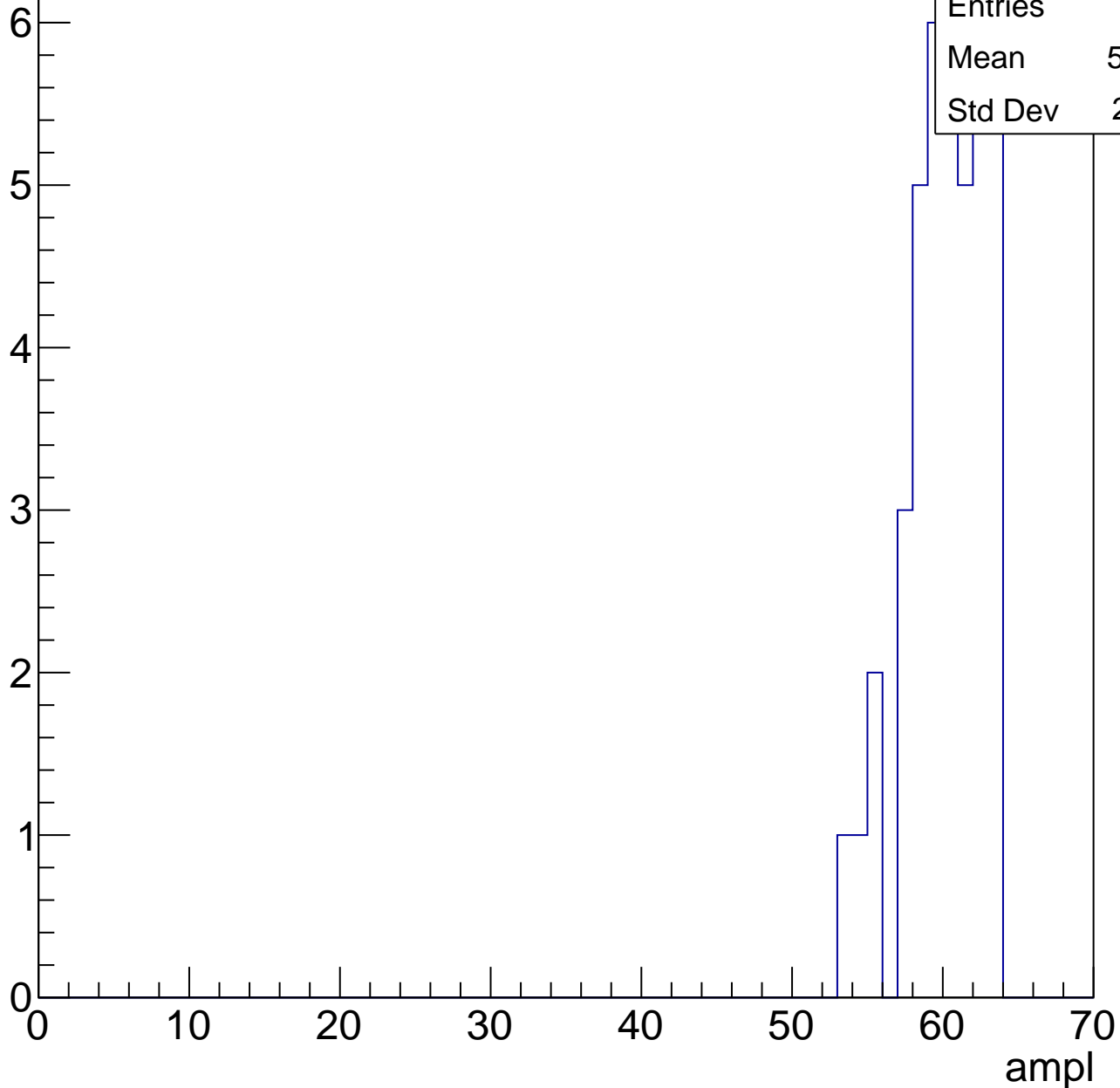
| | |
|---------|-------|
| Entries | 56 |
| Mean | 56.16 |
| Std Dev | 3.172 |



B1L100S, U7-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

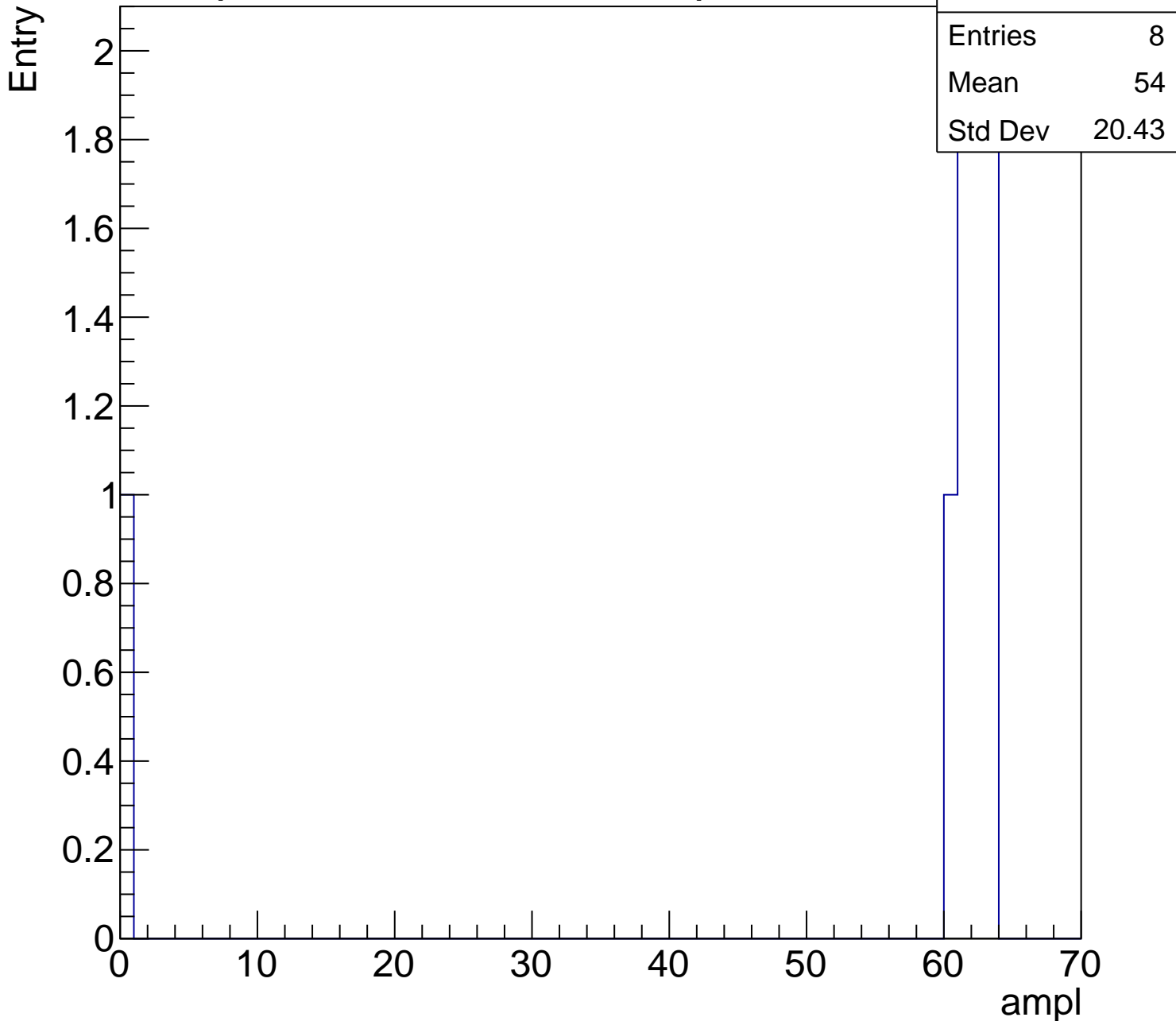
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54 |
| Std Dev | 20.43 |

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B1L100S, U7-ch102, adc0

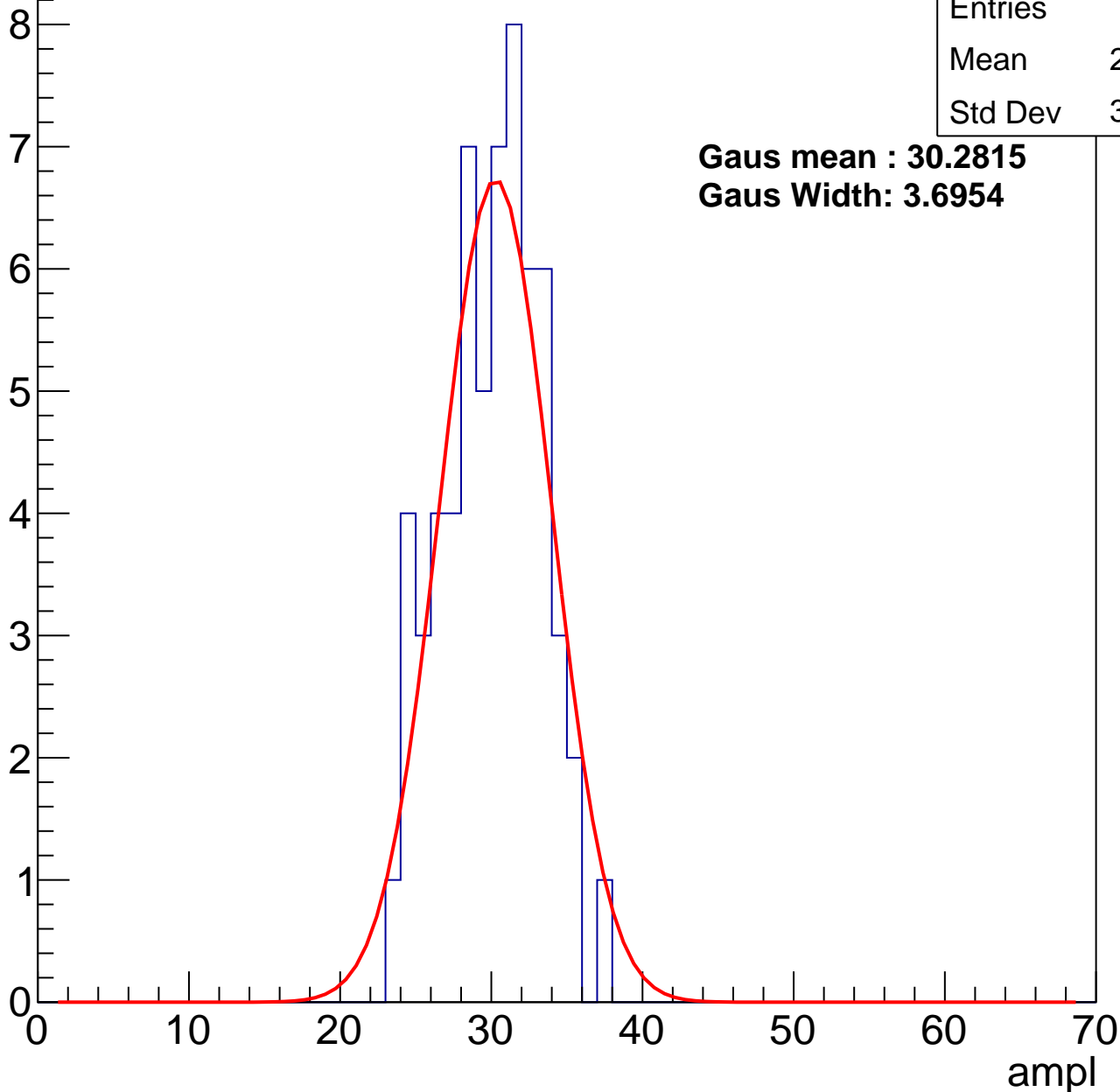
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.57 |
| Std Dev | 3.196 |

Gaus mean : 30.2815

Gaus Width: 3.6954



B1L100S, U7-ch102, adc1

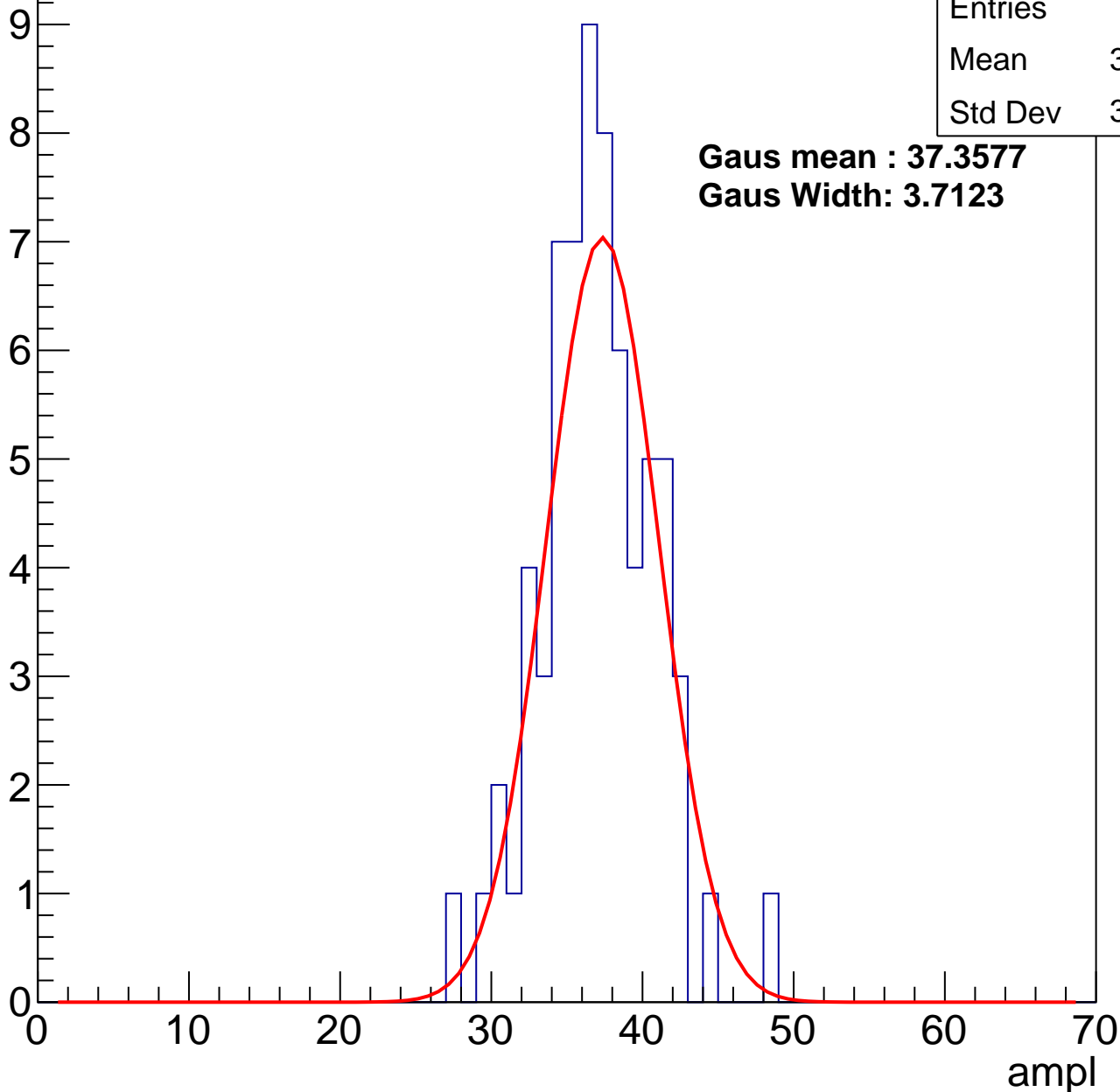
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.53 |
| Std Dev | 3.684 |

Gaus mean : 37.3577

Gaus Width: 3.7123



B1L100S, U7-ch102, adc2

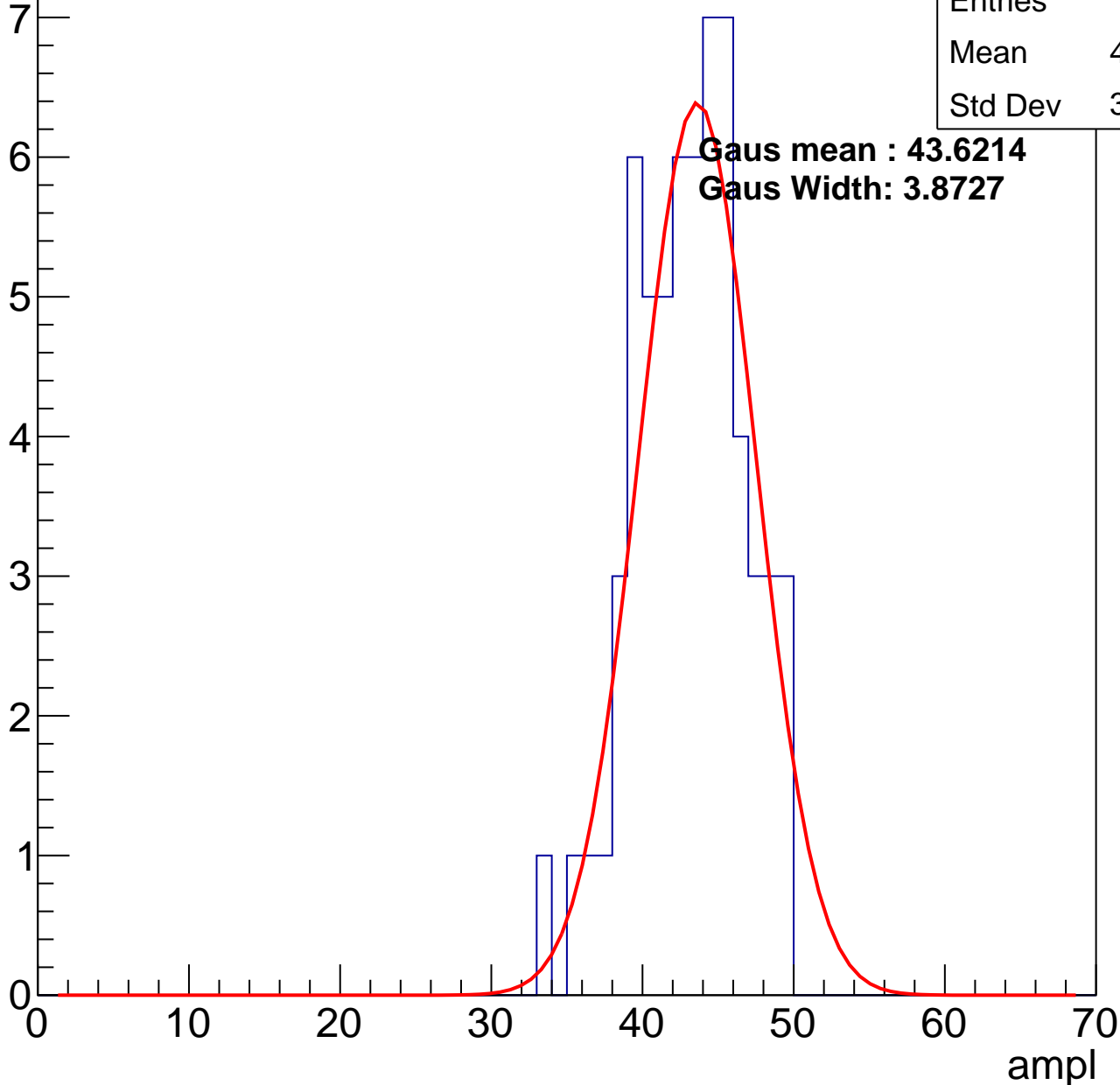
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.63 |
| Std Dev | 3.557 |

Gaus mean : 43.6214

Gaus Width: 3.8727

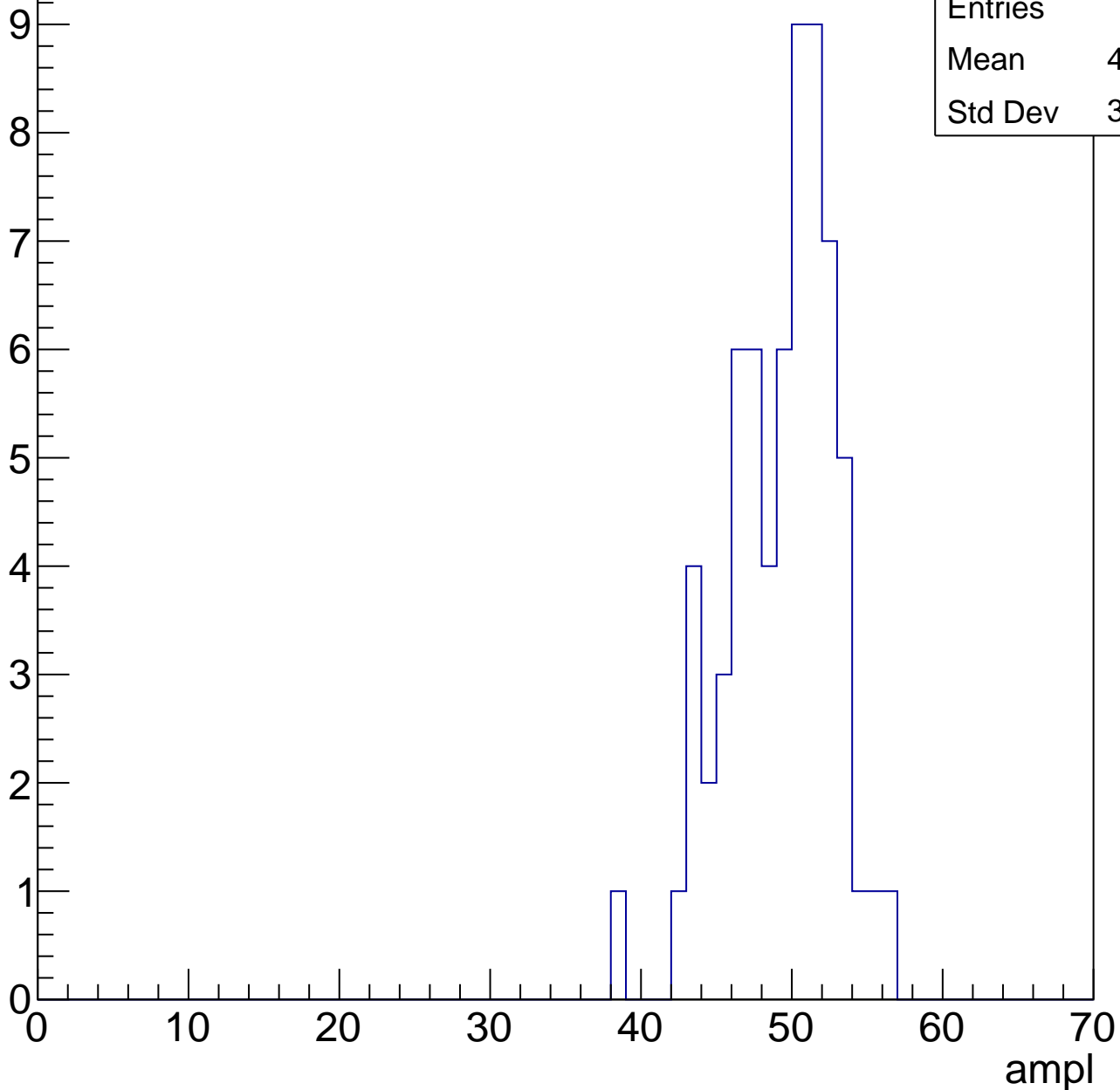


B1L100S, U7-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

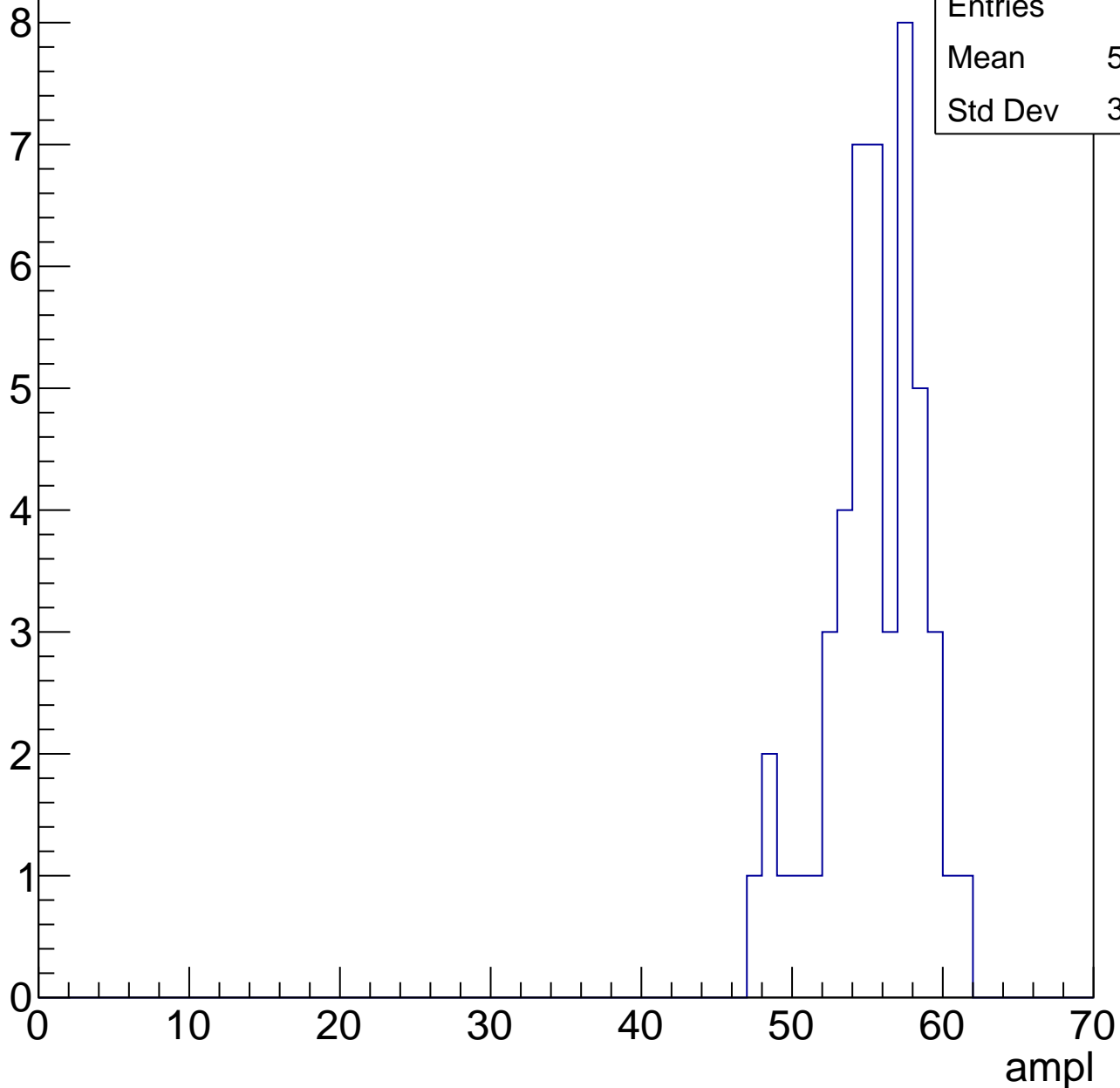
| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.82 |
| Std Dev | 3.459 |



B1L100S, U7-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

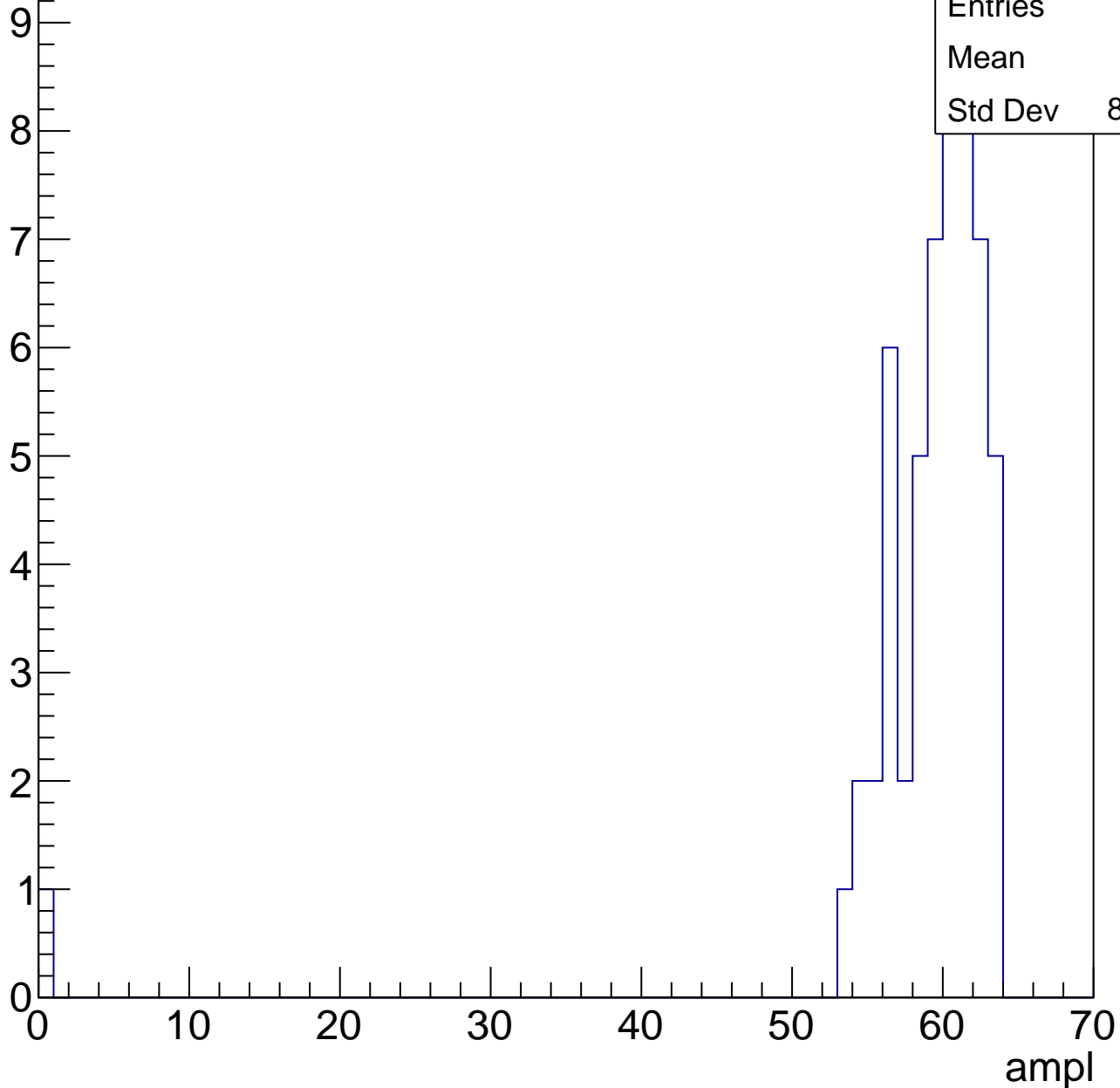
Entry



B1L100S, U7-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

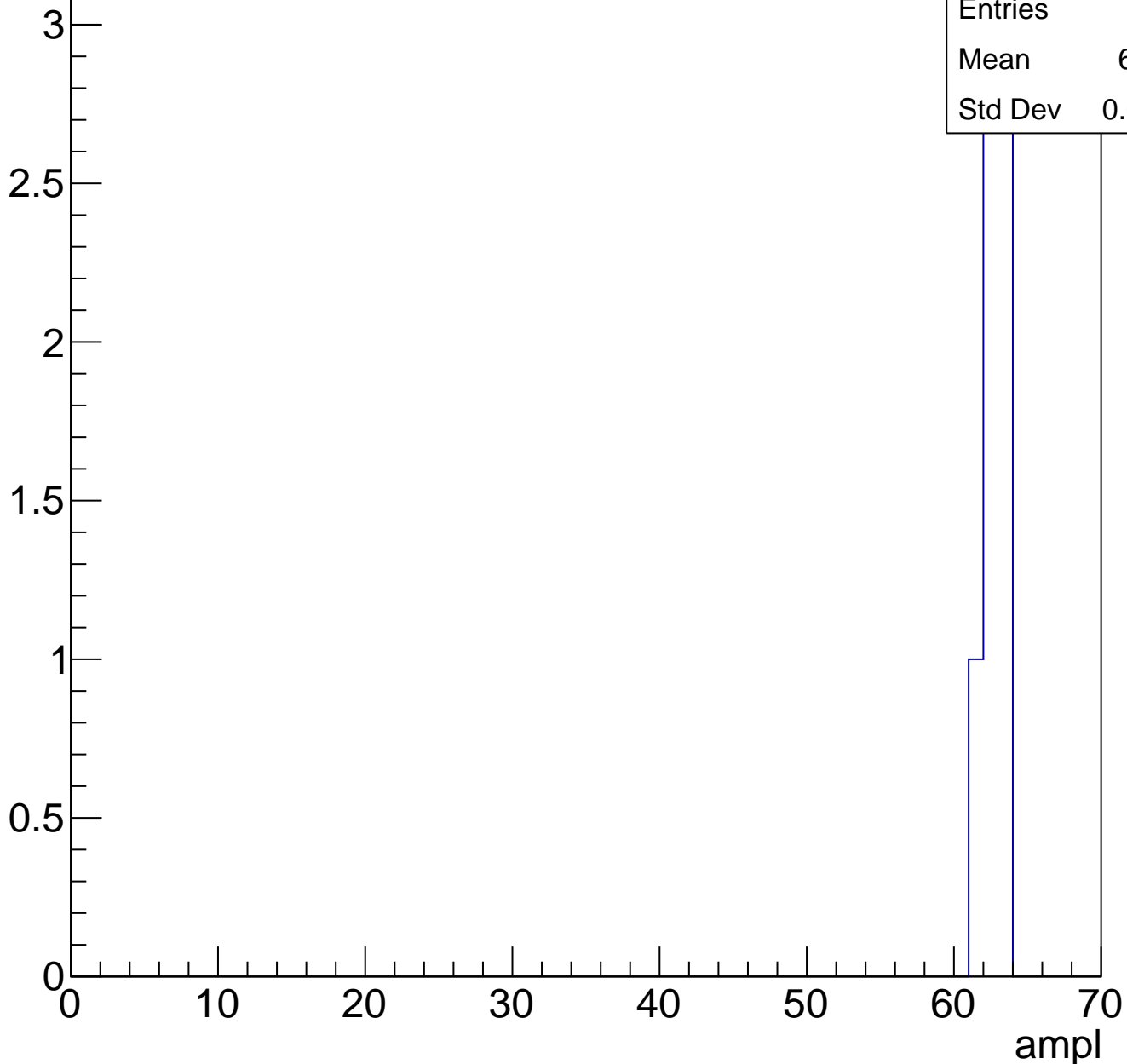
Entry



B1L100S, U7-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch103, adc0

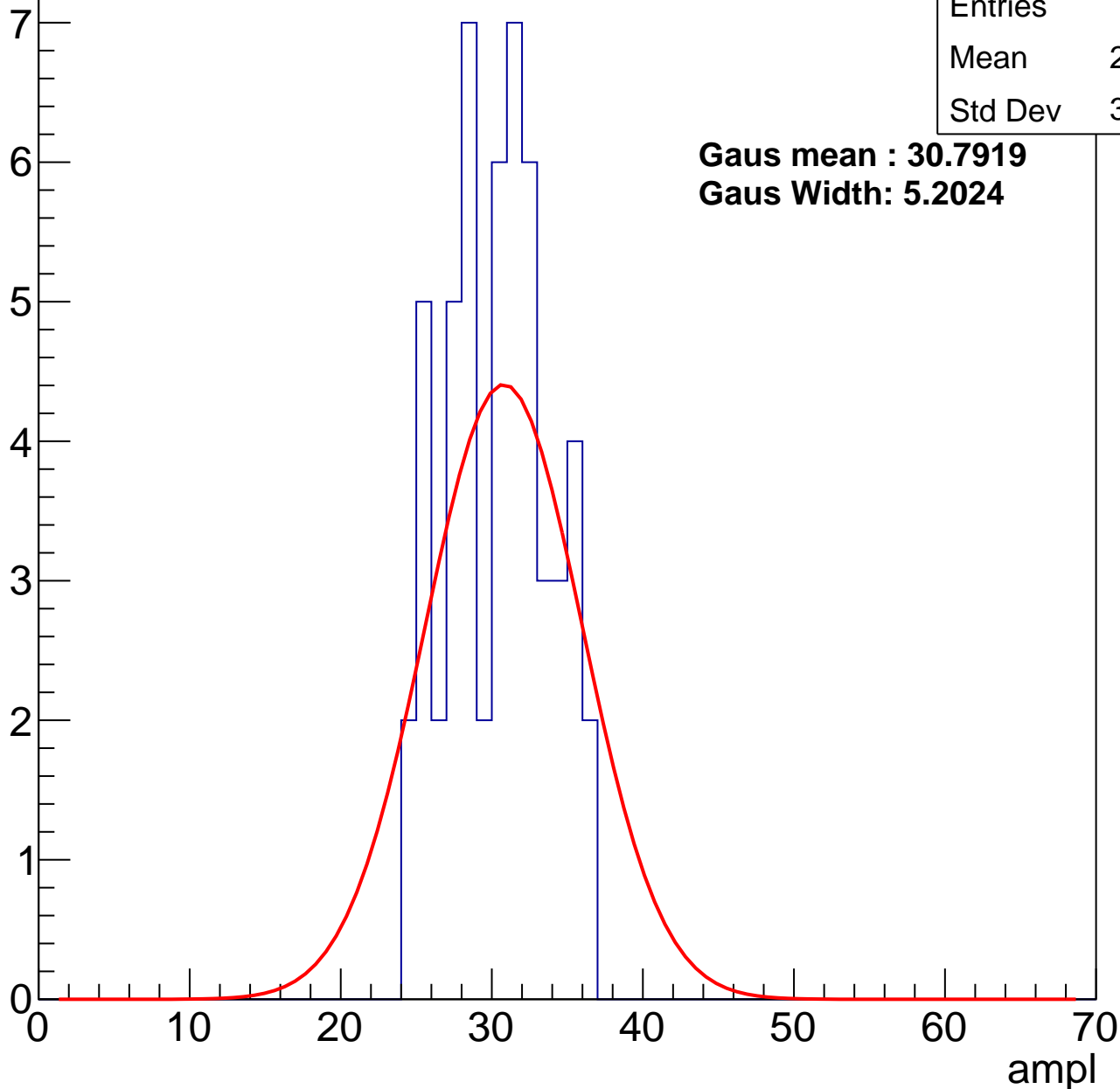
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 29.93 |
| Std Dev | 3.282 |

Gaus mean : 30.7919

Gaus Width: 5.2024



B1L100S, U7-ch103, adc1

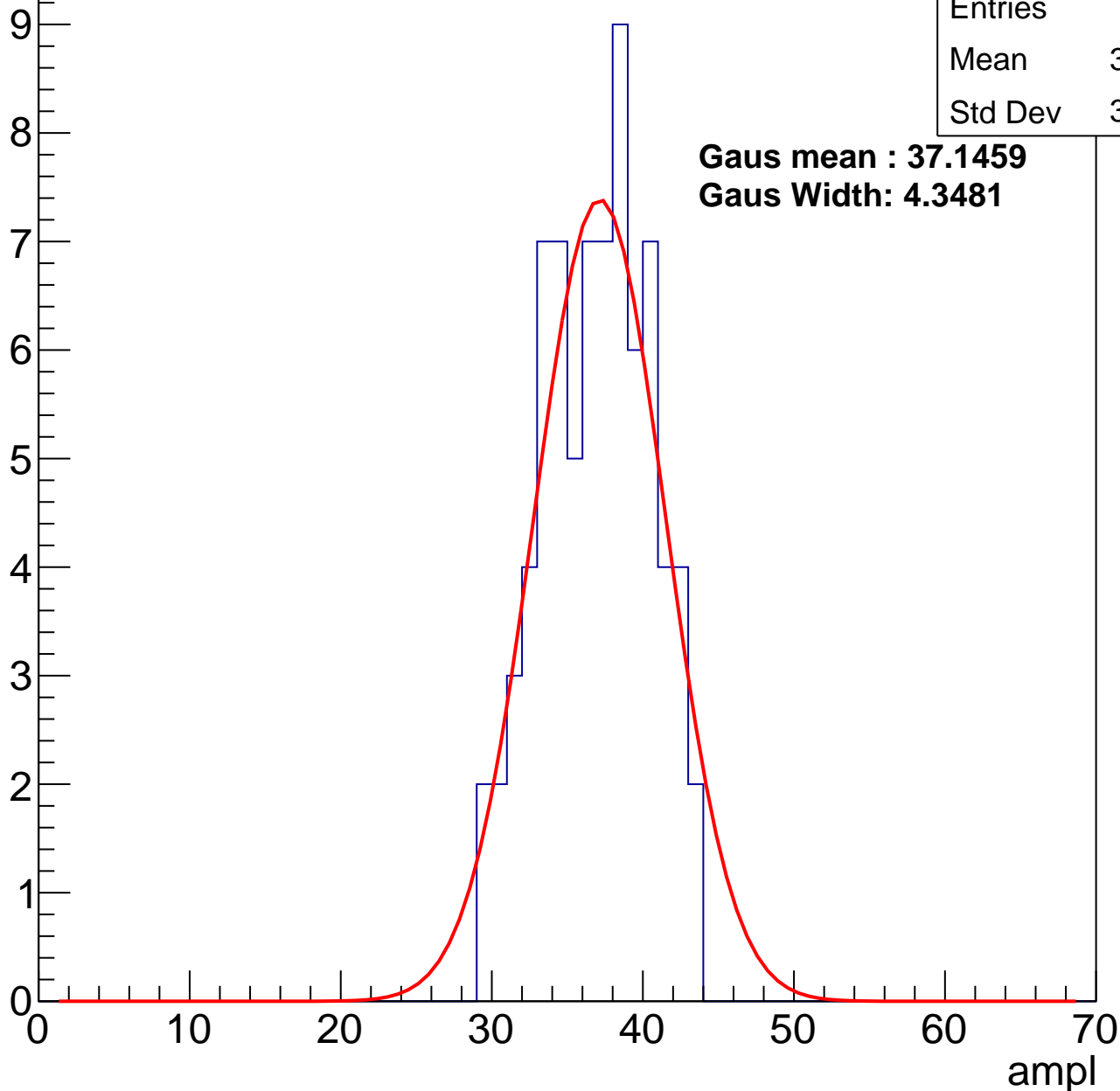
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.42 |
| Std Dev | 3.522 |

Gaus mean : 37.1459

Gaus Width: 4.3481



B1L100S, U7-ch103, adc2

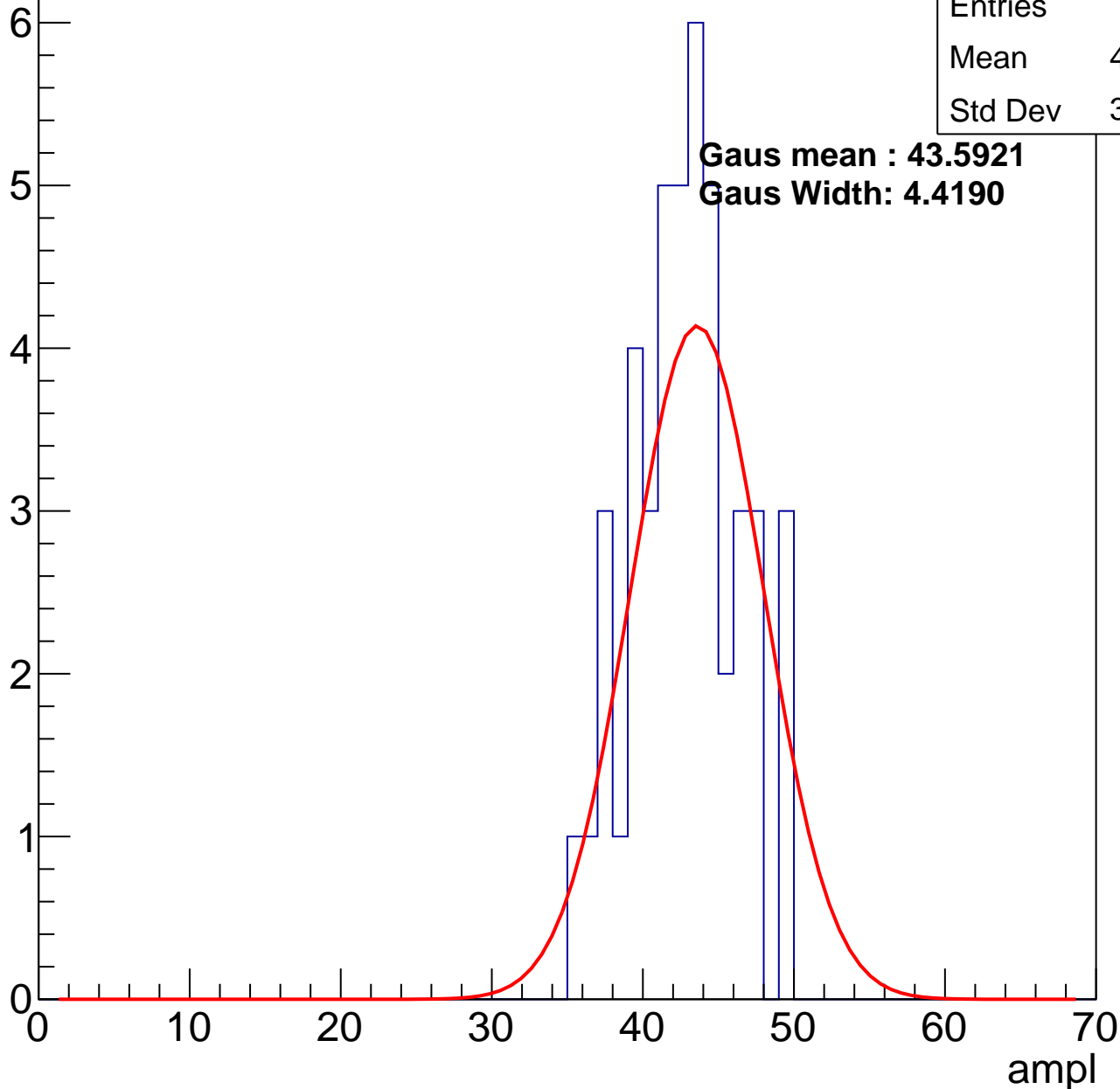
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 42.33 |
| Std Dev | 3.458 |

Gaus mean : 43.5921

Gaus Width: 4.4190



B1L100S, U7-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

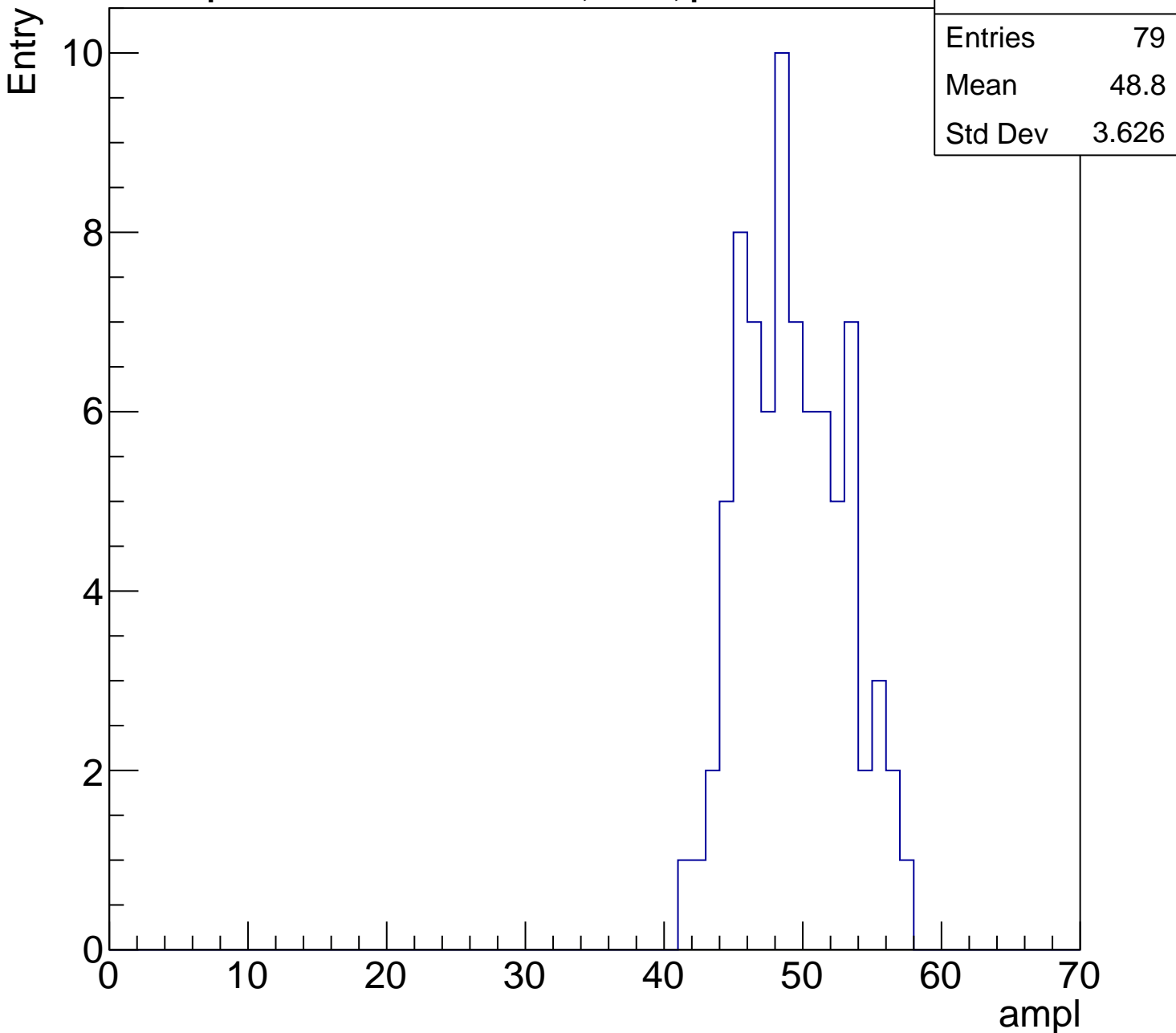
| | |
|---------|-------|
| Entries | 79 |
| Mean | 48.8 |
| Std Dev | 3.626 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

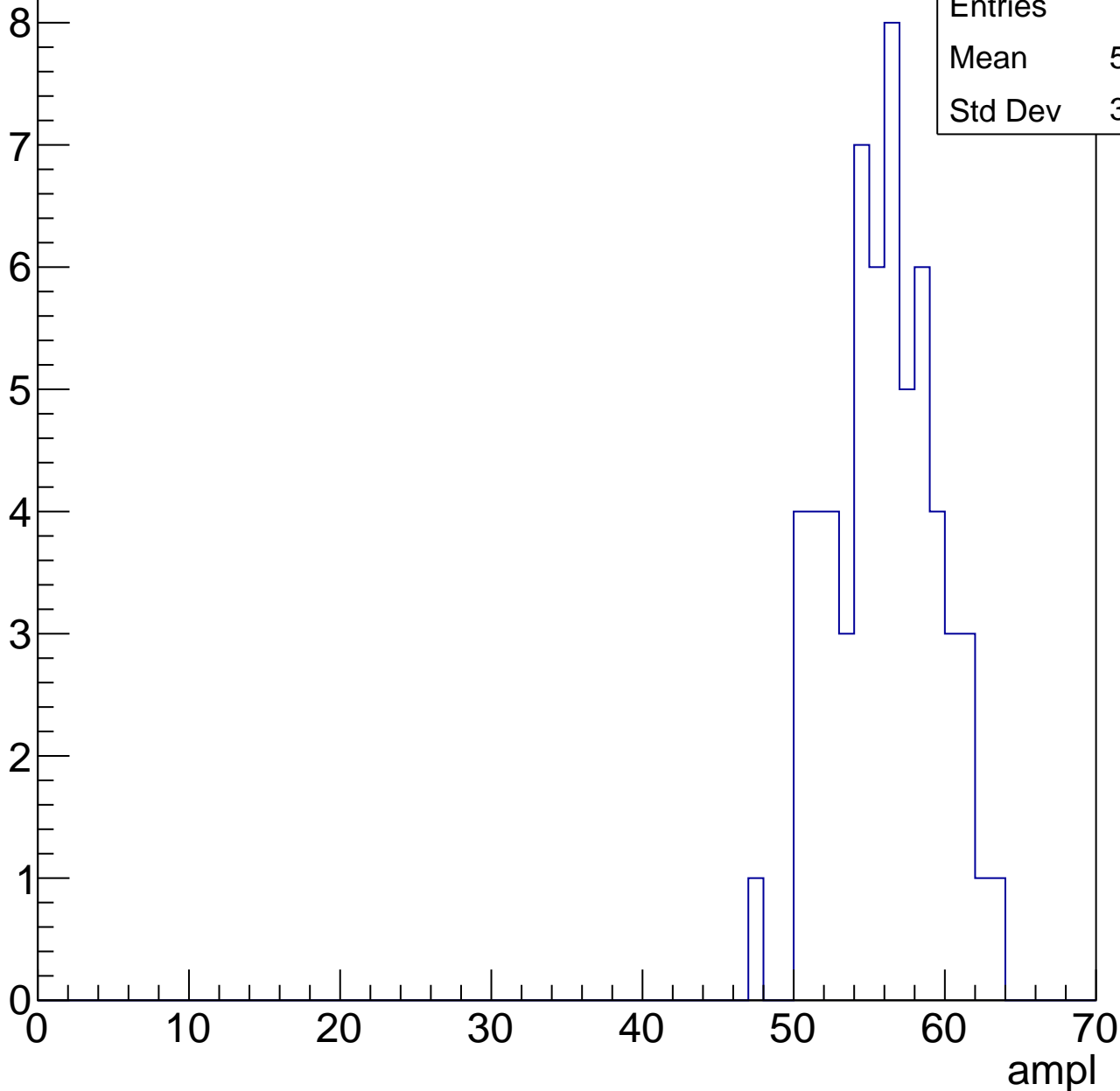


B1L100S, U7-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 55.52 |
| Std Dev | 3.437 |



B1L100S, U7-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.71 |
| Std Dev | 2.443 |

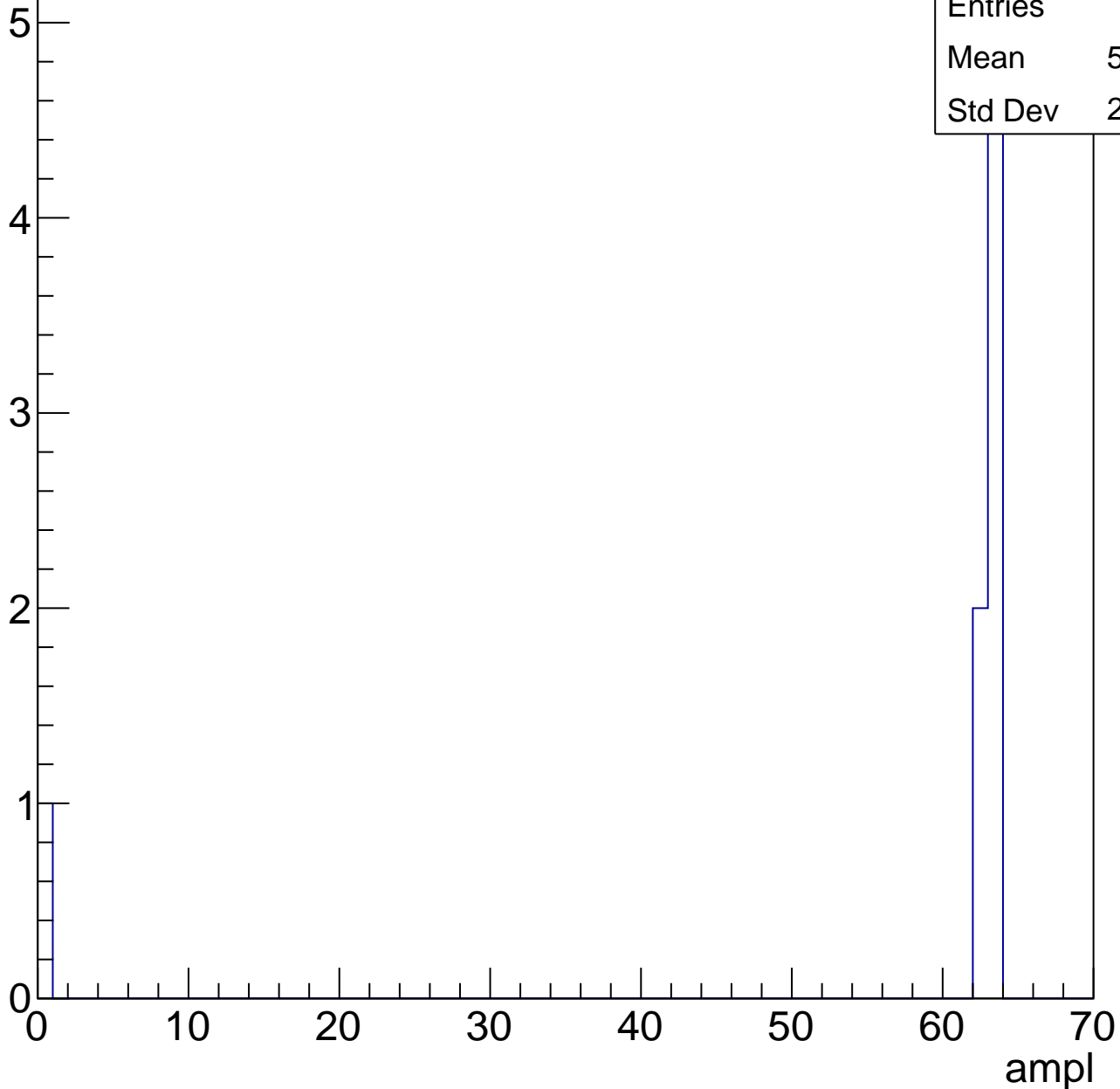
ampl

B1L100S, U7-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54.88 |
| Std Dev | 20.75 |



B1L100S, U7-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch104, adc0

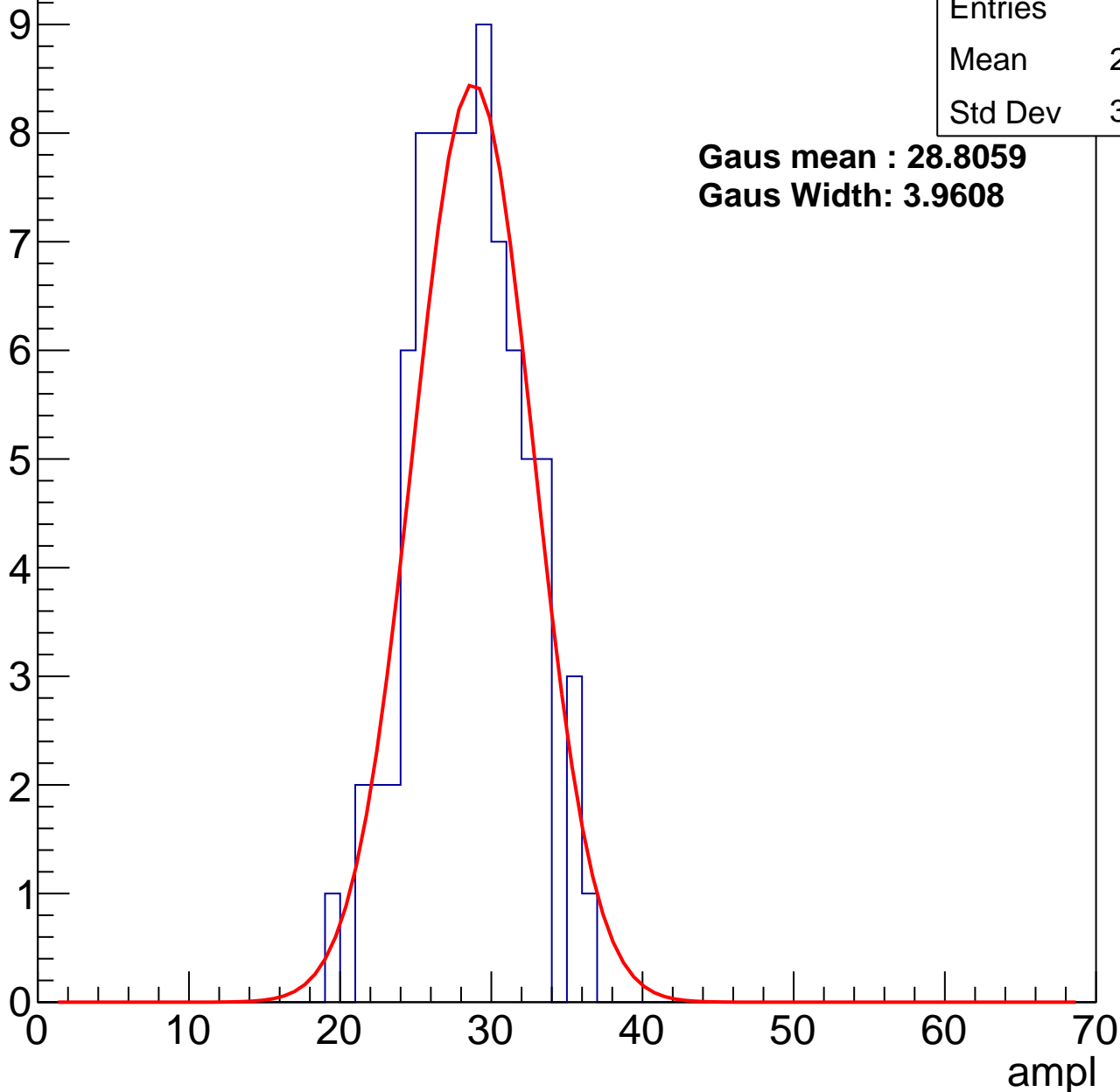
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 27.98 |
| Std Dev | 3.542 |

Gaus mean : 28.8059

Gaus Width: 3.9608



B1L100S, U7-ch104, adc1

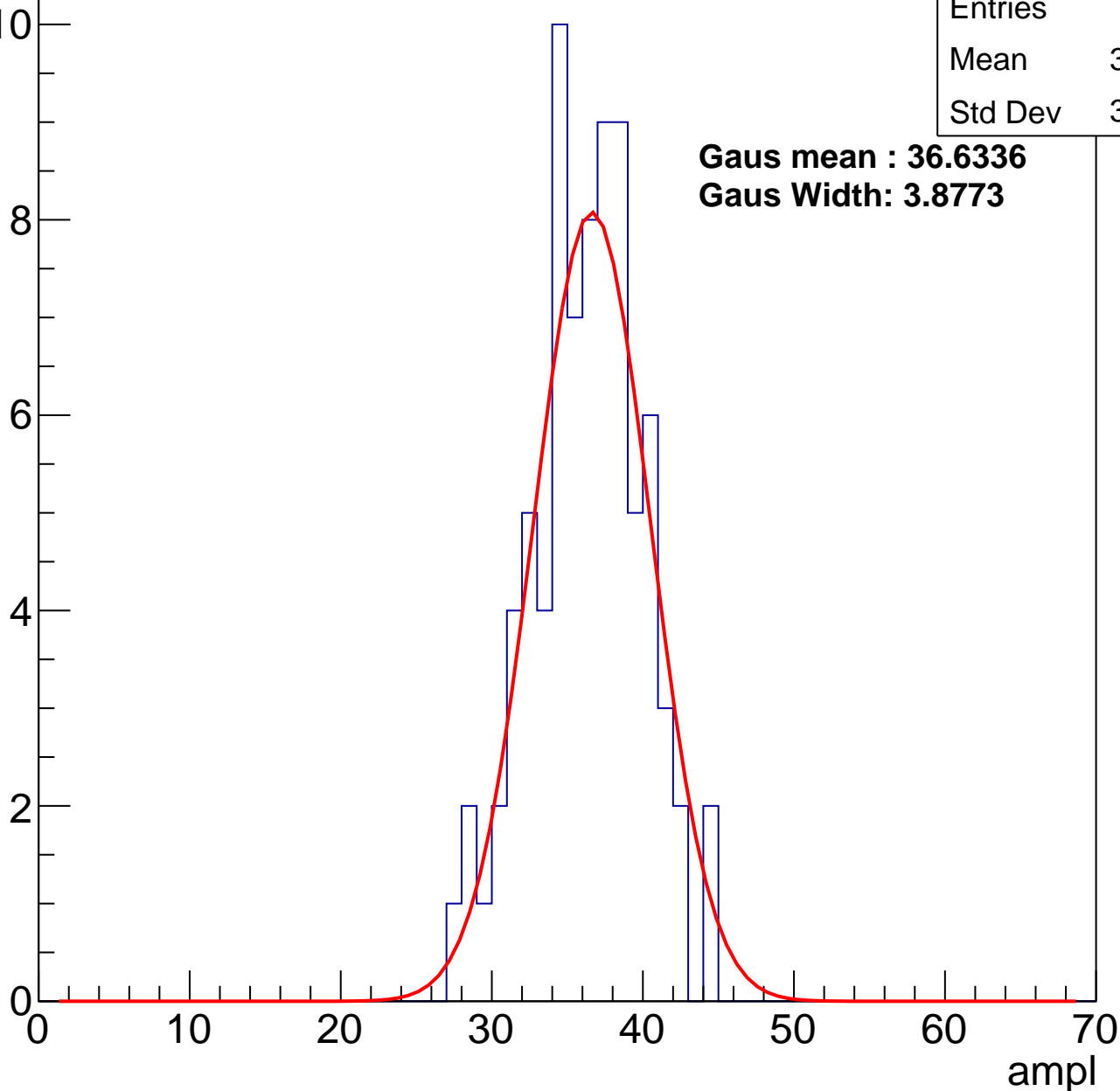
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 35.83 |
| Std Dev | 3.626 |

Gaus mean : 36.6336

Gaus Width: 3.8773



B1L100S, U7-ch104, adc2

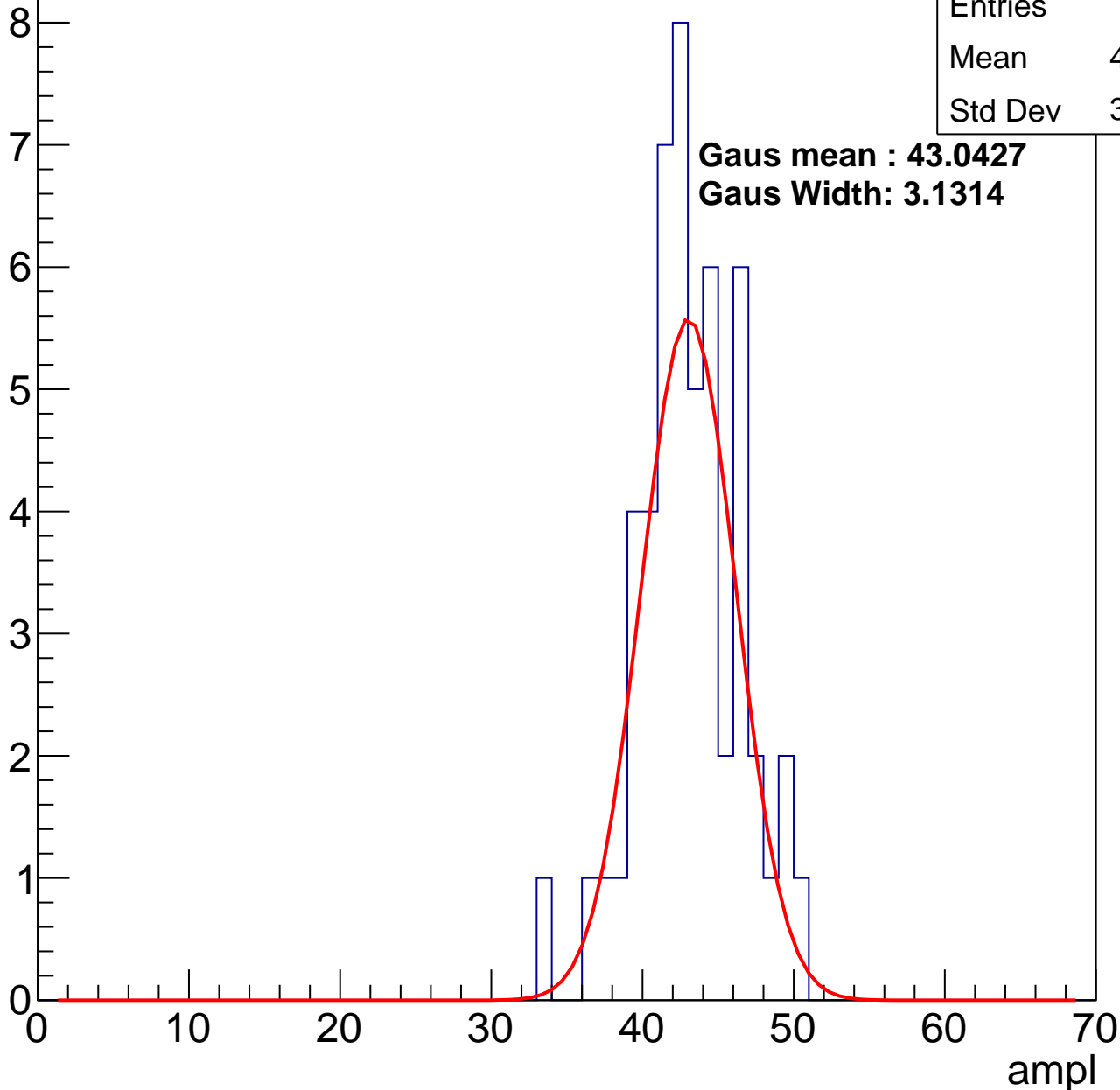
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 42.65 |
| Std Dev | 3.356 |

Gaus mean : 43.0427

Gaus Width: 3.1314

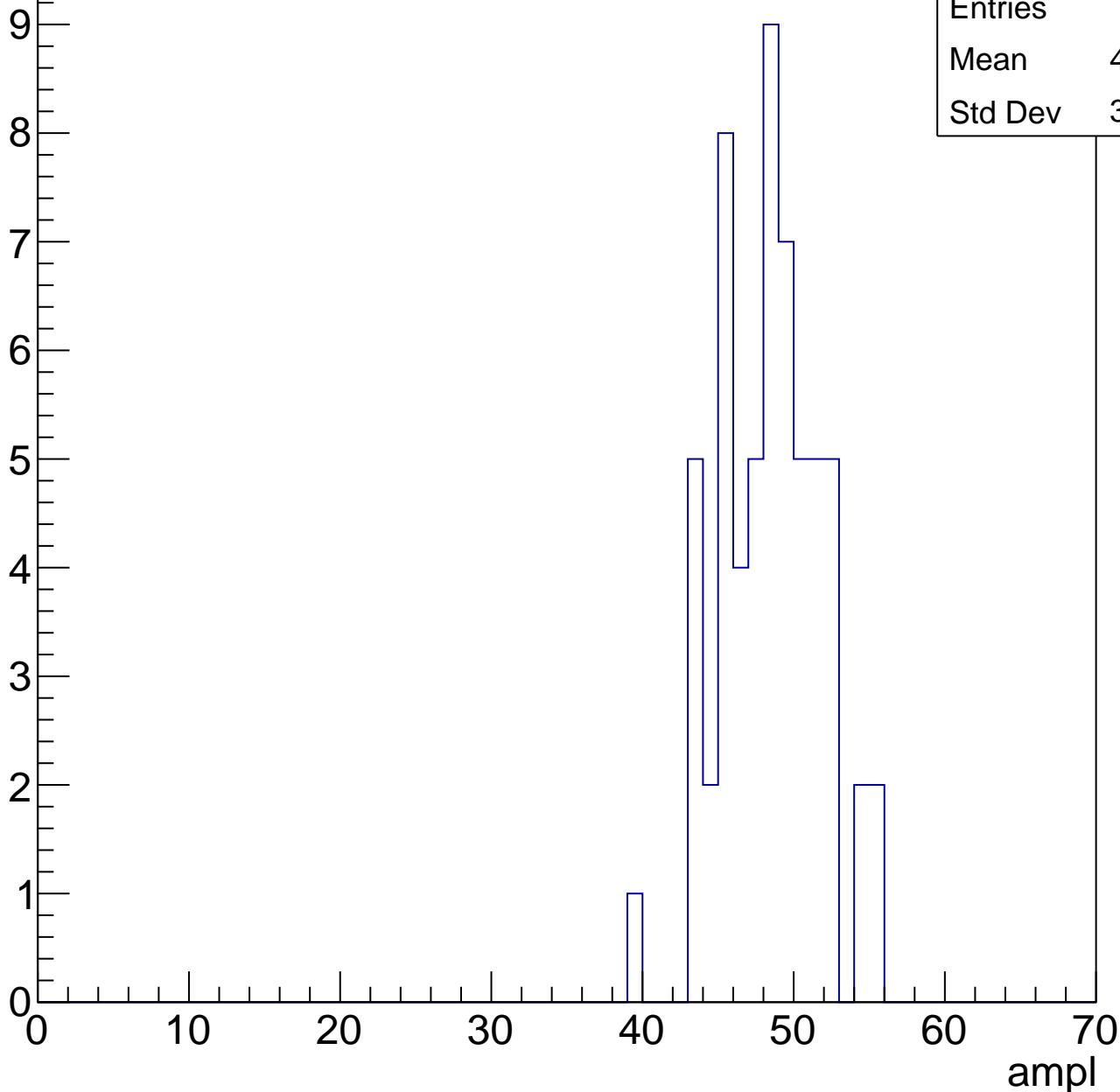


B1L100S, U7-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

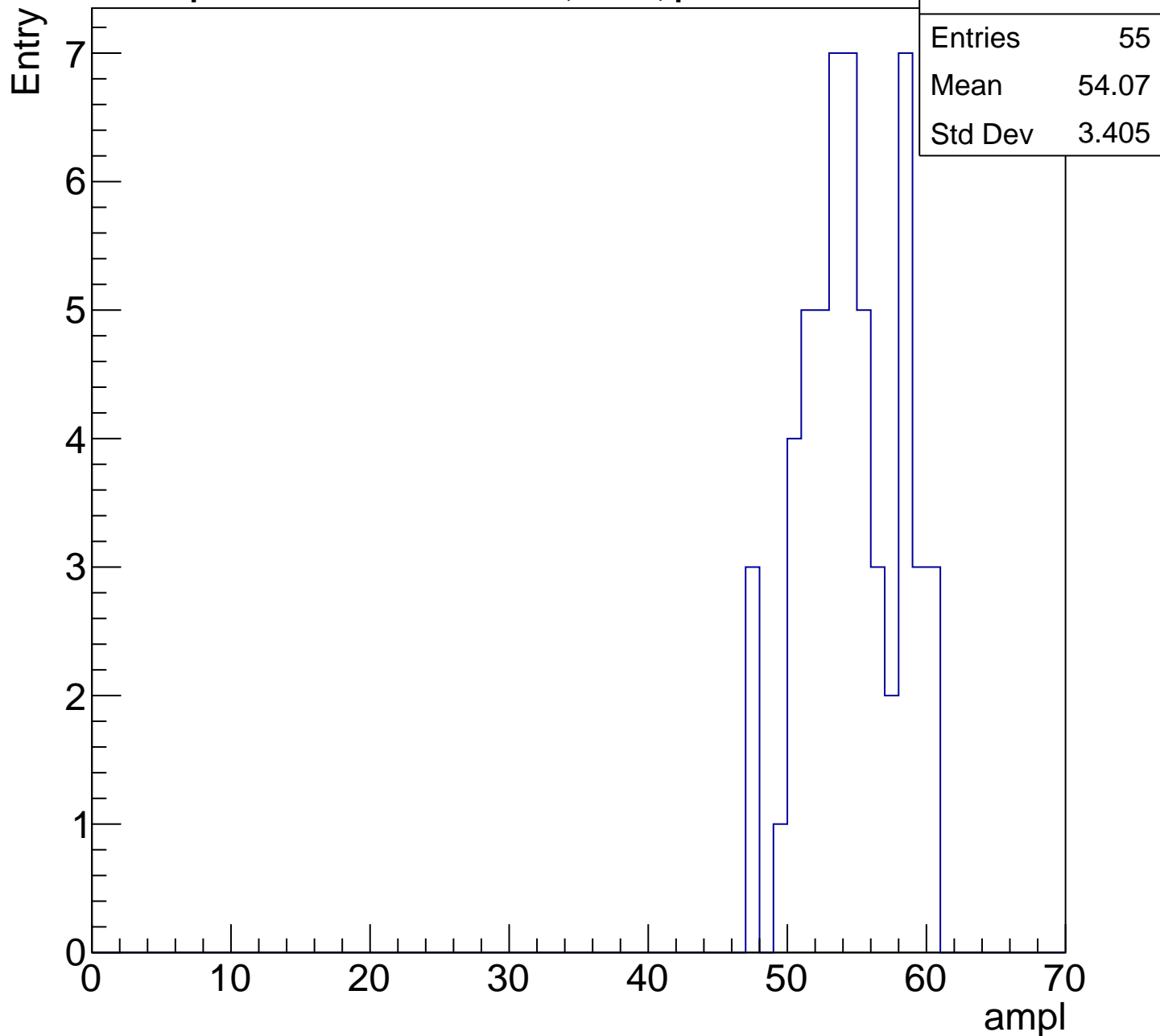
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.98 |
| Std Dev | 3.299 |



B1L100S, U7-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

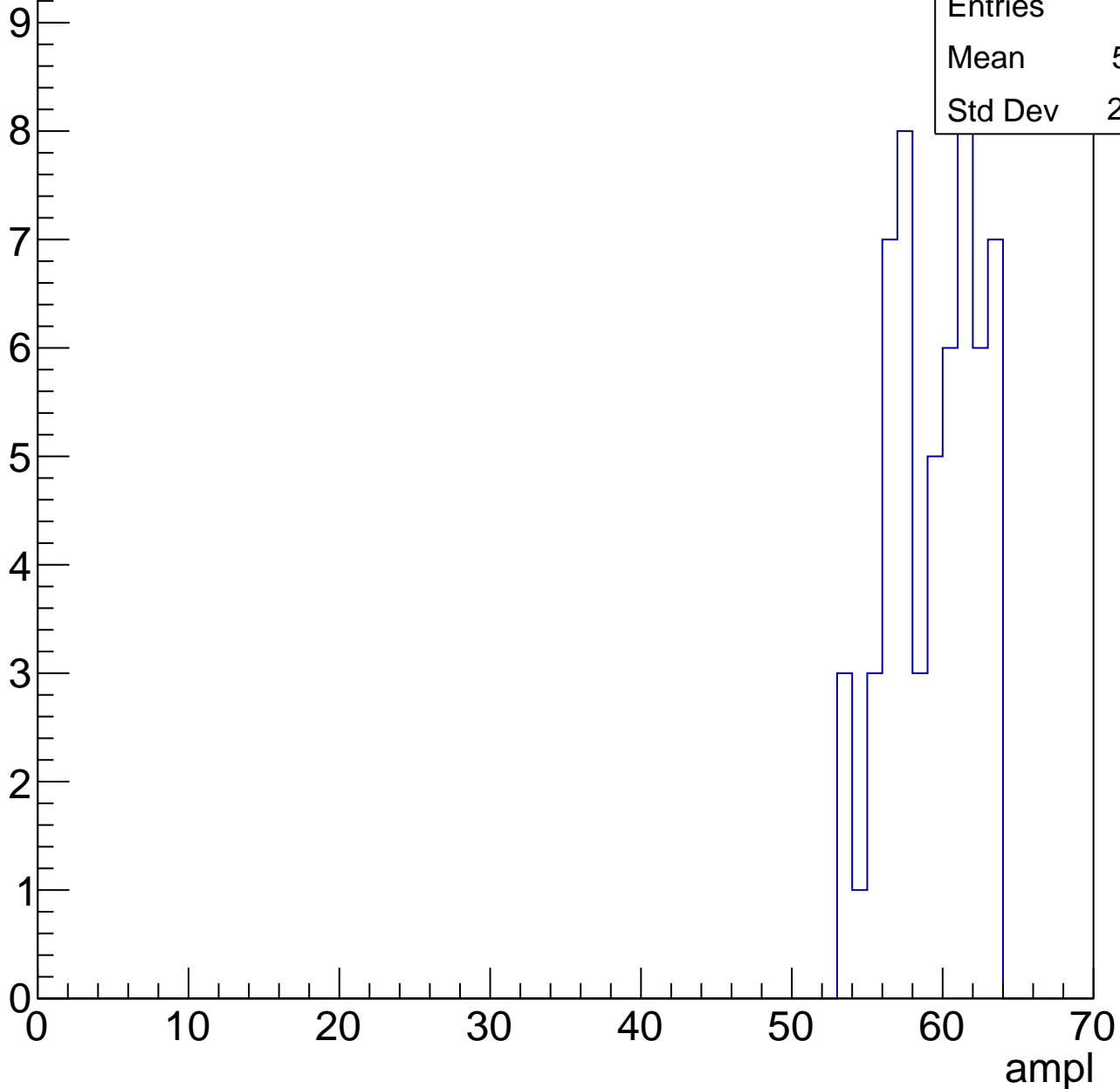


B1L100S, U7-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

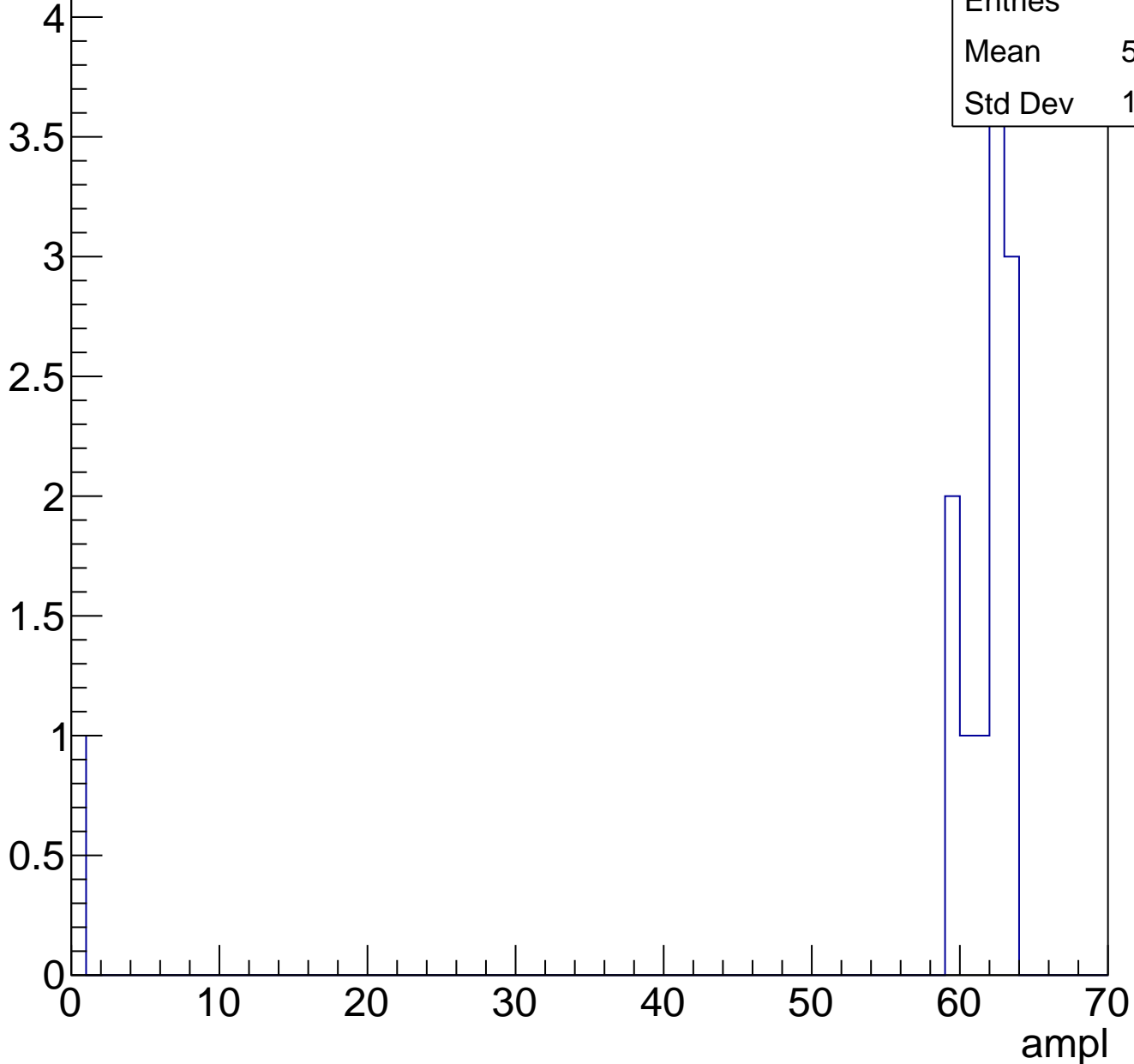
| | |
|---------|-------|
| Entries | 58 |
| Mean | 58.91 |
| Std Dev | 2.896 |



B1L100S, U7-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch105, adc0

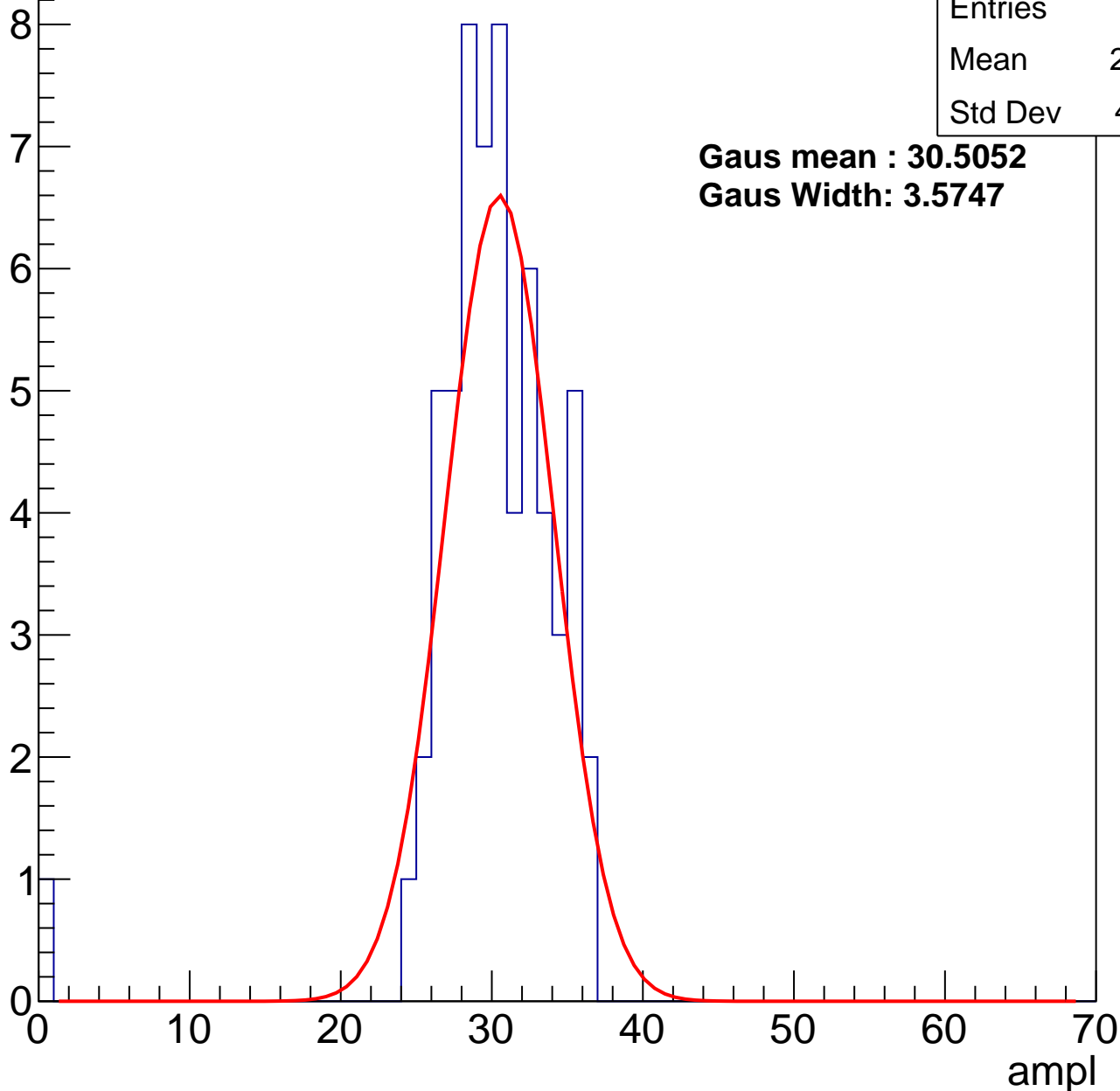
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.56 |
| Std Dev | 4.871 |

Gaus mean : 30.5052

Gaus Width: 3.5747



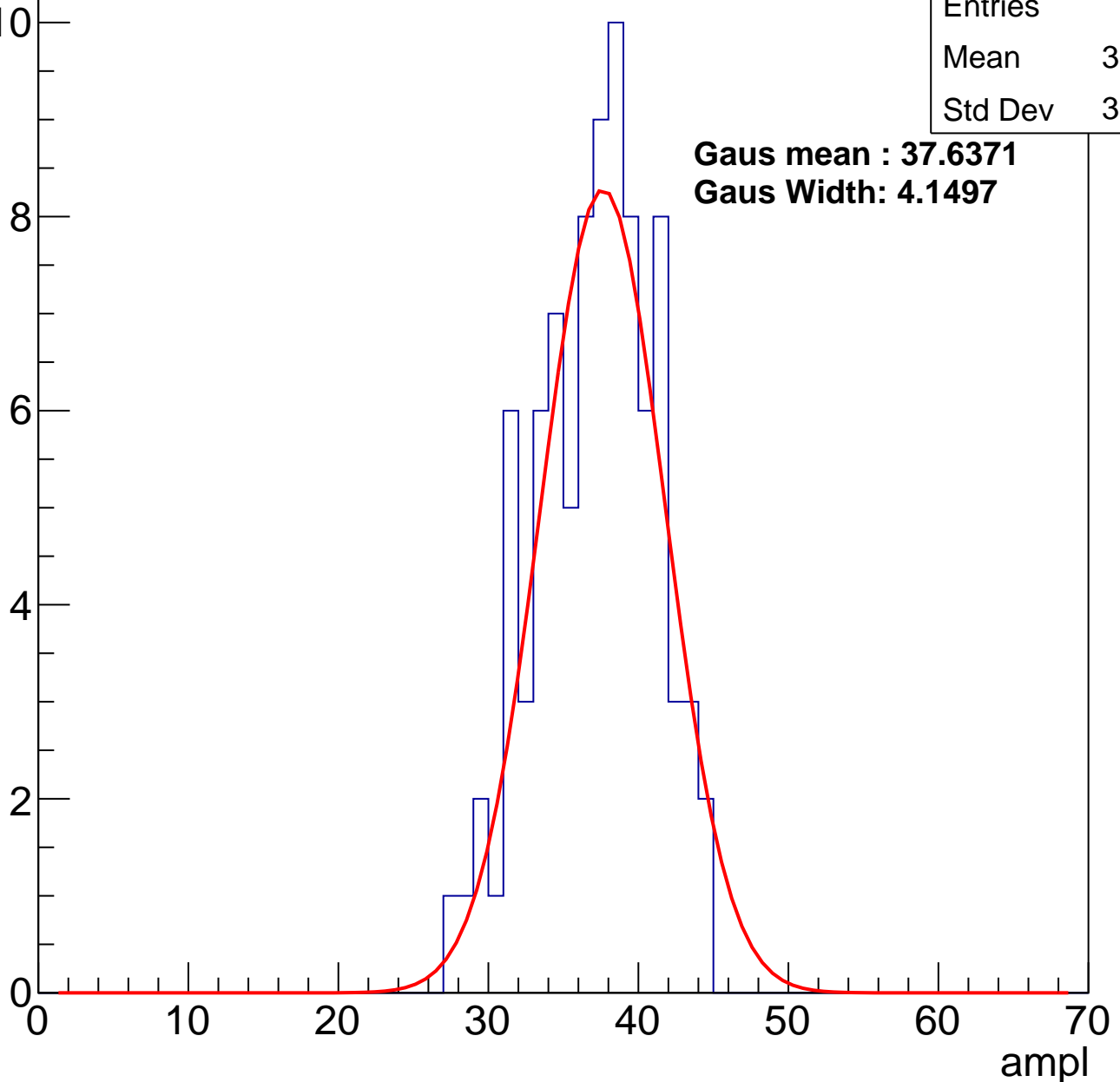
B1L100S, U7-ch105, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 36.63 |
| Std Dev | 3.864 |

Gaus mean : 37.6371
Gaus Width: 4.1497



B1L100S, U7-ch105, adc2

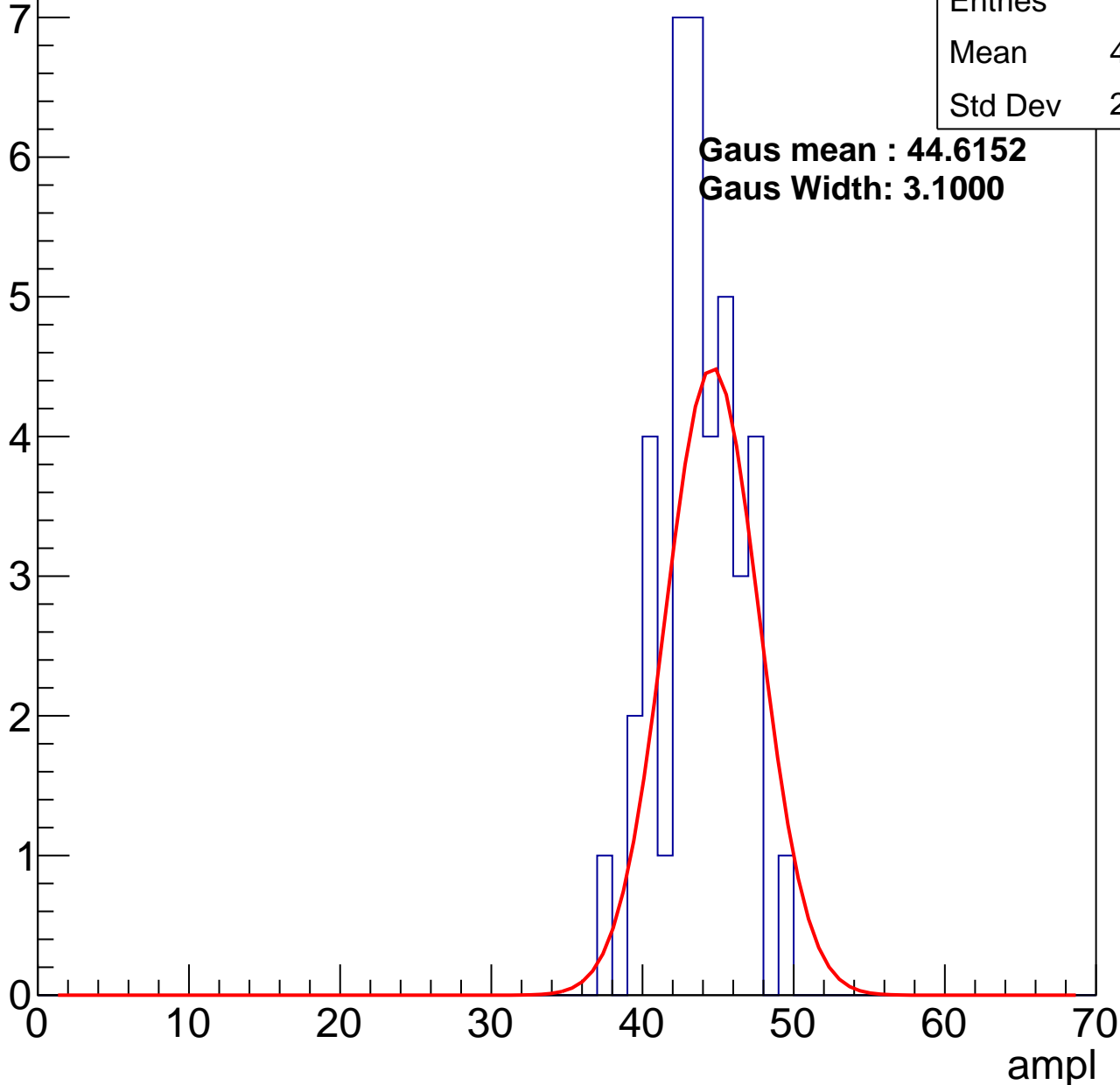
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 43.26 |
| Std Dev | 2.599 |

Gaus mean : 44.6152

Gaus Width: 3.1000

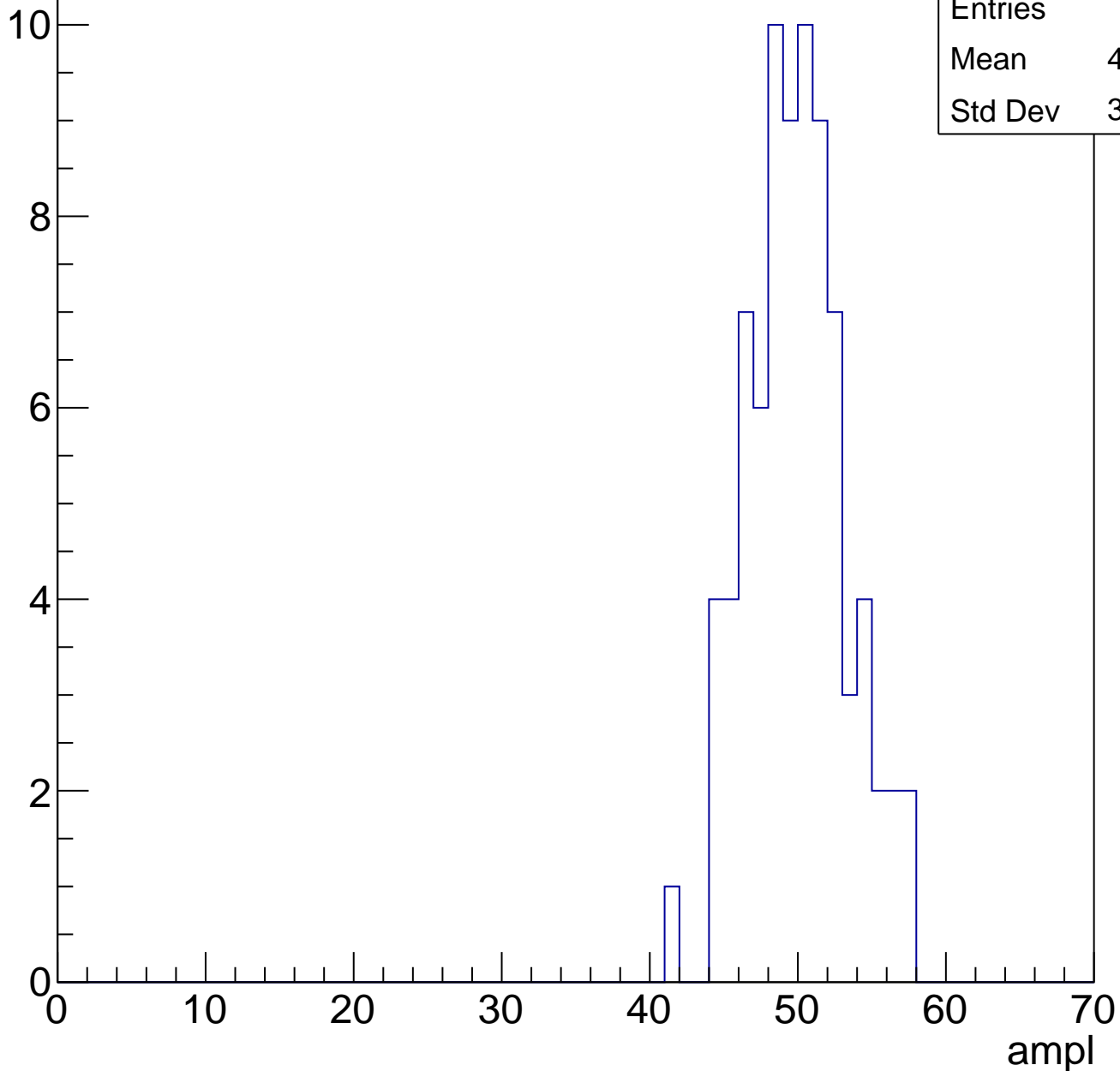


B1L100S, U7-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

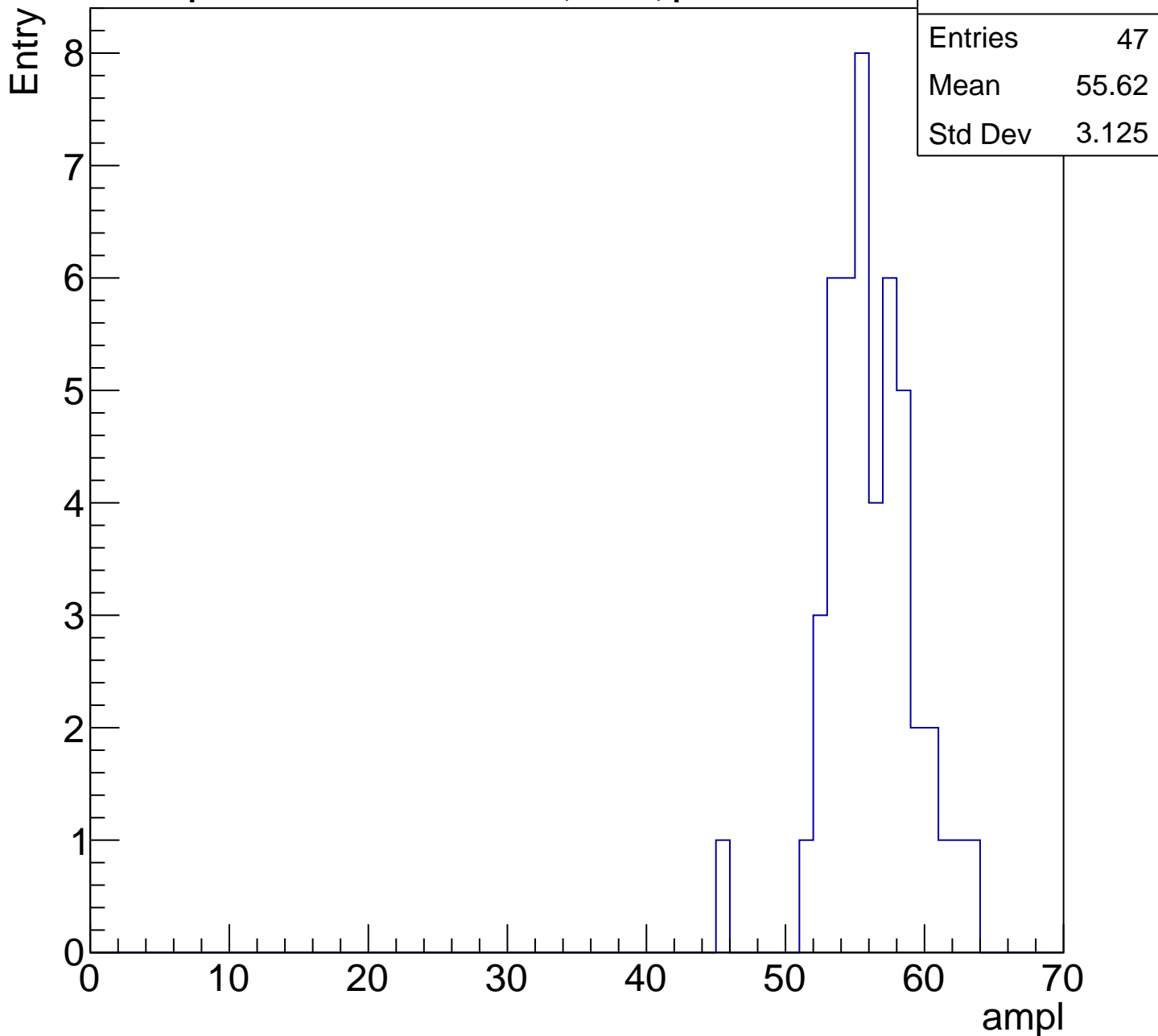
| | |
|---------|-------|
| Entries | 80 |
| Mean | 49.45 |
| Std Dev | 3.286 |

Entry



B1L100S, U7-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

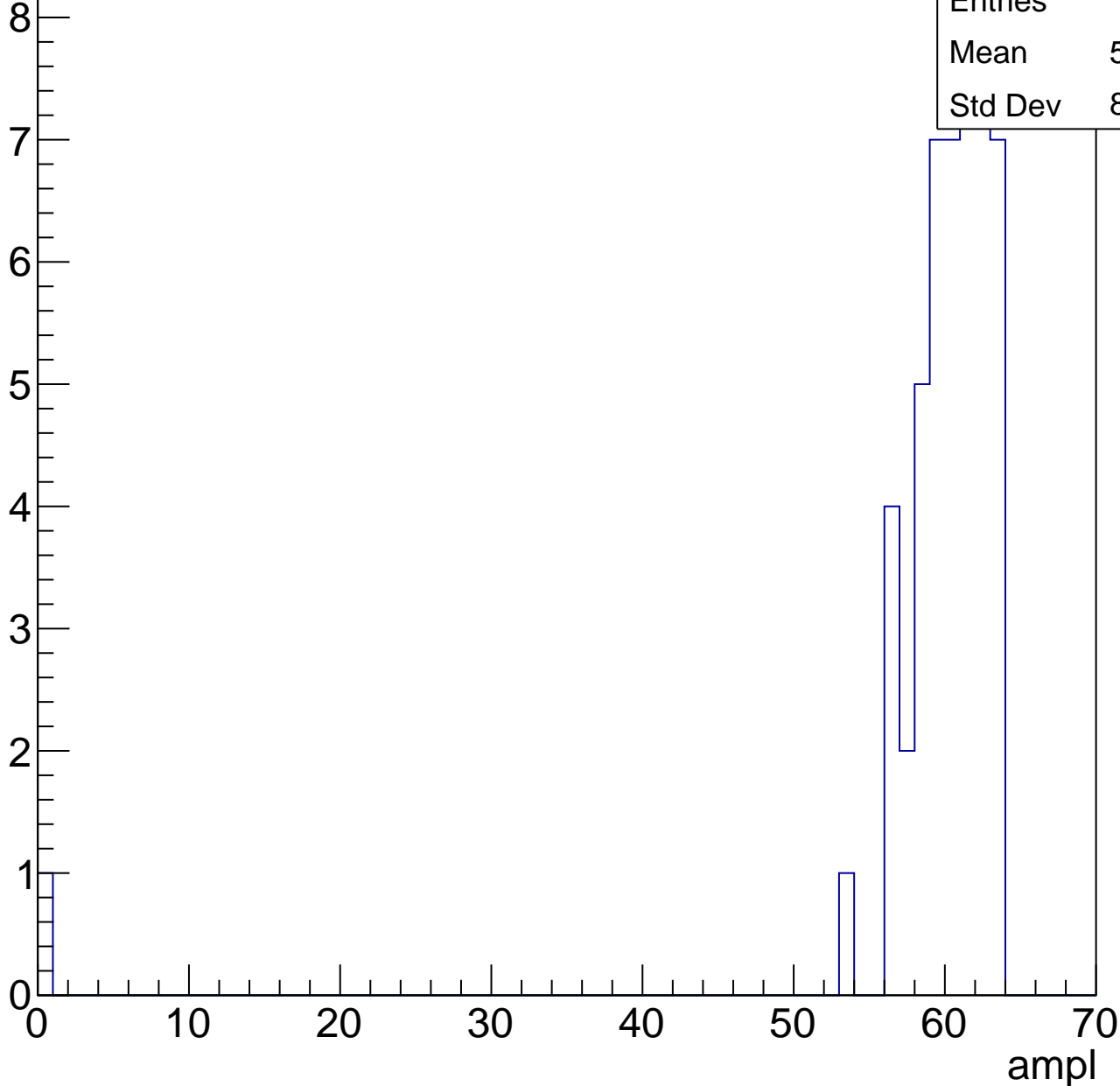


B1L100S, U7-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.78 |
| Std Dev | 8.702 |



B1L100S, U7-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

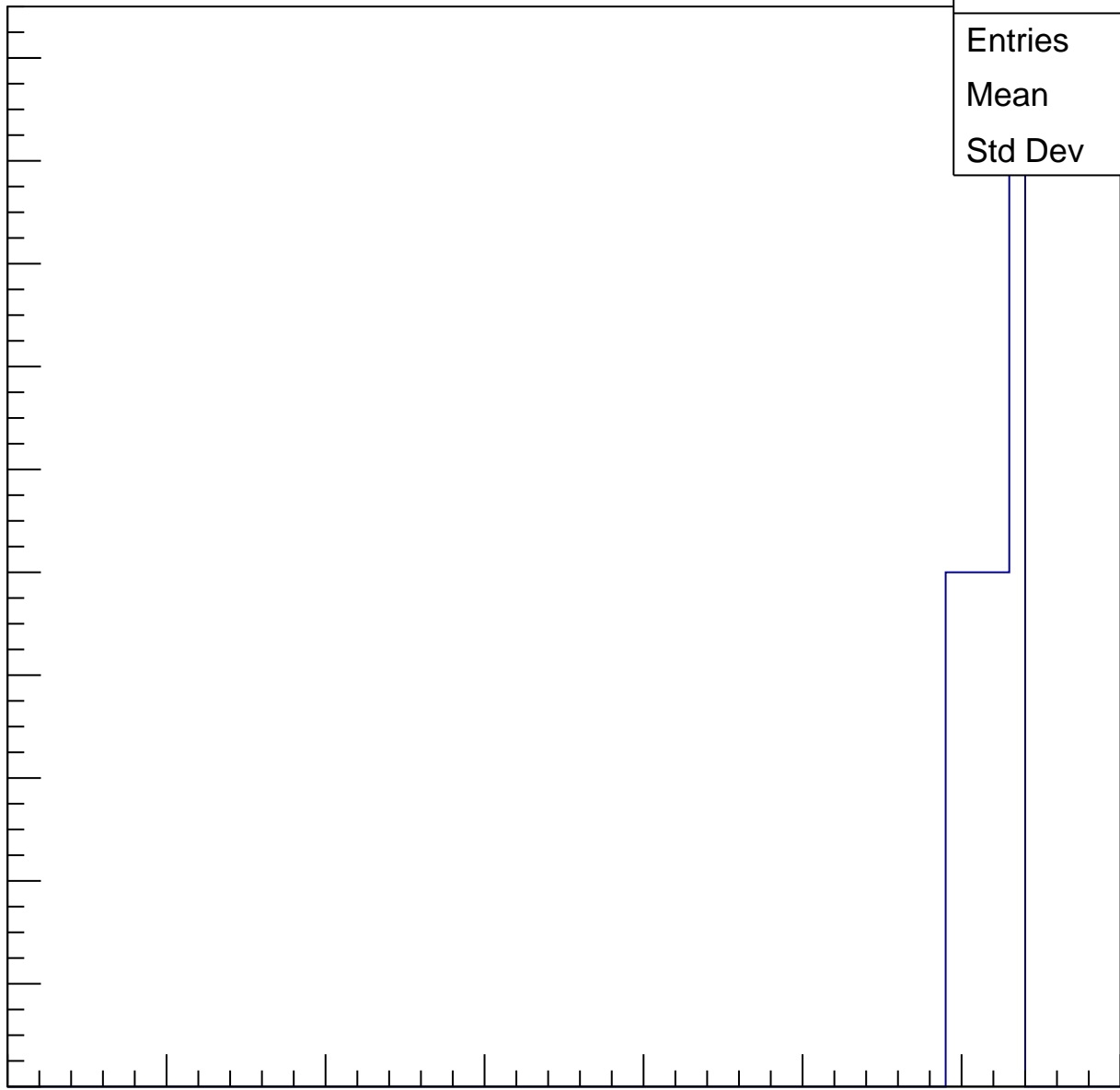
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.33 |
| Std Dev | 1.491 |

ampl

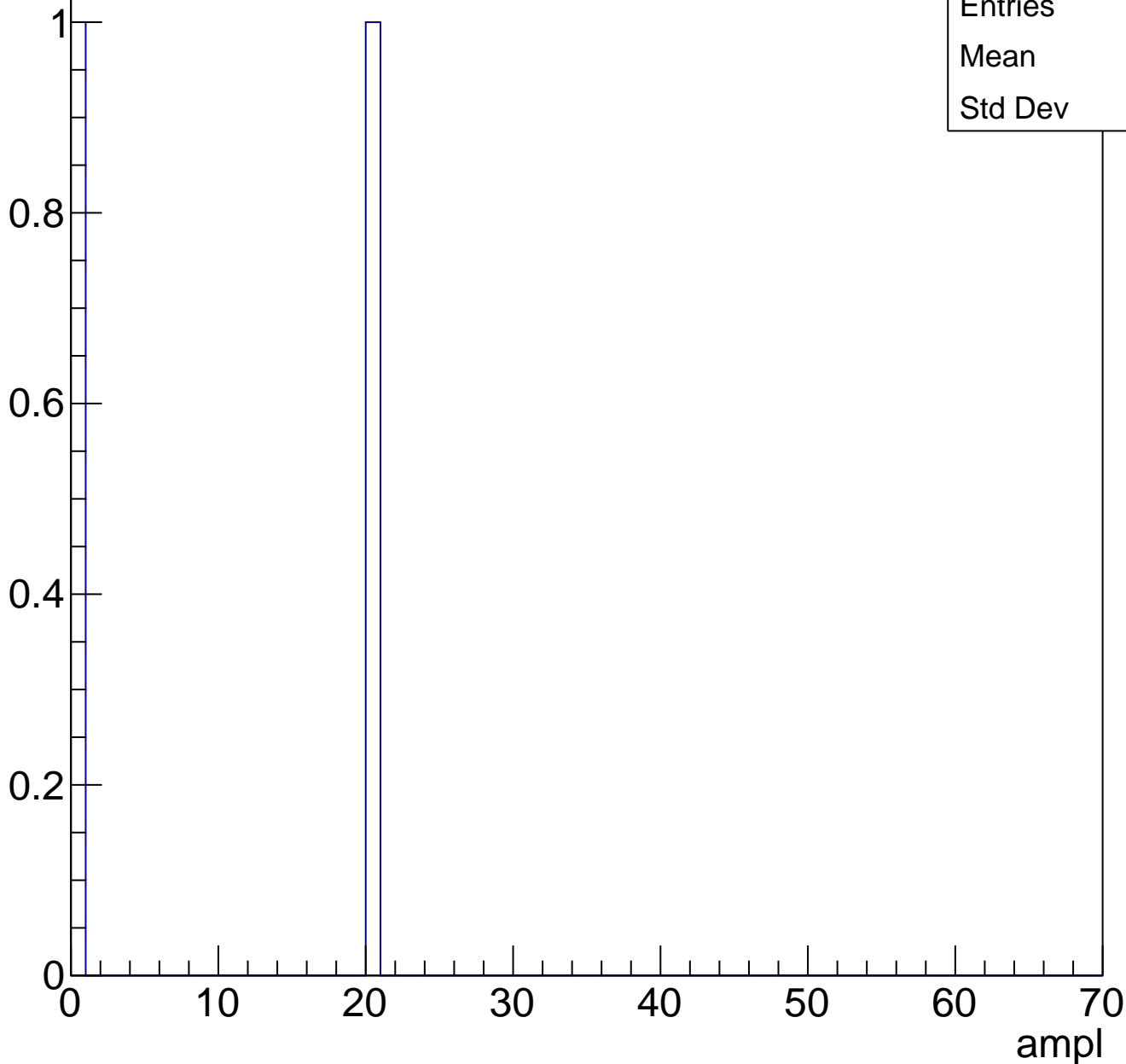
0 10 20 30 40 50 60 70



B1L100S, U7-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch106, adc0

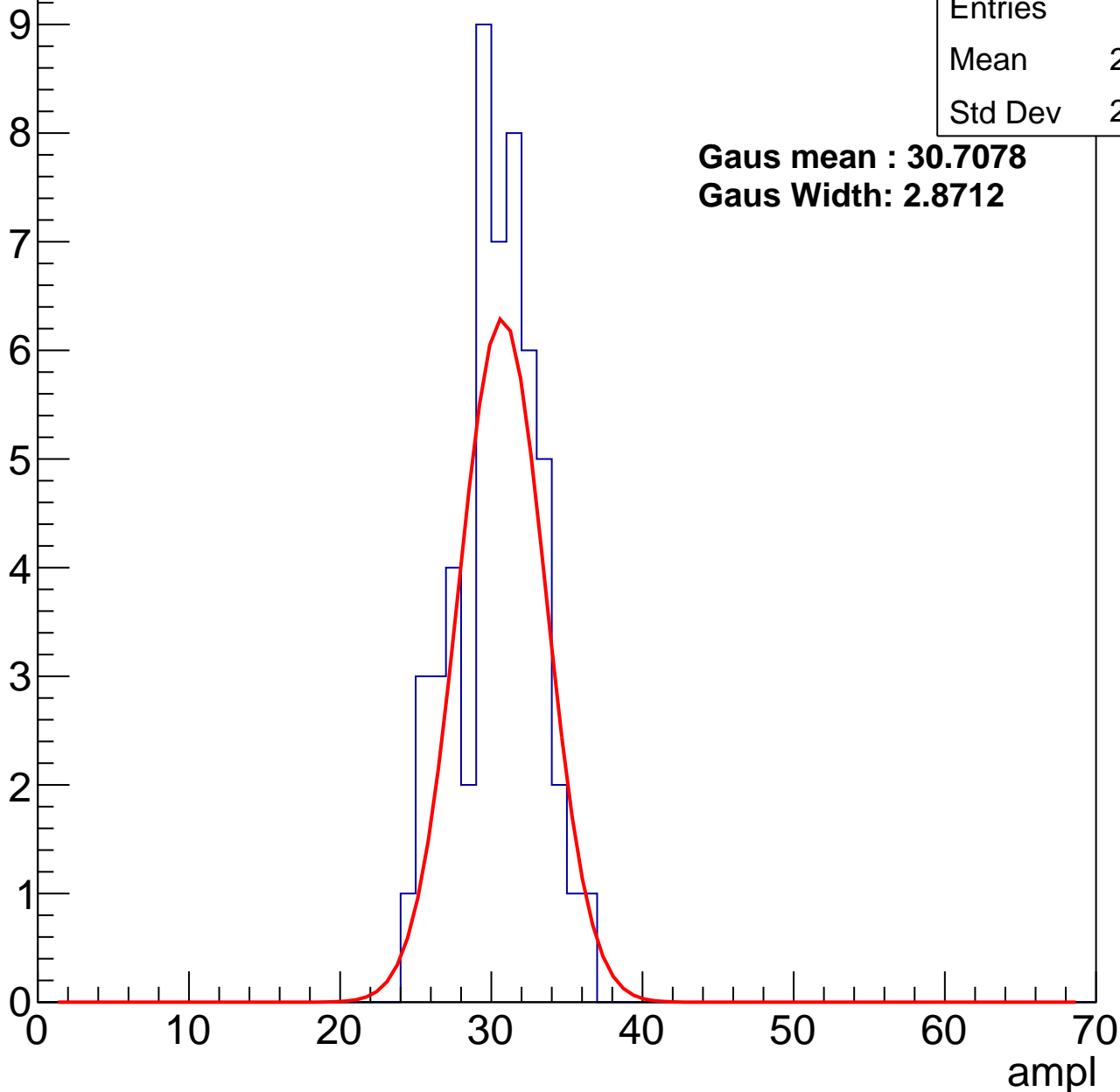
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 29.92 |
| Std Dev | 2.709 |

Gaus mean : 30.7078

Gaus Width: 2.8712



B1L100S, U7-ch106, adc1

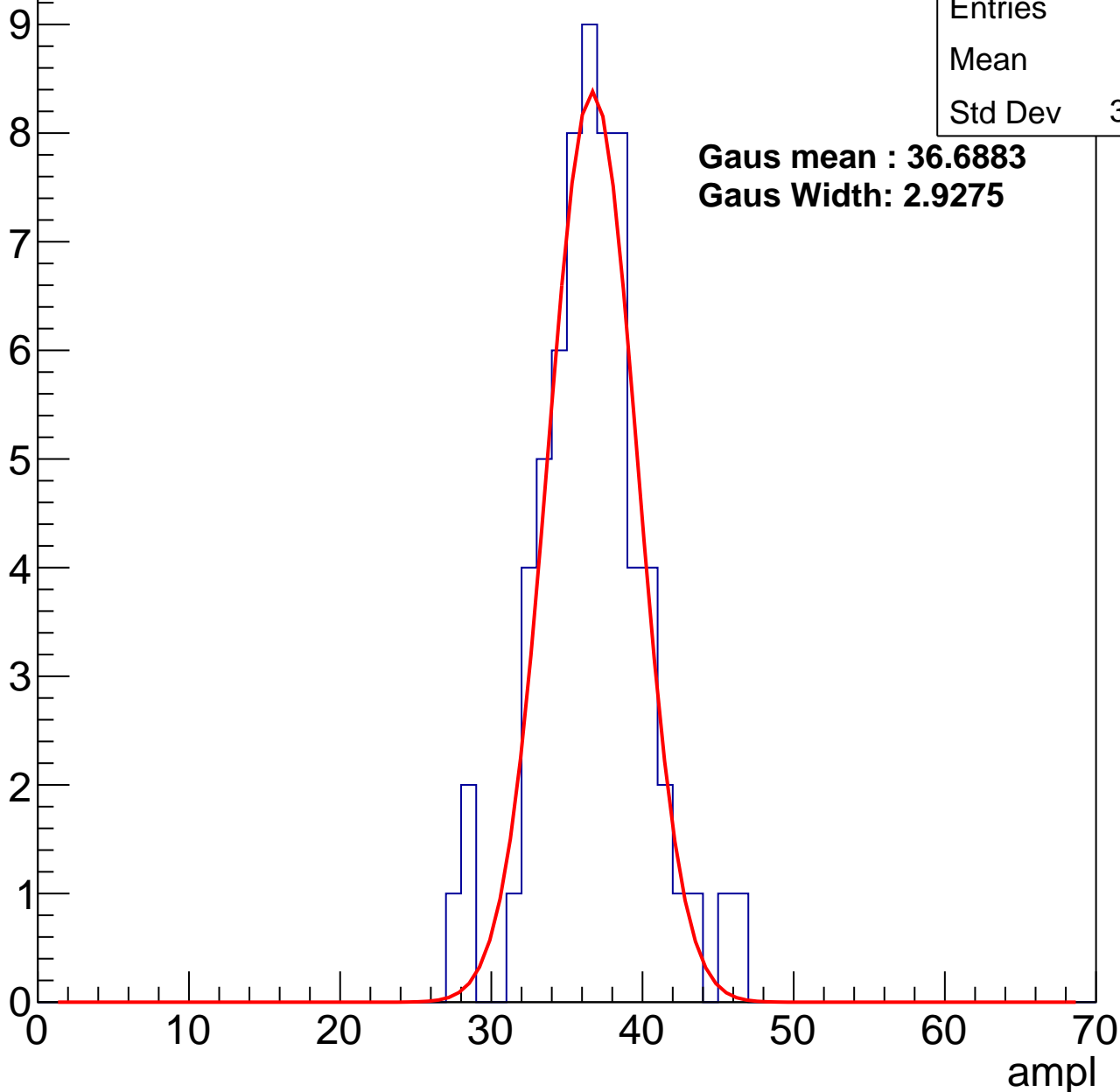
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.2 |
| Std Dev | 3.543 |

Gaus mean : 36.6883

Gaus Width: 2.9275



B1L100S, U7-ch106, adc2

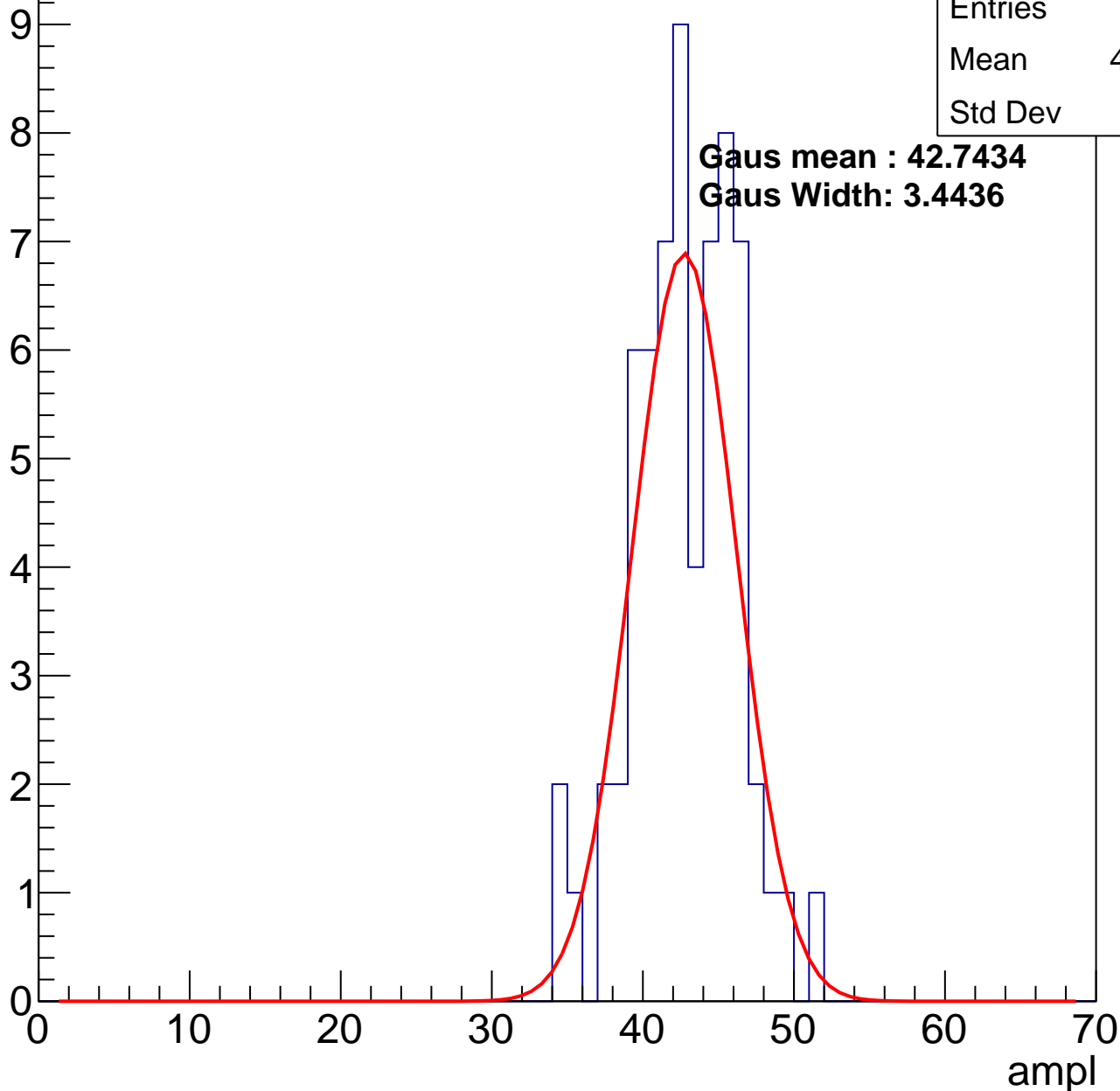
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 42.36 |
| Std Dev | 3.41 |

Gaus mean : 42.7434

Gaus Width: 3.4436



B1L100S, U7-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.45 |
| Std Dev | 3.844 |

Entry

10

8

6

4

2

0

0

10

20

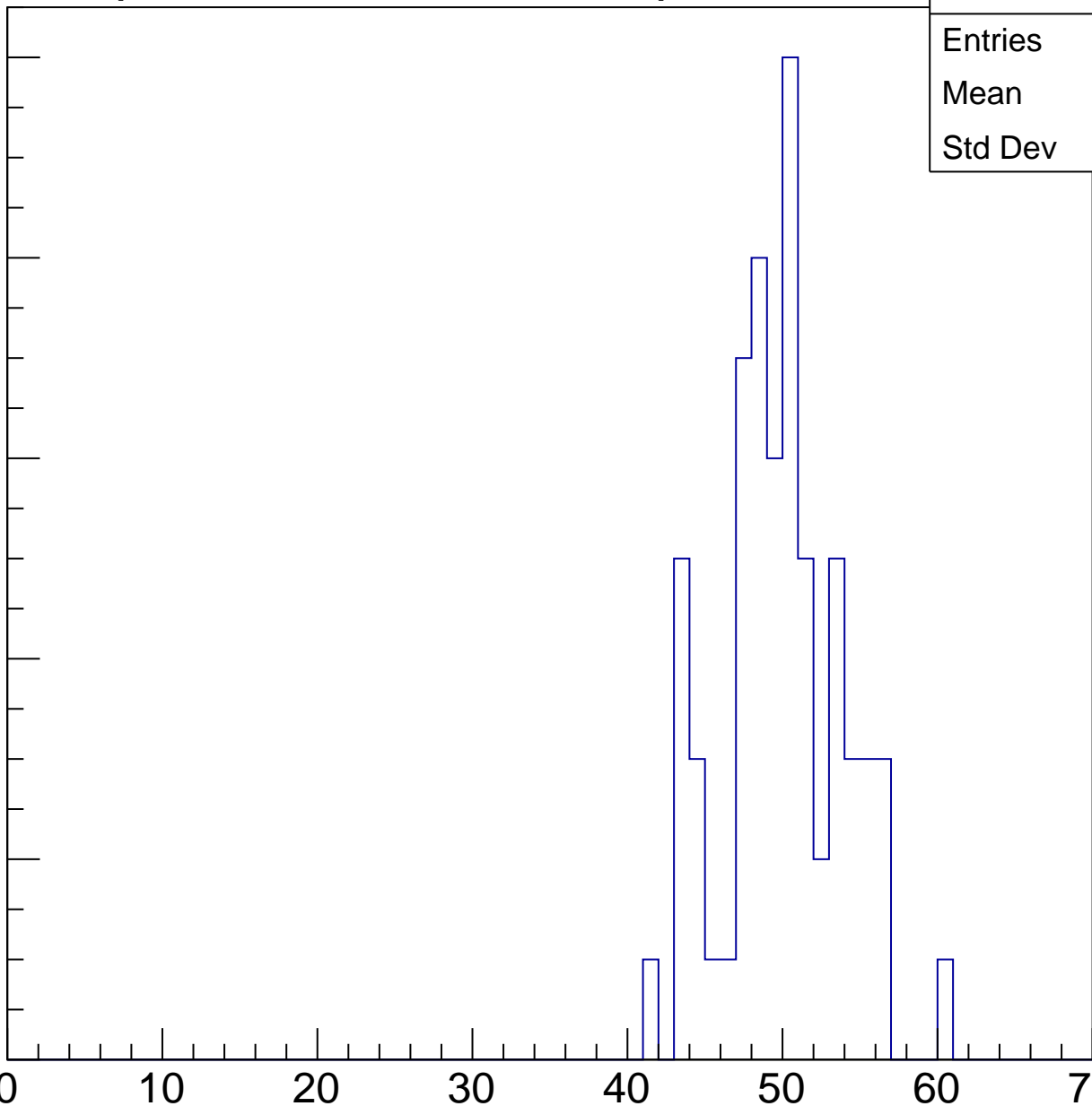
30

40

50

60

ampl

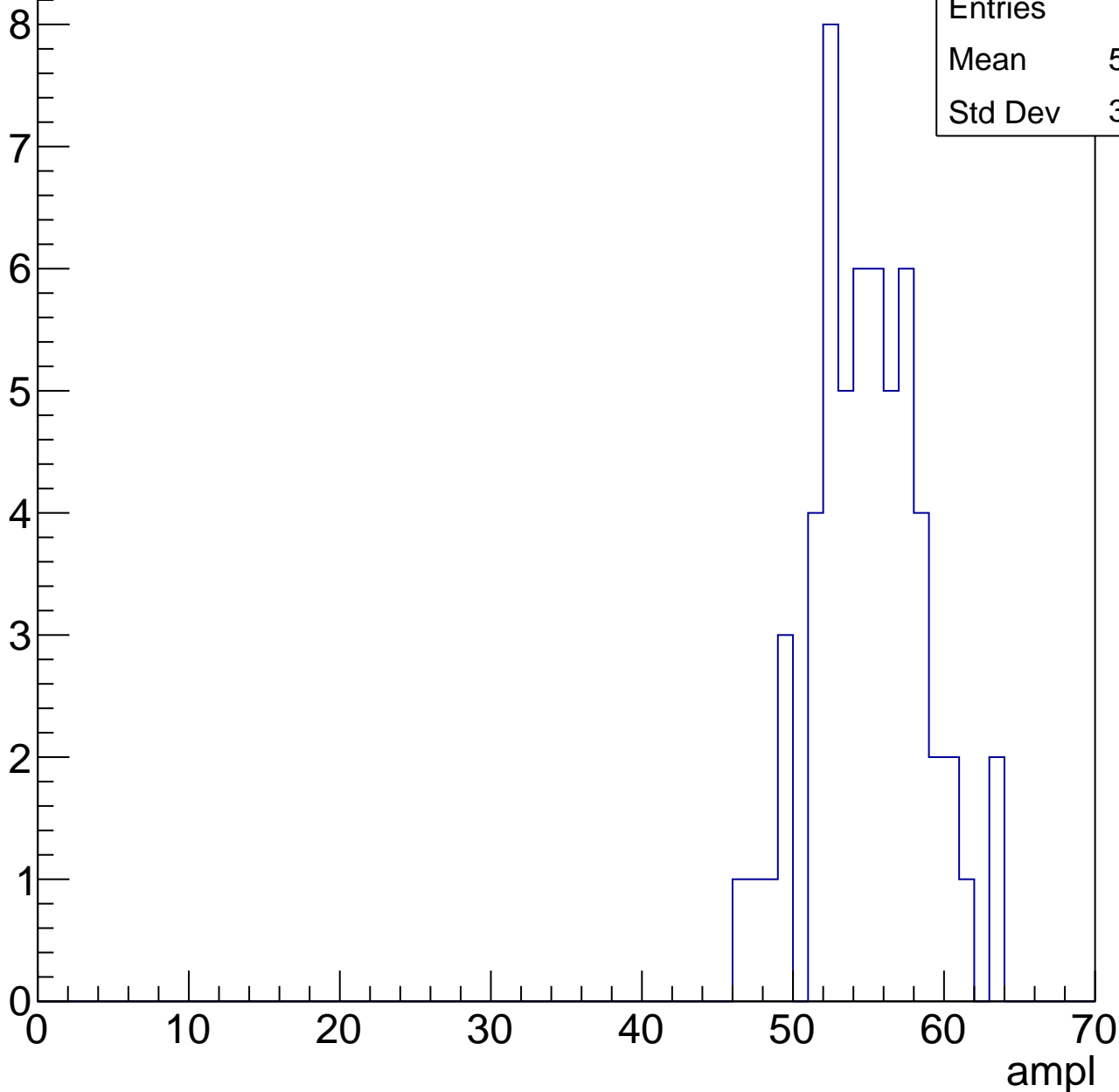


B1L100S, U7-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.49 |
| Std Dev | 3.652 |



B1L100S, U7-ch106, adc5

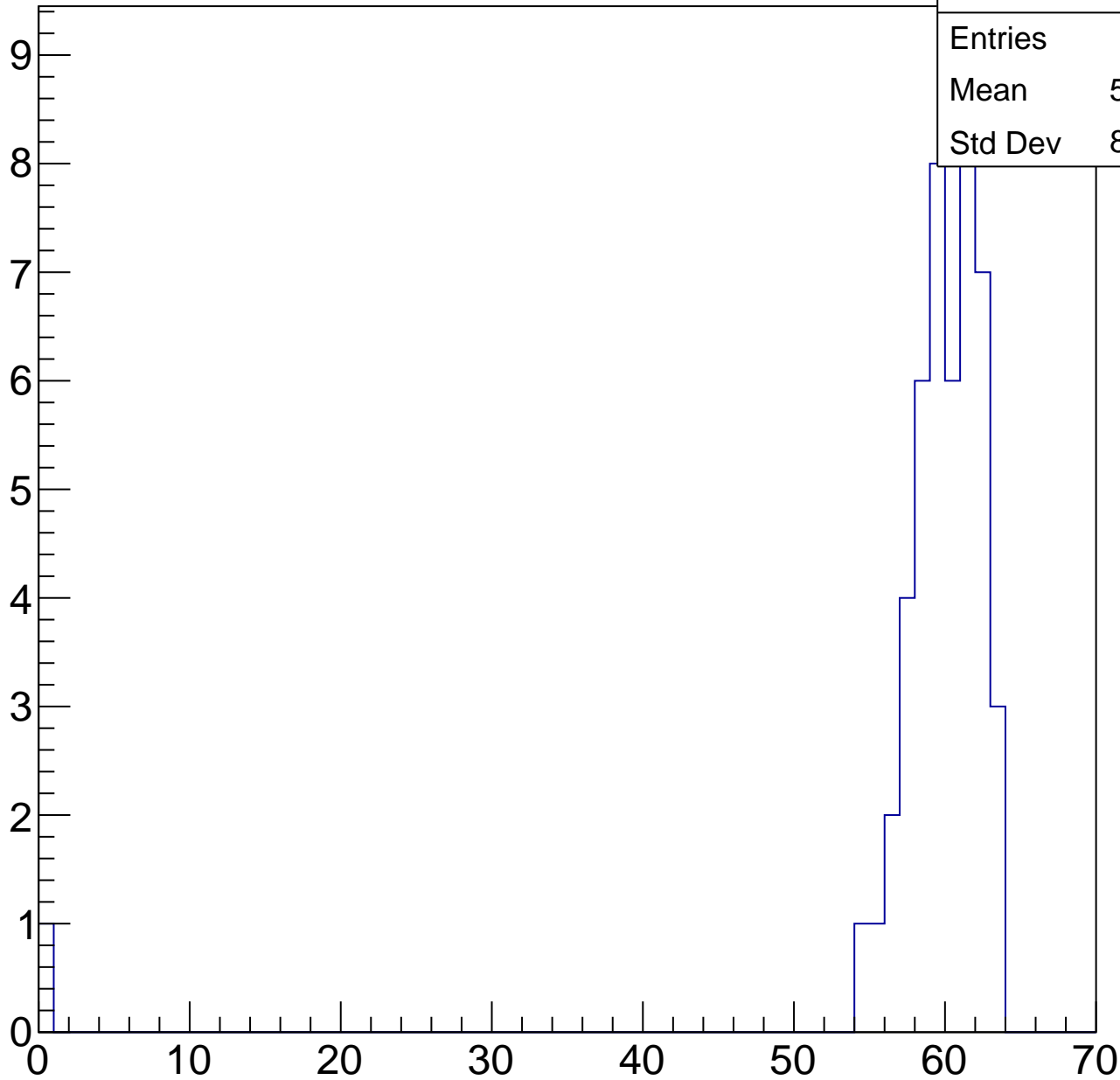
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.35 |
| Std Dev | 8.774 |

ampl

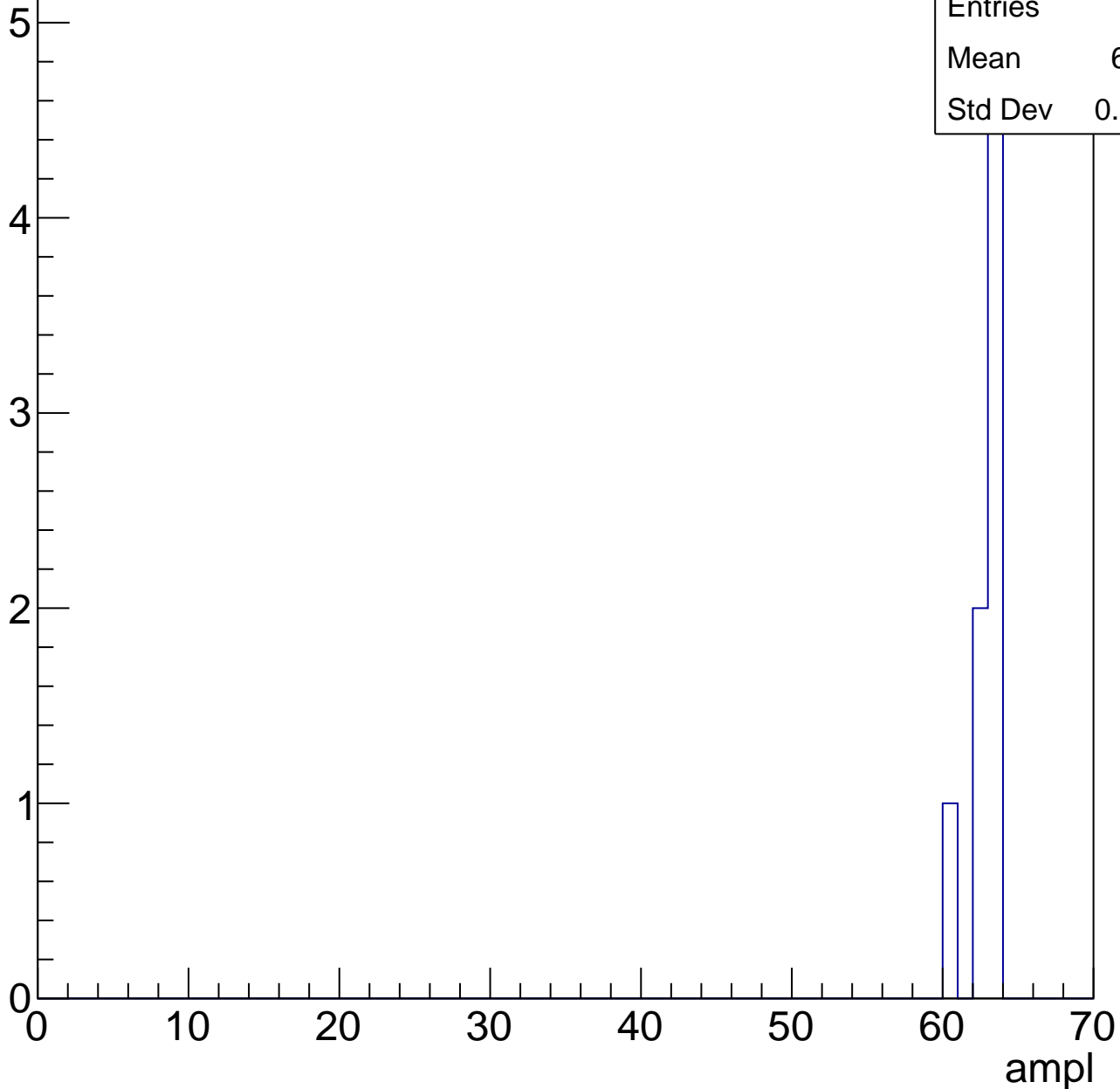


B1L100S, U7-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

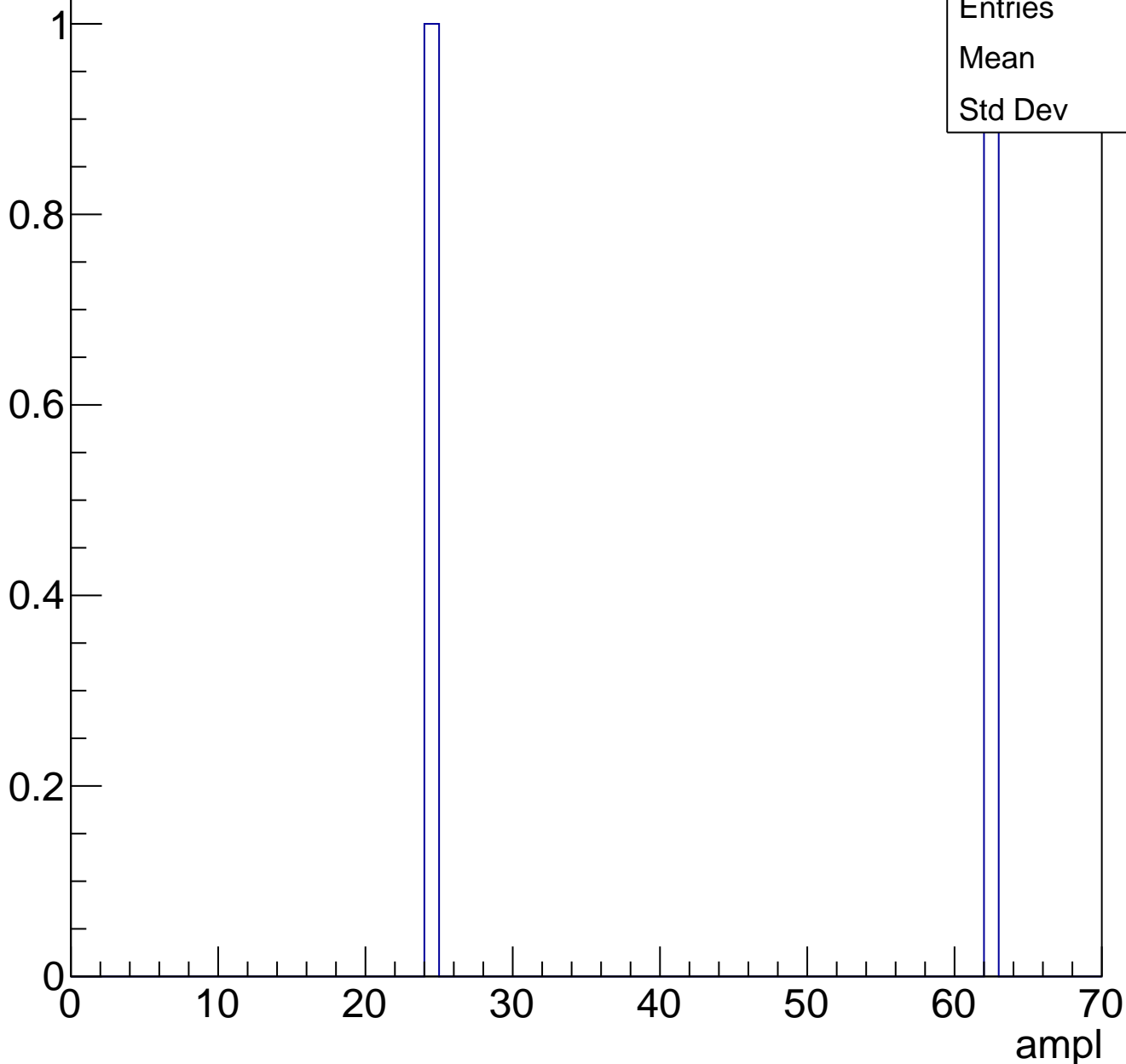
| | |
|---------|--------|
| Entries | 8 |
| Mean | 62.38 |
| Std Dev | 0.9922 |



B1L100S, U7-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch107, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.25 |
| Std Dev | 3.164 |

Gaus mean : 30.9712

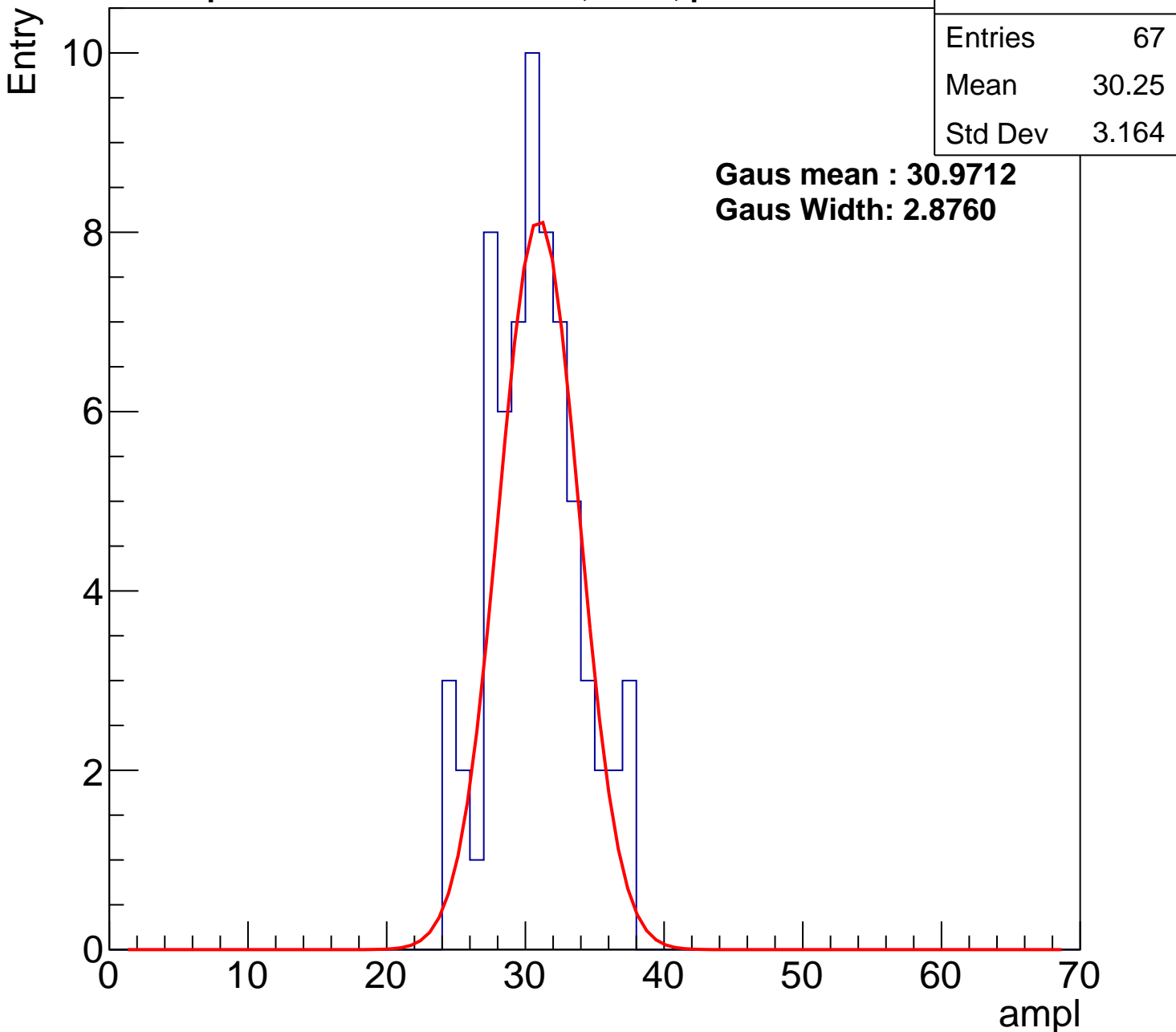
Gaus Width: 2.8760

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch107, adc1

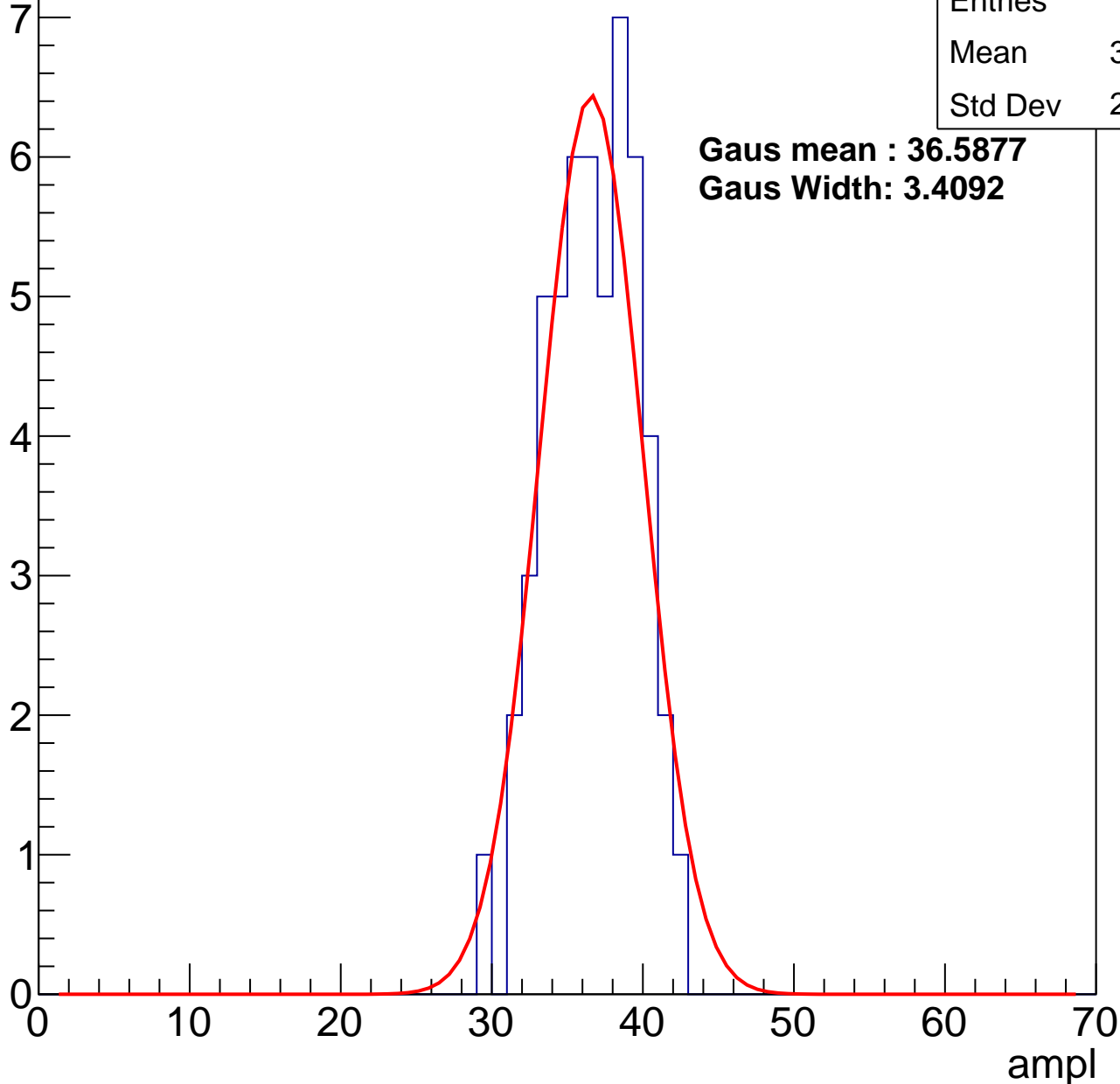
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 36.17 |
| Std Dev | 2.925 |

Gaus mean : 36.5877

Gaus Width: 3.4092



B1L100S, U7-ch107, adc2

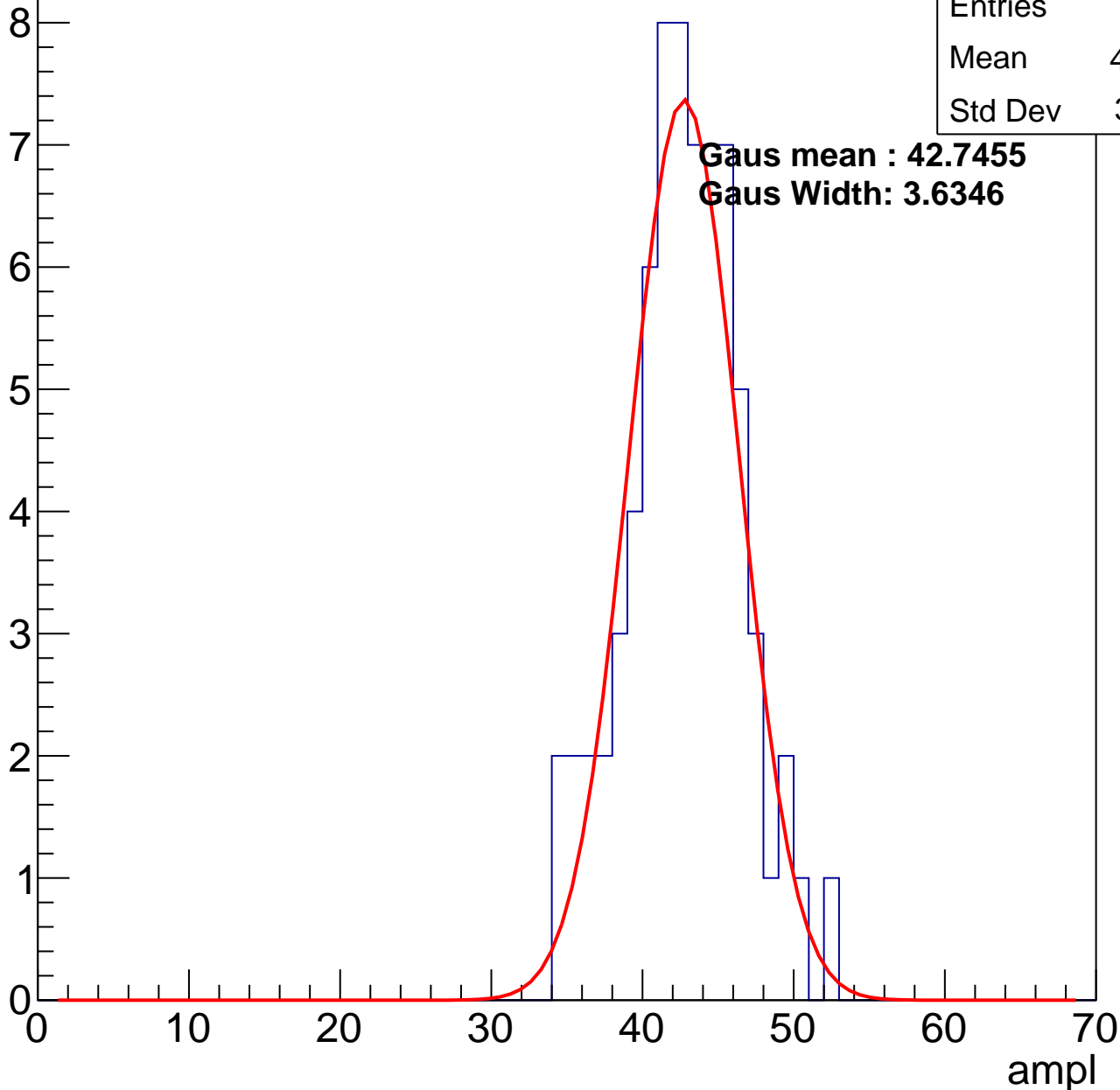
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.27 |
| Std Dev | 3.771 |

Gaus mean : 42.7455

Gaus Width: 3.6346

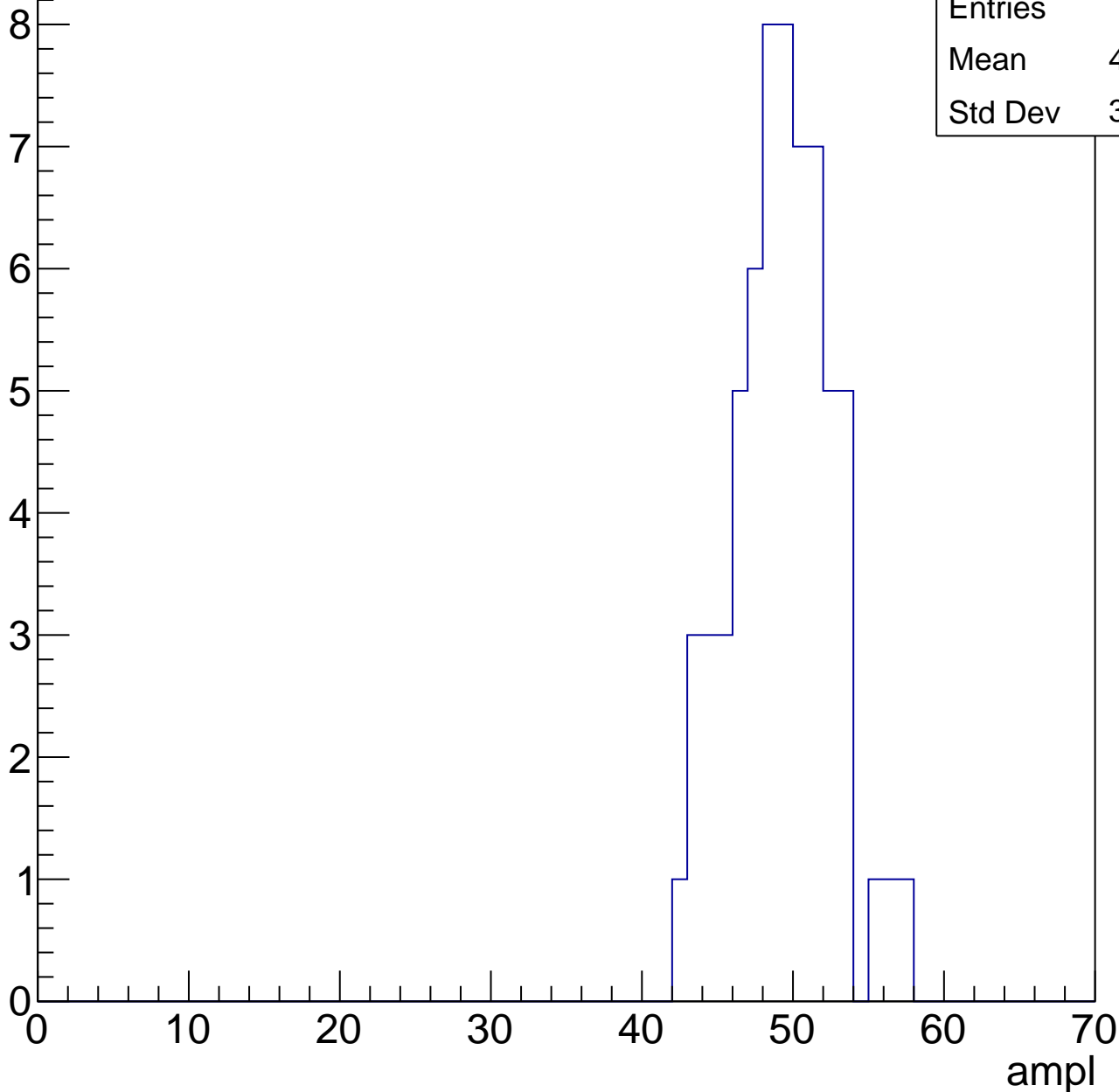


B1L100S, U7-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.84 |
| Std Dev | 3.222 |

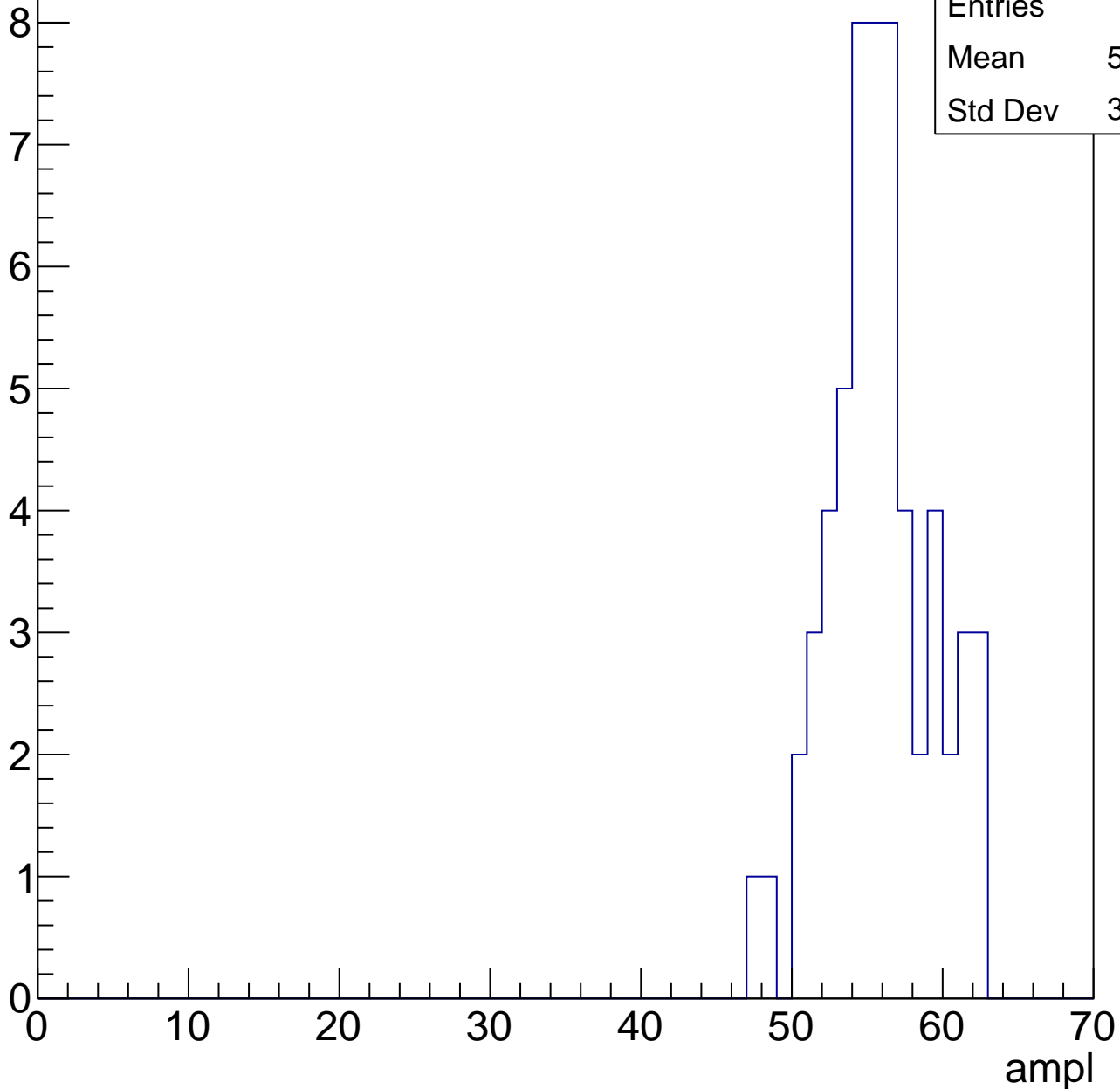


B1L100S, U7-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

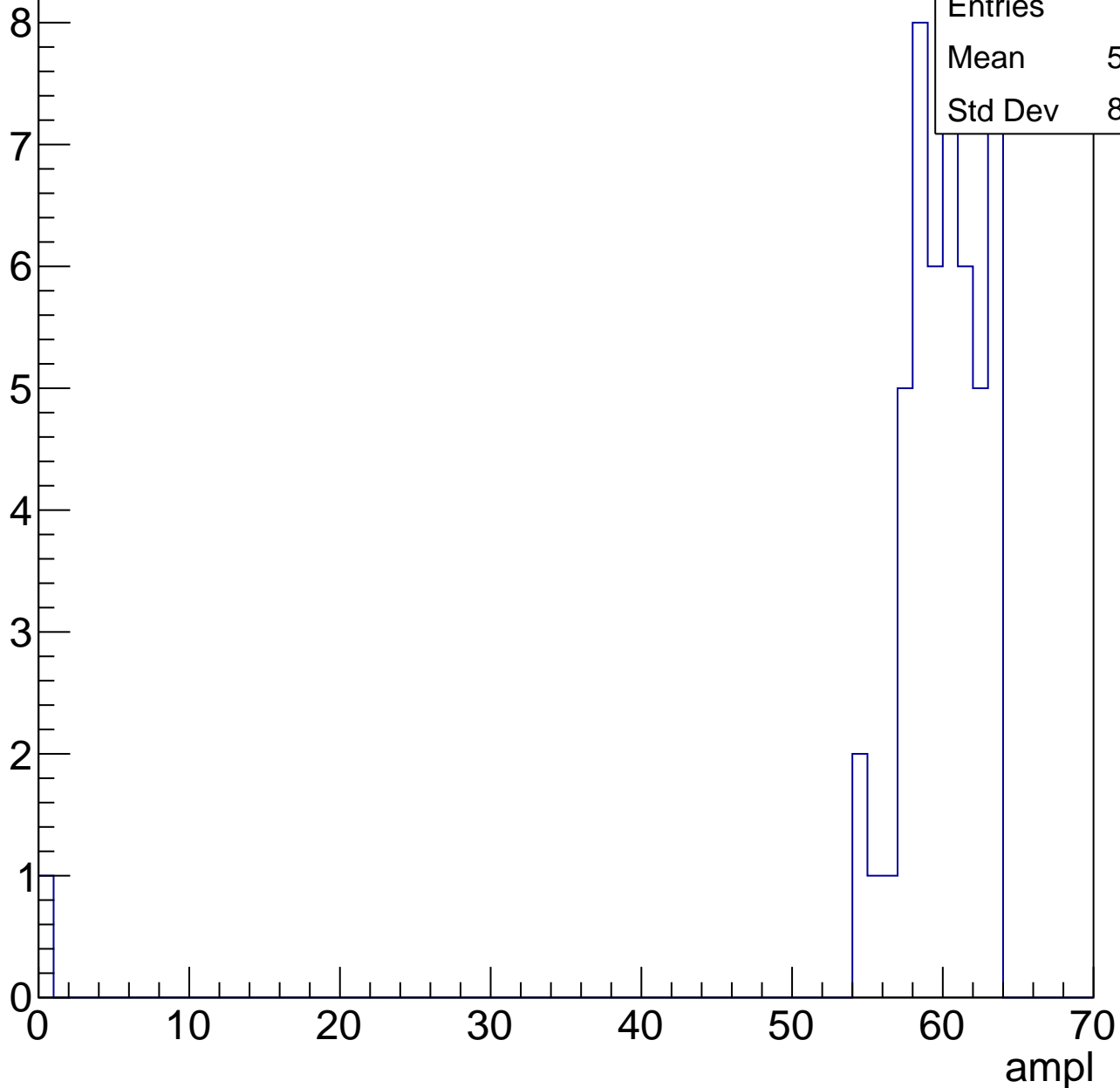
| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.34 |
| Std Dev | 3.422 |



B1L100S, U7-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch107, adc6

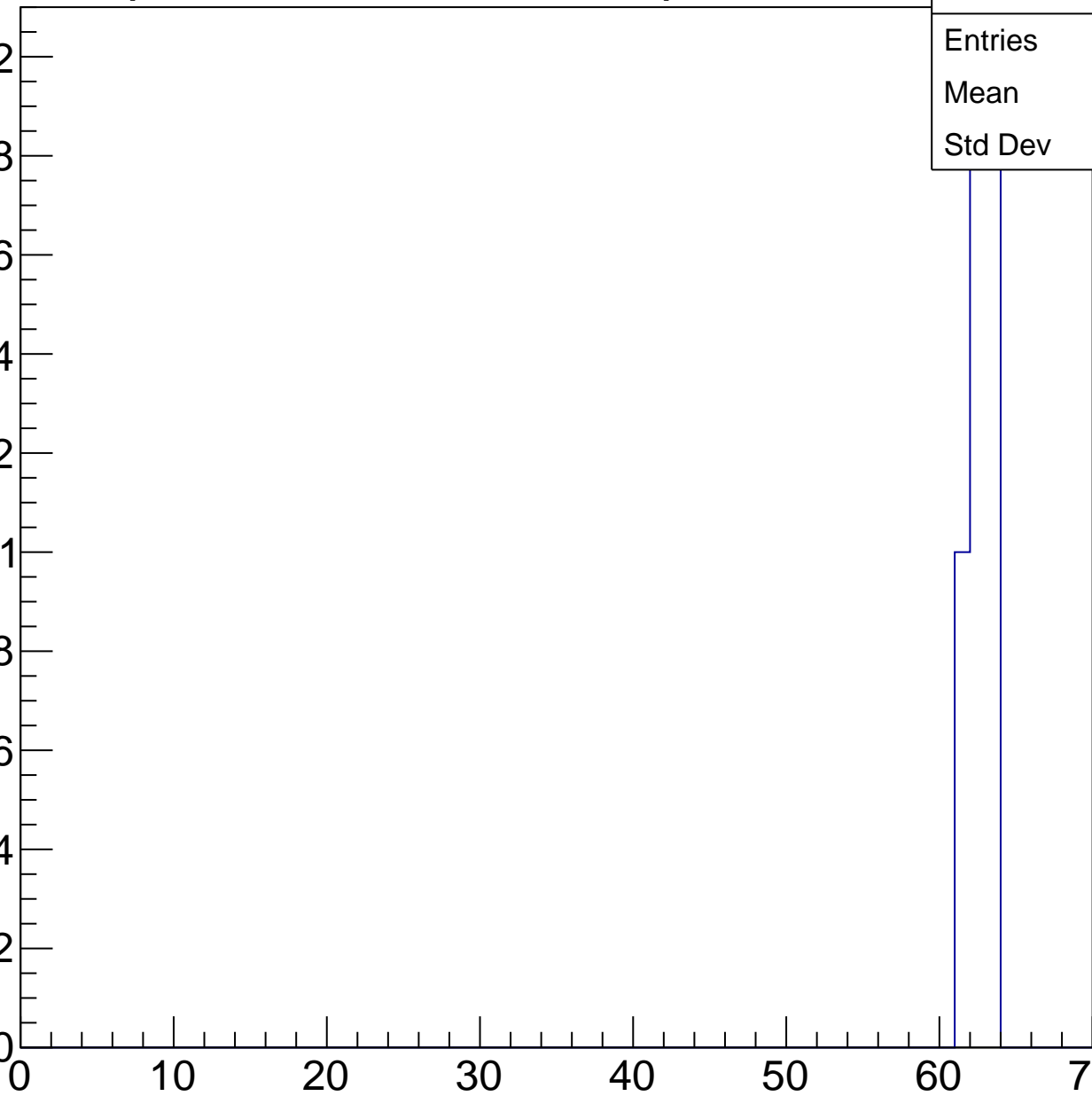
calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 5 |
| Mean | 62.2 |
| Std Dev | 0.7483 |

ampl



B1L100S, U7-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch108, adc0

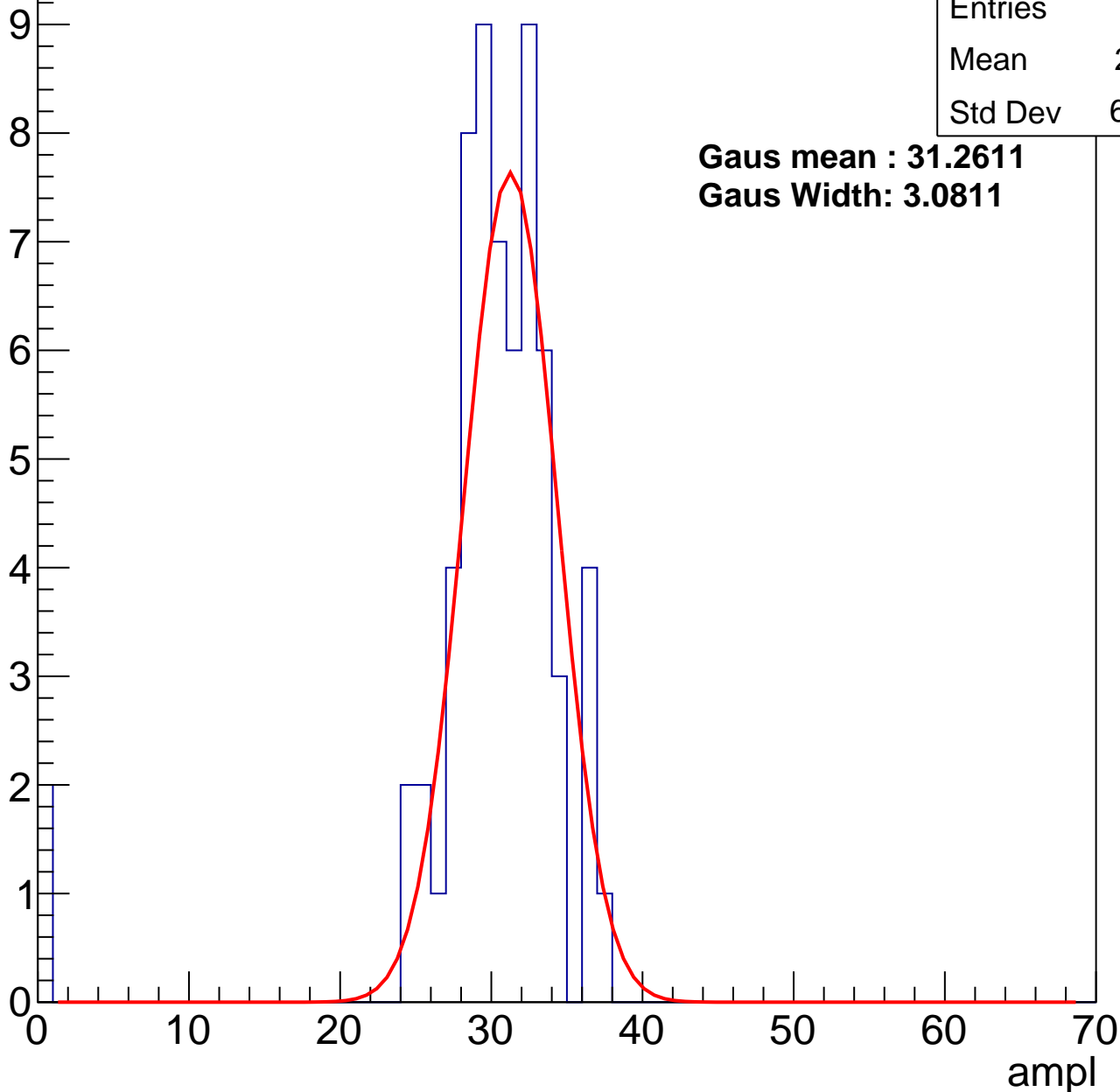
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 29.41 |
| Std Dev | 6.033 |

Gaus mean : 31.2611

Gaus Width: 3.0811

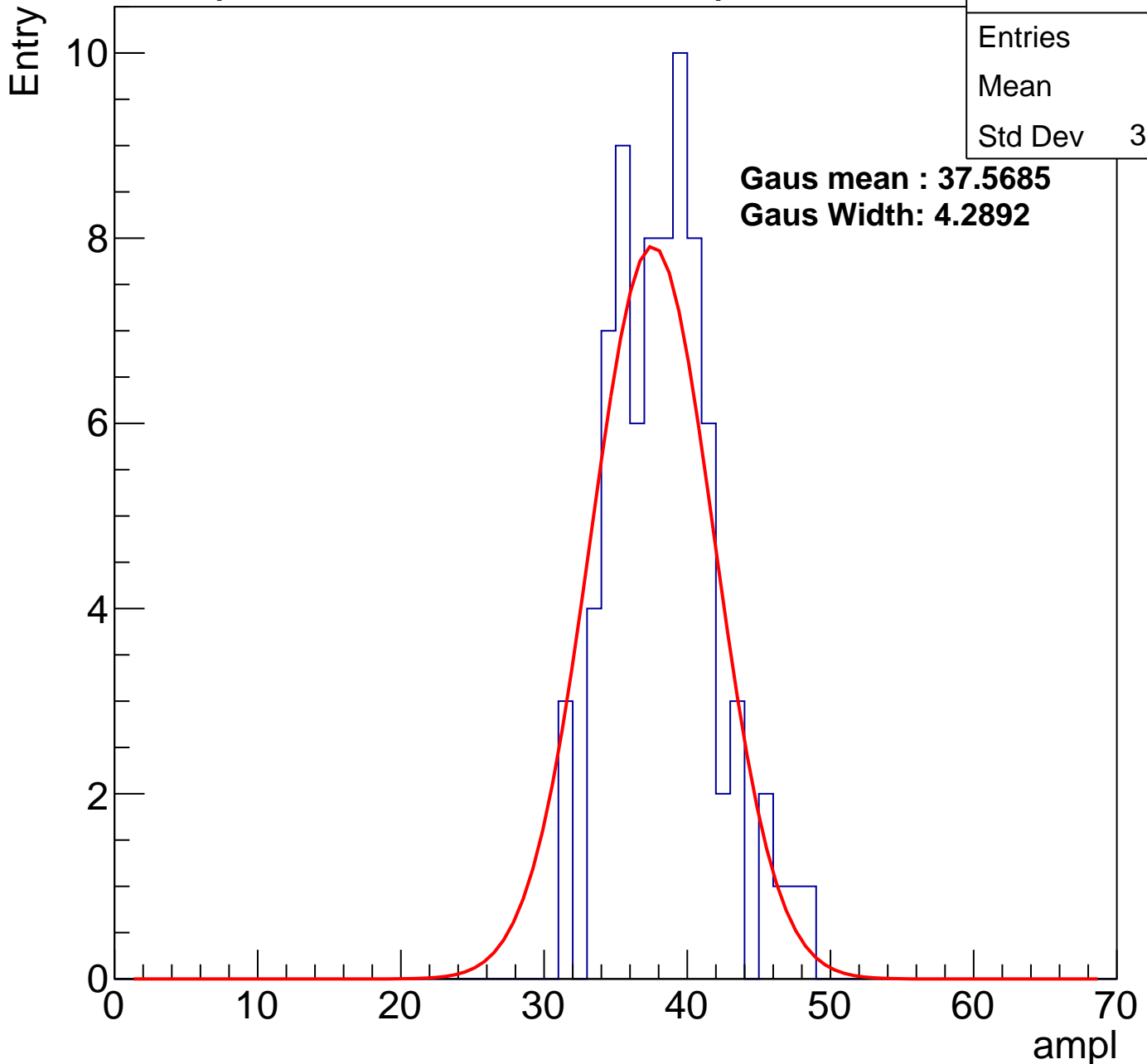


B1L100S, U7-ch108, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 37.9 |
| Std Dev | 3.588 |

Gaus mean : 37.5685
Gaus Width: 4.2892



B1L100S, U7-ch108, adc2

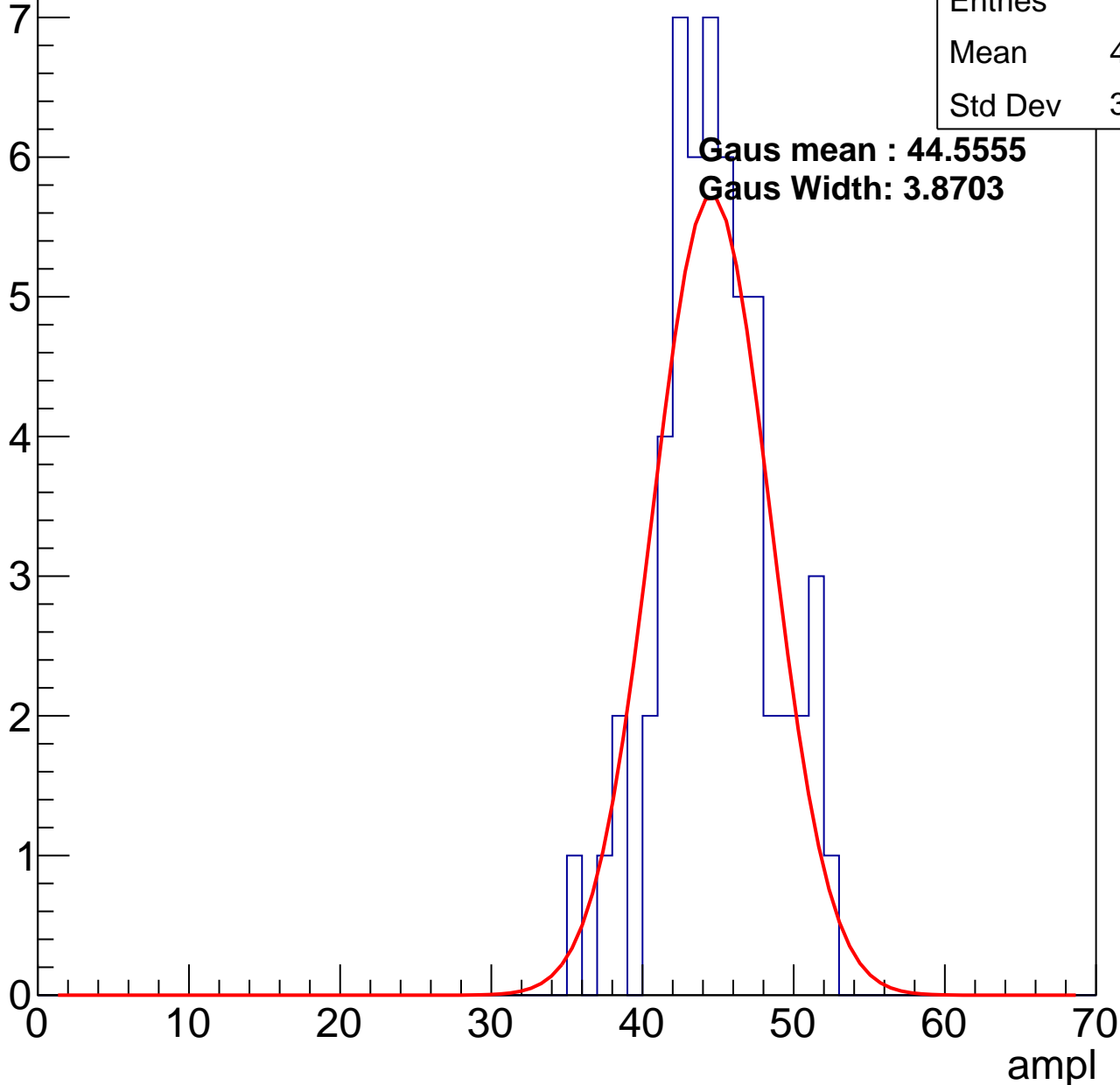
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 44.39 |
| Std Dev | 3.614 |

Gaus mean : 44.5555

Gaus Width: 3.8703

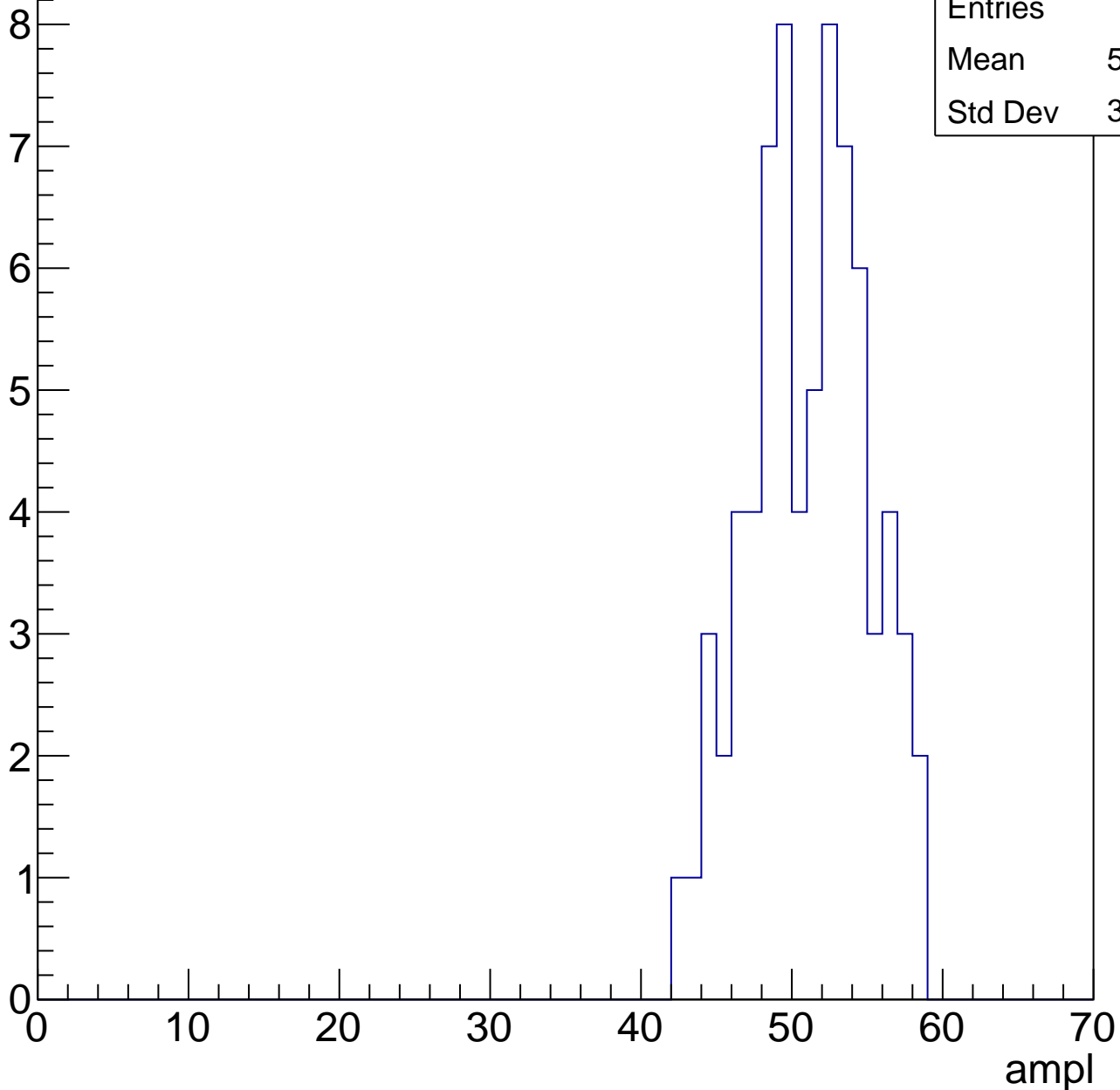


B1L100S, U7-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

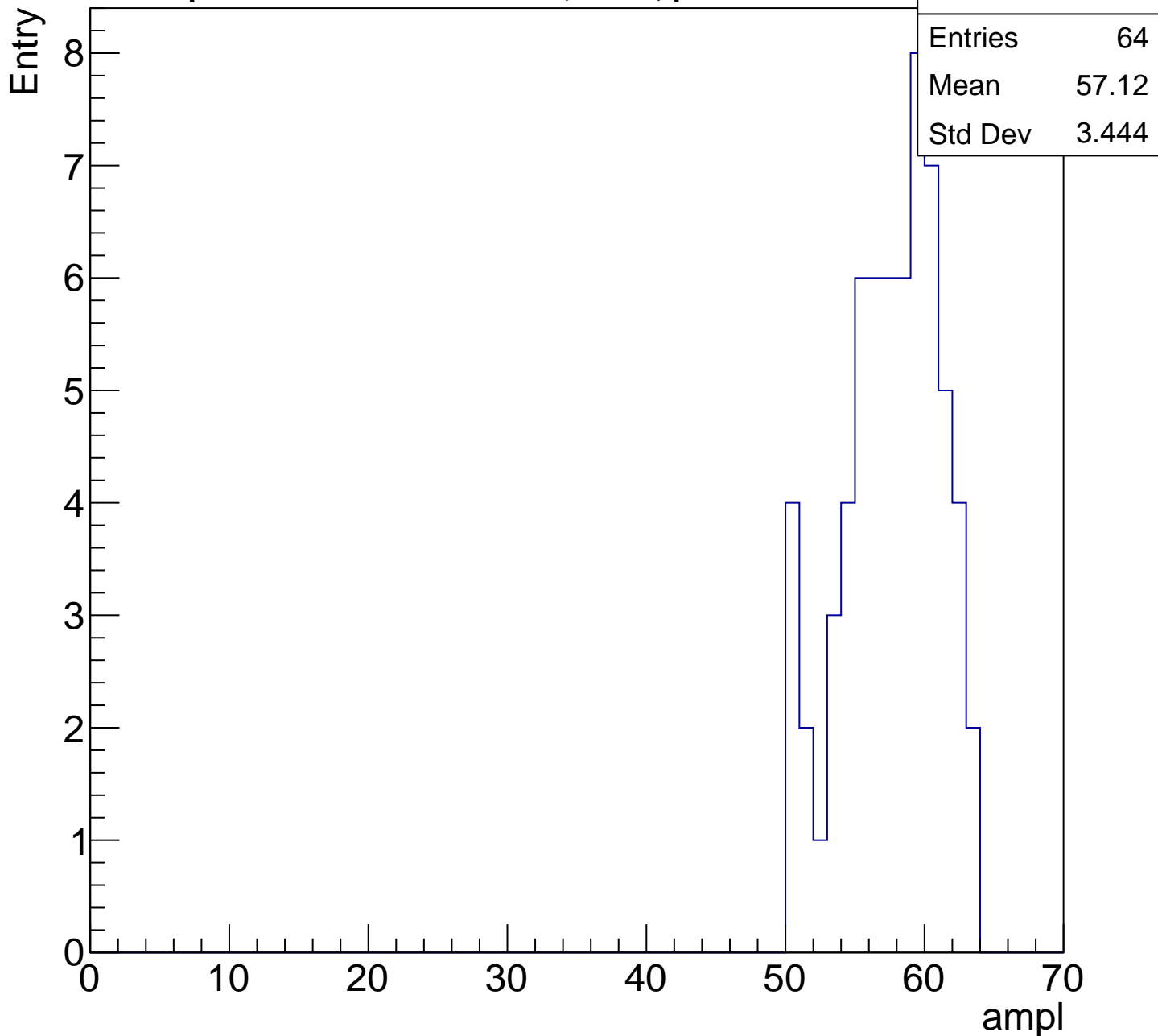
Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 50.68 |
| Std Dev | 3.844 |



B1L100S, U7-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch108, adc5

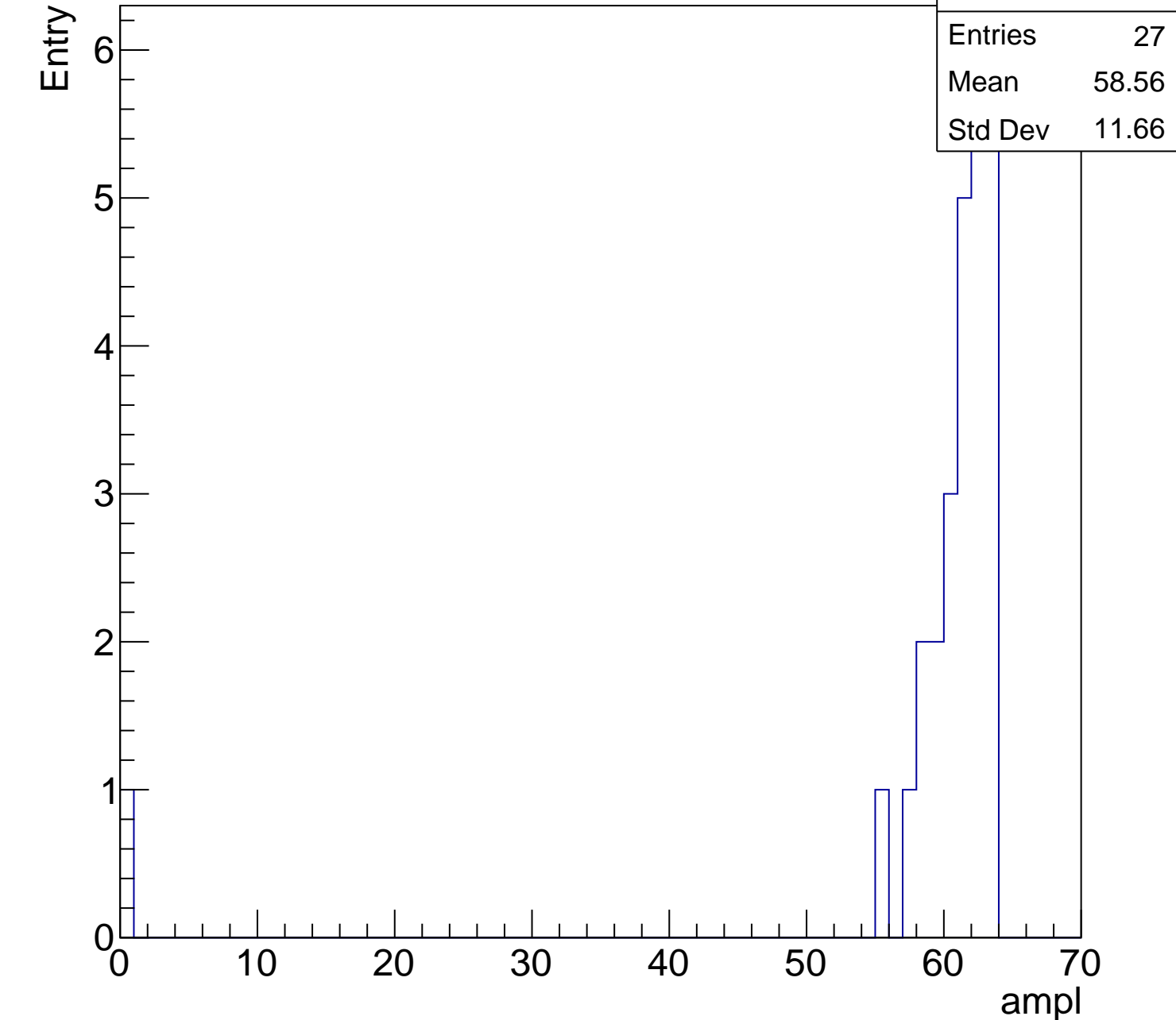
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 27 |
| Mean | 58.56 |
| Std Dev | 11.66 |

ampl



B1L100S, U7-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

ampl

B1L100S, U7-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 9 |
| Std Dev | 9 |

B1L100S, U7-ch109, adc0

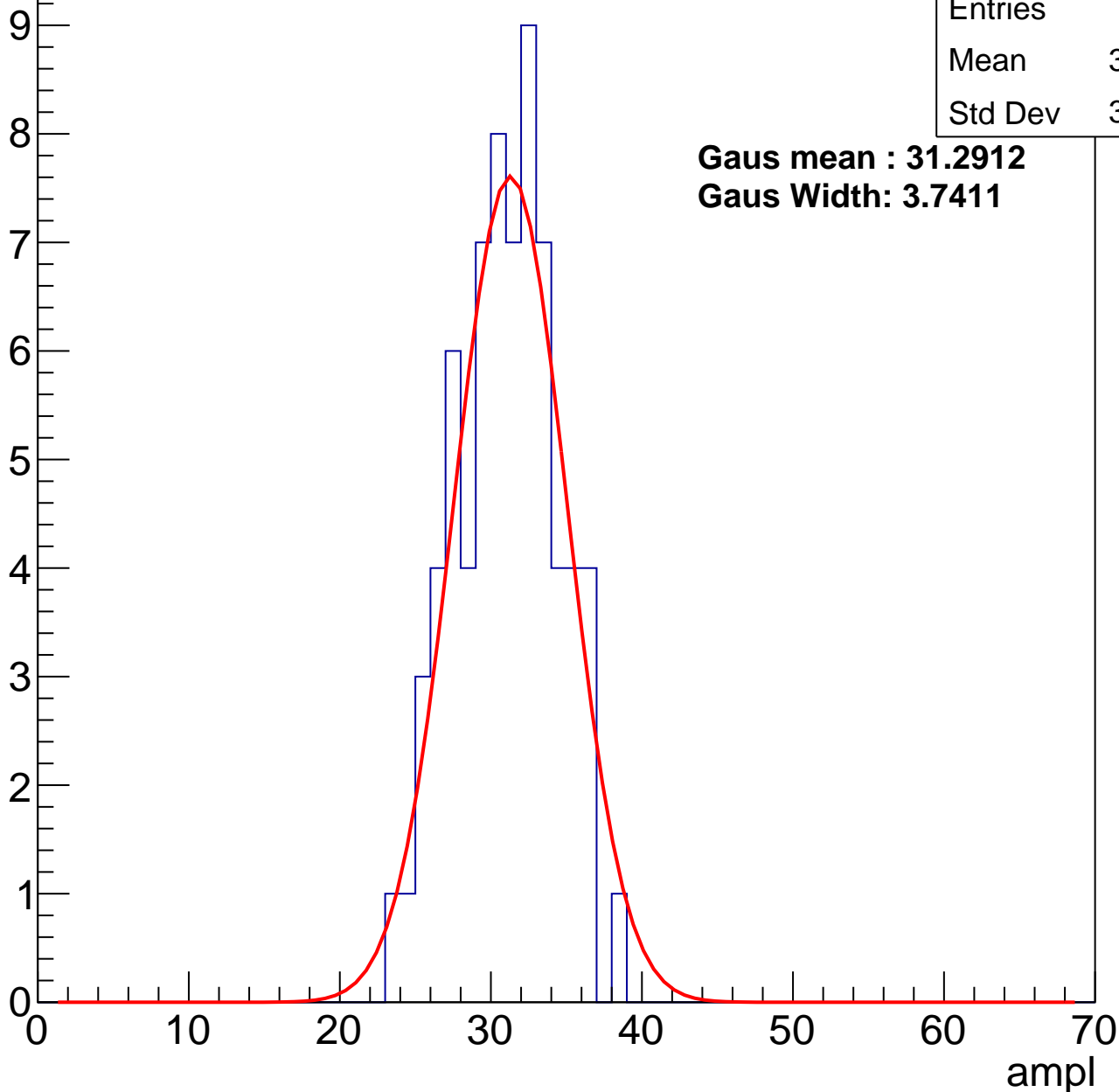
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 30.53 |
| Std Dev | 3.294 |

Gaus mean : 31.2912

Gaus Width: 3.7411



B1L100S, U7-ch109, adc1

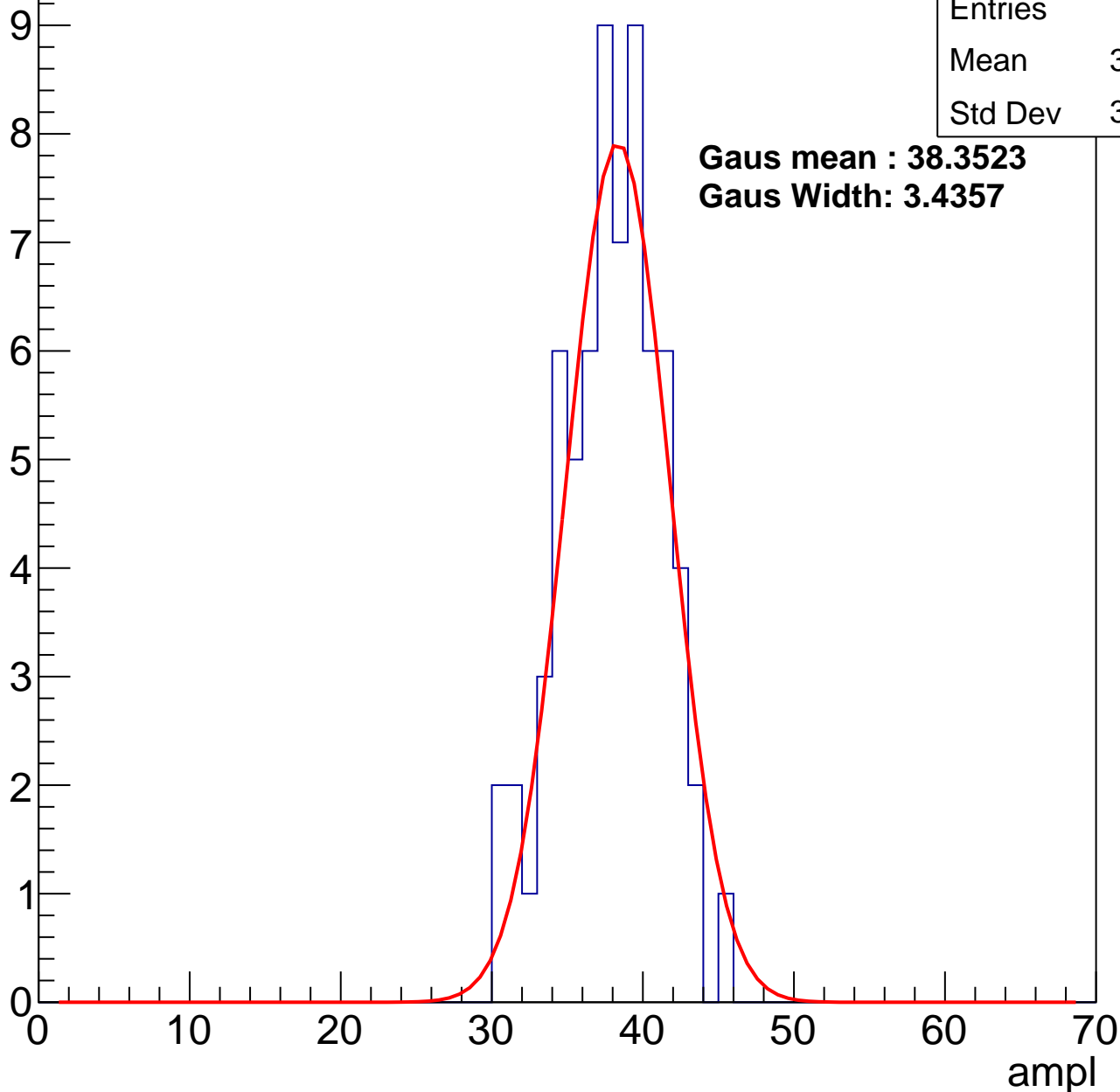
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 37.43 |
| Std Dev | 3.273 |

Gaus mean : 38.3523

Gaus Width: 3.4357



B1L100S, U7-ch109, adc2

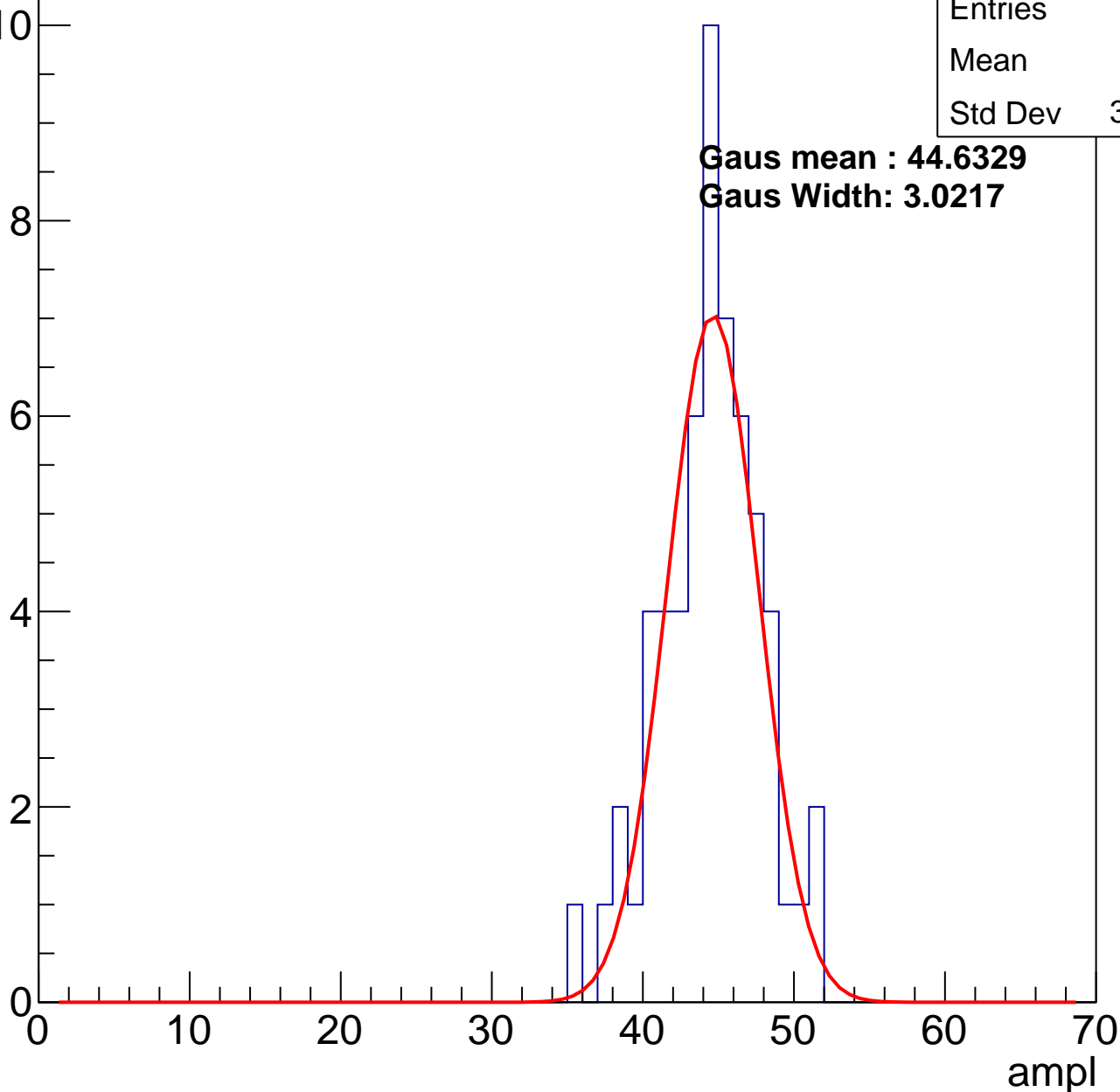
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 44 |
| Std Dev | 3.314 |

Gaus mean : 44.6329

Gaus Width: 3.0217

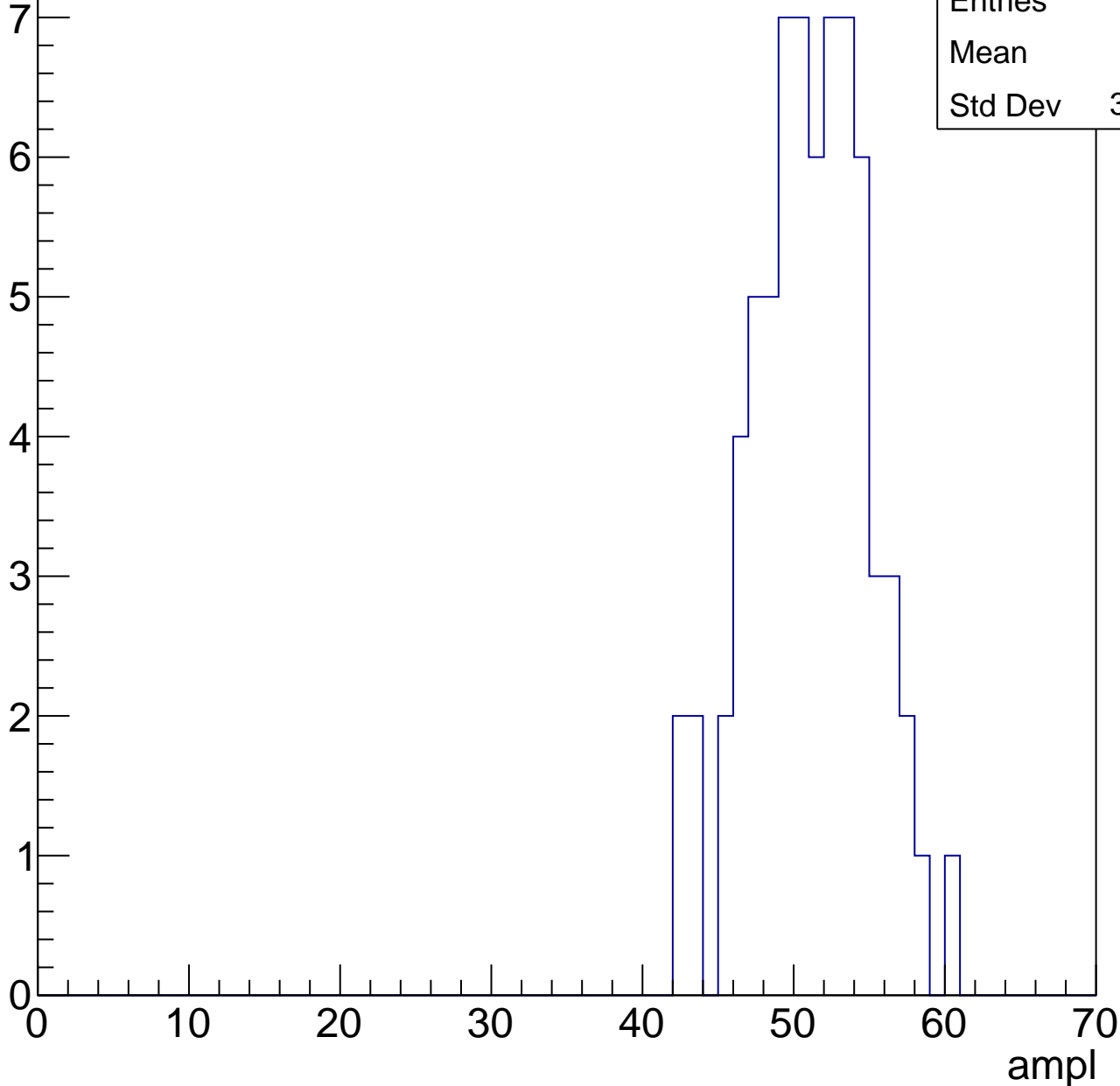


B1L100S, U7-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

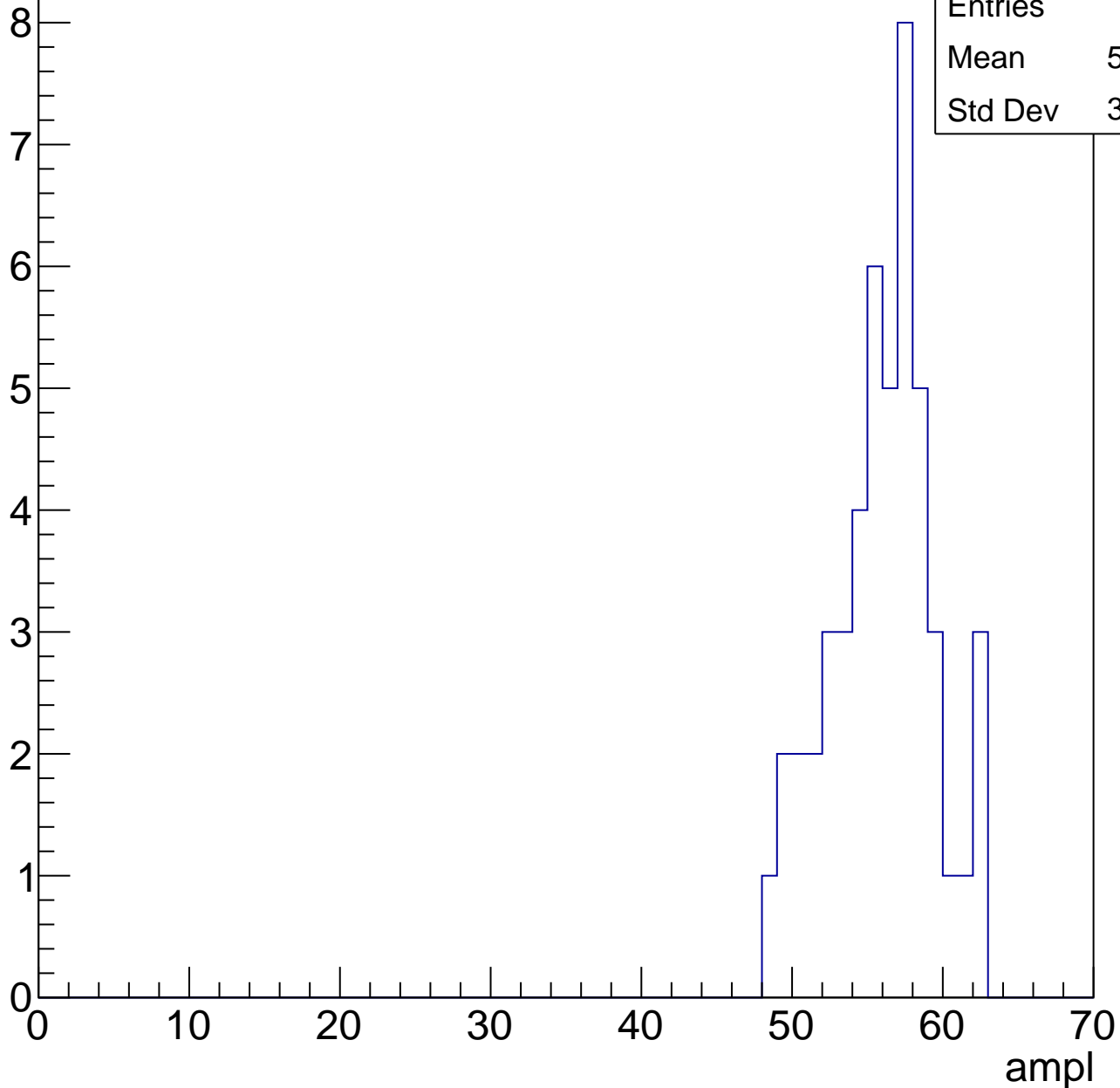
| | |
|---------|-------|
| Entries | 70 |
| Mean | 50.6 |
| Std Dev | 3.826 |



B1L100S, U7-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

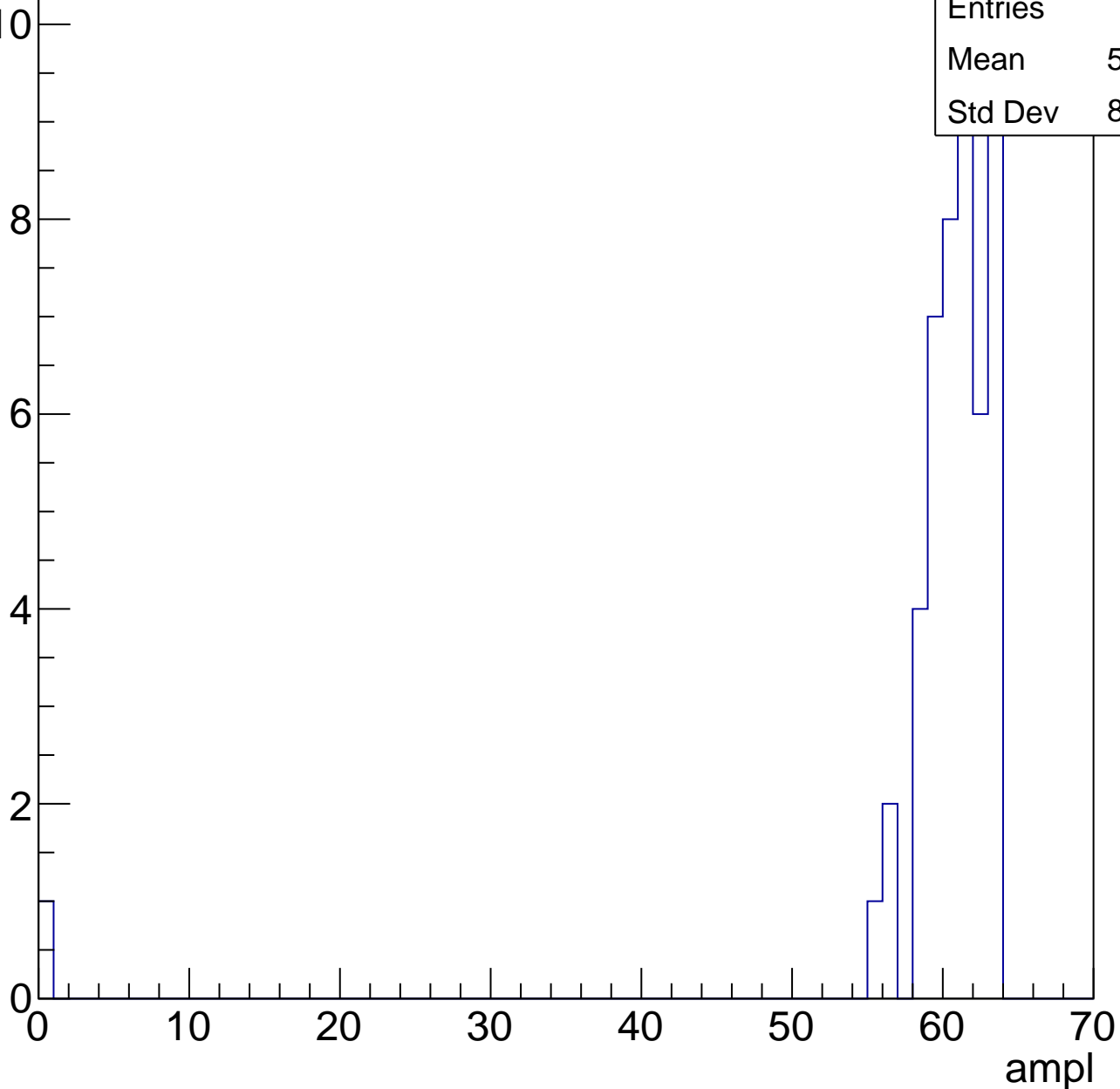


B1L100S, U7-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.23 |
| Std Dev | 8.868 |



B1L100S, U7-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch110, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 28.3 |
| Std Dev | 4.902 |

Gaus mean : 29.7802

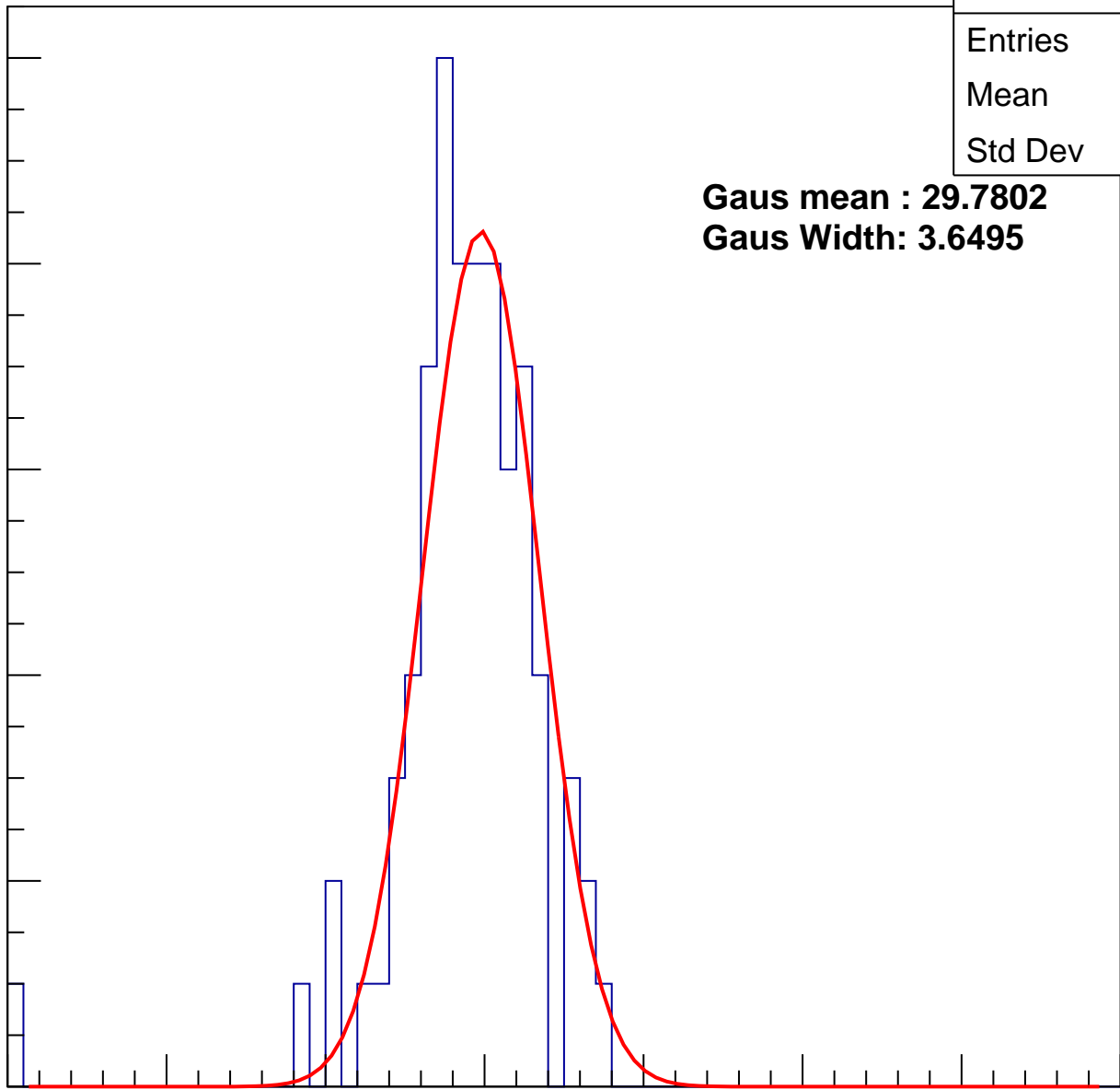
Gaus Width: 3.6495

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch110, adc1

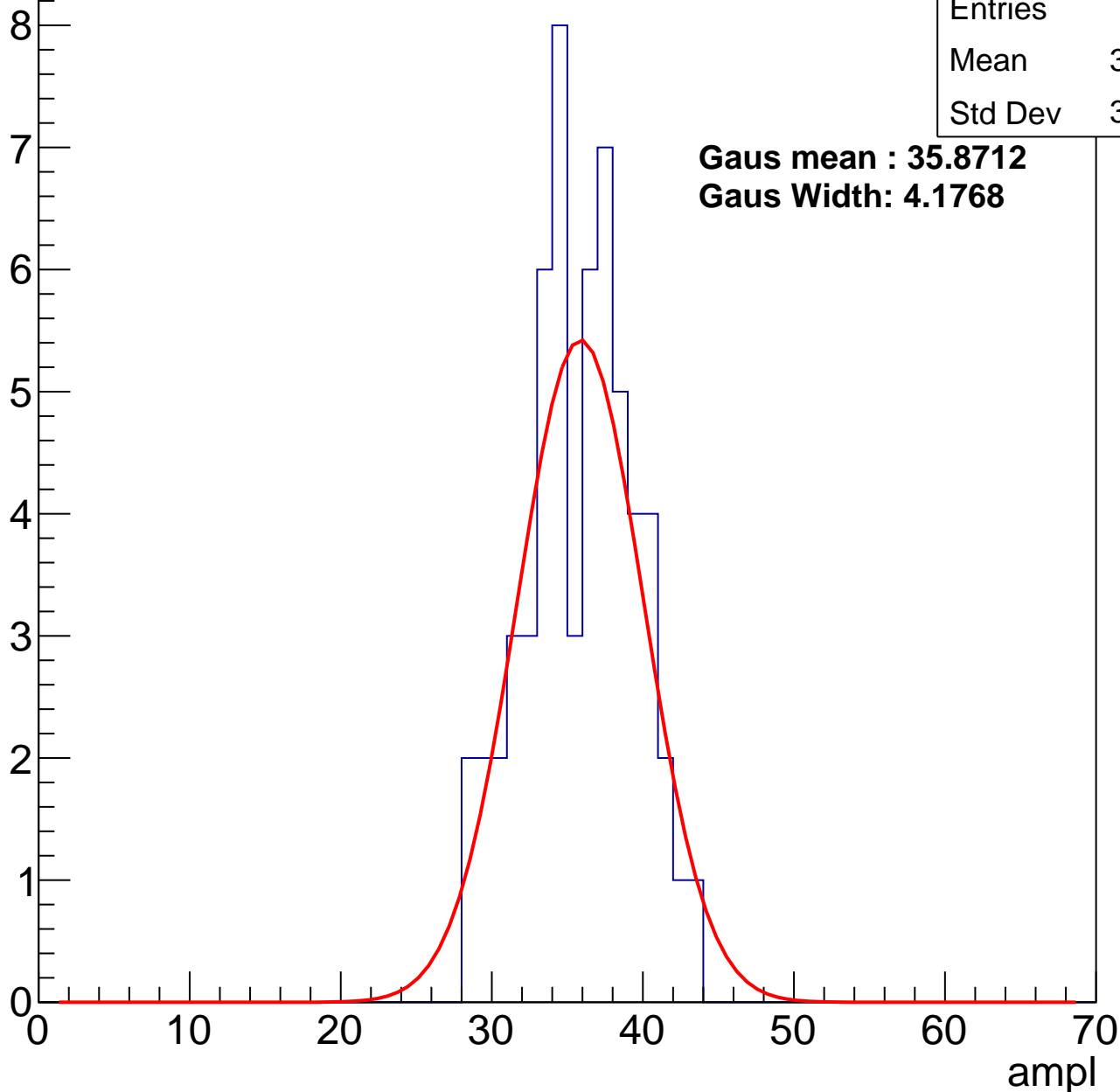
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.36 |
| Std Dev | 3.559 |

Gaus mean : 35.8712

Gaus Width: 4.1768



B1L100S, U7-ch110, adc2

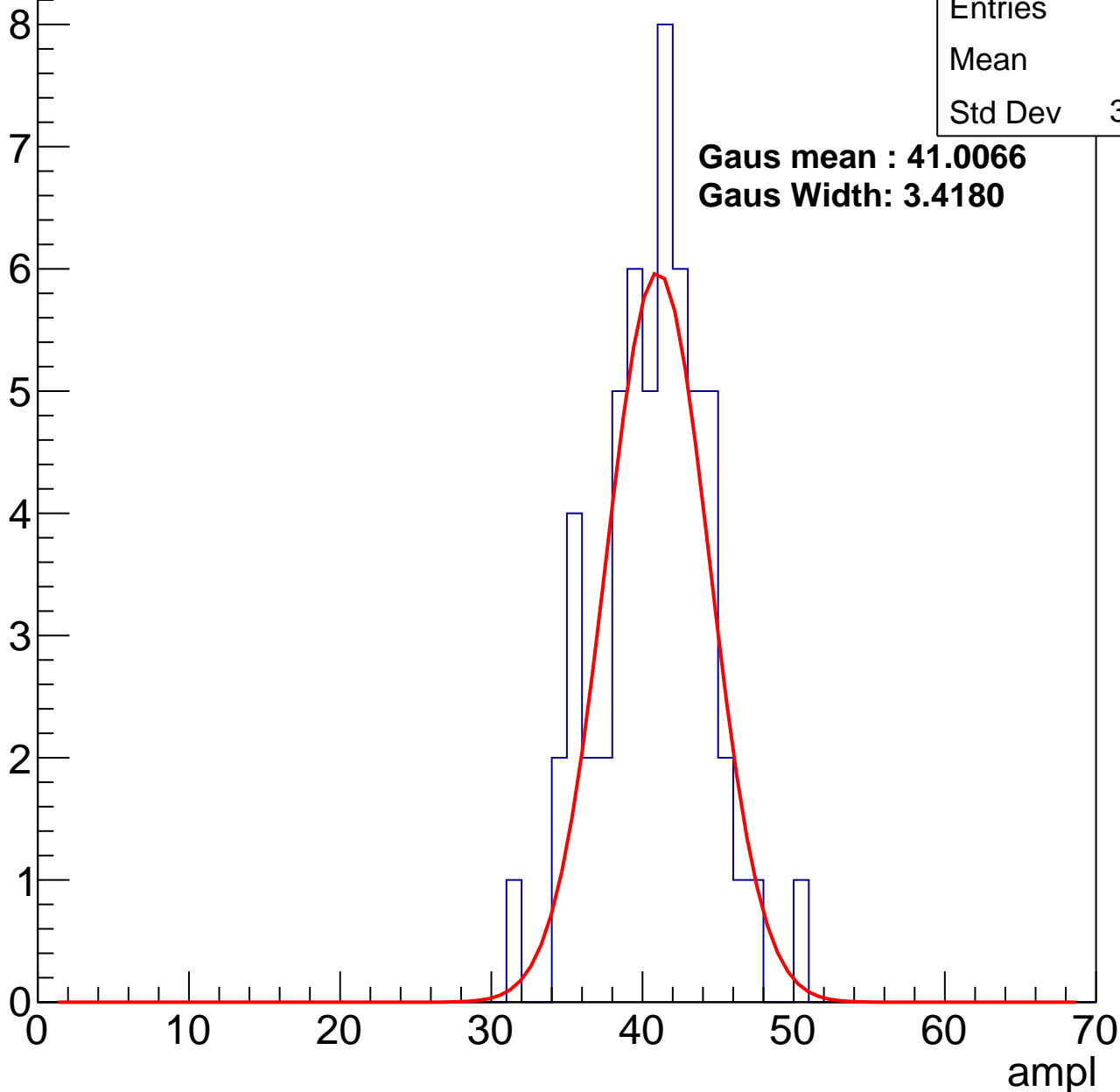
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 40.3 |
| Std Dev | 3.575 |

Gaus mean : 41.0066

Gaus Width: 3.4180



B1L100S, U7-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

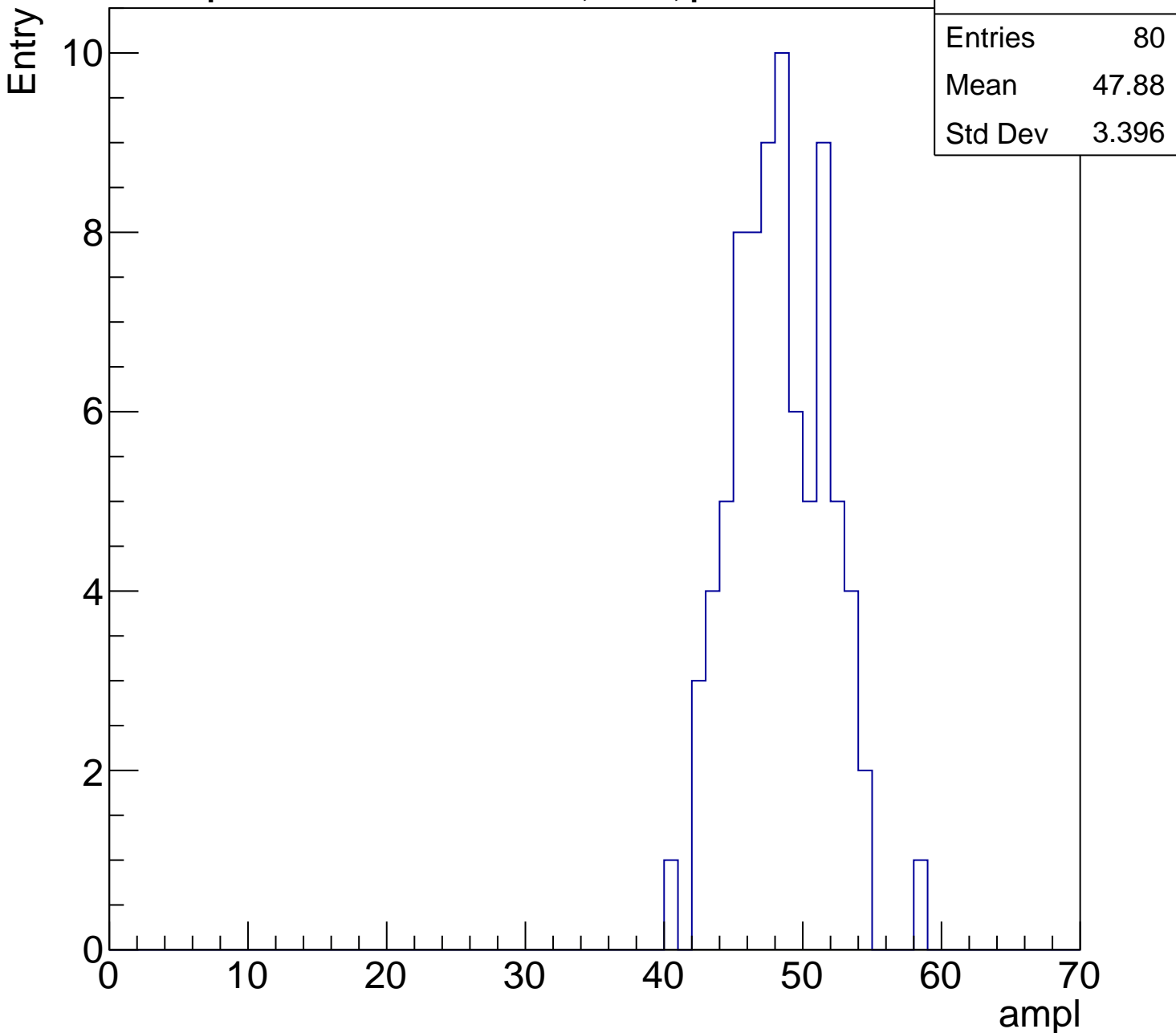
| | |
|---------|-------|
| Entries | 80 |
| Mean | 47.88 |
| Std Dev | 3.396 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

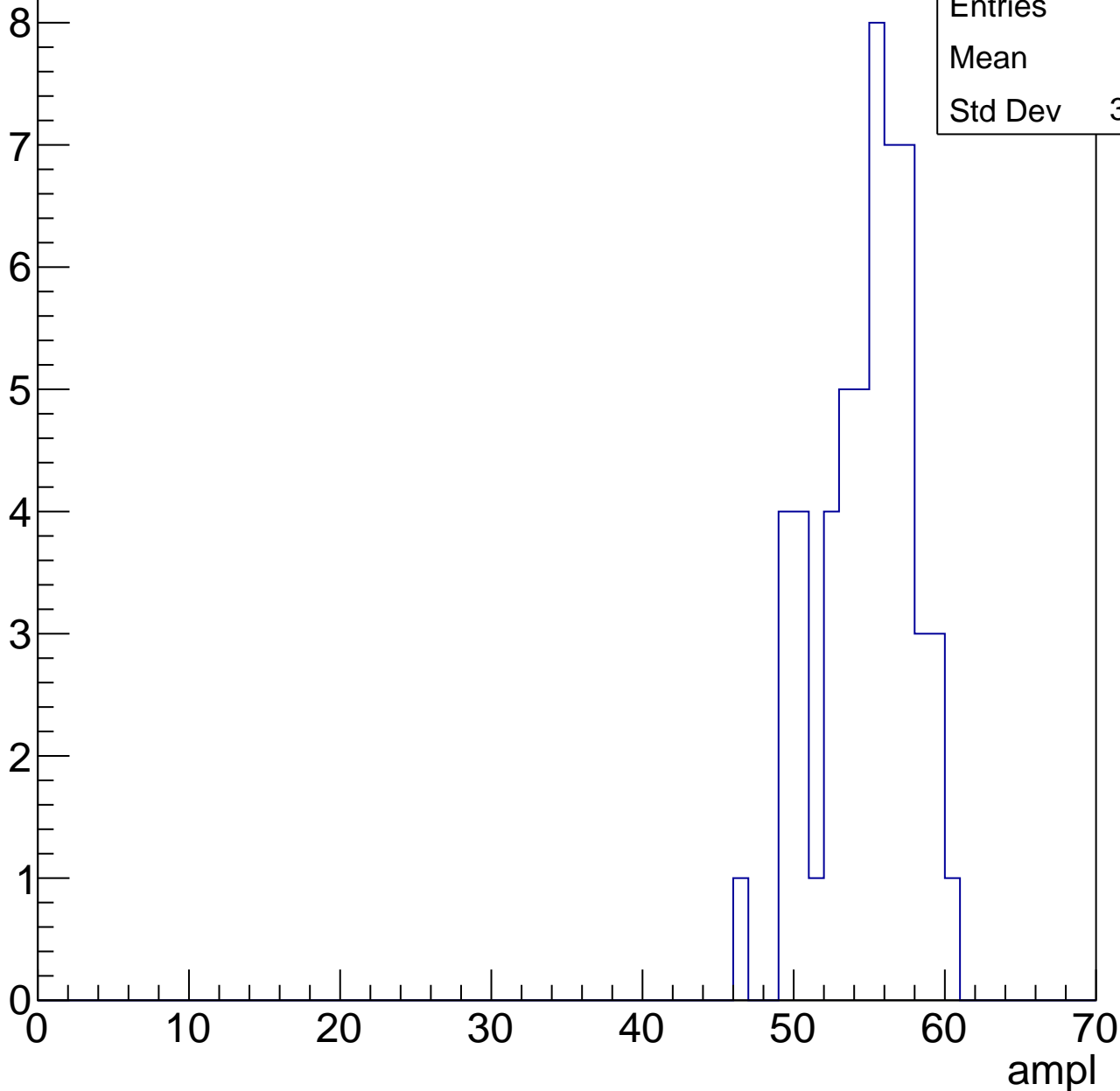


B1L100S, U7-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 54.3 |
| Std Dev | 3.106 |

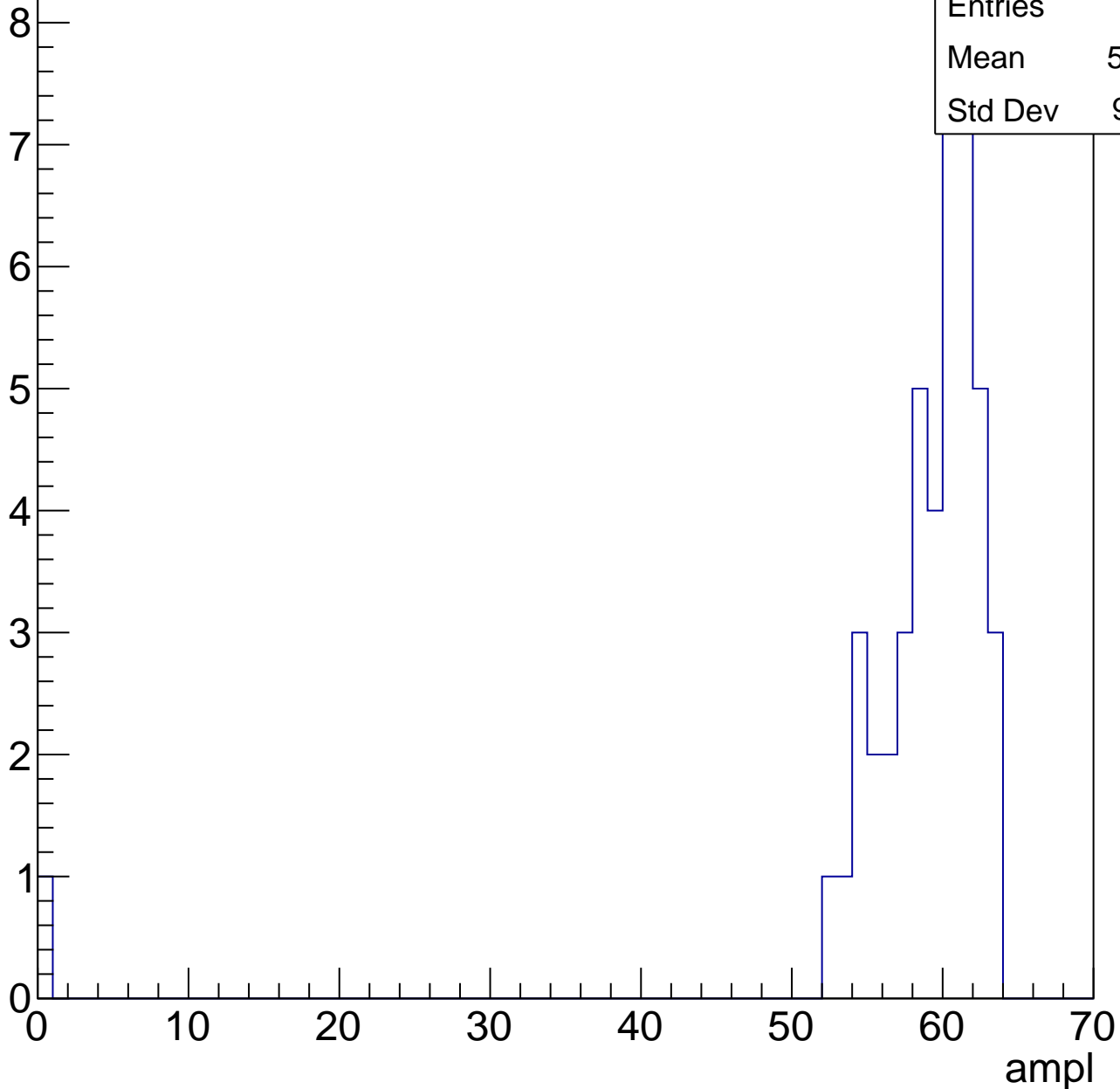


B1L100S, U7-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 57.67 |
| Std Dev | 9.041 |

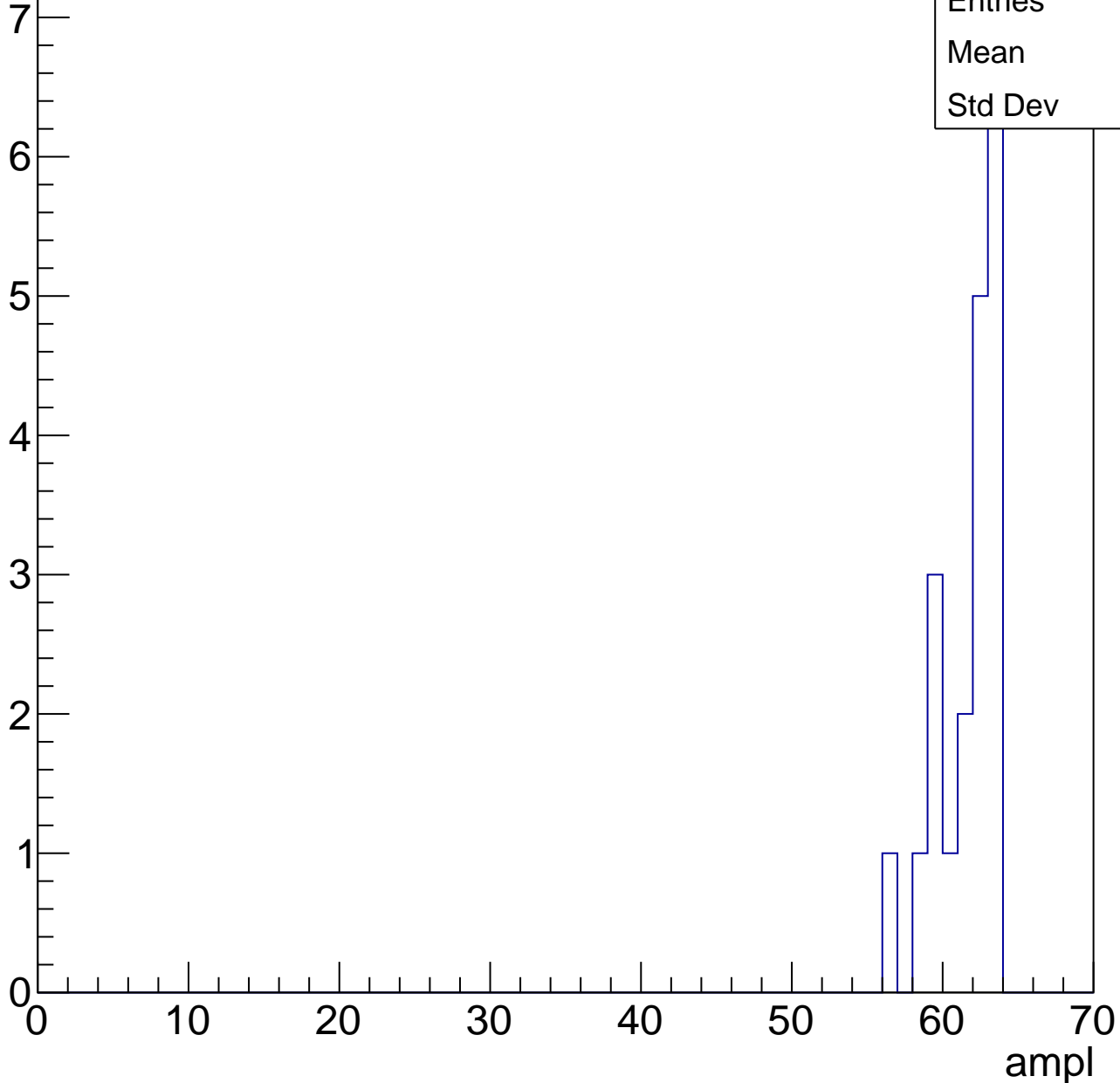


B1L100S, U7-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|------|
| Entries | 20 |
| Mean | 61.2 |
| Std Dev | 1.99 |



B1L100S, U7-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch111, adc0

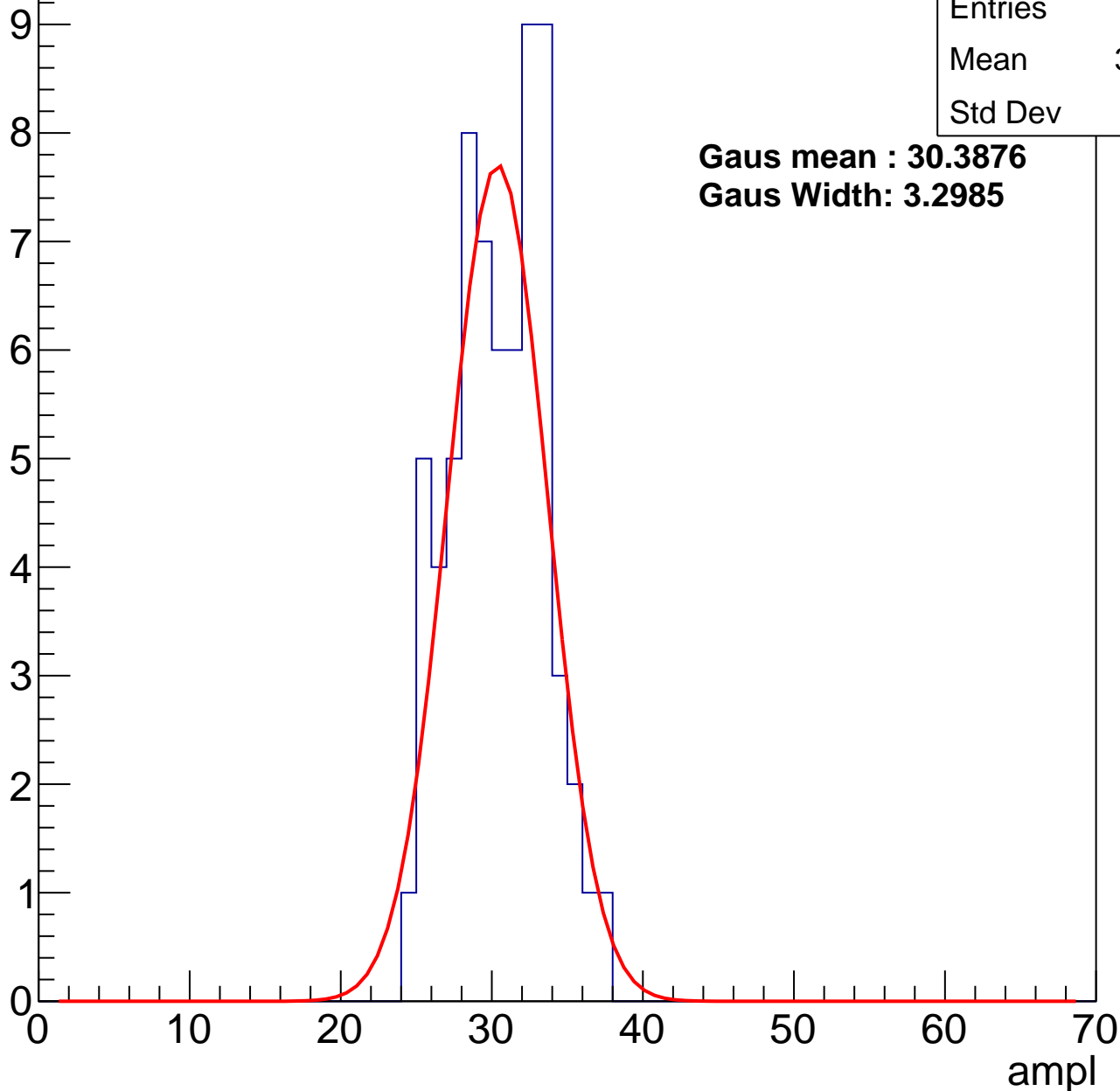
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.01 |
| Std Dev | 3.03 |

Gaus mean : 30.3876

Gaus Width: 3.2985



B1L100S, U7-ch111, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 79 |
| Mean | 37.78 |
| Std Dev | 3.669 |

Gaus mean : 38.8049

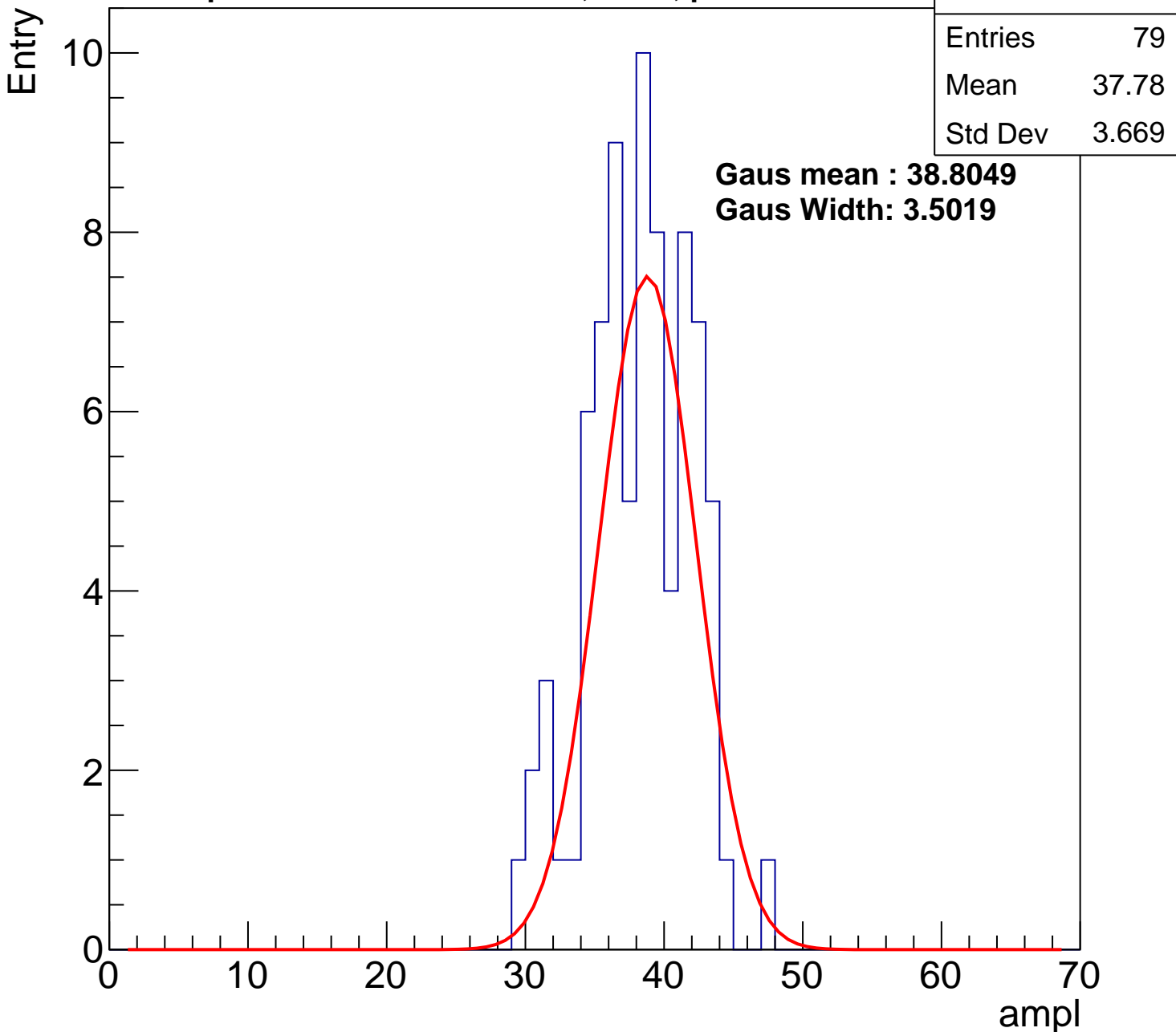
Gaus Width: 3.5019

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U7-ch111, adc2

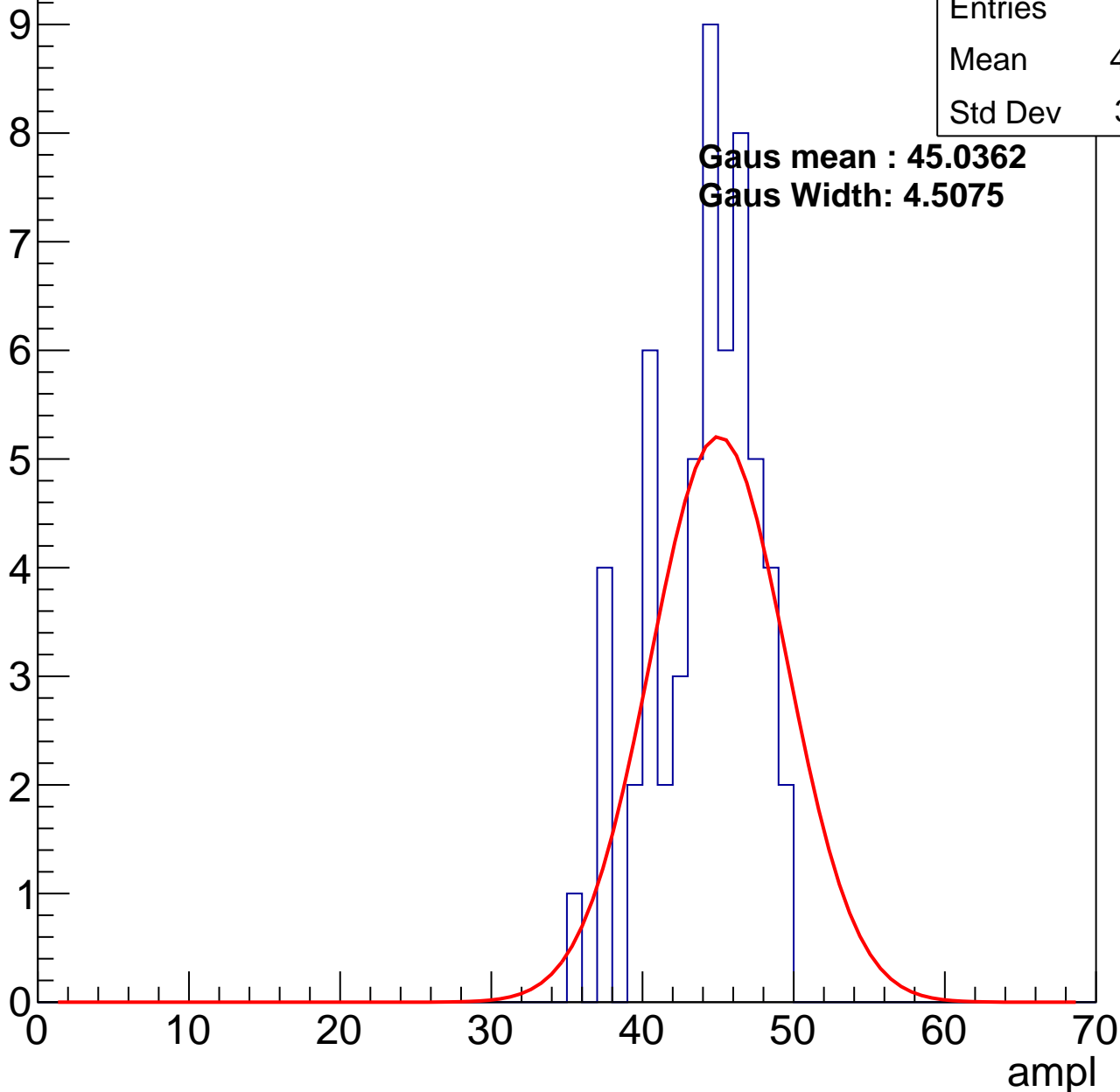
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 43.56 |
| Std Dev | 3.361 |

Gaus mean : 45.0362

Gaus Width: 4.5075



B1L100S, U7-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

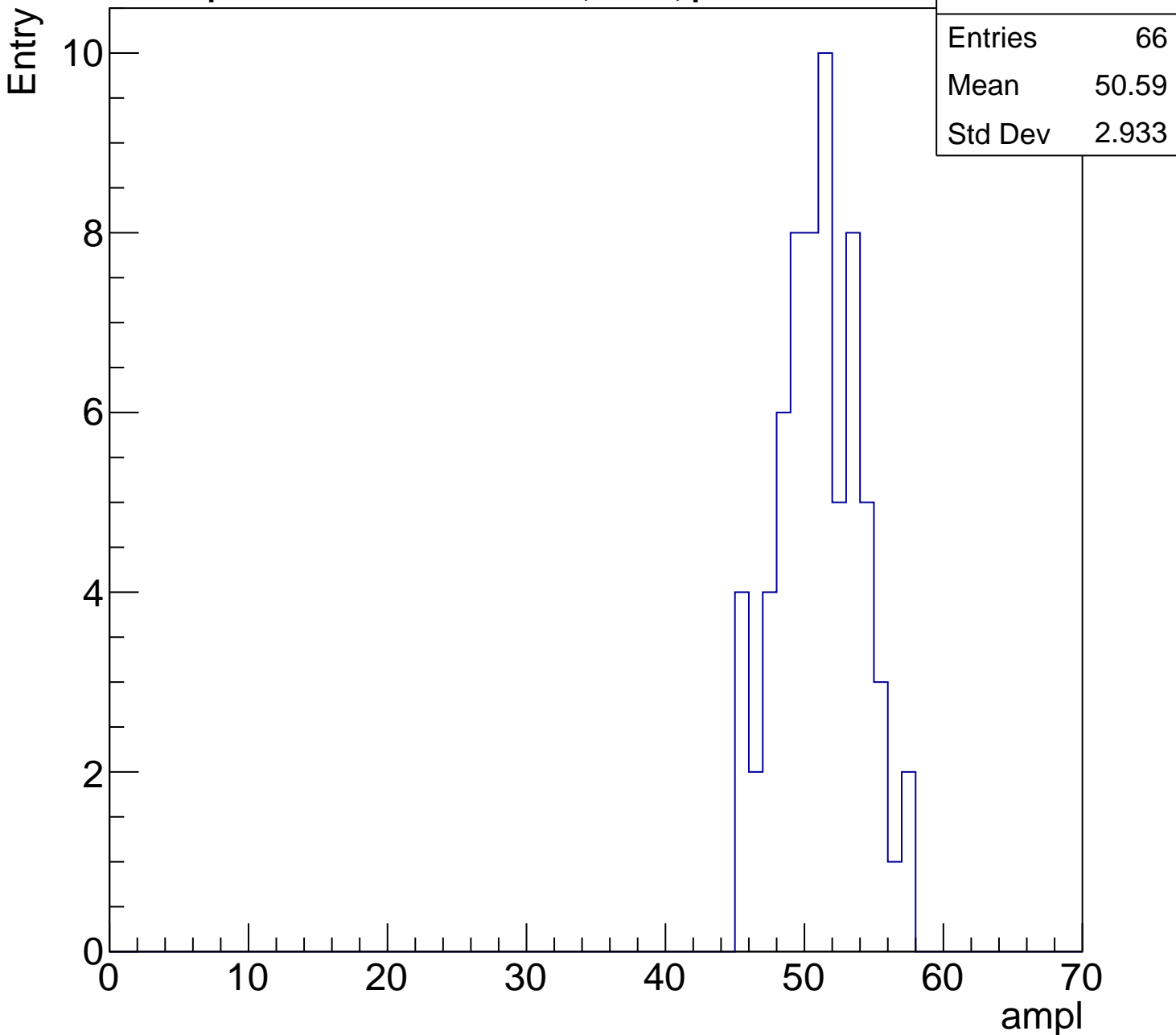
| | |
|---------|-------|
| Entries | 66 |
| Mean | 50.59 |
| Std Dev | 2.933 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

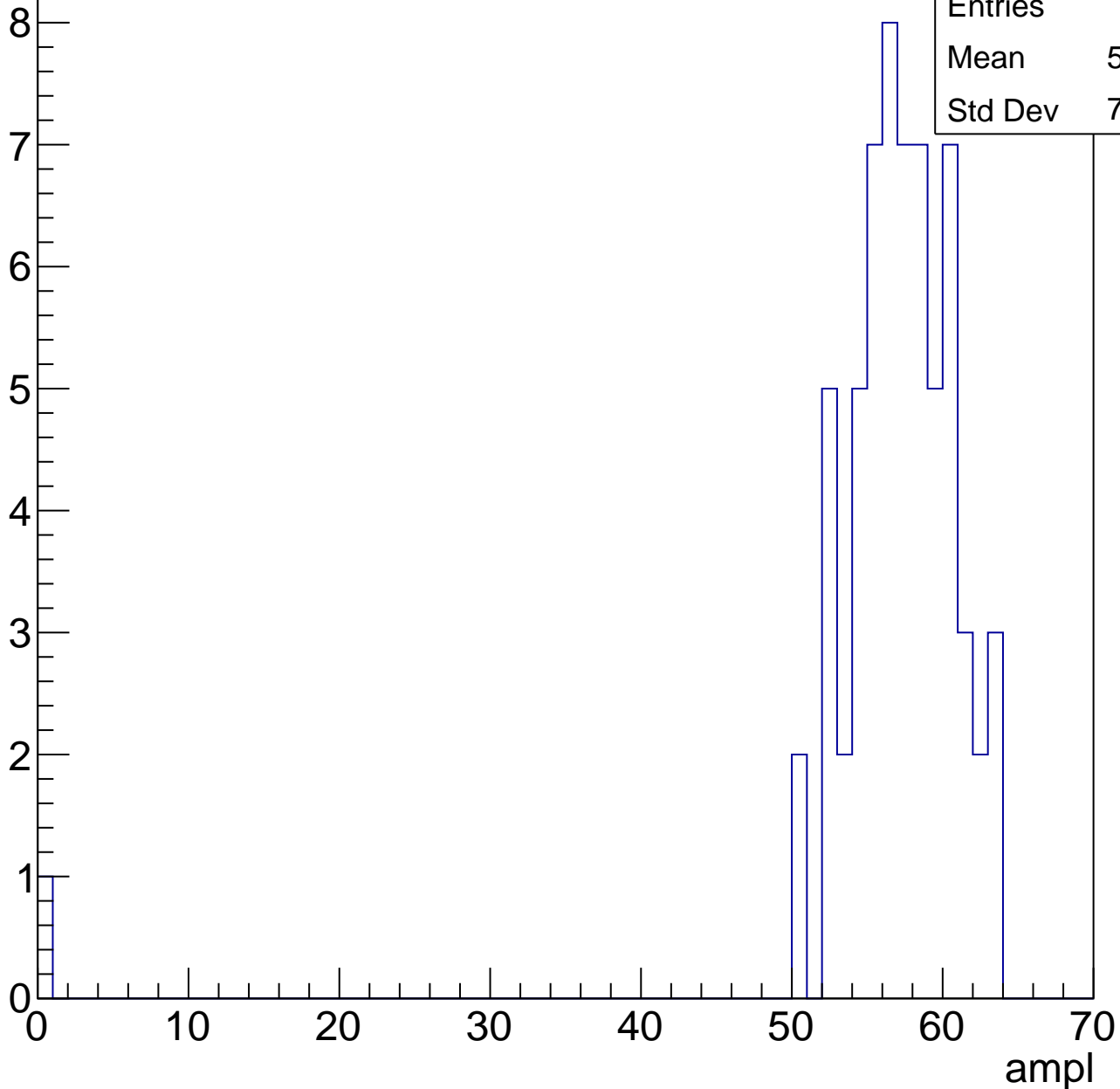


B1L100S, U7-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 56.02 |
| Std Dev | 7.725 |

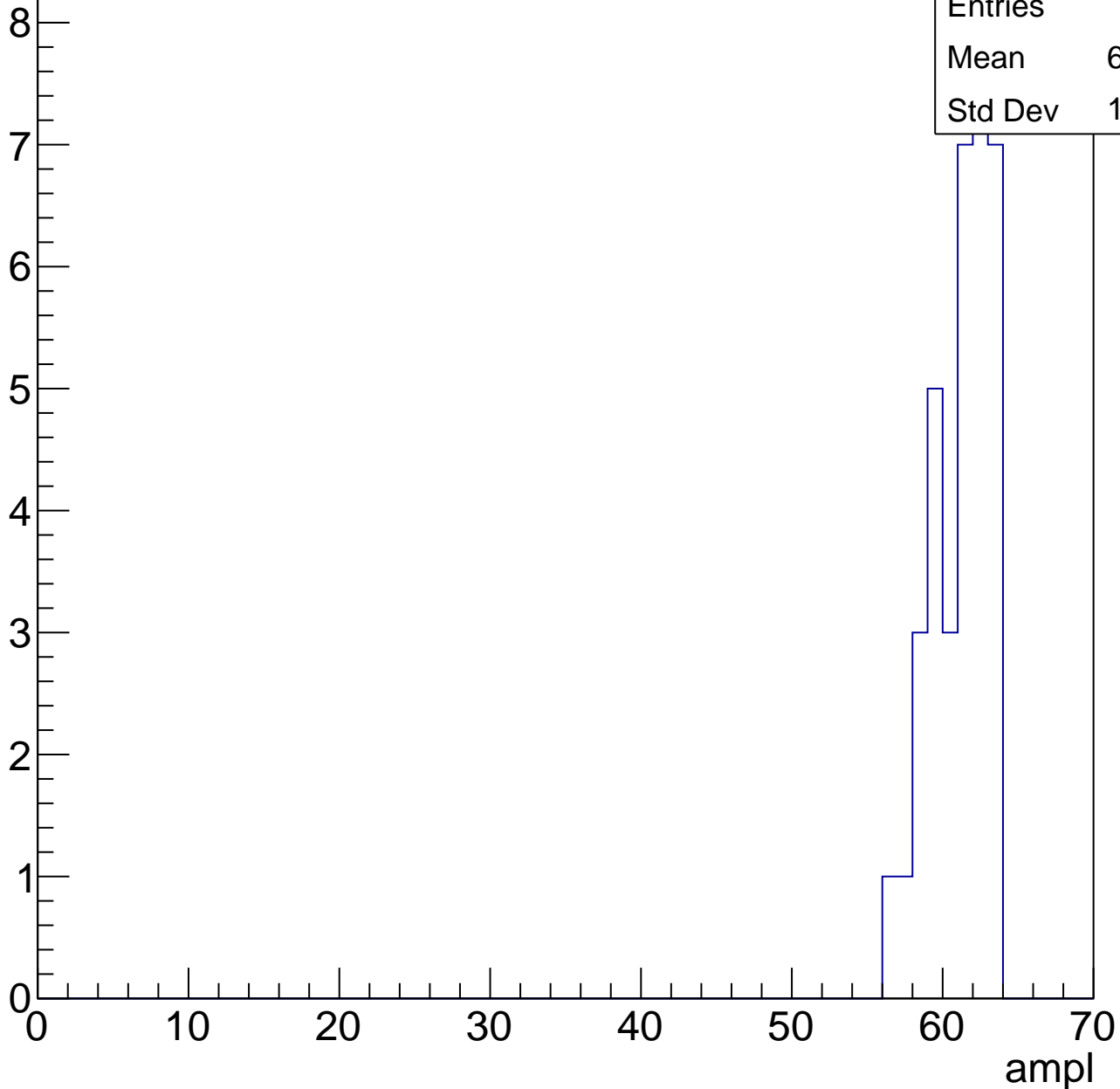


B1L100S, U7-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 60.74 |
| Std Dev | 1.887 |



B1L100S, U7-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch112, adc0

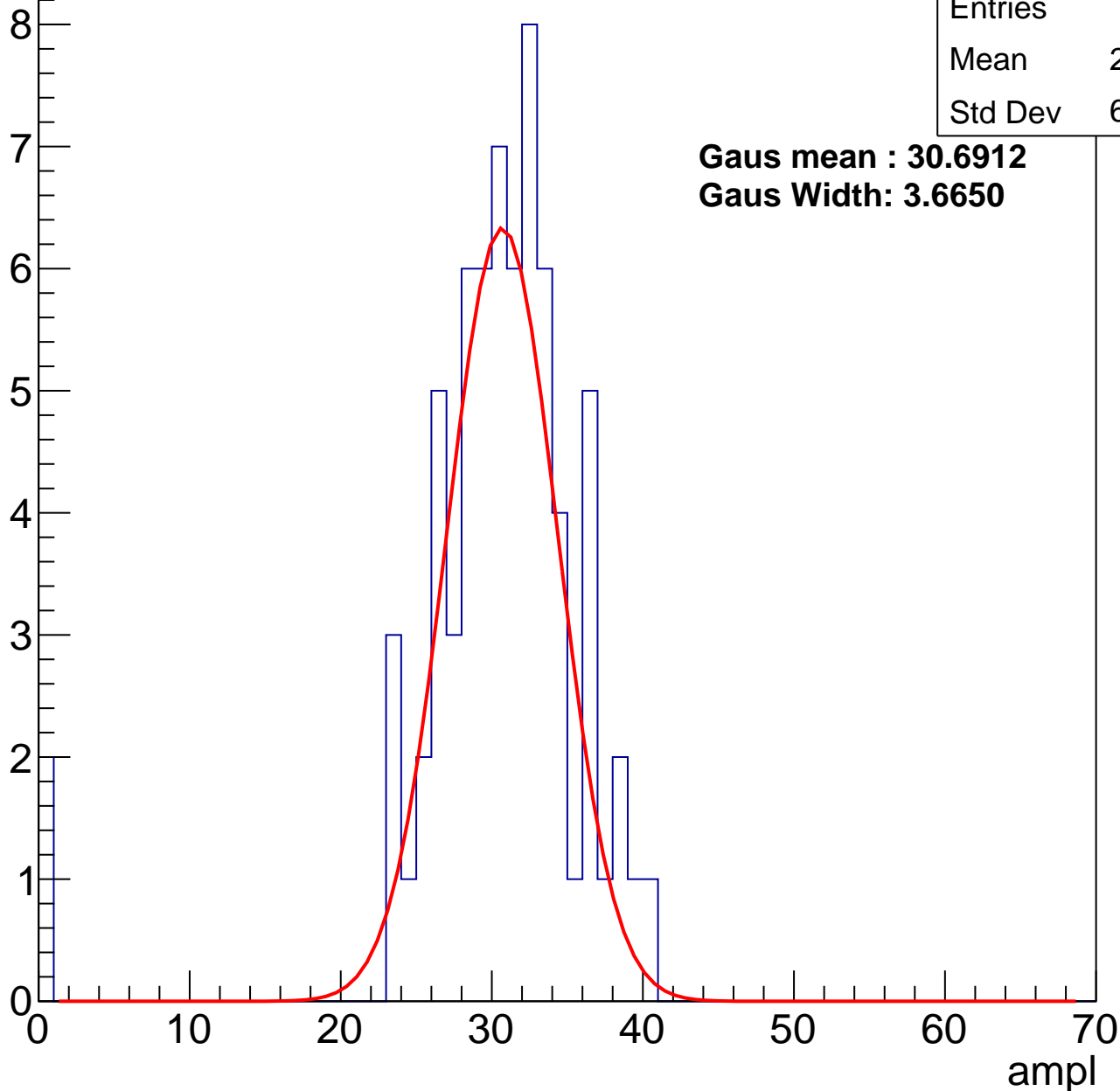
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 29.84 |
| Std Dev | 6.422 |

Gaus mean : 30.6912

Gaus Width: 3.6650



B1L100S, U7-ch112, adc1

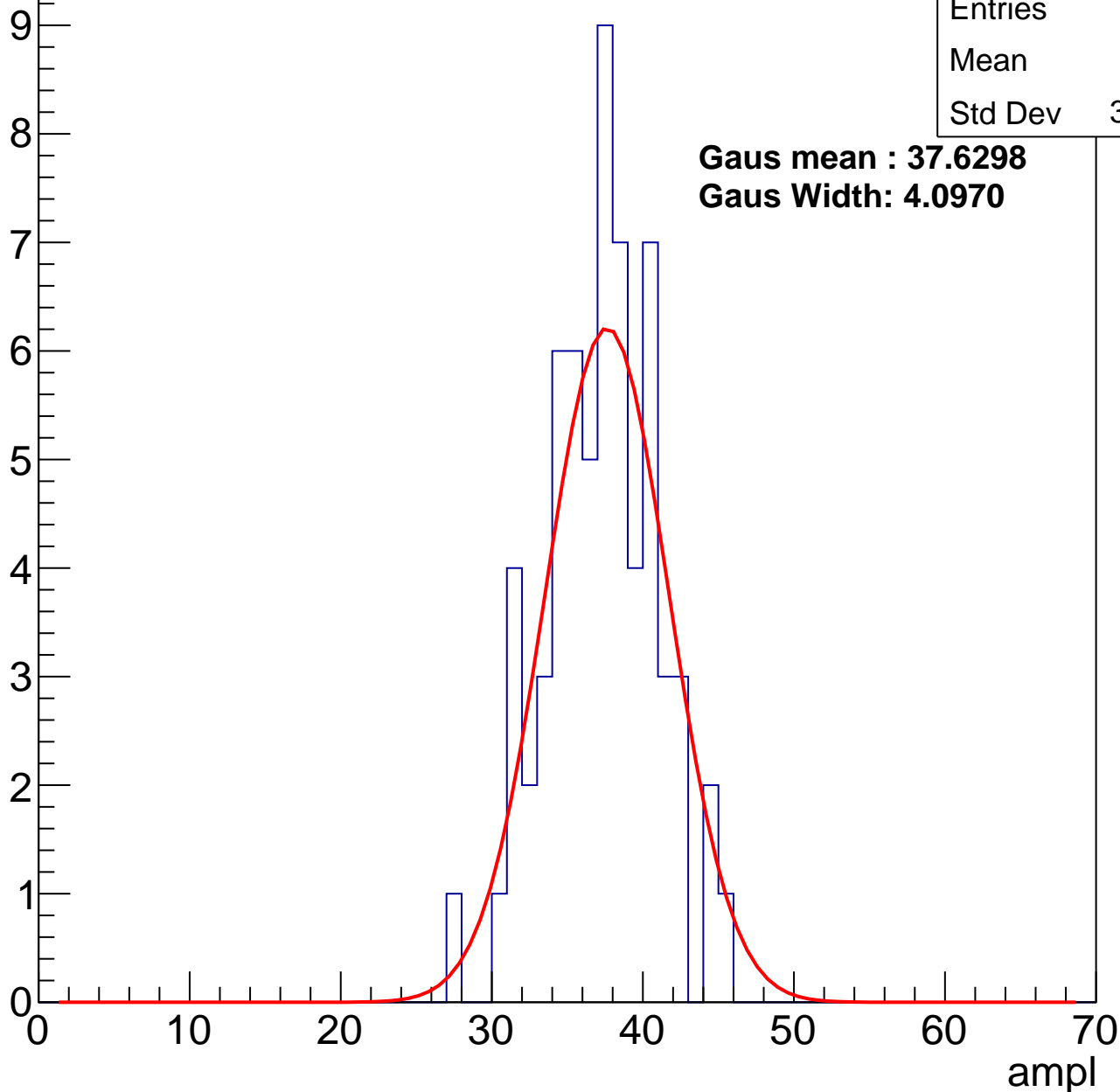
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.8 |
| Std Dev | 3.628 |

Gaus mean : 37.6298

Gaus Width: 4.0970



B1L100S, U7-ch112, adc2

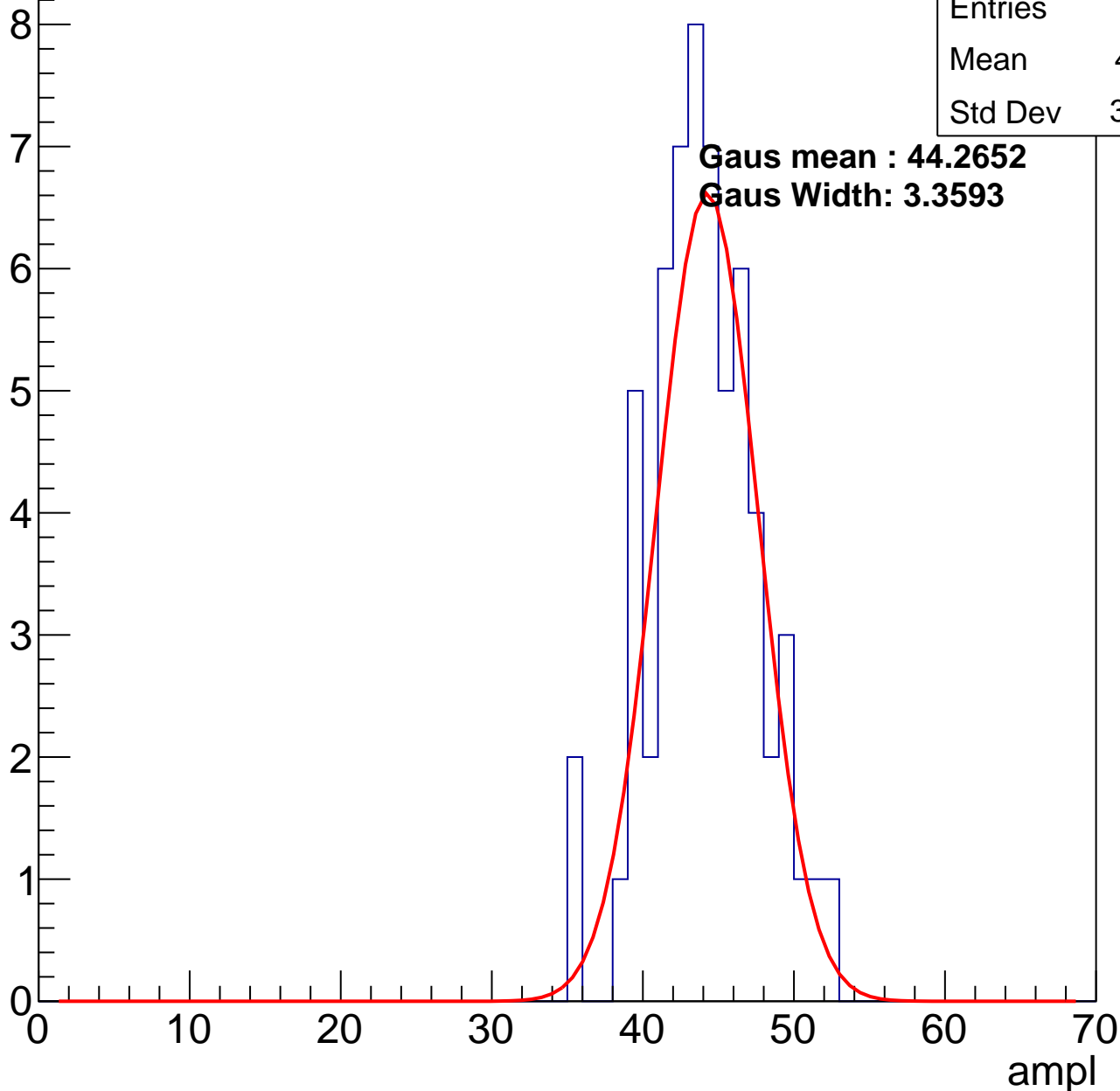
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 43.61 |
| Std Dev | 3.522 |

Gaus mean : 44.2652

Gaus Width: 3.3593

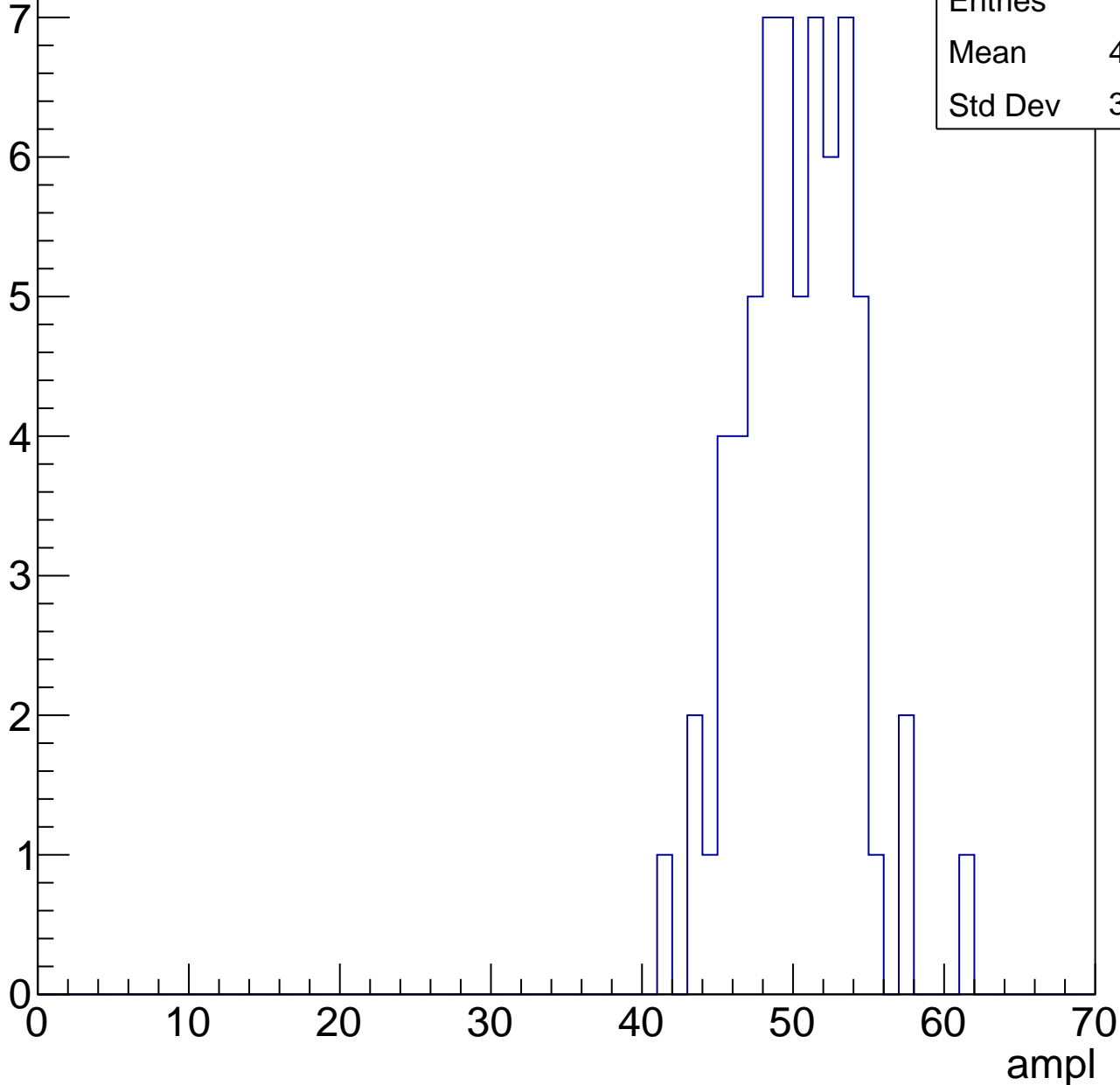


B1L100S, U7-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

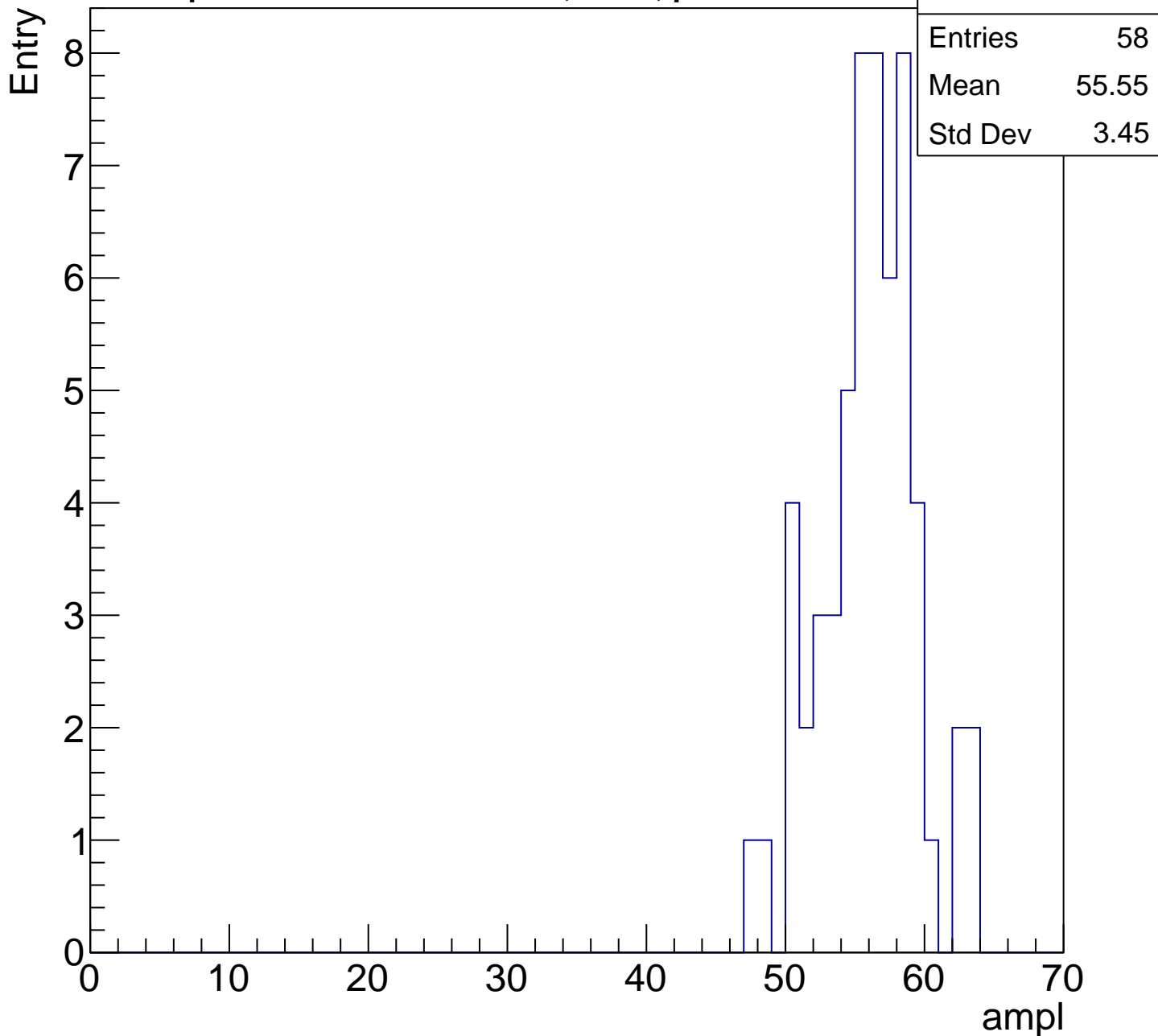
Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 49.83 |
| Std Dev | 3.669 |



B1L100S, U7-ch112, adc4

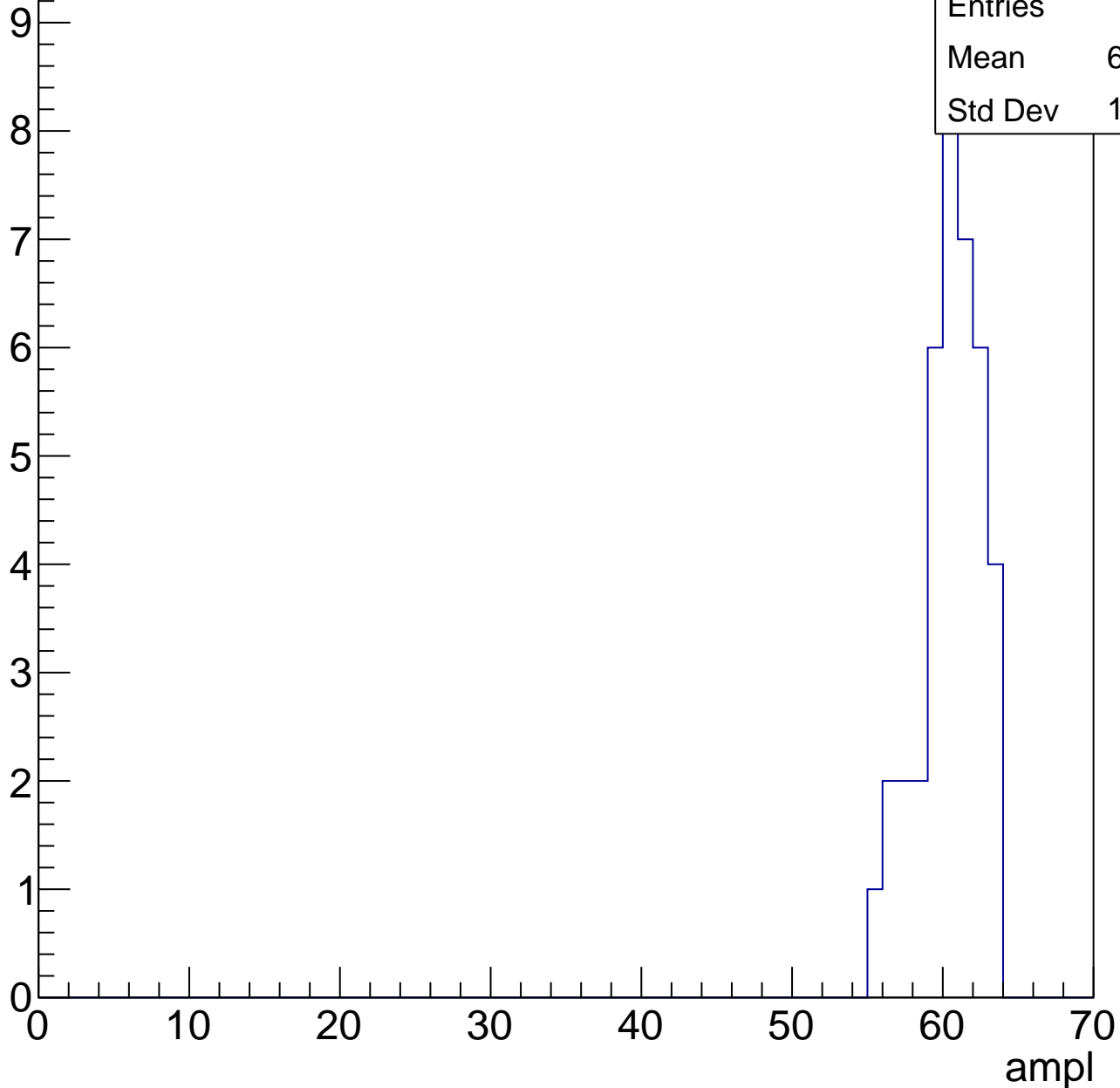
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U7-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

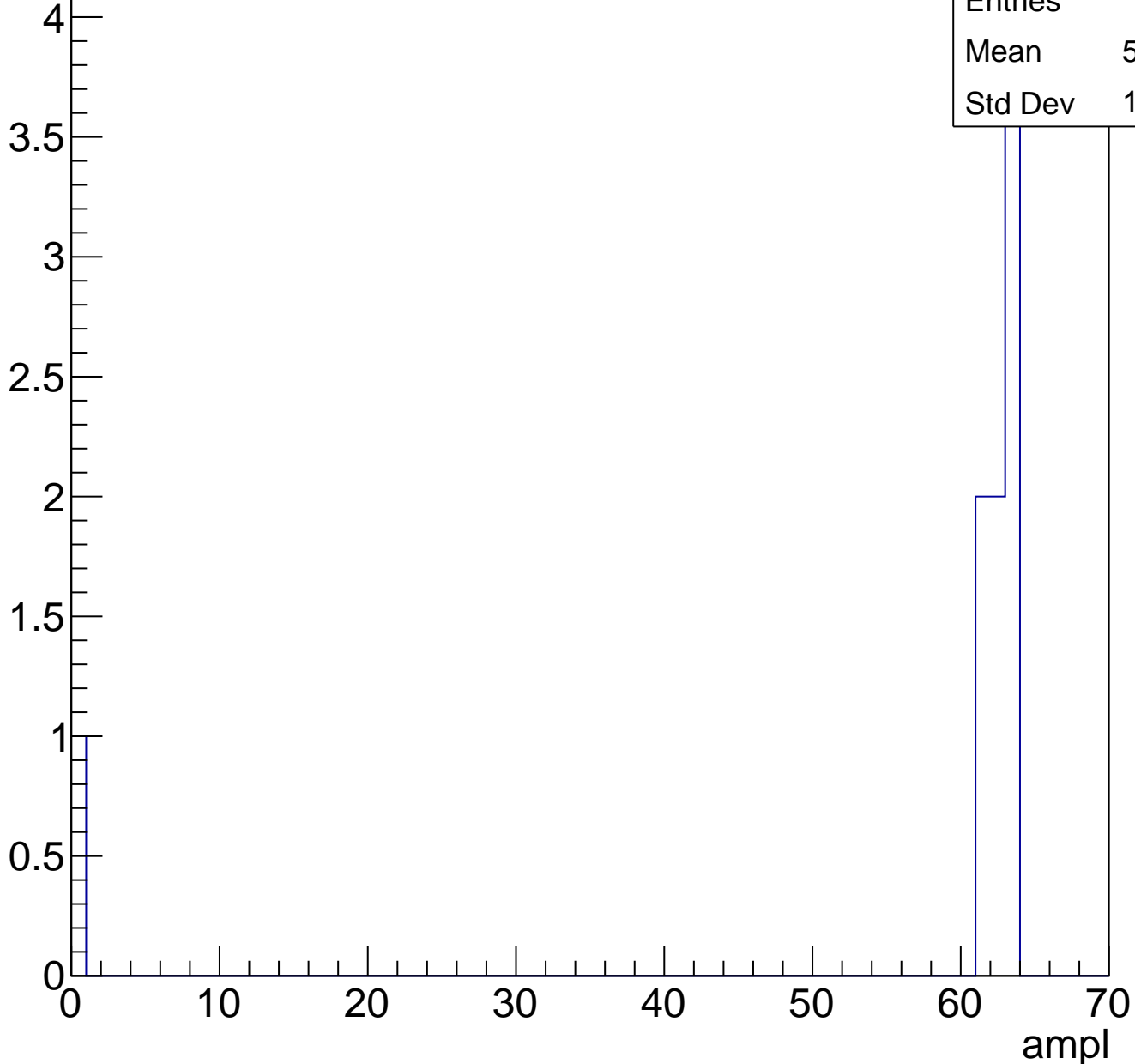
Entry



B1L100S, U7-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 9 |
| Mean | 55.33 |
| Std Dev | 19.58 |

B1L100S, U7-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch113, adc0

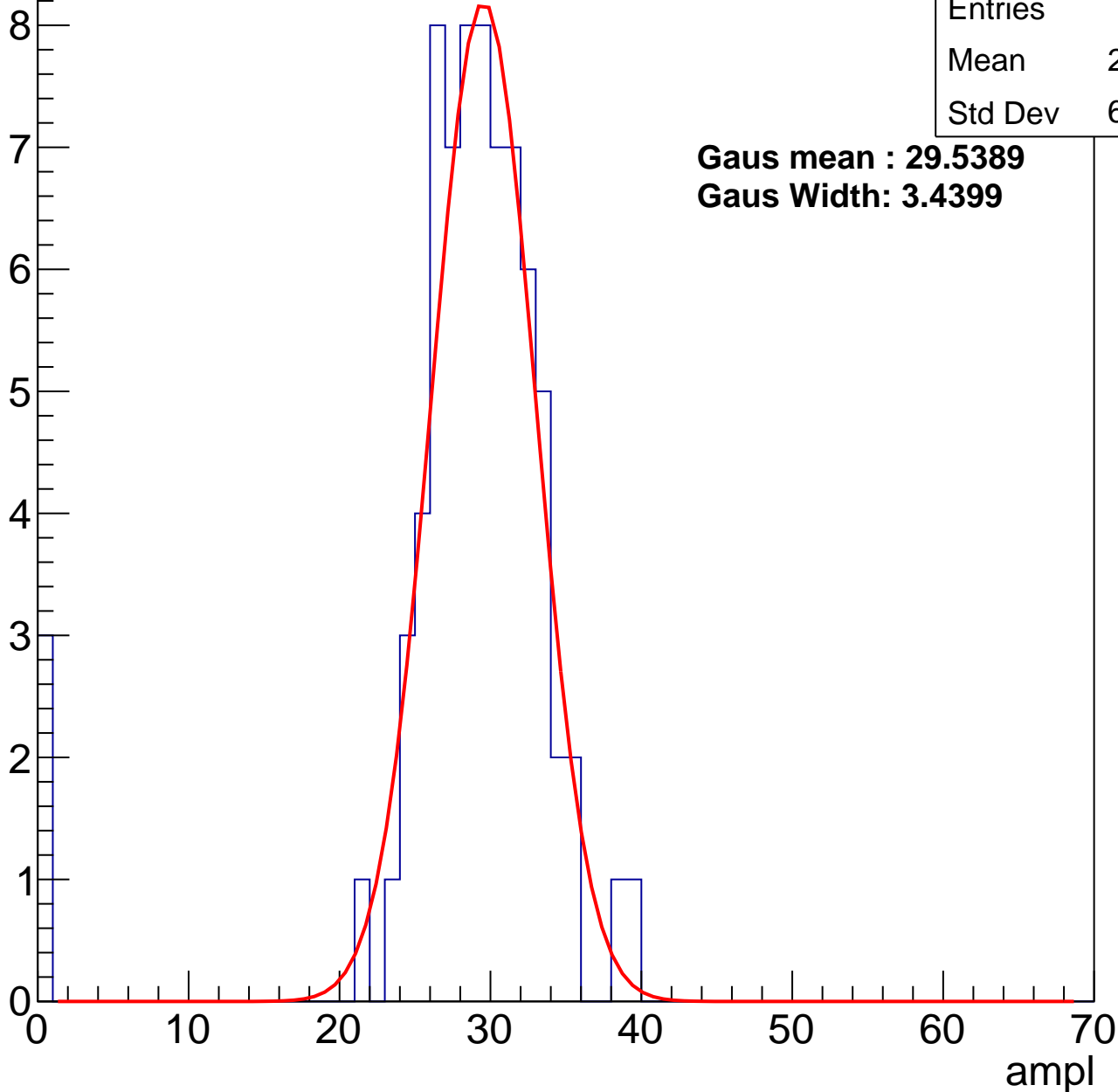
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 27.95 |
| Std Dev | 6.637 |

Gaus mean : 29.5389

Gaus Width: 3.4399



B1L100S, U7-ch113, adc1

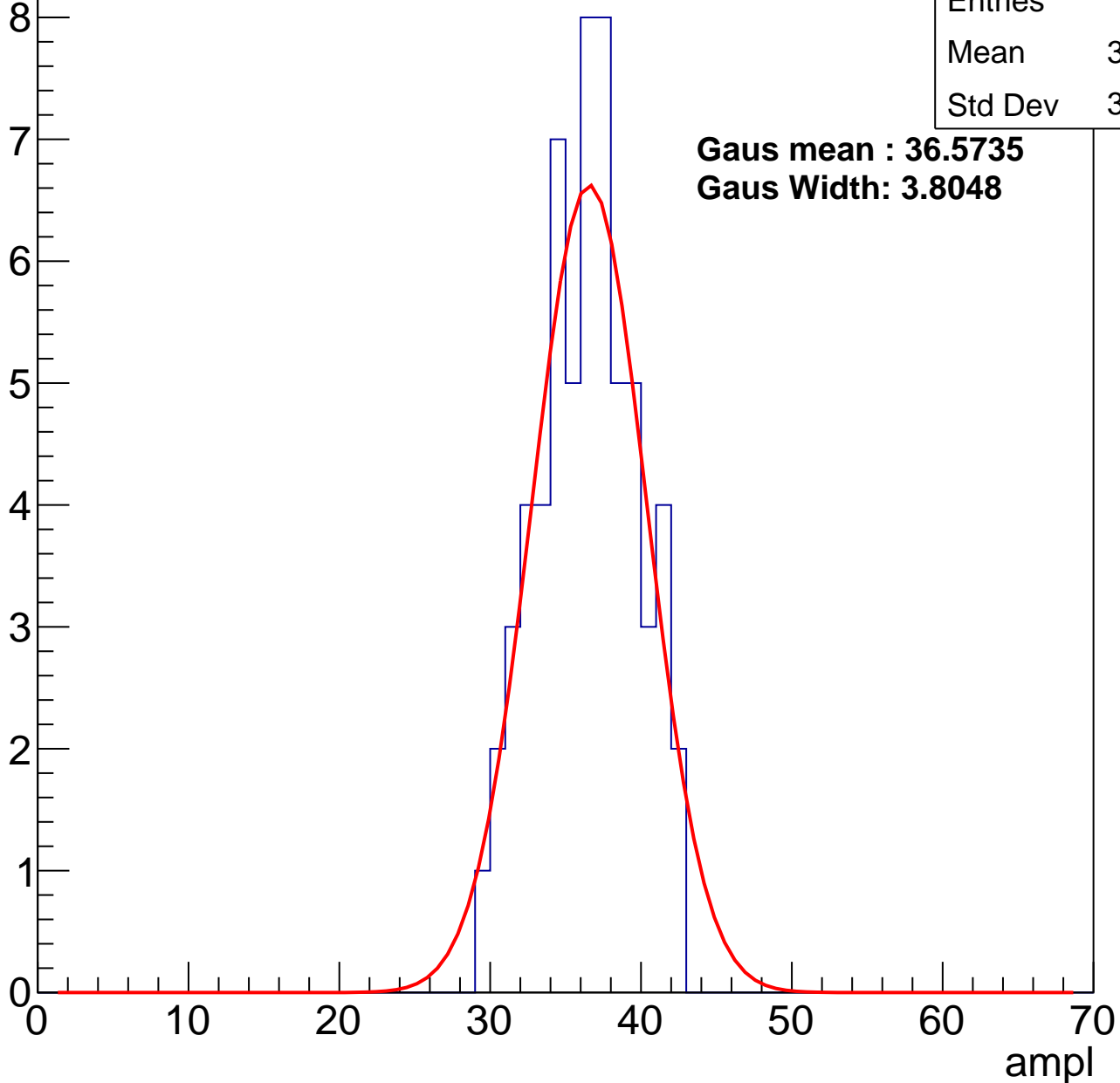
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.93 |
| Std Dev | 3.193 |

Gaus mean : 36.5735

Gaus Width: 3.8048



B1L100S, U7-ch113, adc2

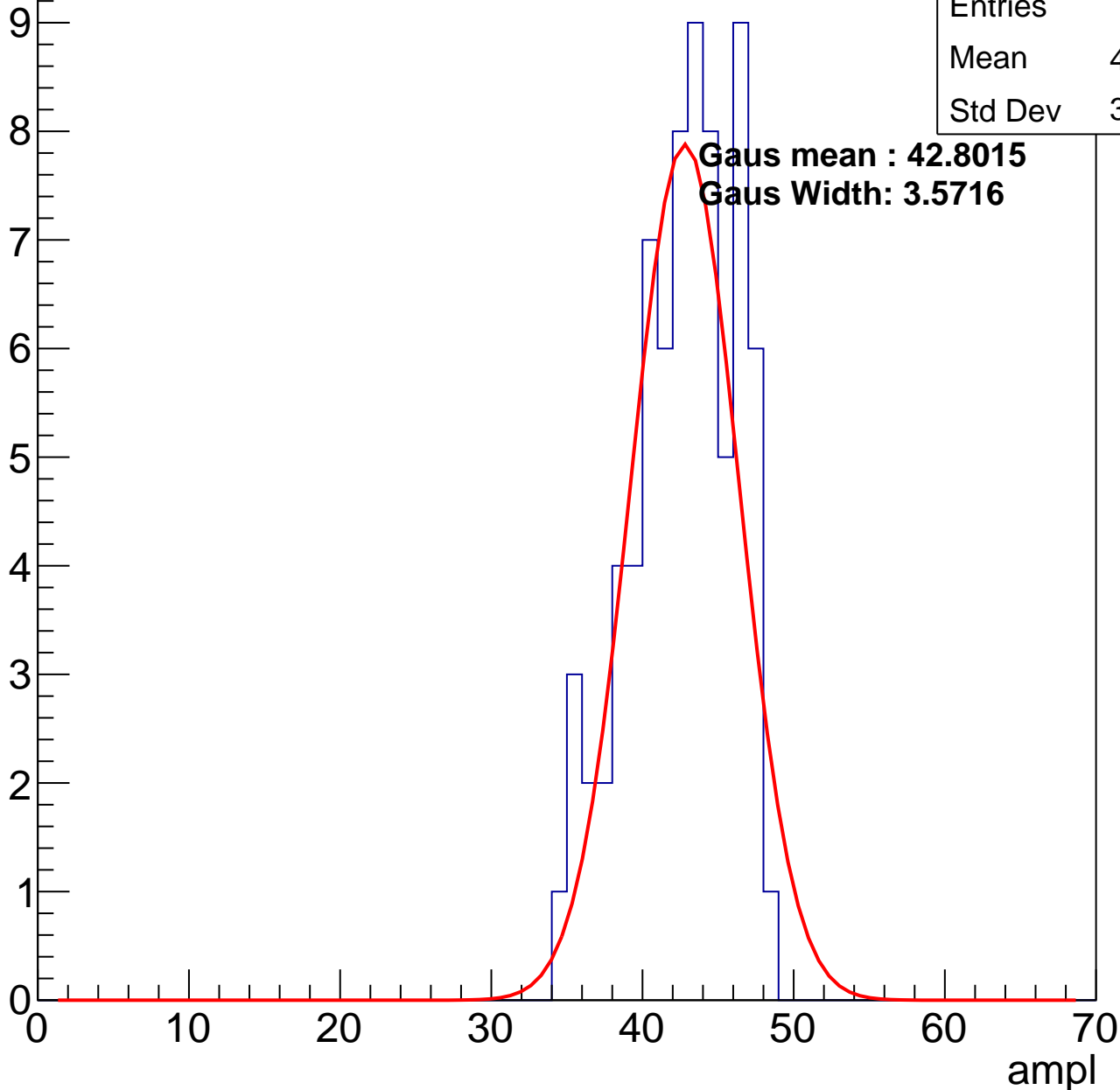
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 42.17 |
| Std Dev | 3.427 |

Gaus mean : 42.8015

Gaus Width: 3.5716

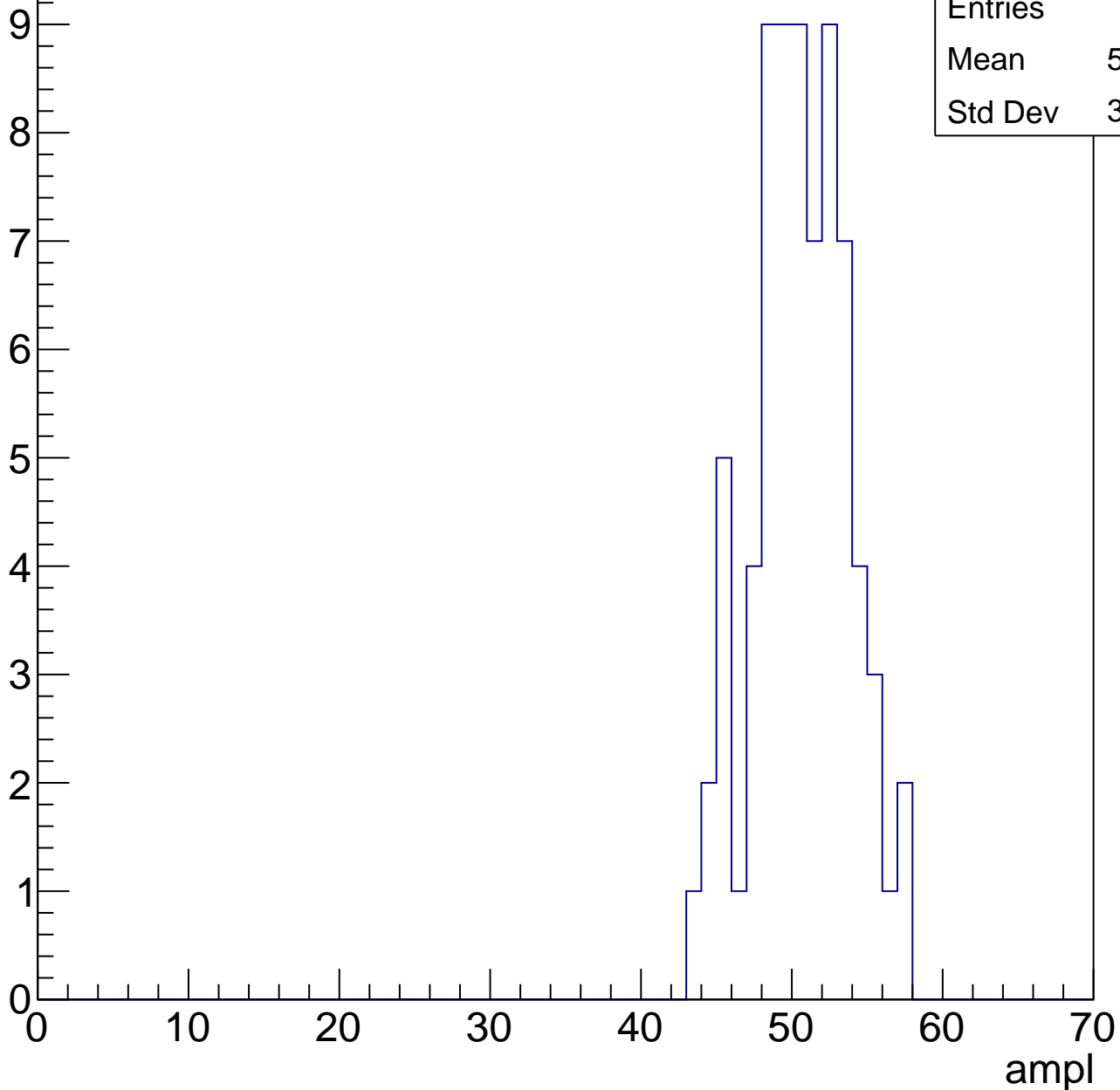


B1L100S, U7-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

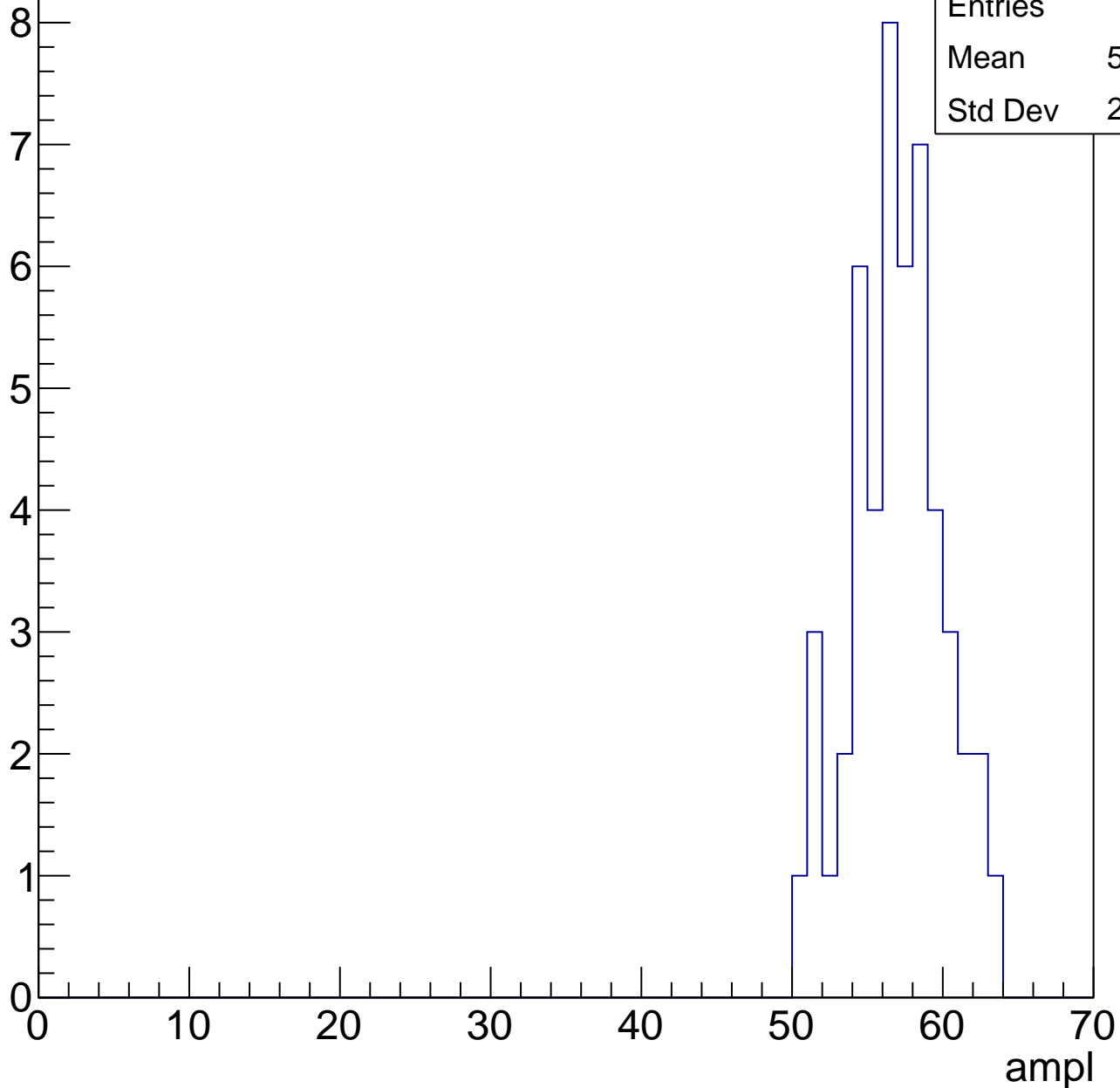
| | |
|---------|-------|
| Entries | 73 |
| Mean | 50.14 |
| Std Dev | 3.142 |



B1L100S, U7-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

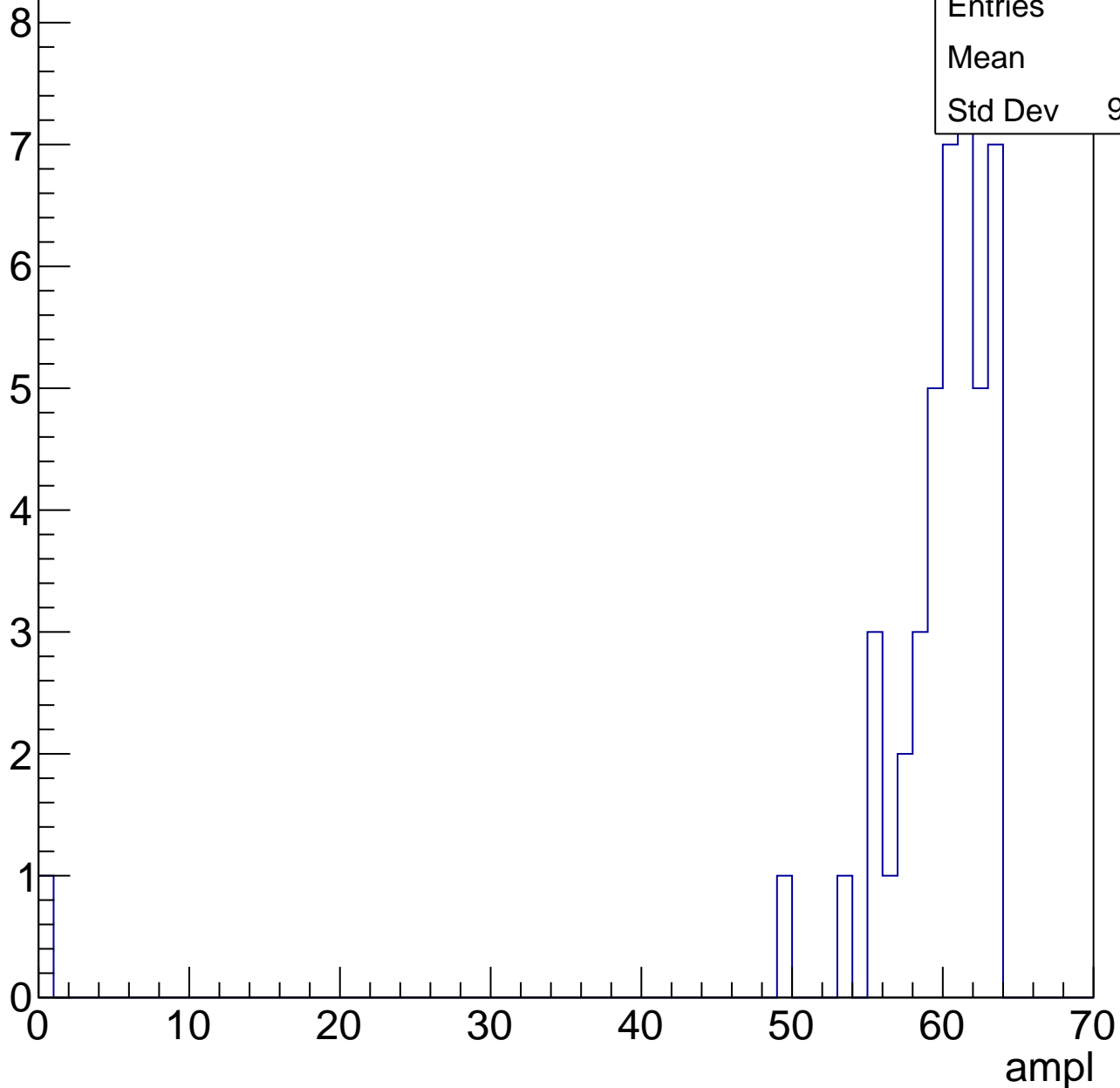


| | |
|---------|-------|
| Entries | 50 |
| Mean | 56.52 |
| Std Dev | 2.995 |

B1L100S, U7-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

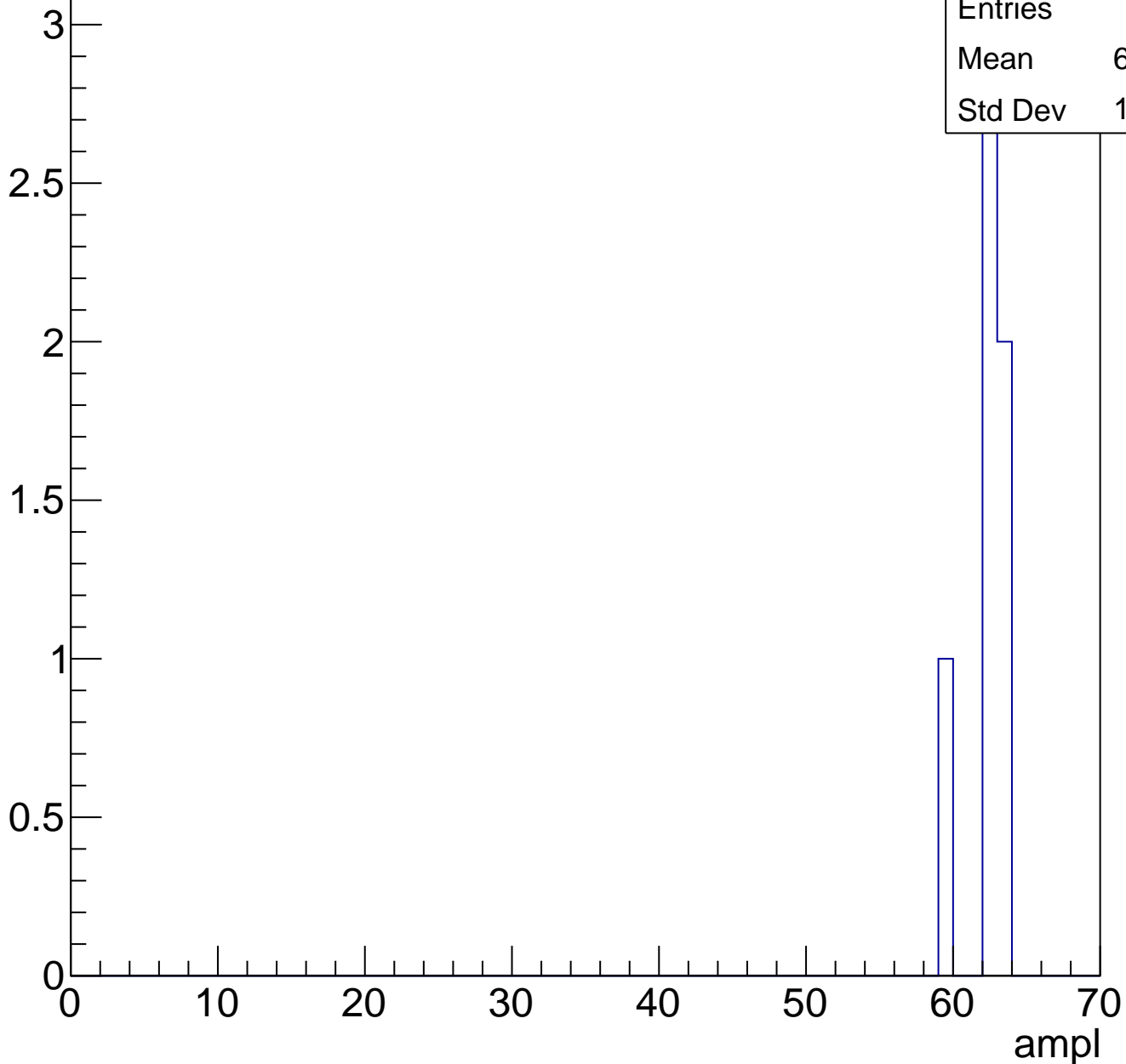
Entry



B1L100S, U7-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

ampl

B1L100S, U7-ch114, adc0

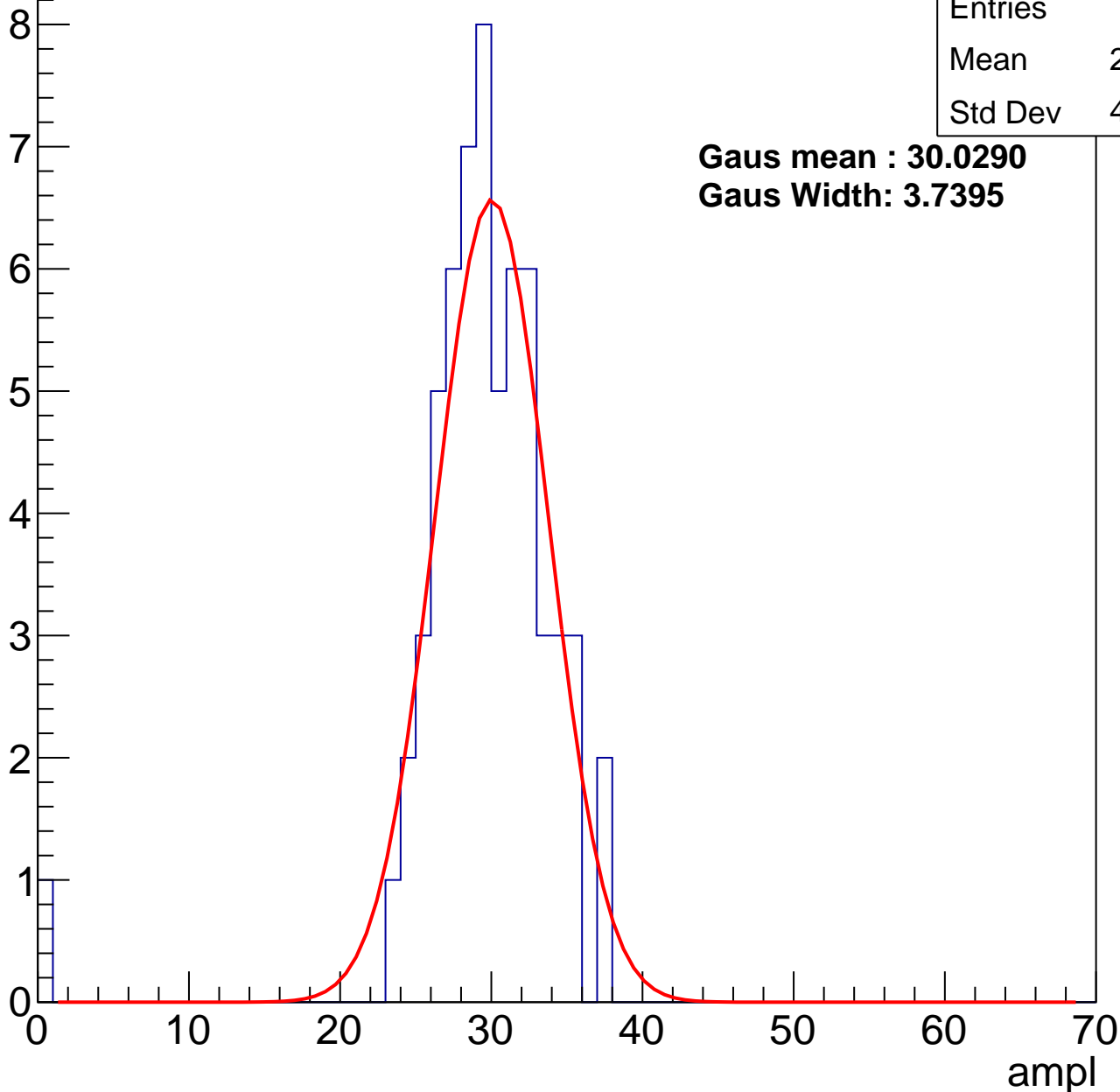
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.08 |
| Std Dev | 4.947 |

Gaus mean : 30.0290

Gaus Width: 3.7395



B1L100S, U7-ch114, adc1

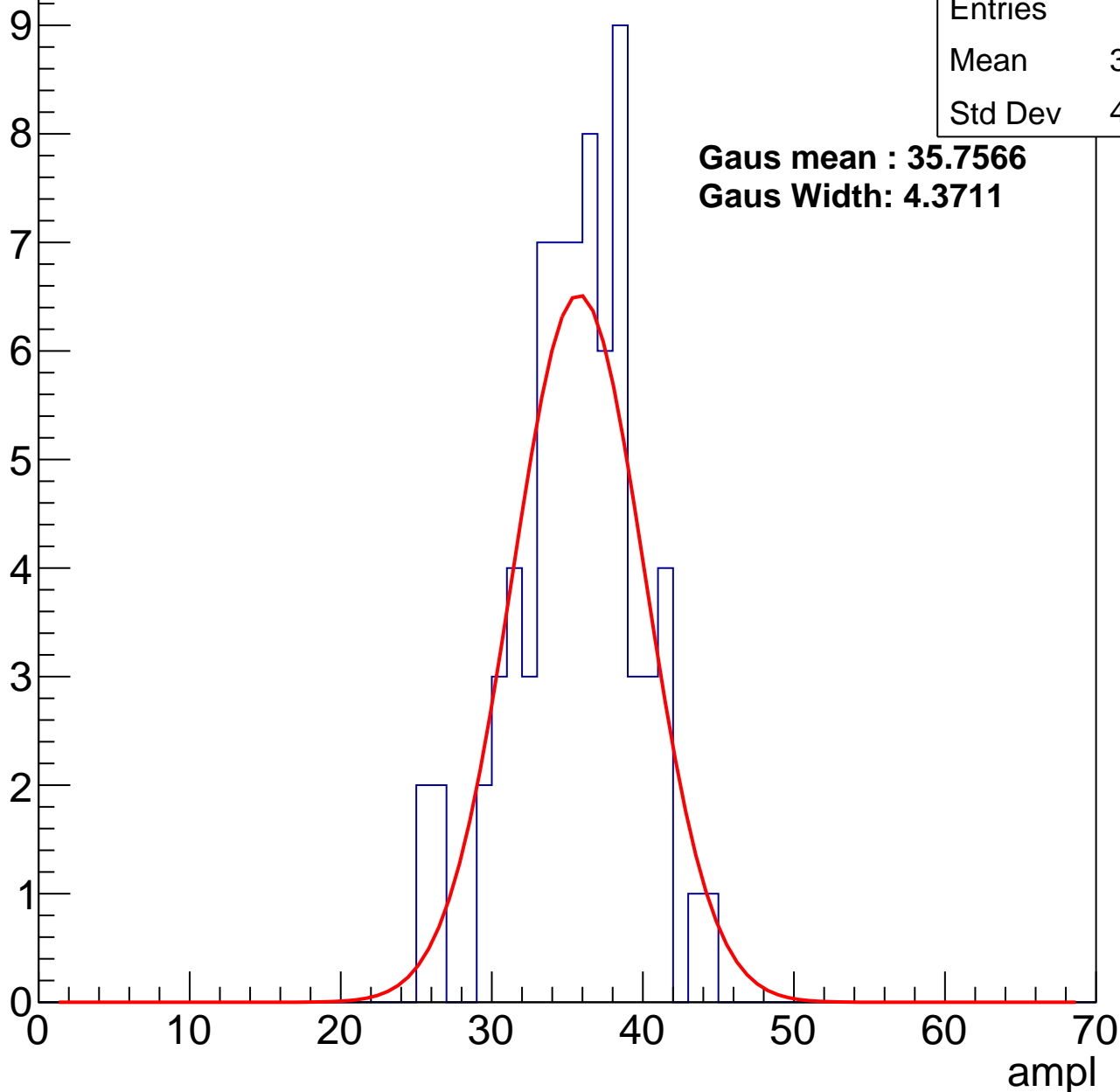
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.06 |
| Std Dev | 4.013 |

Gaus mean : 35.7566

Gaus Width: 4.3711



B1L100S, U7-ch114, adc2

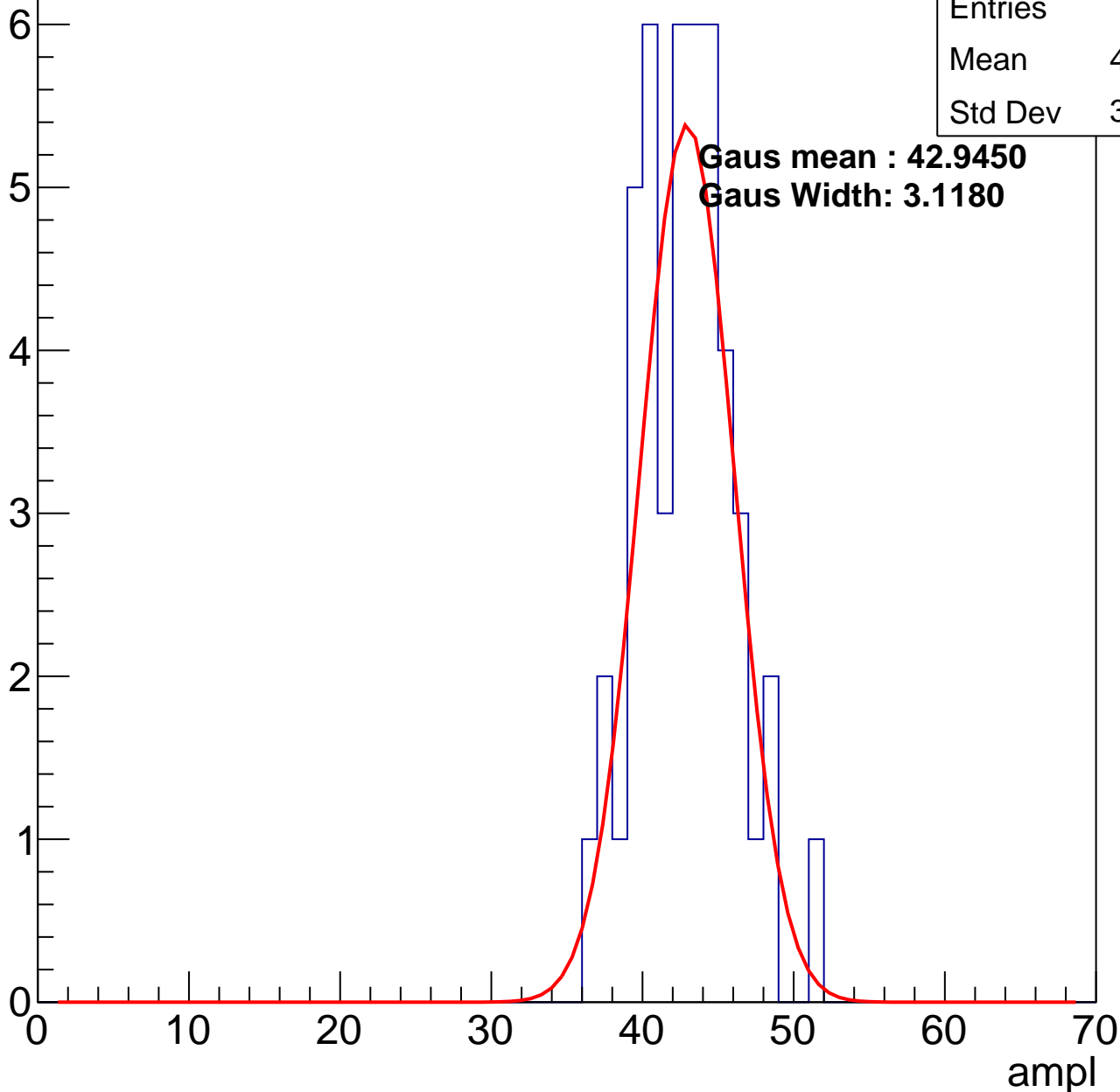
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 42.38 |
| Std Dev | 3.125 |

Gaus mean : 42.9450

Gaus Width: 3.1180

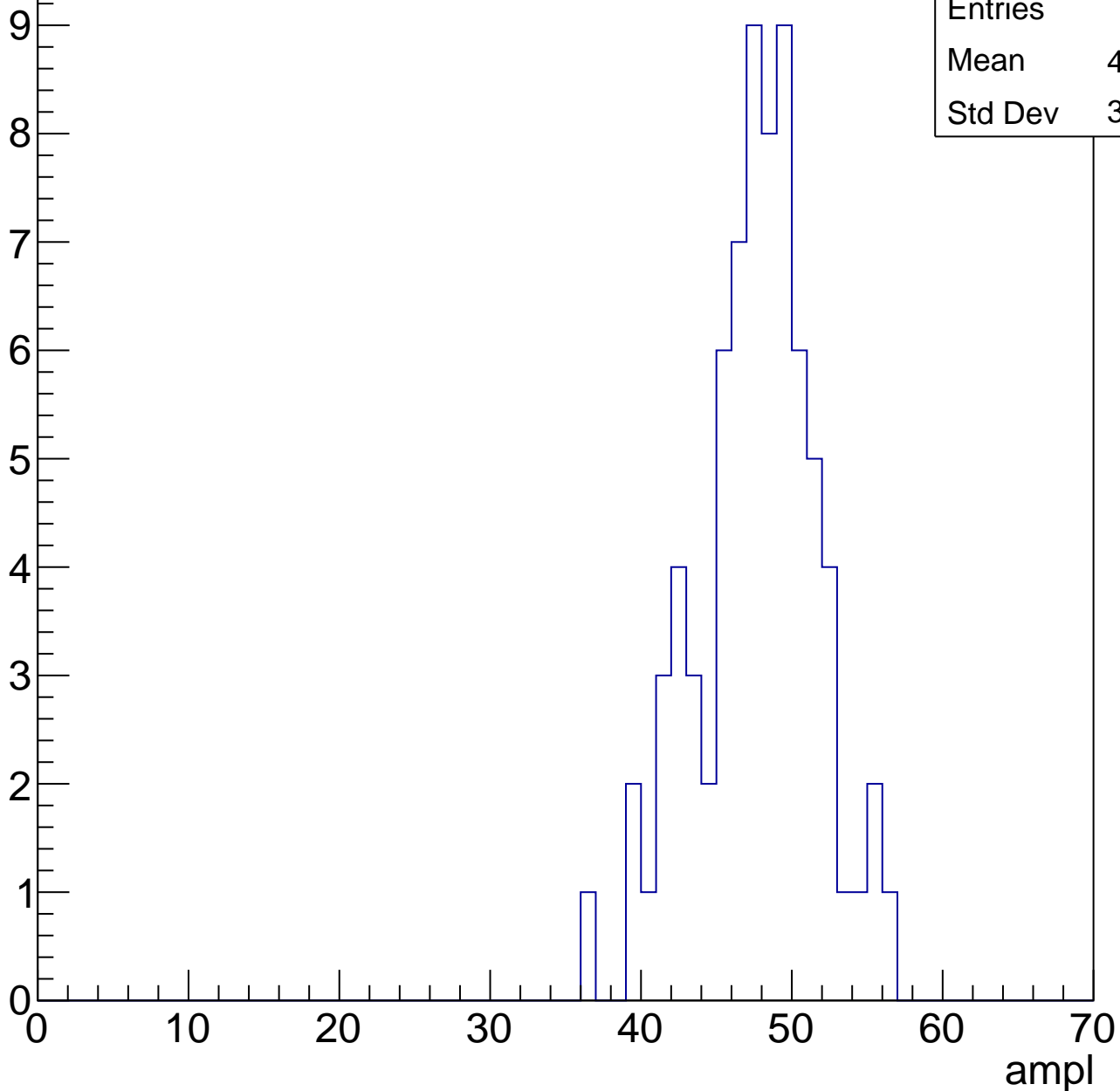


B1L100S, U7-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 47.17 |
| Std Dev | 3.954 |

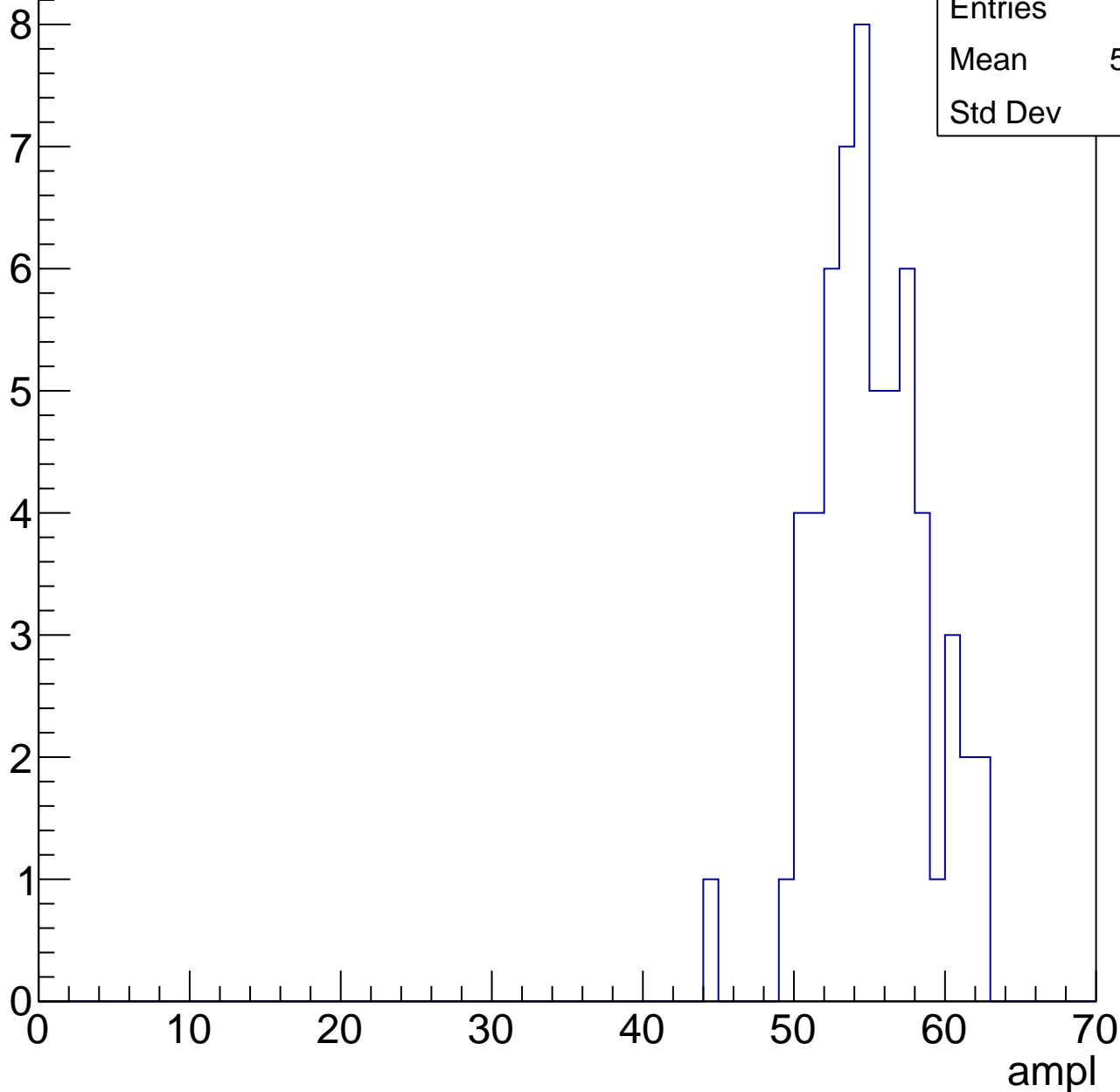


B1L100S, U7-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

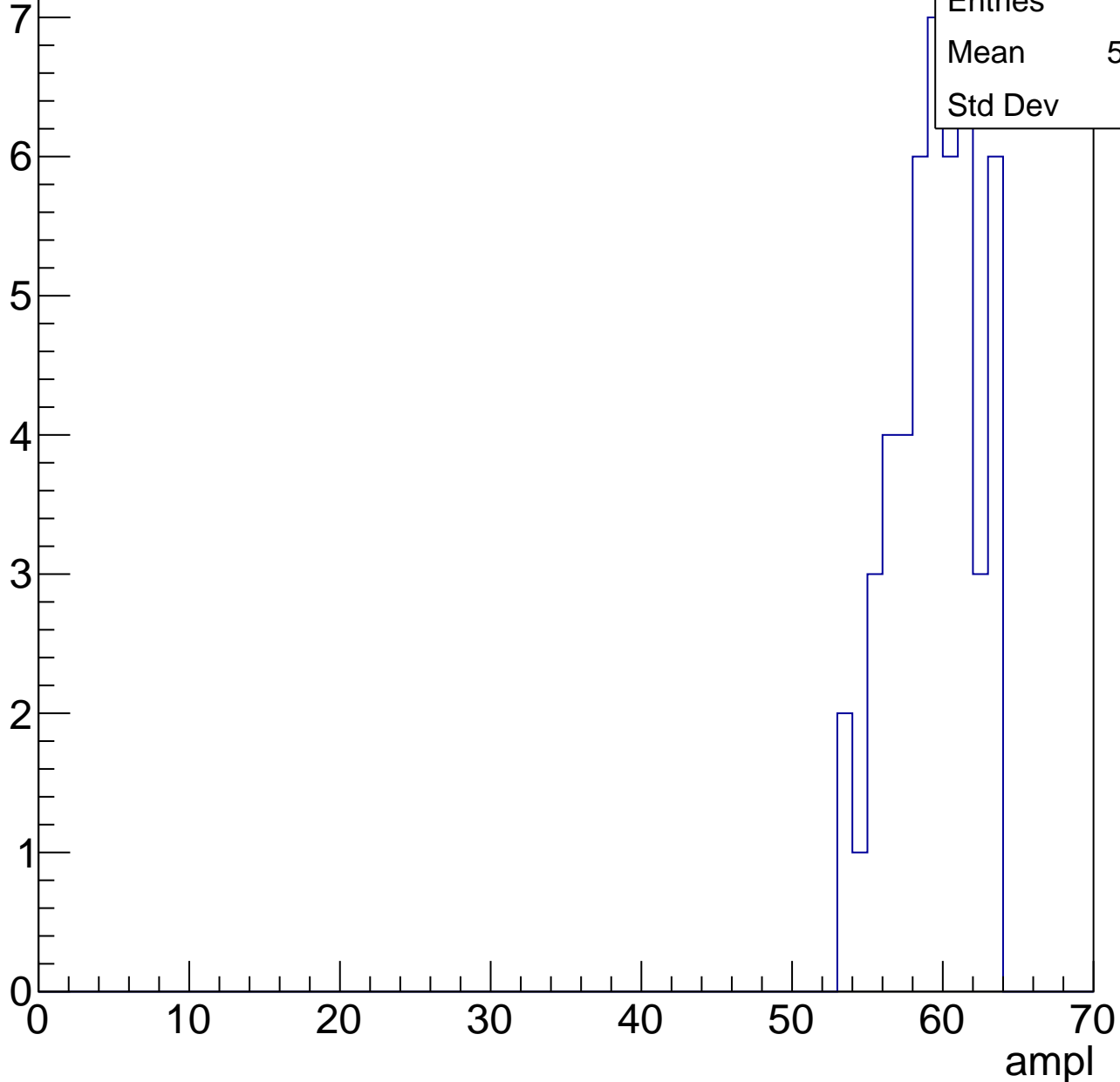
| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.68 |
| Std Dev | 3.52 |



B1L100S, U7-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

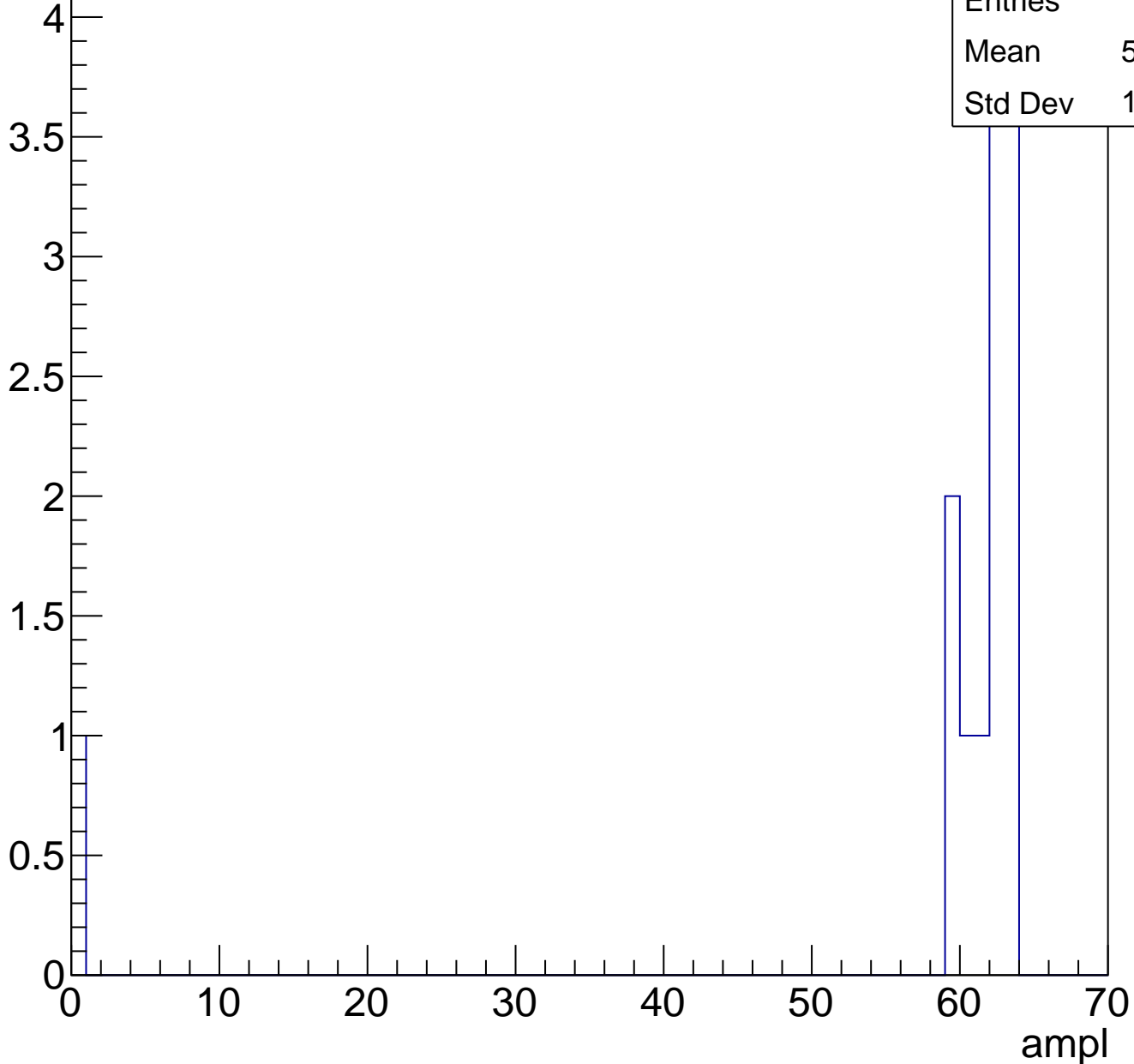


| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.96 |
| Std Dev | 2.71 |

B1L100S, U7-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

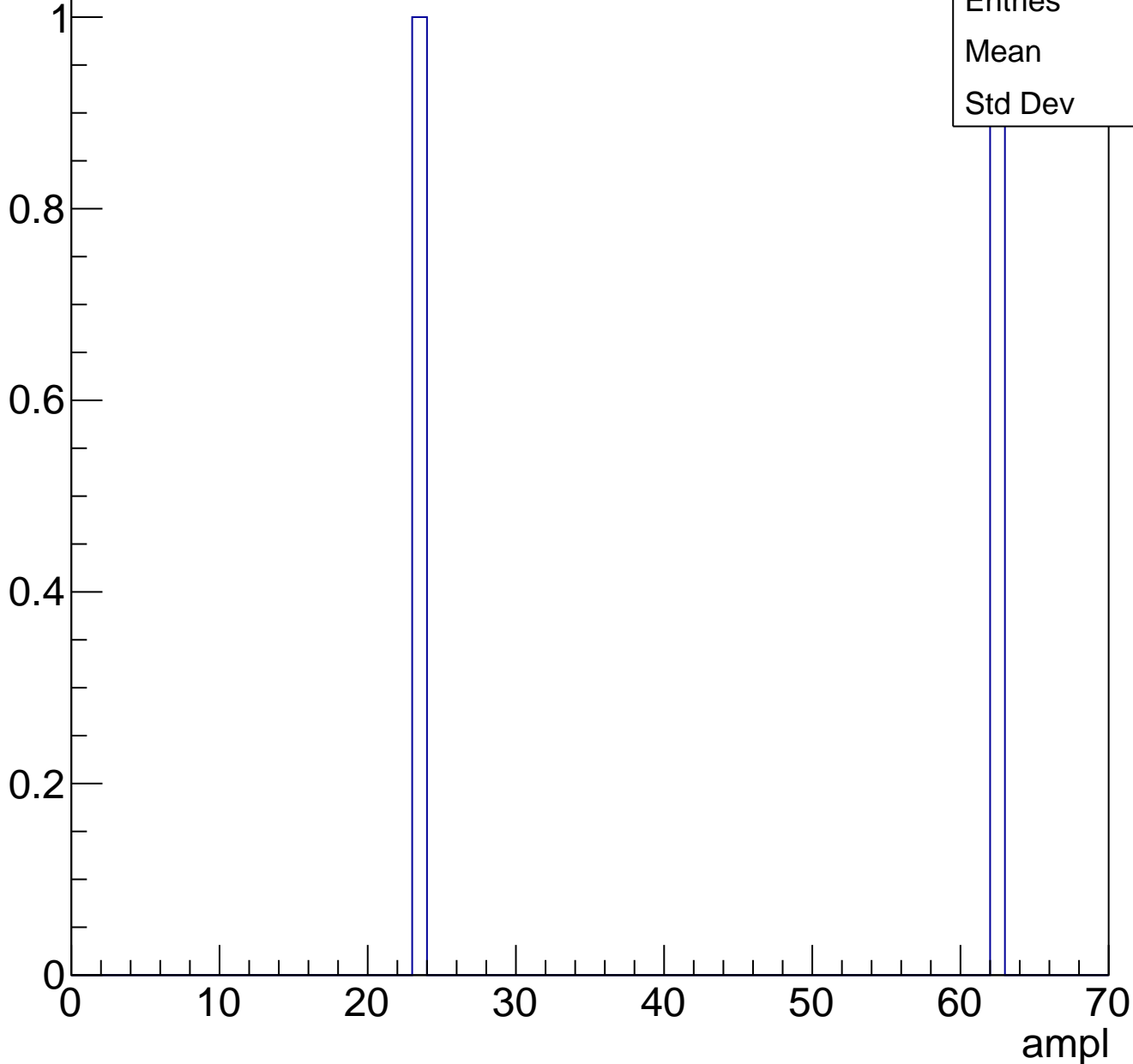
Entry



B1L100S, U7-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch115, adc0

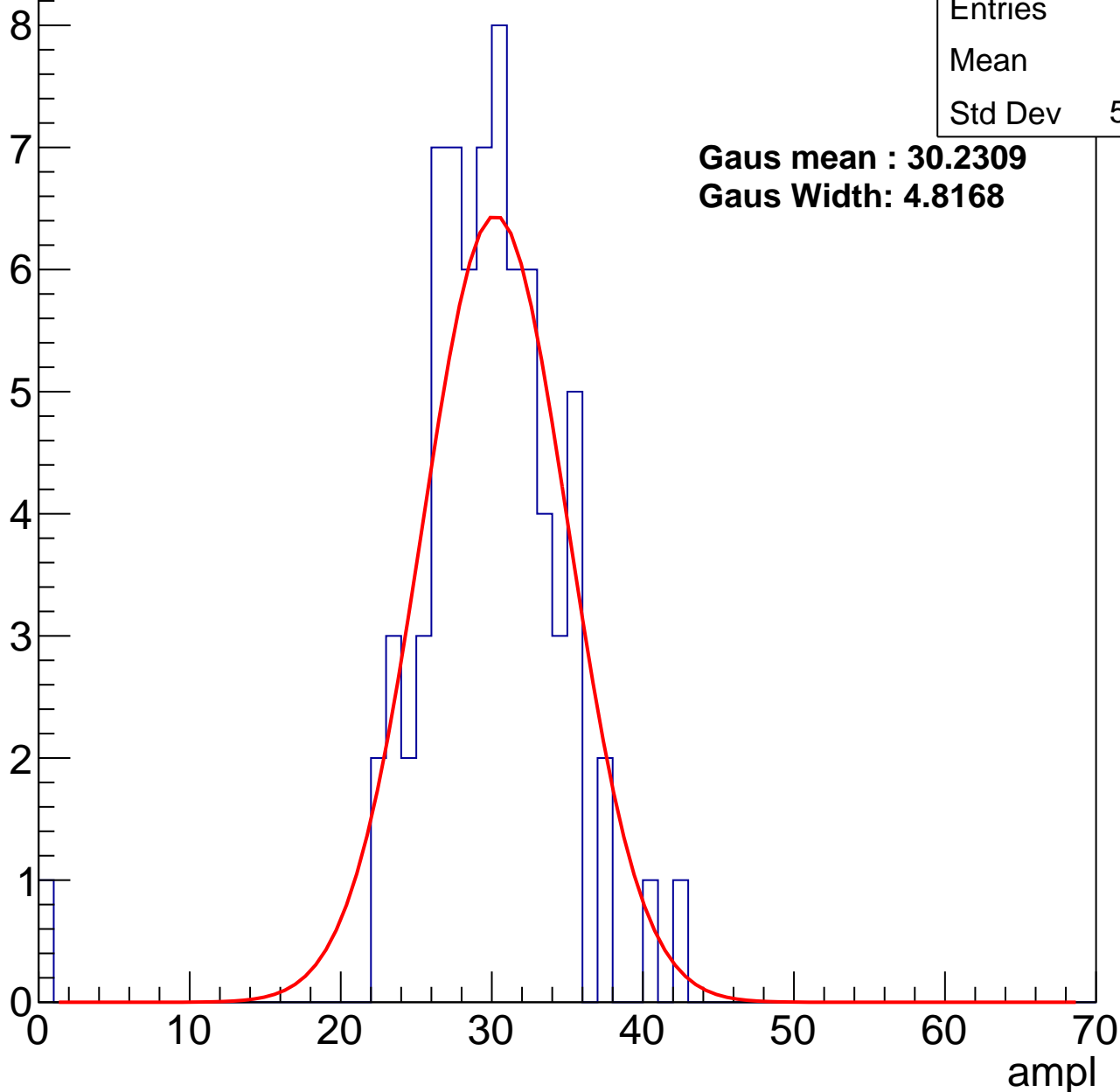
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.2 |
| Std Dev | 5.279 |

Gaus mean : 30.2309

Gaus Width: 4.8168



B1L100S, U7-ch115, adc1

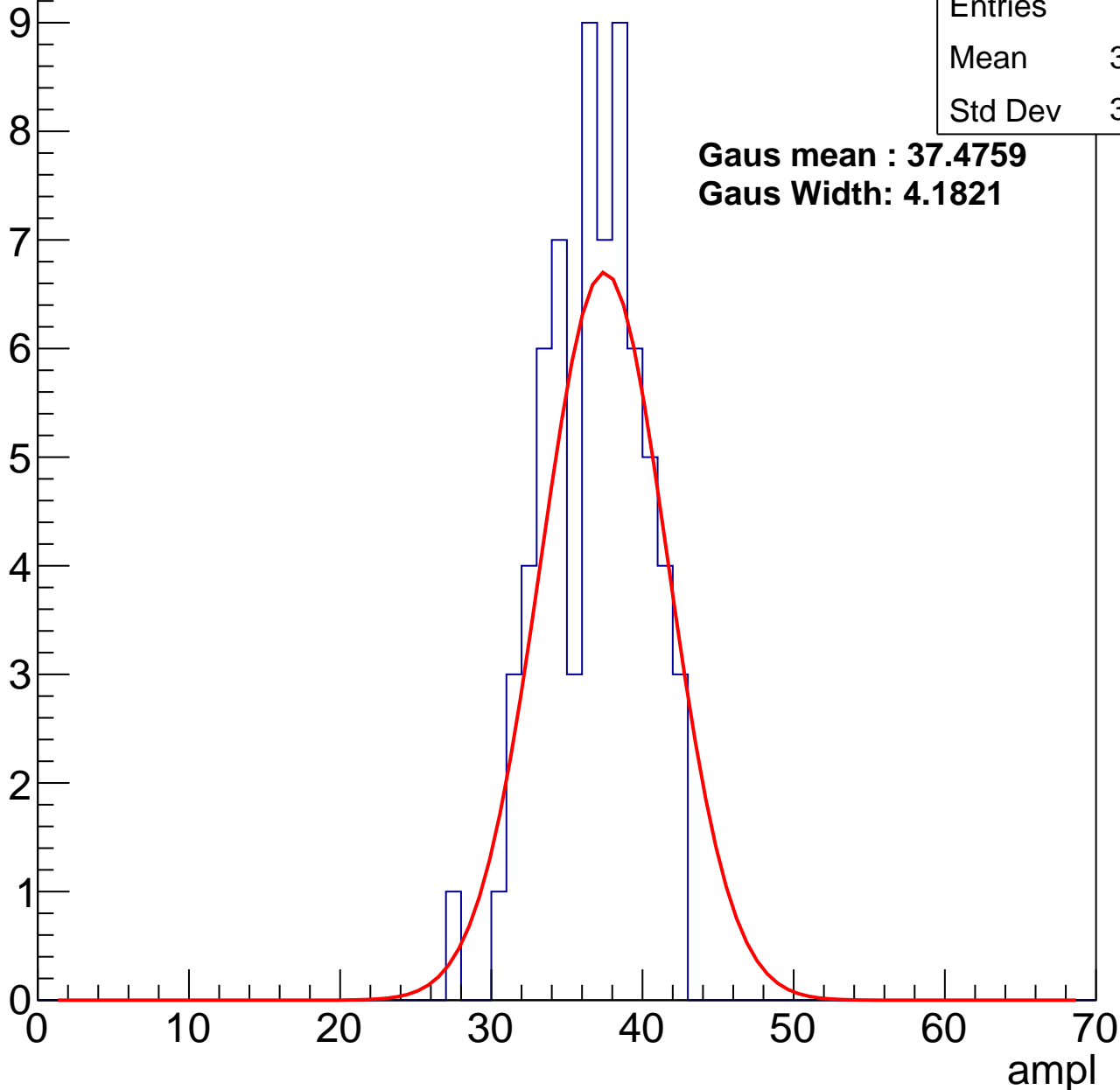
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.29 |
| Std Dev | 3.254 |

Gaus mean : 37.4759

Gaus Width: 4.1821



B1L100S, U7-ch115, adc2

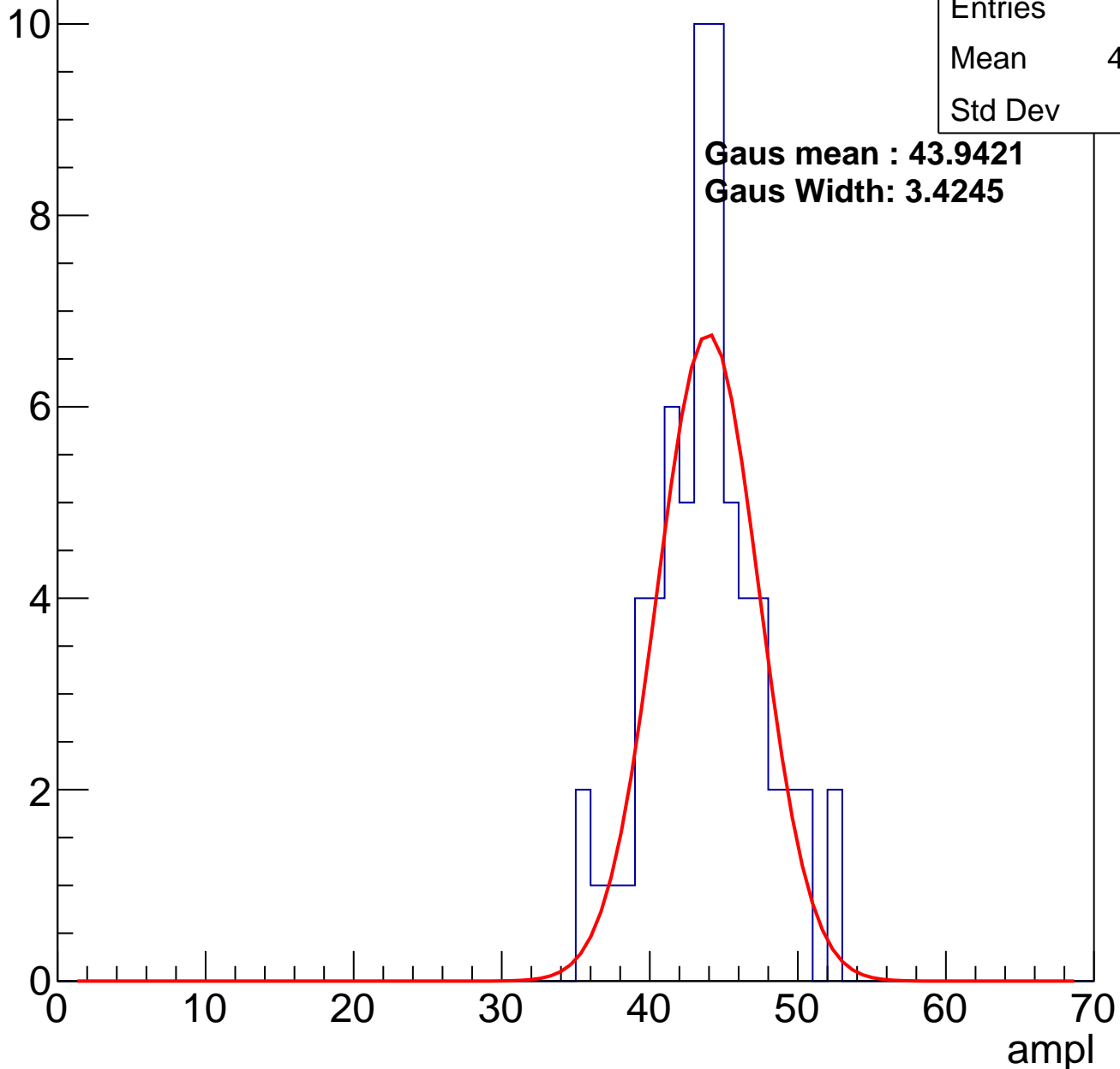
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 65 |
| Mean | 43.35 |
| Std Dev | 3.66 |

Gaus mean : 43.9421

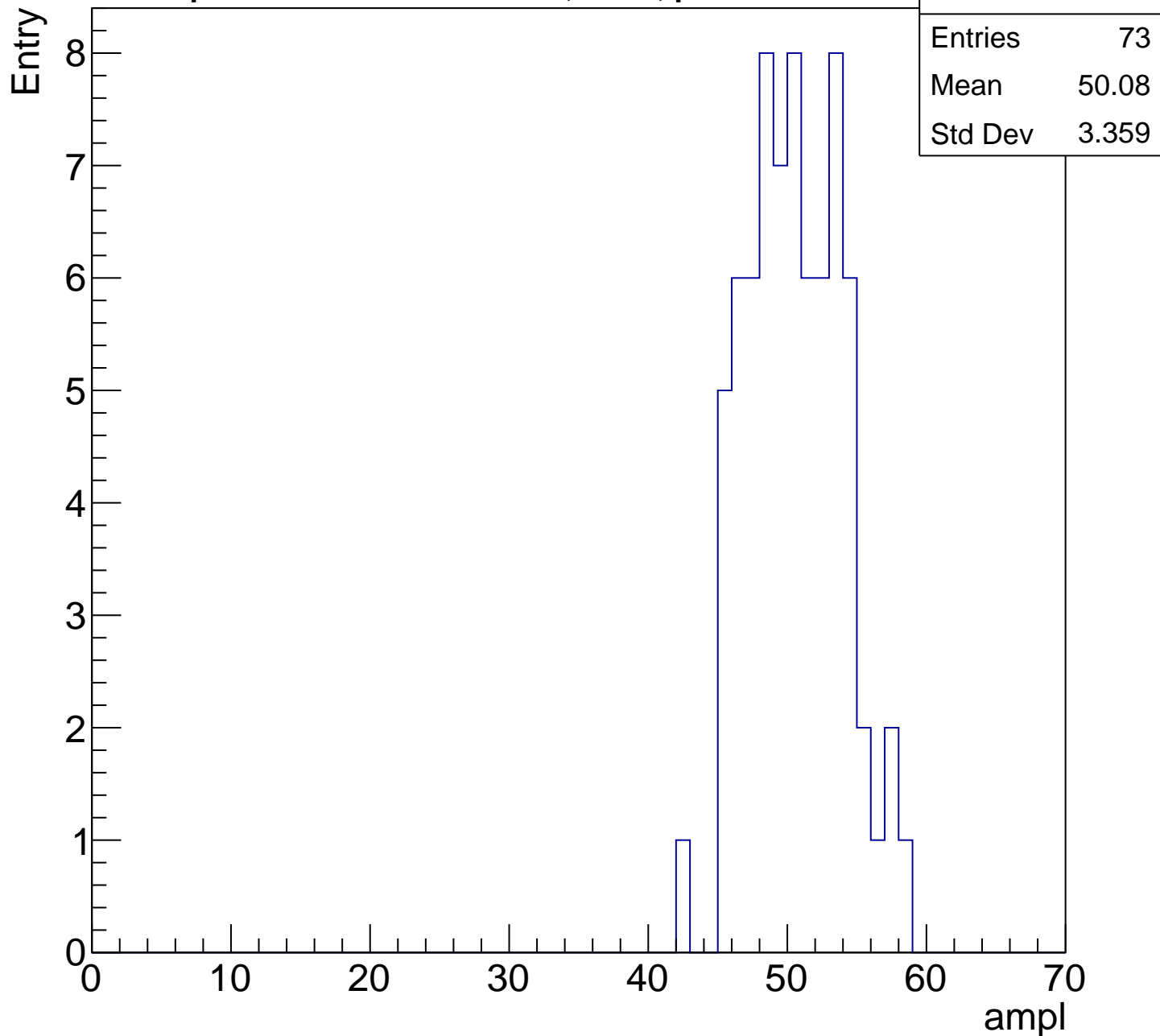
Gaus Width: 3.4245

Entry



B1L100S, U7-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

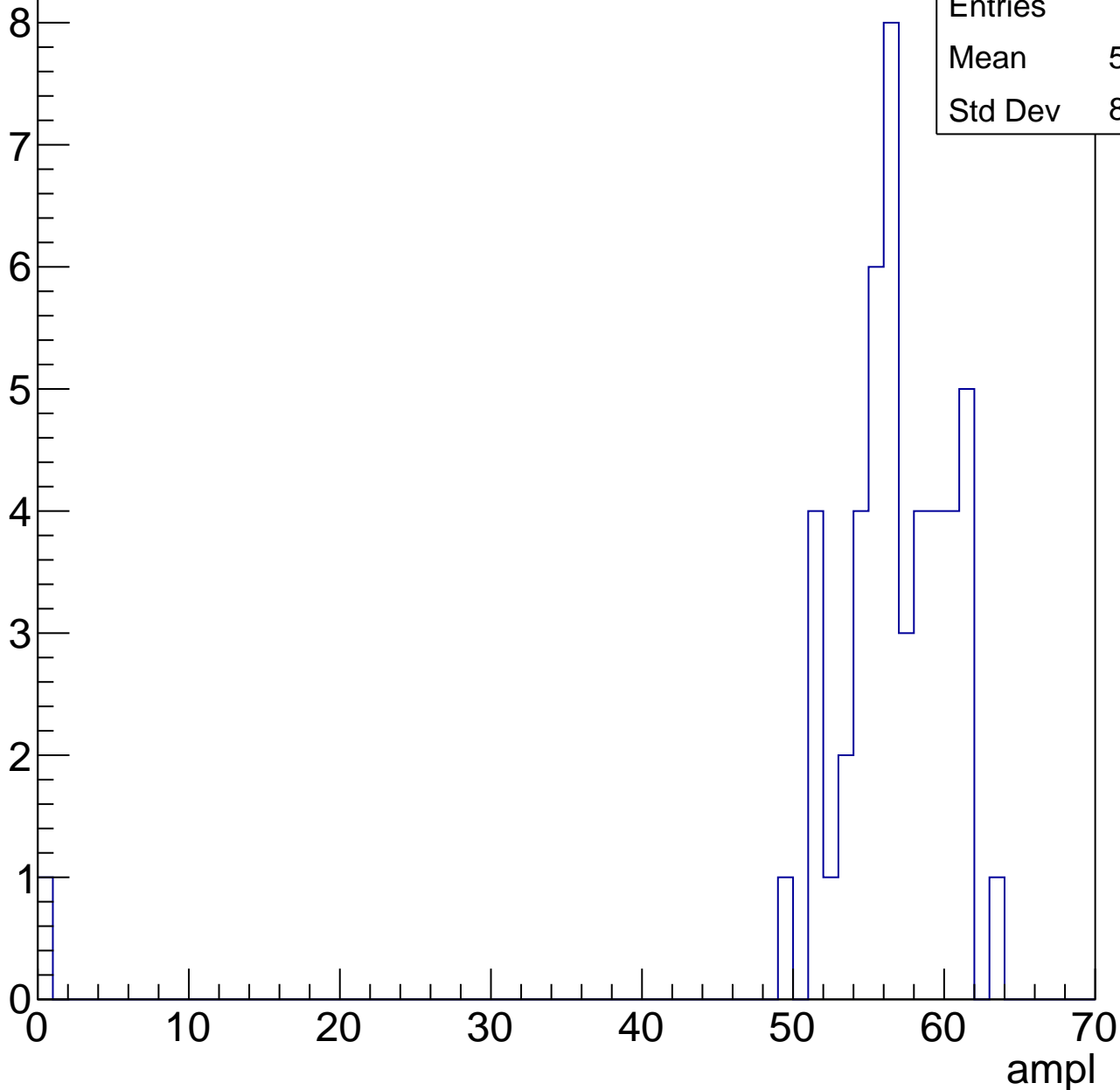


B1L100S, U7-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 55.25 |
| Std Dev | 8.669 |

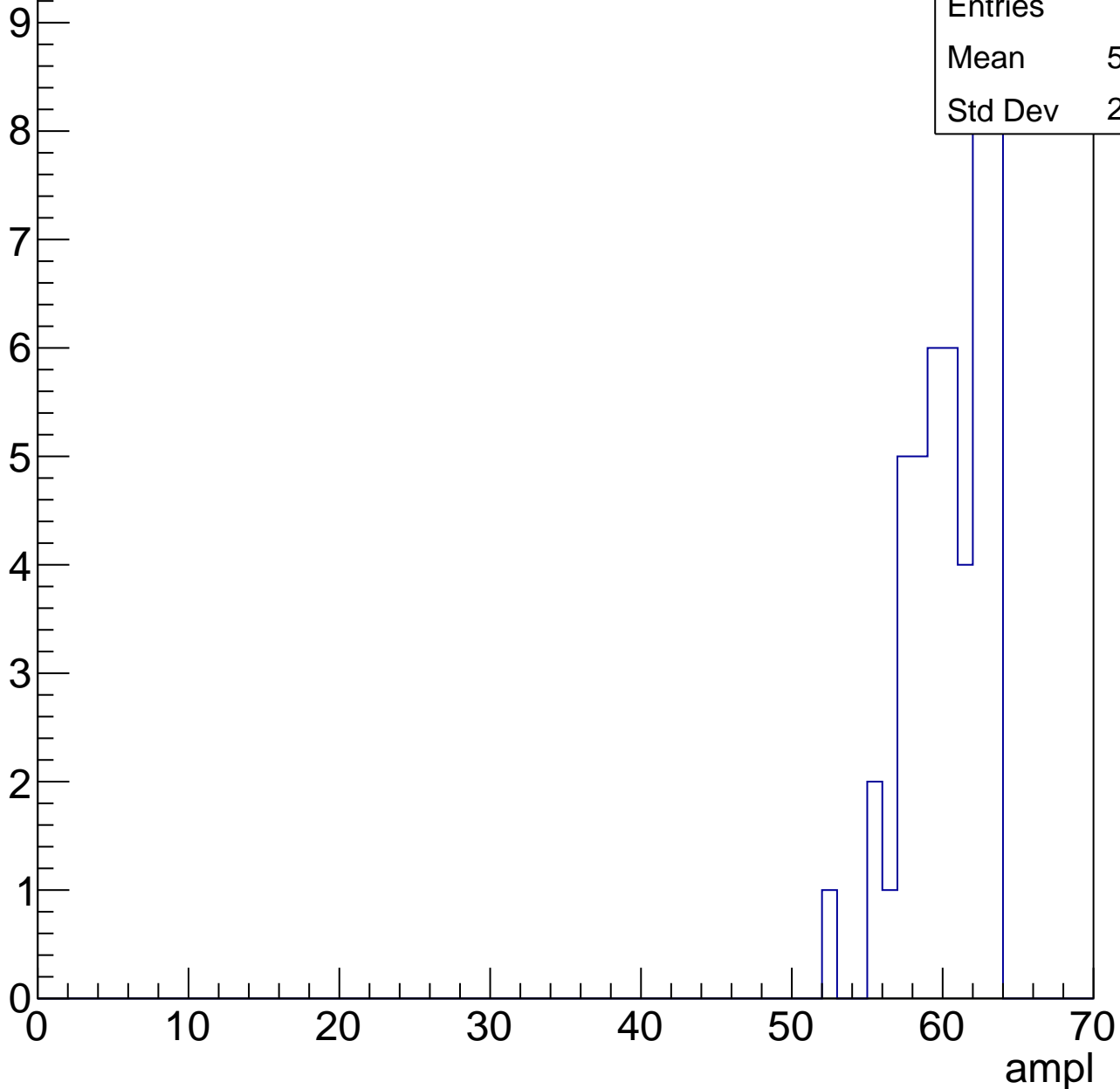


B1L100S, U7-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 59.85 |
| Std Dev | 2.576 |



B1L100S, U7-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch116, adc0

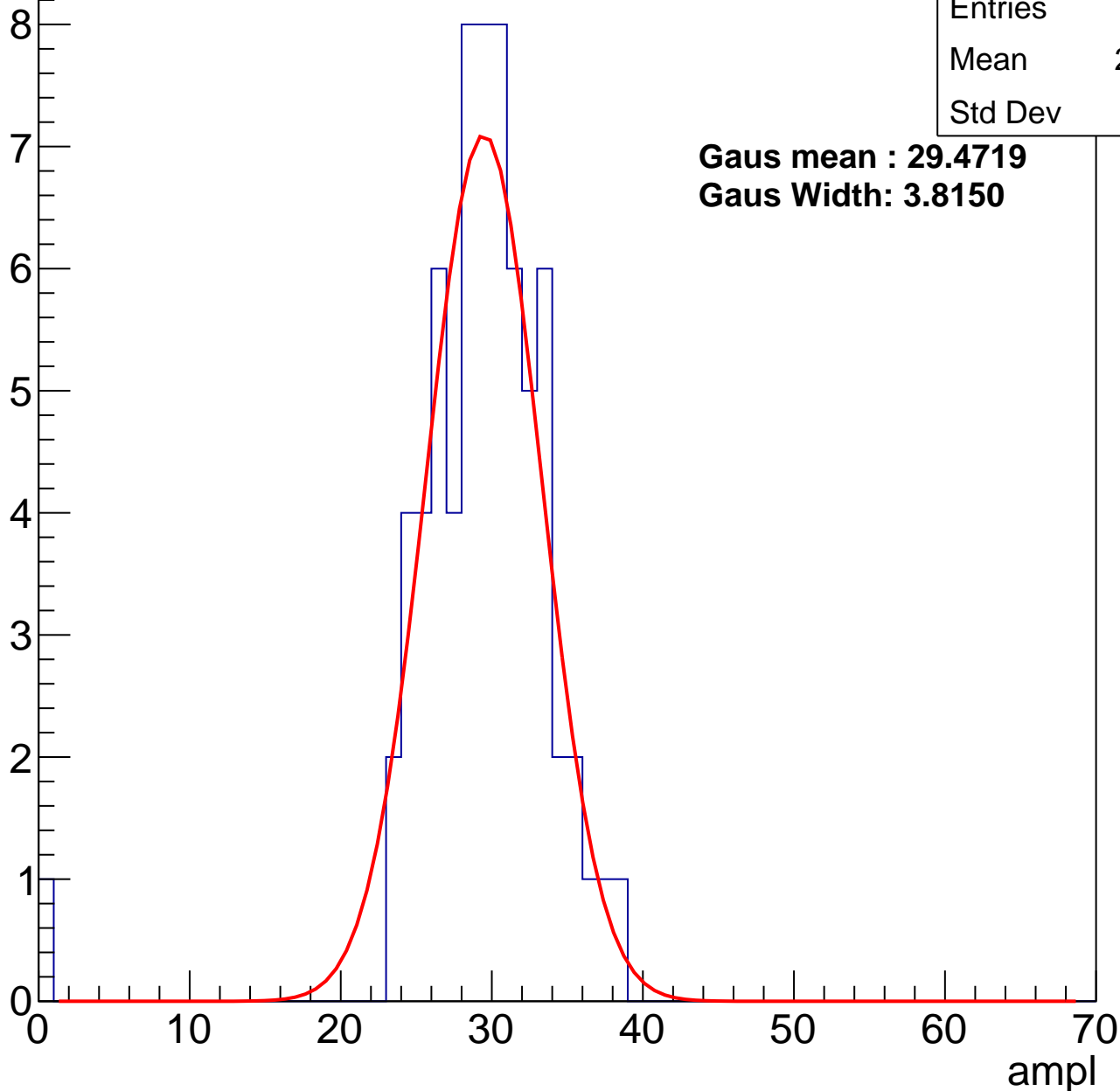
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.91 |
| Std Dev | 4.88 |

Gaus mean : 29.4719

Gaus Width: 3.8150



B1L100S, U7-ch116, adc1

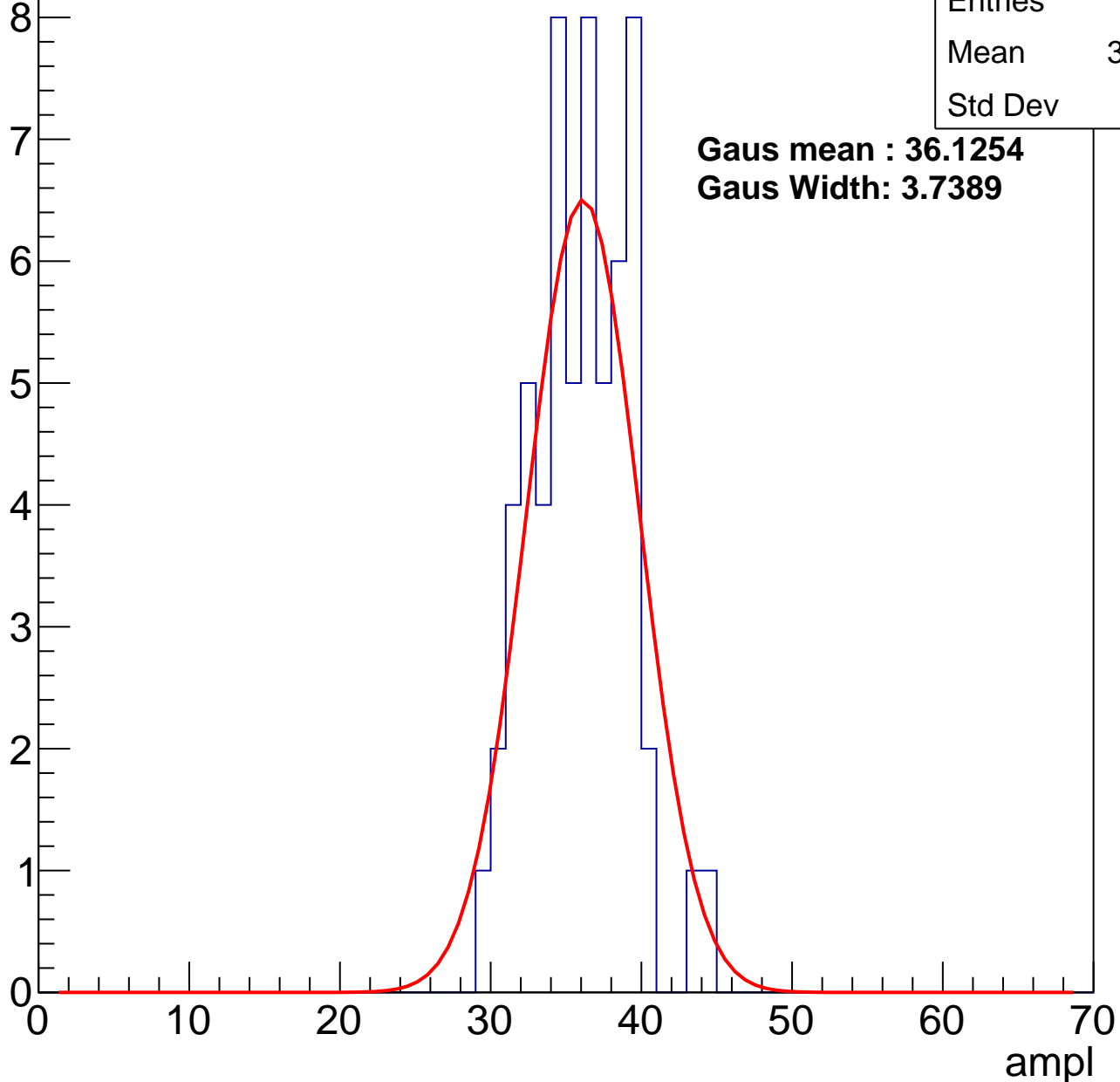
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 35.53 |
| Std Dev | 3.18 |

Gaus mean : 36.1254

Gaus Width: 3.7389



B1L100S, U7-ch116, adc2

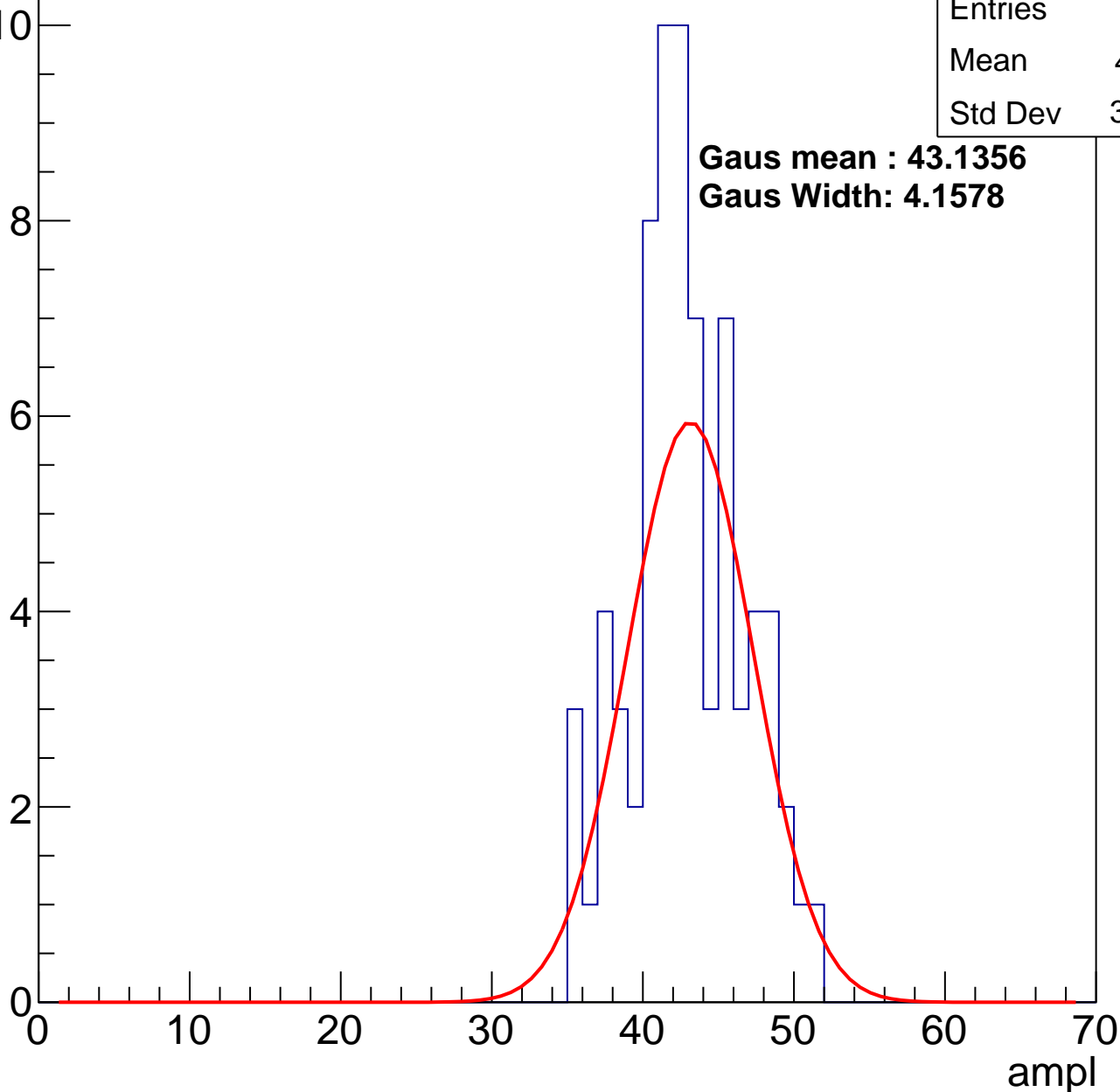
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 42.41 |
| Std Dev | 3.693 |

Gaus mean : 43.1356

Gaus Width: 4.1578

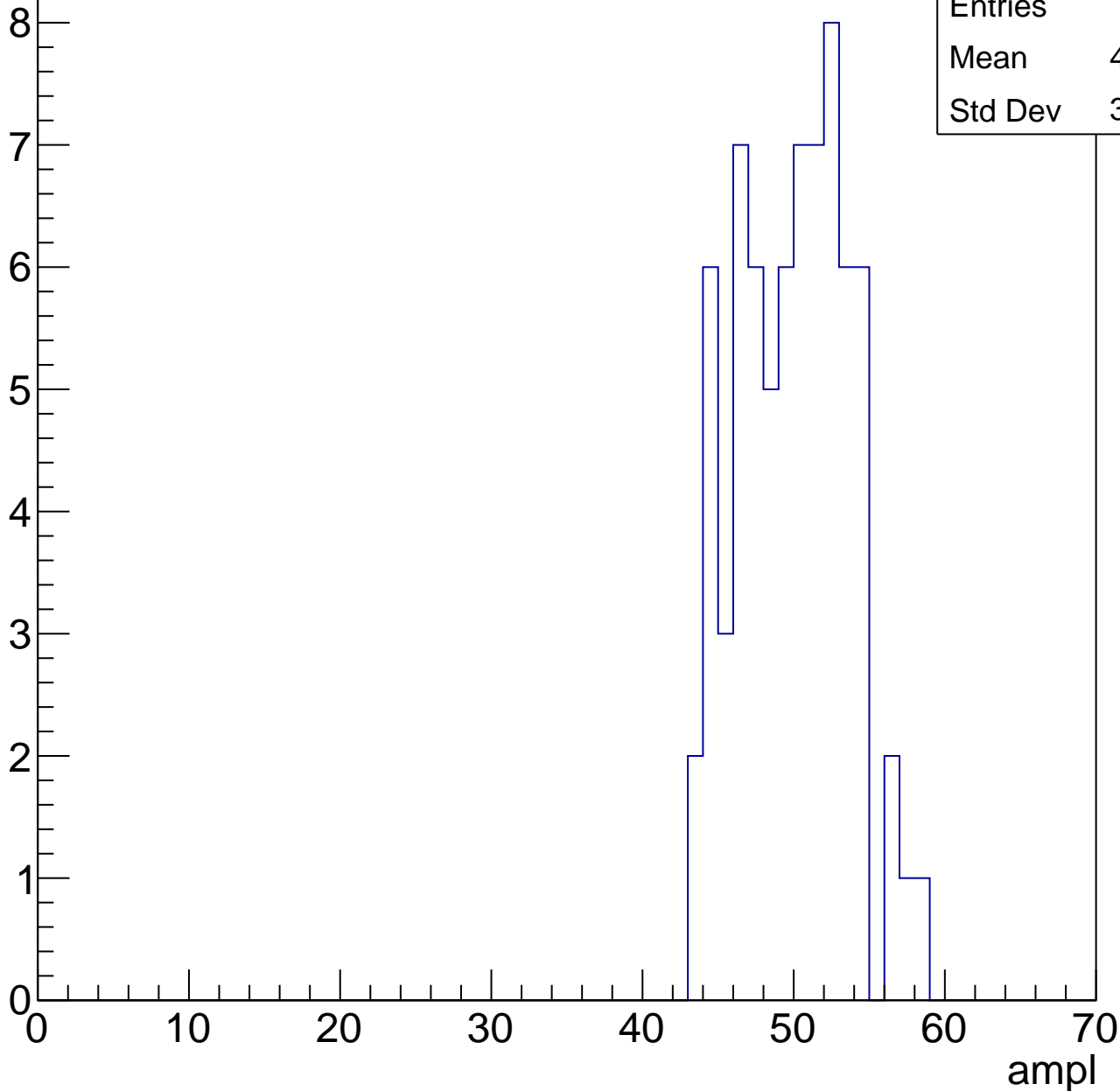


B1L100S, U7-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 49.52 |
| Std Dev | 3.585 |

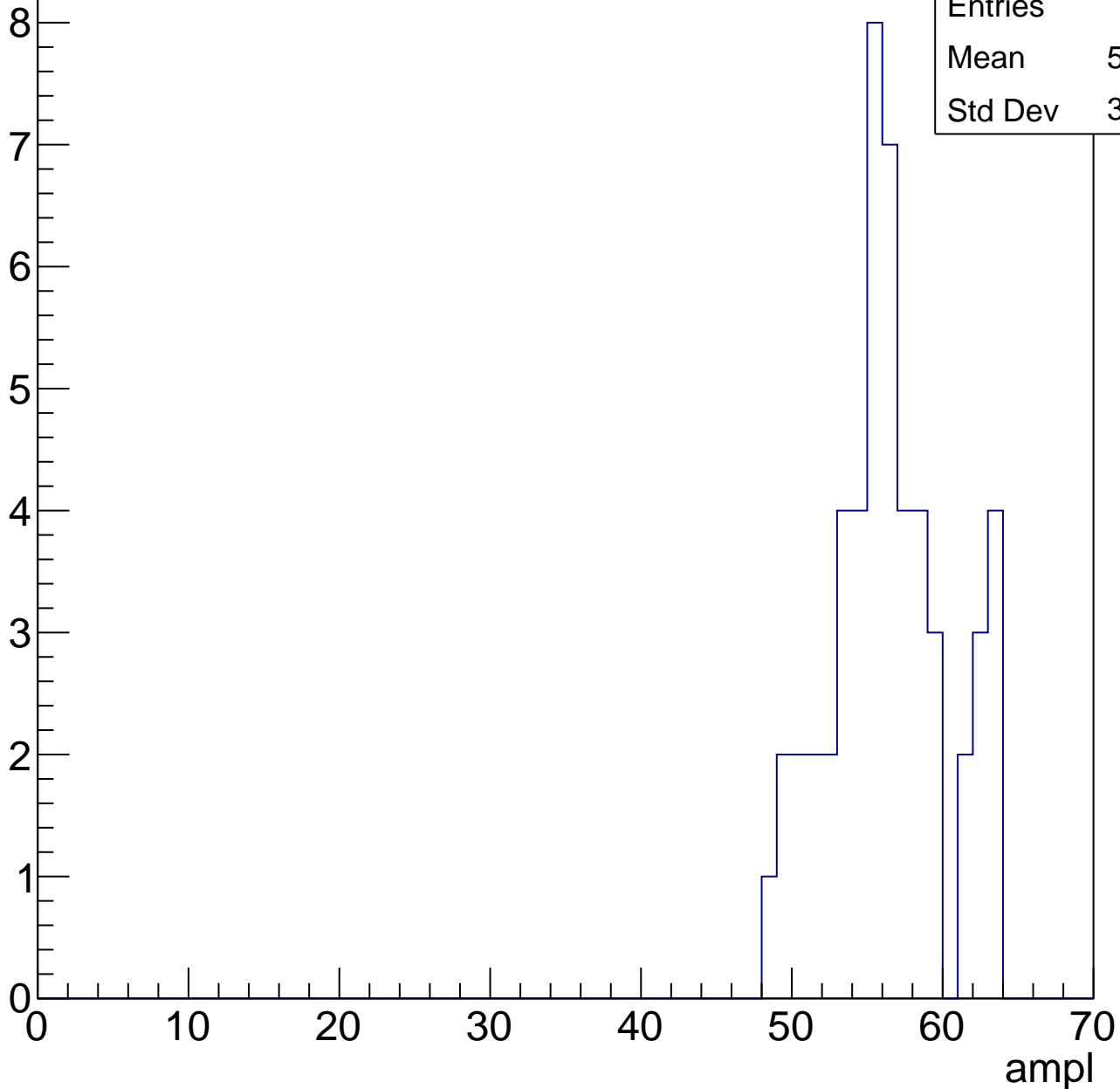


B1L100S, U7-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

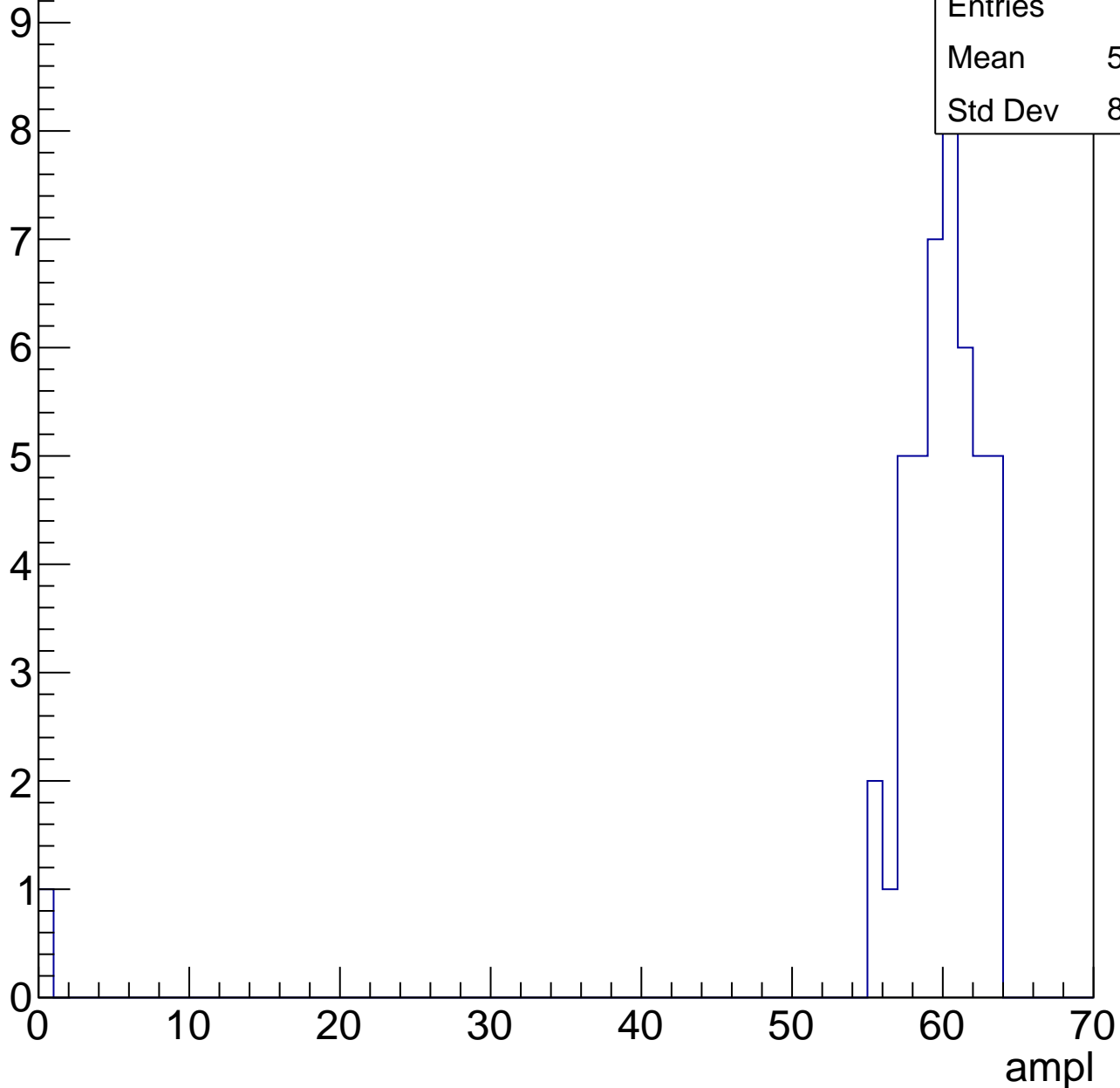
| | |
|---------|-------|
| Entries | 52 |
| Mean | 55.94 |
| Std Dev | 3.865 |



B1L100S, U7-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 6 |
| Mean | 61.5 |
| Std Dev | 0.9574 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U7-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch117, adc0

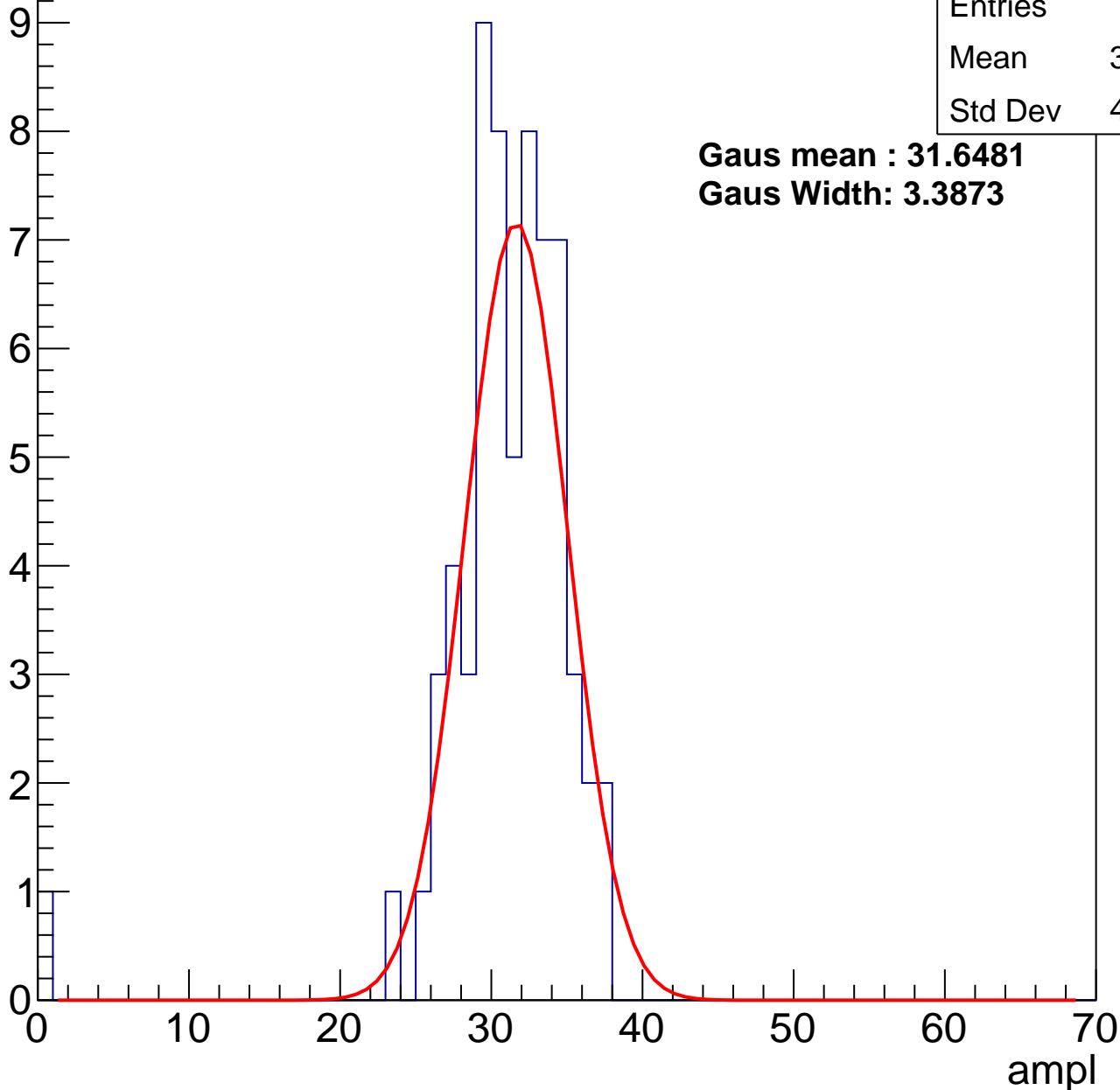
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.47 |
| Std Dev | 4.877 |

Gaus mean : 31.6481

Gaus Width: 3.3873



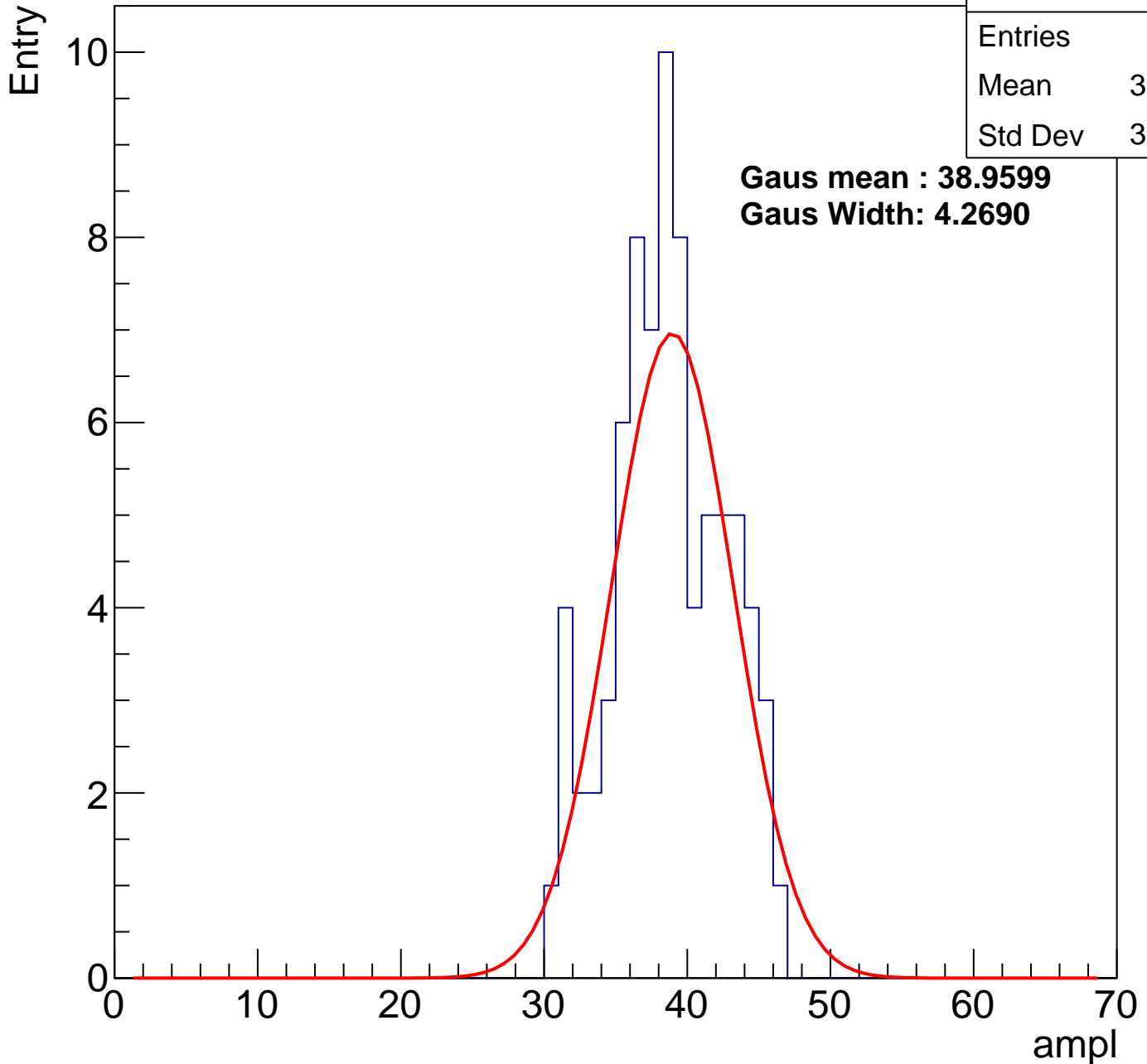
B1L100S, U7-ch117, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 78 |
| Mean | 38.23 |
| Std Dev | 3.836 |

Gaus mean : 38.9599

Gaus Width: 4.2690



B1L100S, U7-ch117, adc2

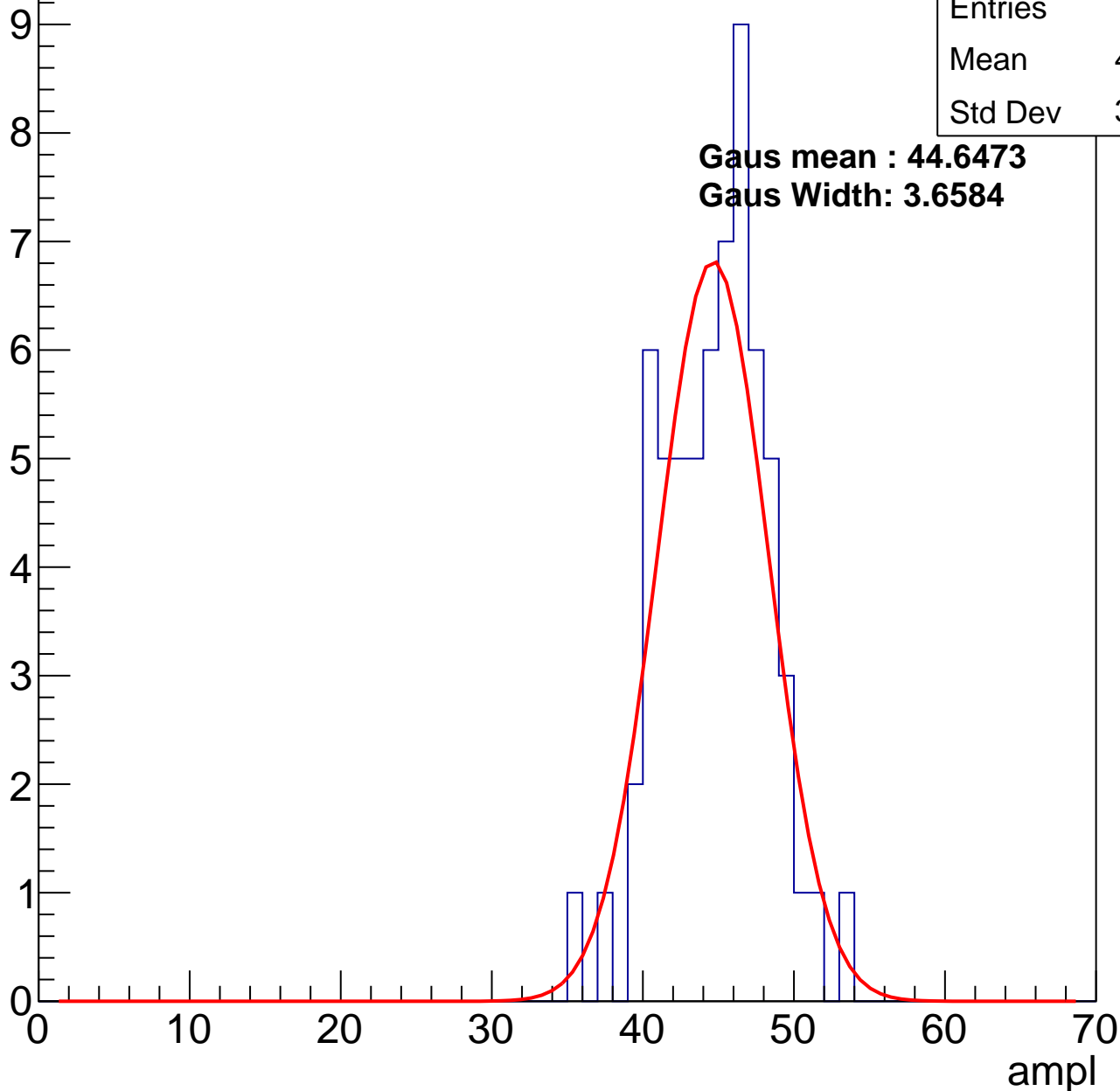
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 44.31 |
| Std Dev | 3.441 |

Gaus mean : 44.6473

Gaus Width: 3.6584

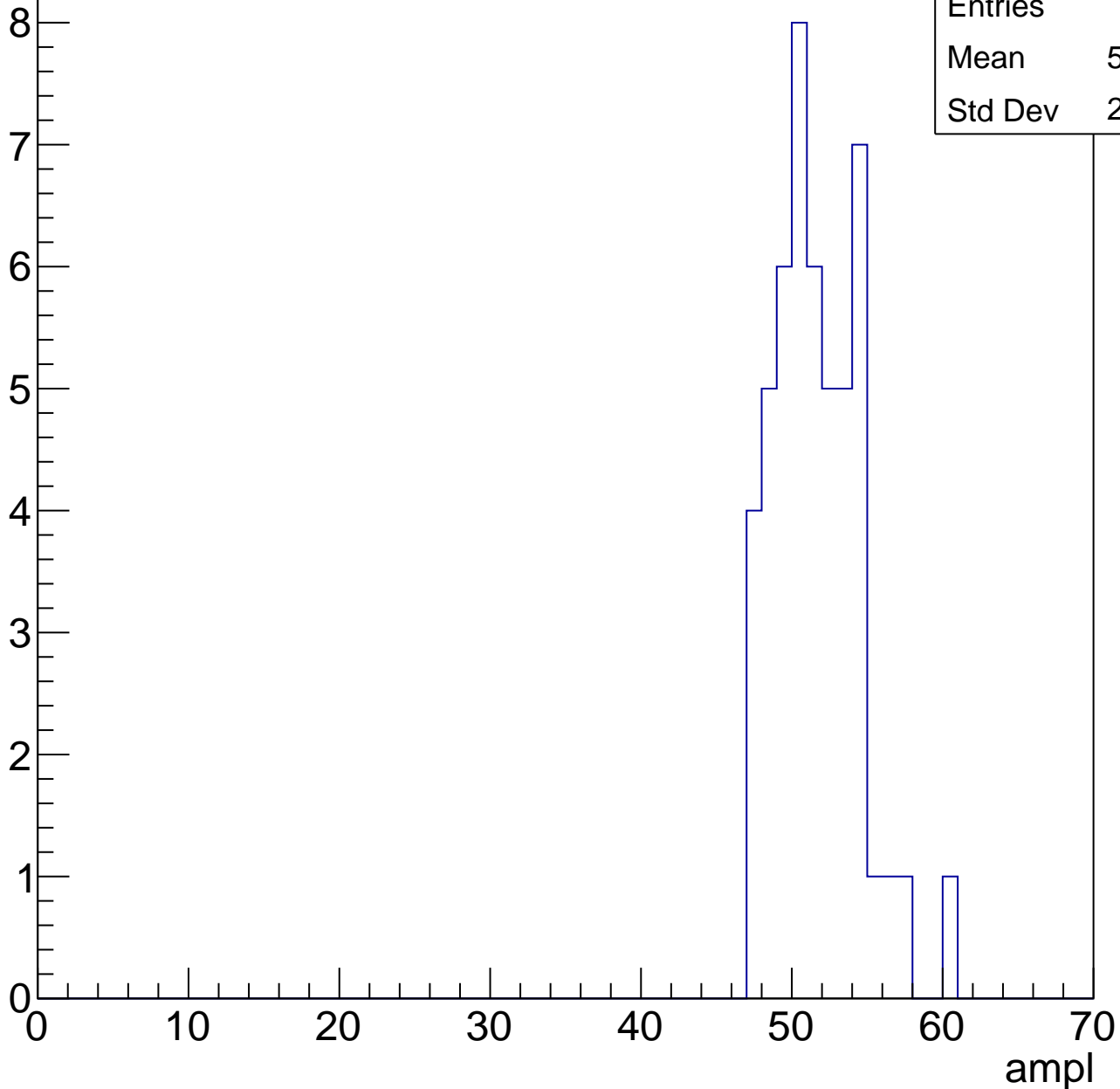


B1L100S, U7-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

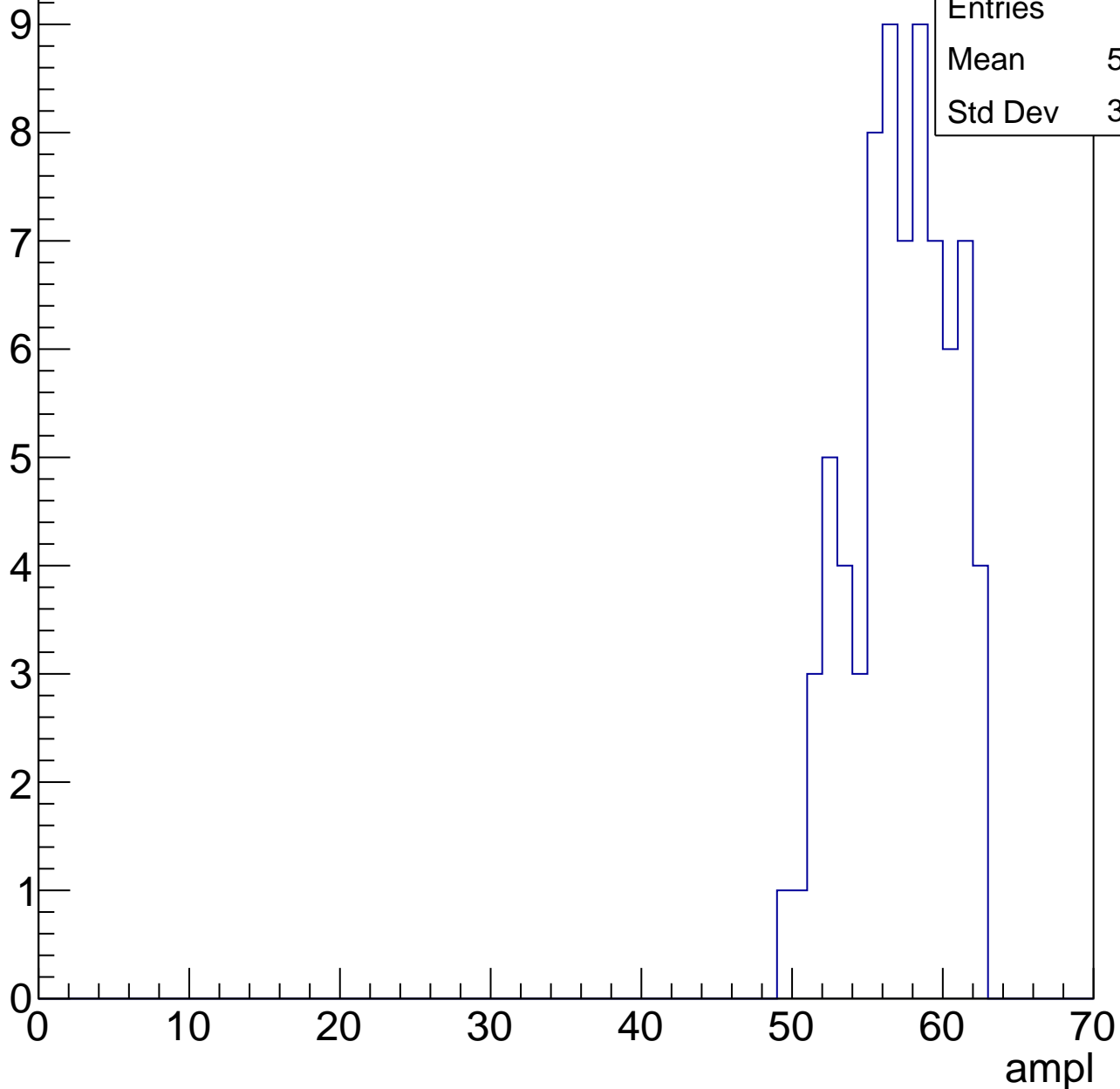
| | |
|---------|-------|
| Entries | 50 |
| Mean | 51.18 |
| Std Dev | 2.776 |



B1L100S, U7-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



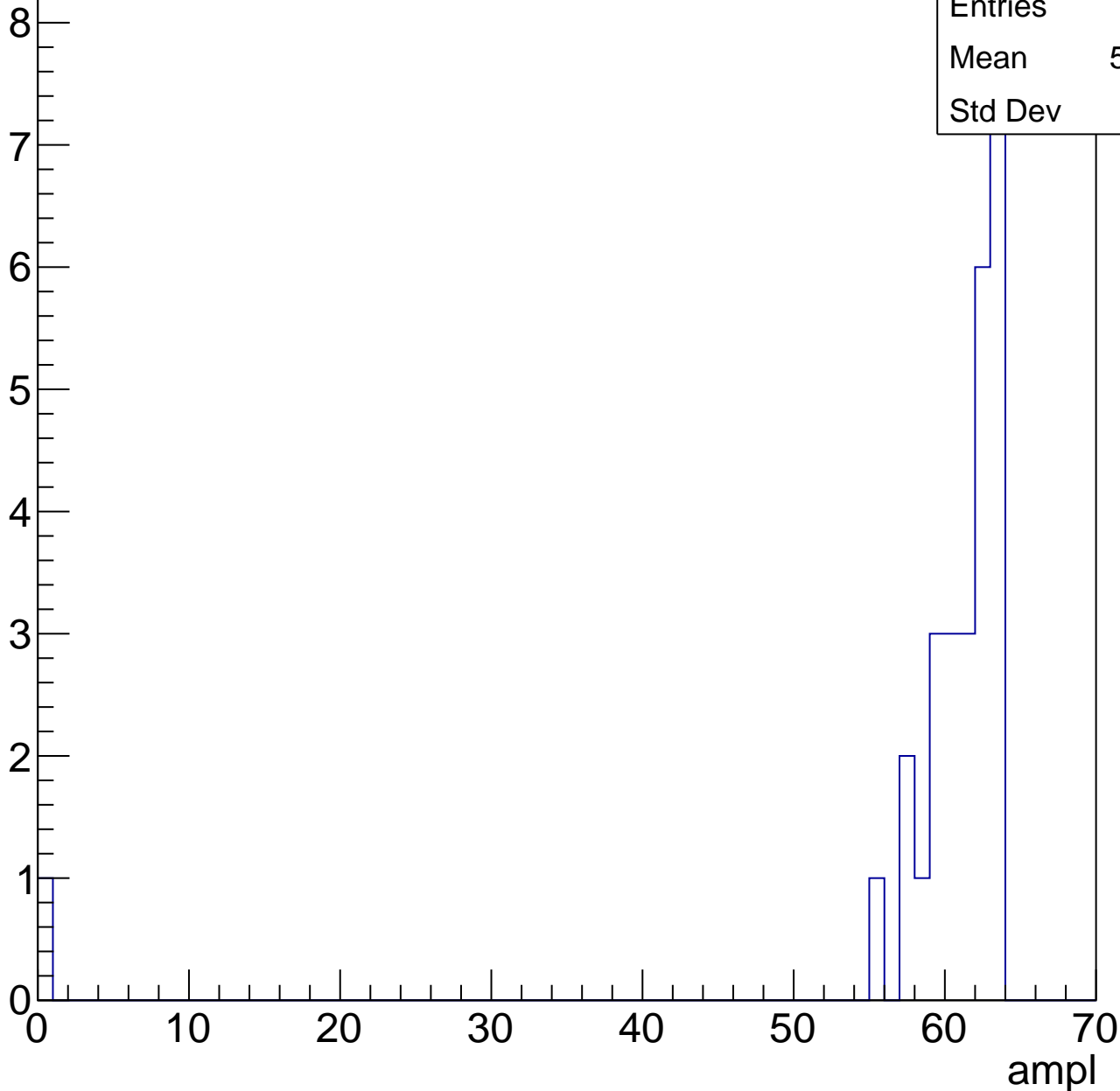
| | |
|---------|-------|
| Entries | 74 |
| Mean | 56.74 |
| Std Dev | 3.239 |

B1L100S, U7-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.68 |
| Std Dev | 11.5 |



B1L100S, U7-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch118, adc0

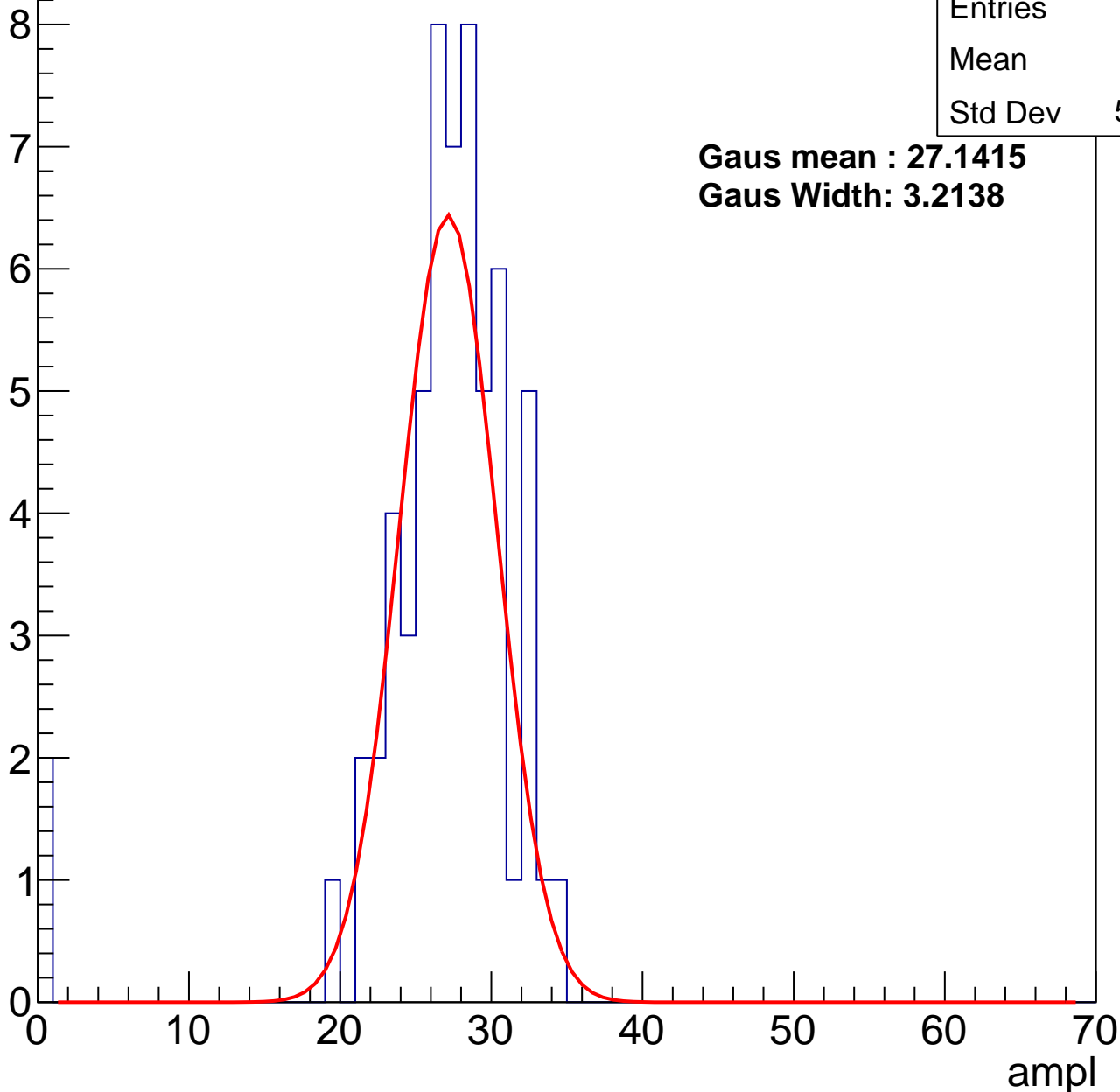
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 26.2 |
| Std Dev | 5.771 |

Gaus mean : 27.1415

Gaus Width: 3.2138



B1L100S, U7-ch118, adc1

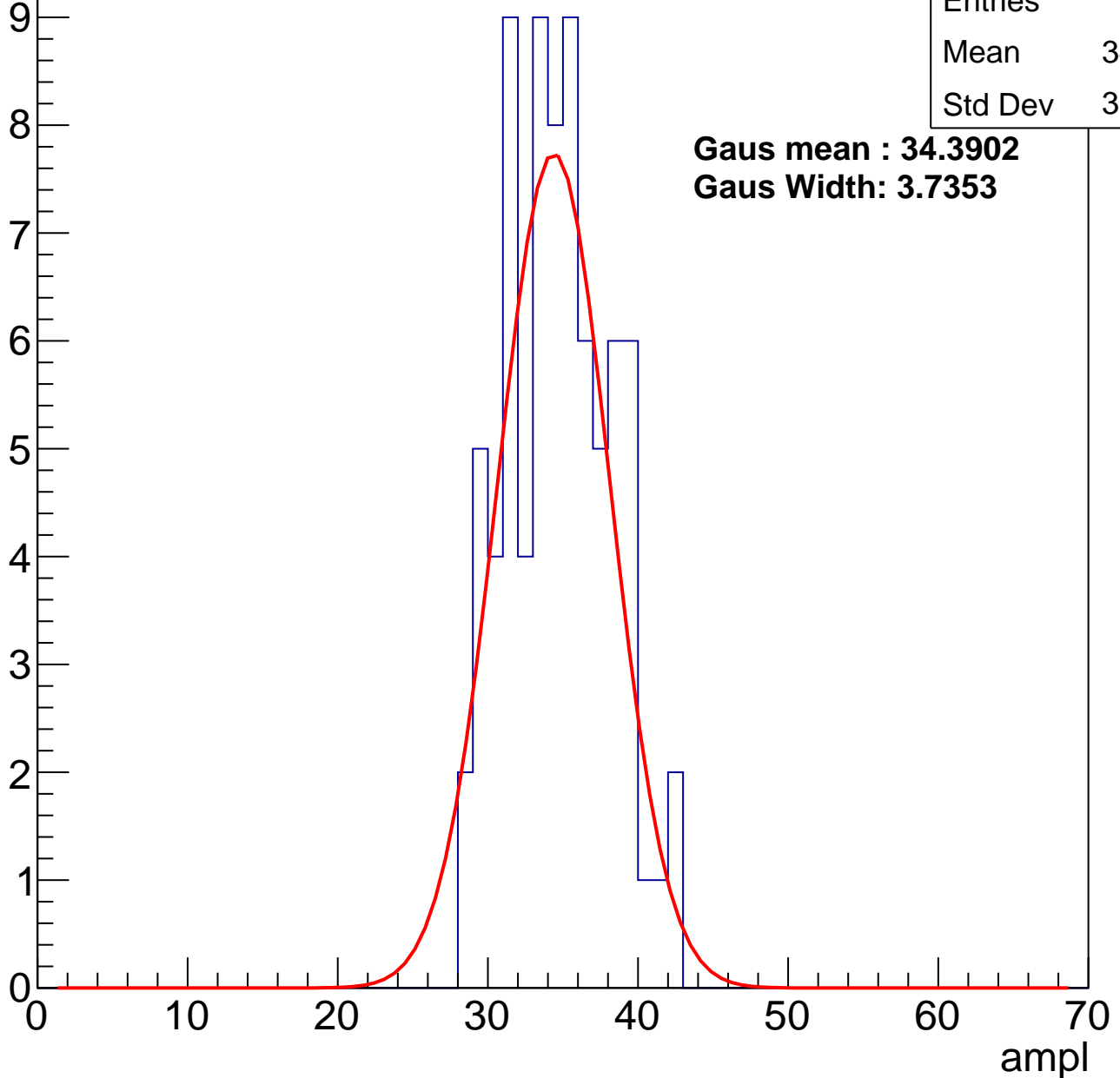
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 34.29 |
| Std Dev | 3.414 |

Gaus mean : 34.3902

Gaus Width: 3.7353



B1L100S, U7-ch118, adc2

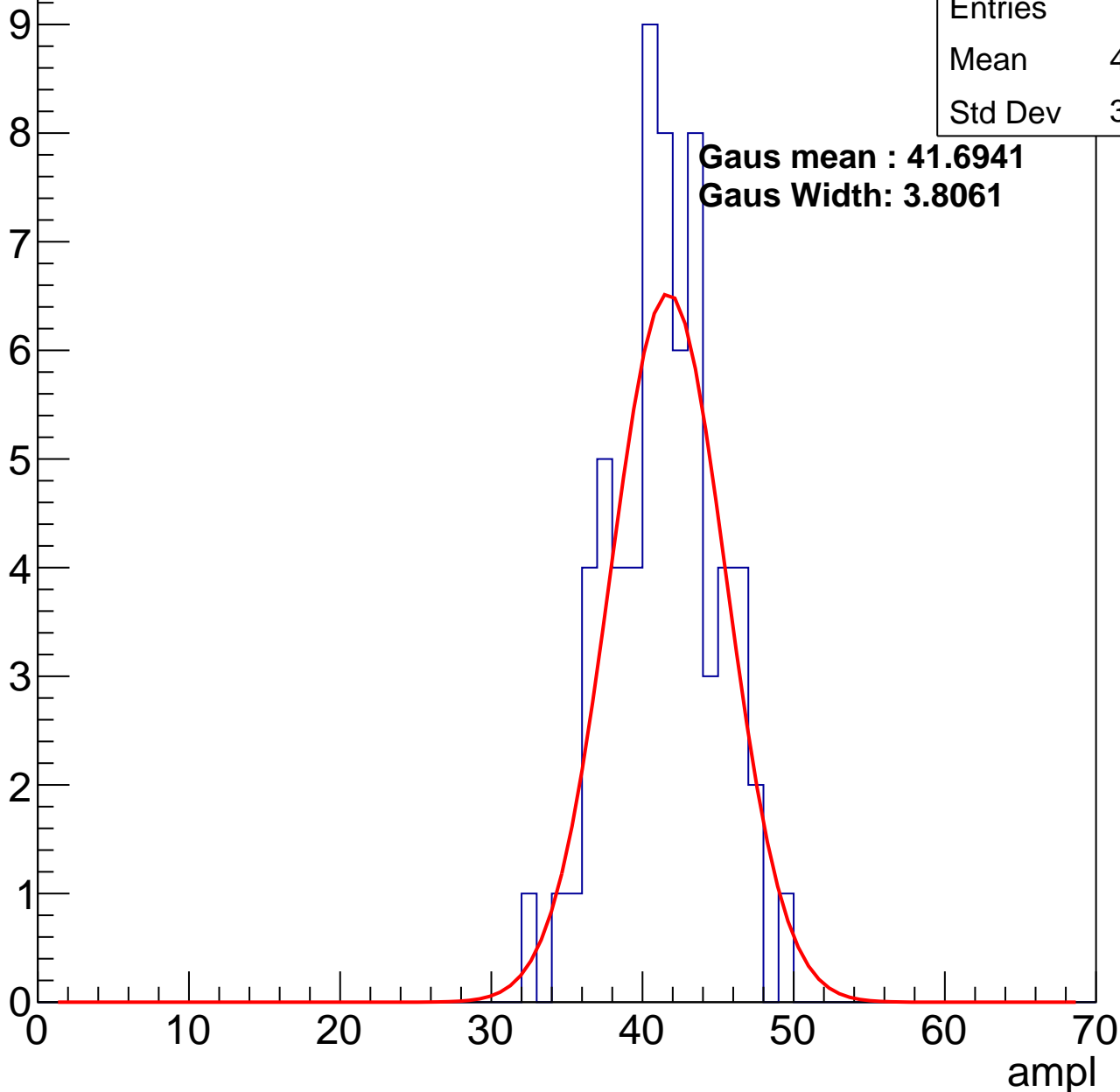
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 40.94 |
| Std Dev | 3.446 |

Gaus mean : 41.6941

Gaus Width: 3.8061

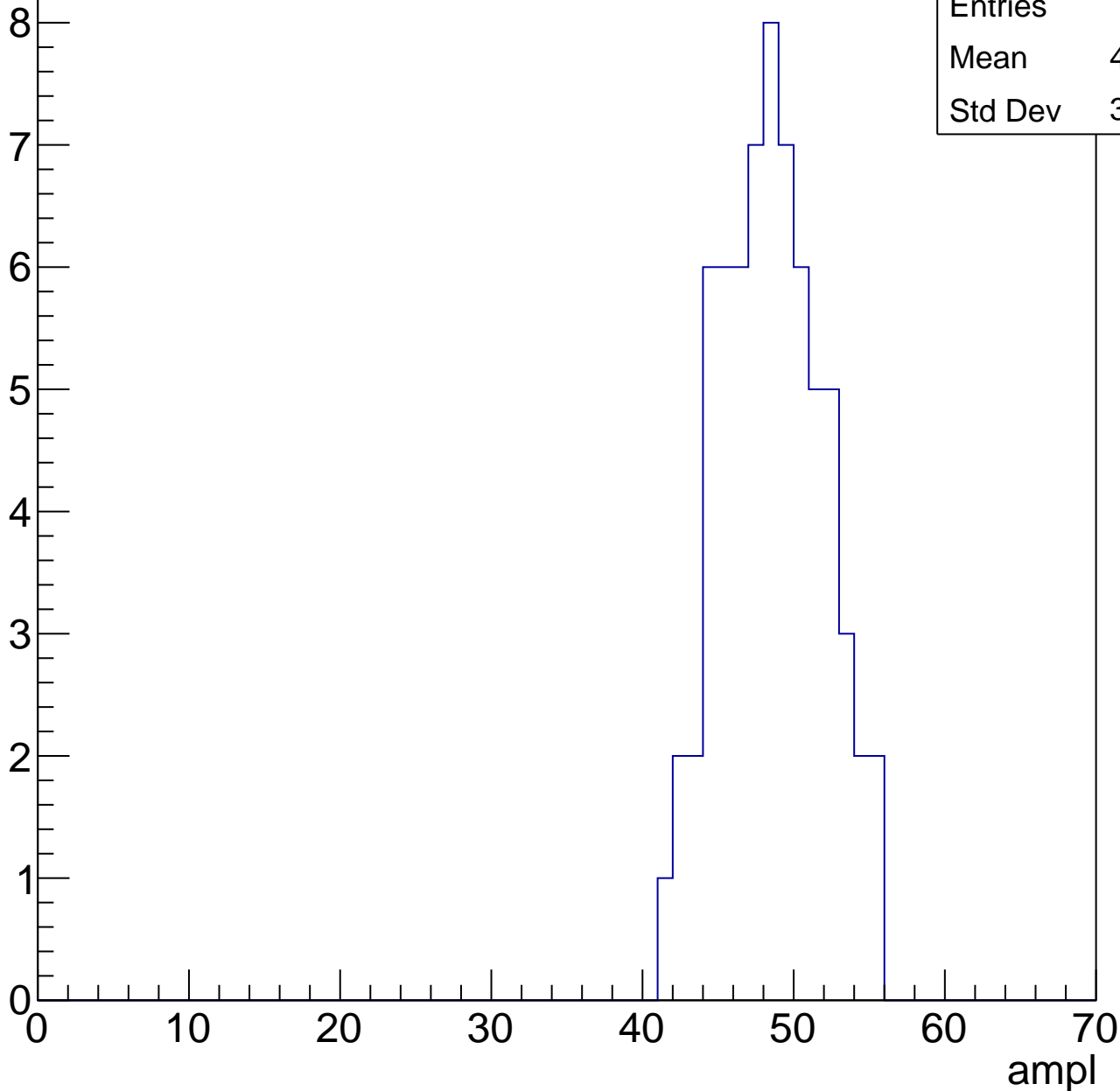


B1L100S, U7-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 48.07 |
| Std Dev | 3.327 |

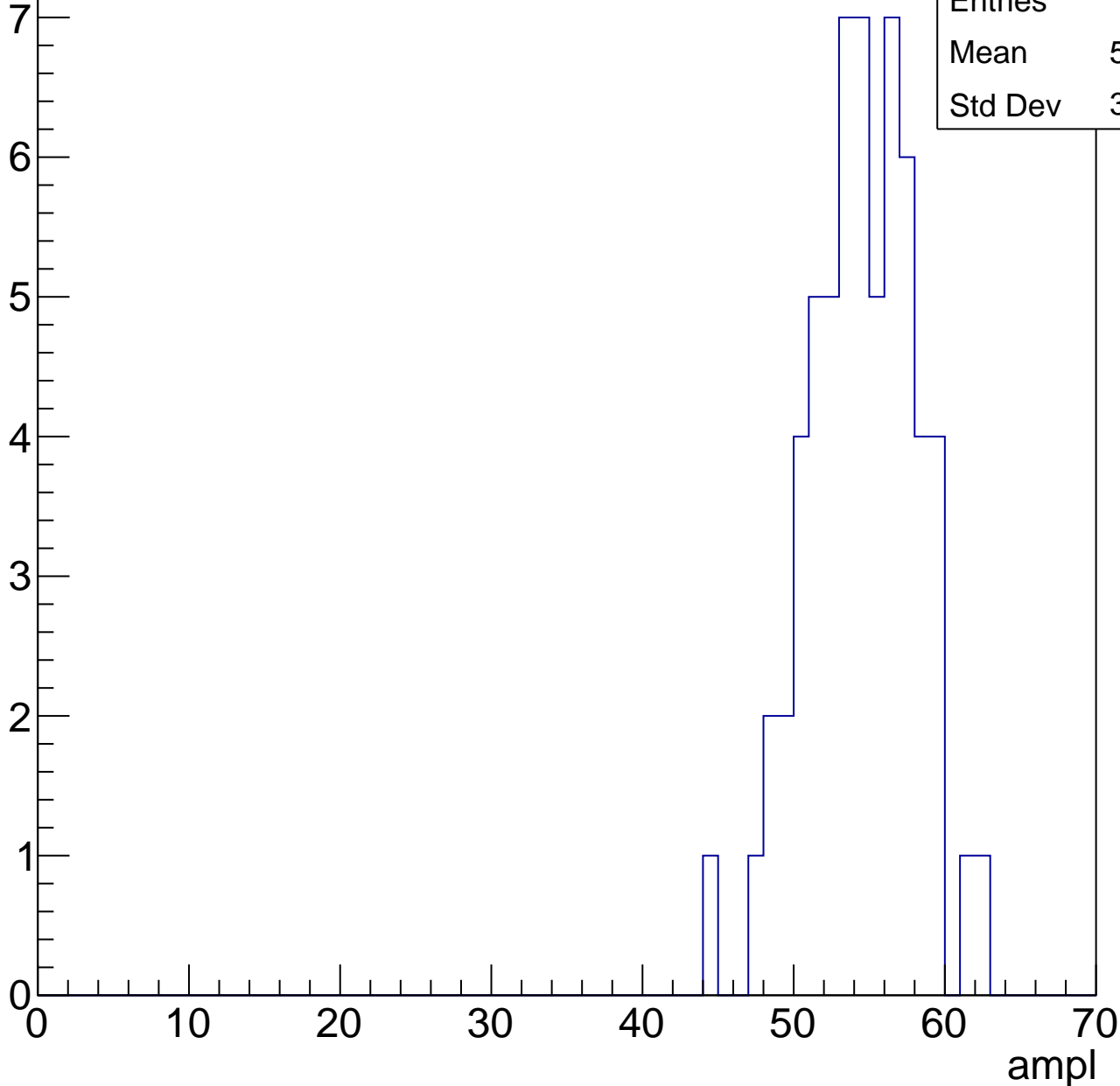


B1L100S, U7-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

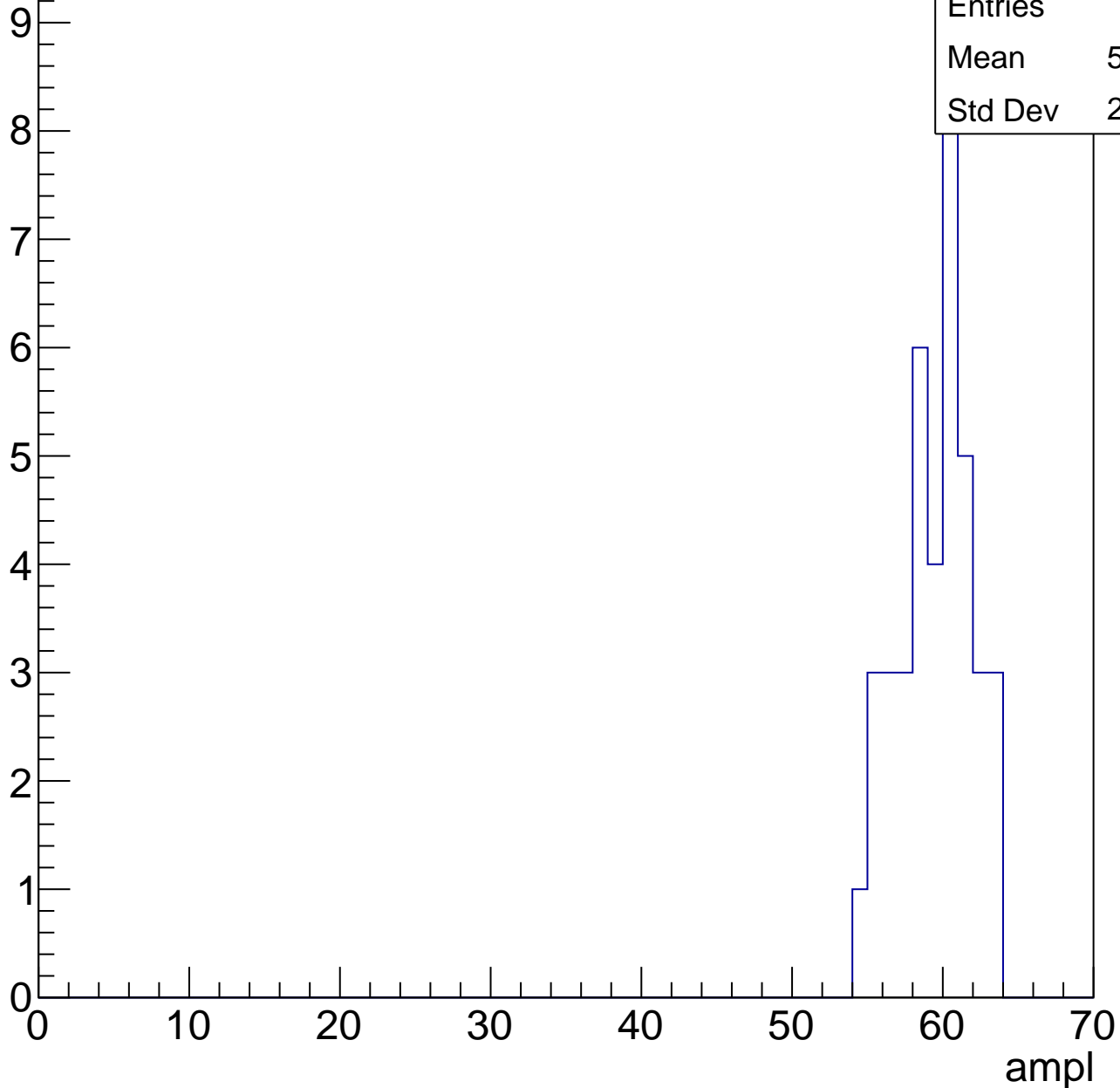
| | |
|---------|-------|
| Entries | 62 |
| Mean | 54.02 |
| Std Dev | 3.526 |



B1L100S, U7-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



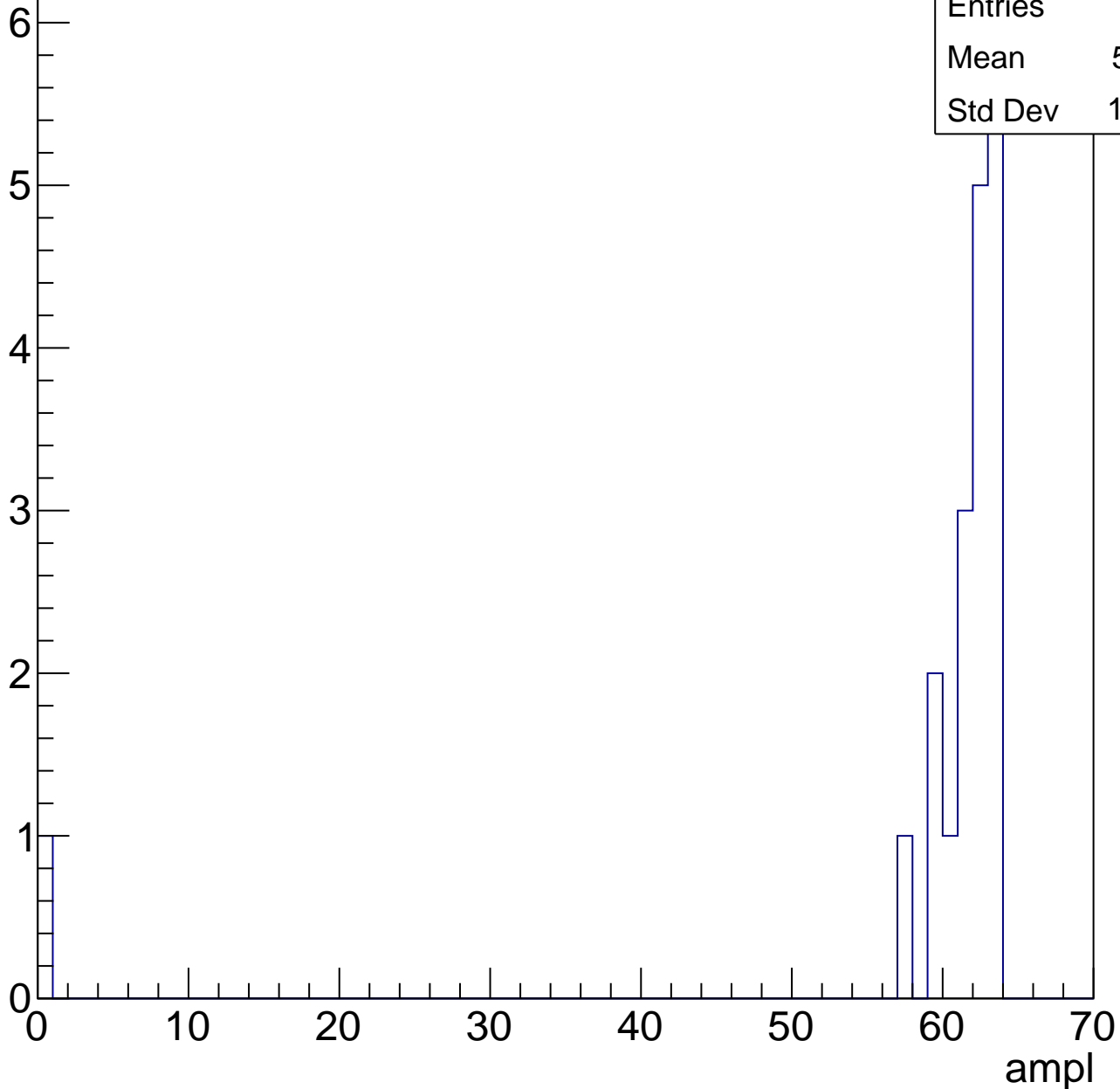
| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.05 |
| Std Dev | 2.355 |

B1L100S, U7-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

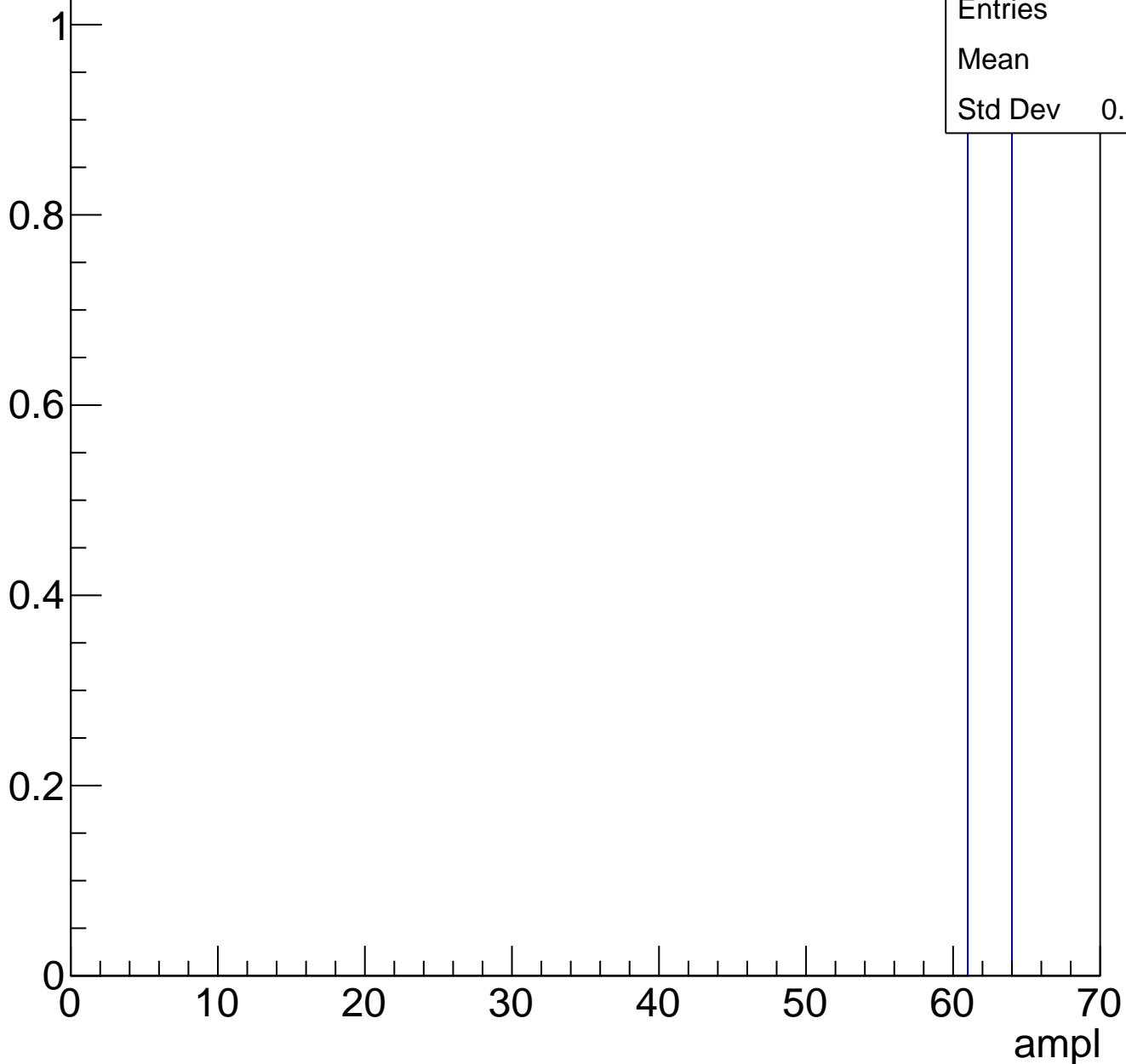
| | |
|---------|-------|
| Entries | 19 |
| Mean | 58.21 |
| Std Dev | 13.82 |



B1L100S, U7-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch119, adc0

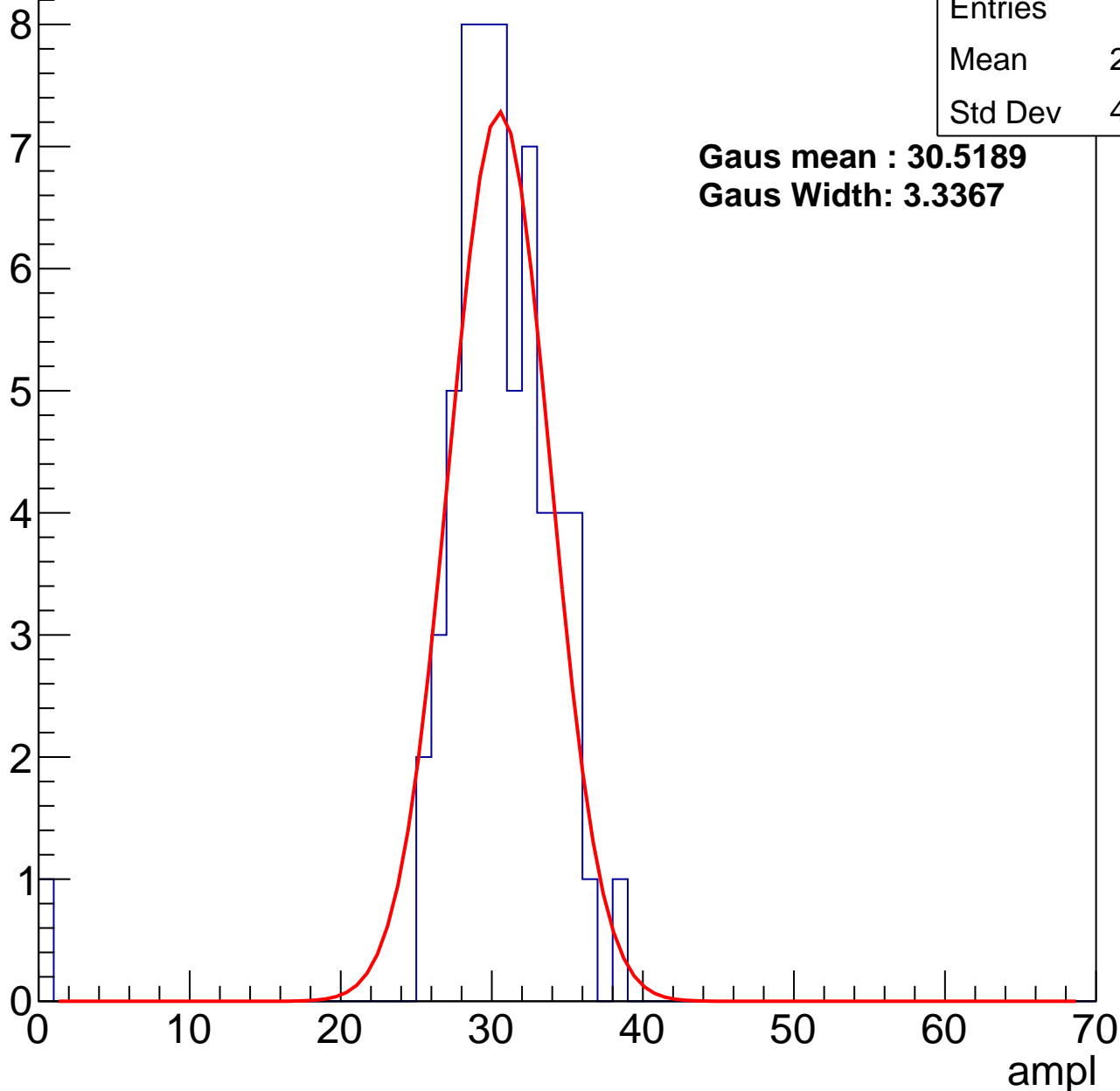
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.84 |
| Std Dev | 4.815 |

Gaus mean : 30.5189

Gaus Width: 3.3367



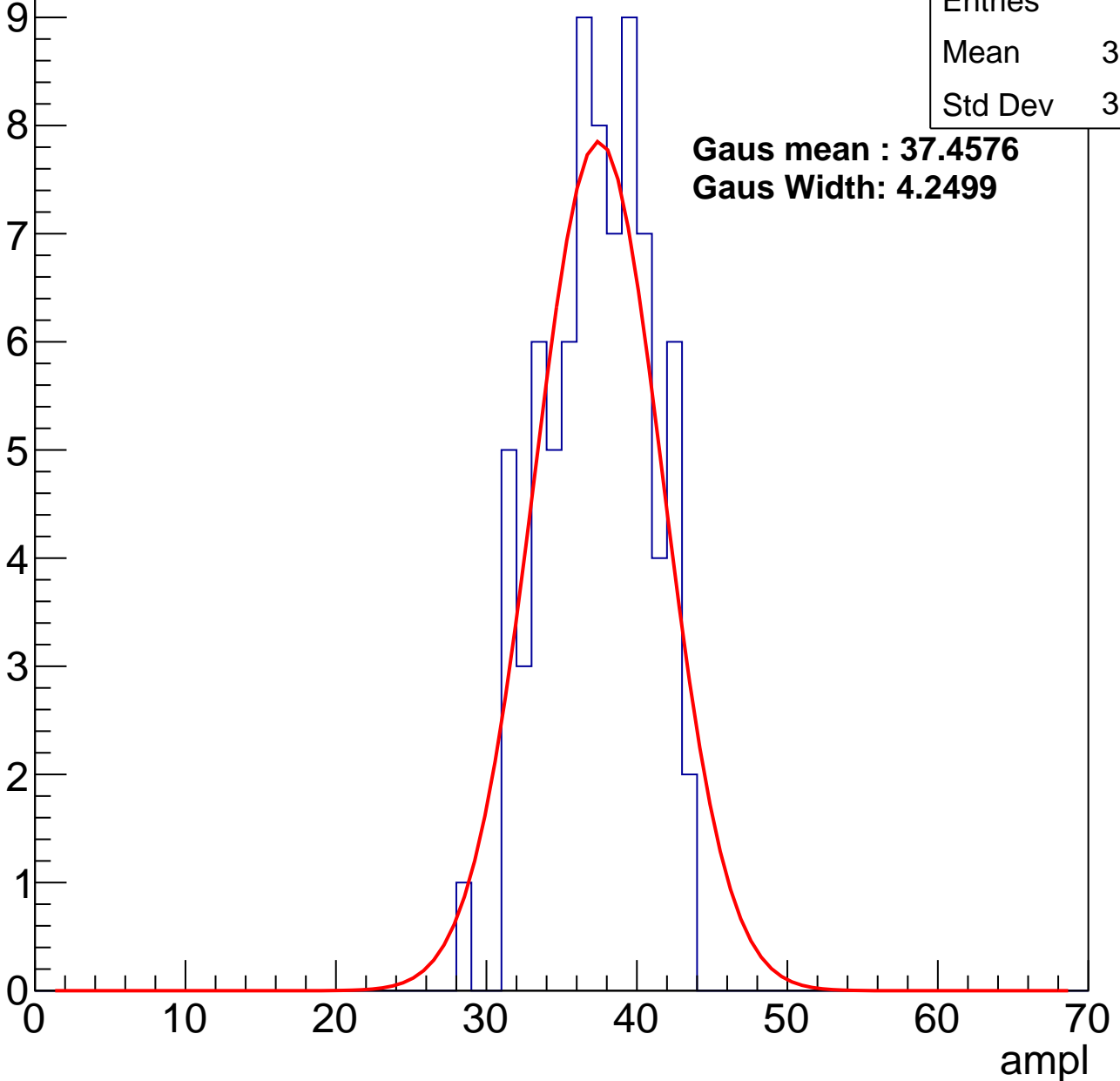
B1L100S, U7-ch119, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 36.87 |
| Std Dev | 3.398 |

Gaus mean : 37.4576
Gaus Width: 4.2499



B1L100S, U7-ch119, adc2

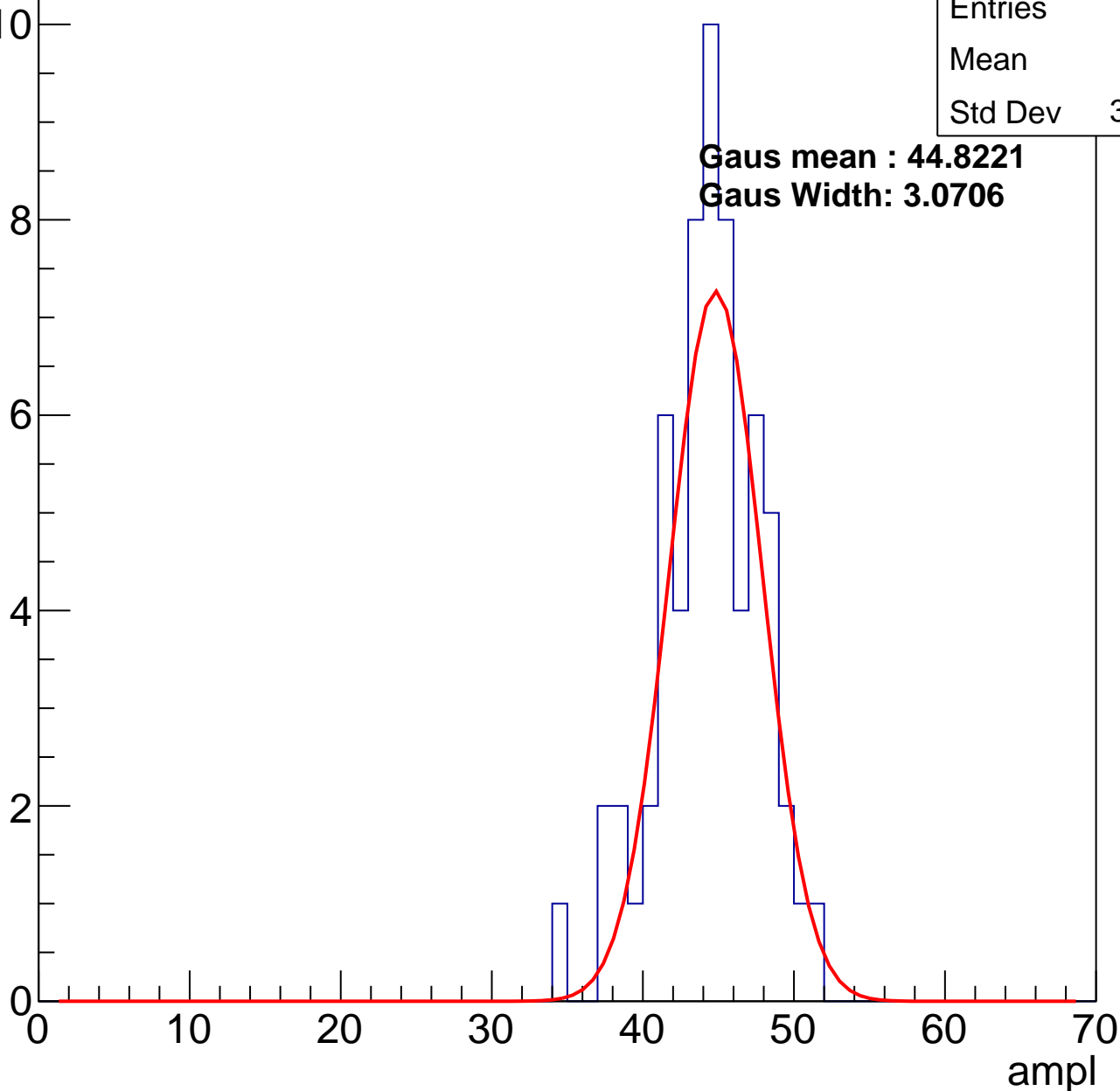
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.9 |
| Std Dev | 3.327 |

Gaus mean : 44.8221

Gaus Width: 3.0706

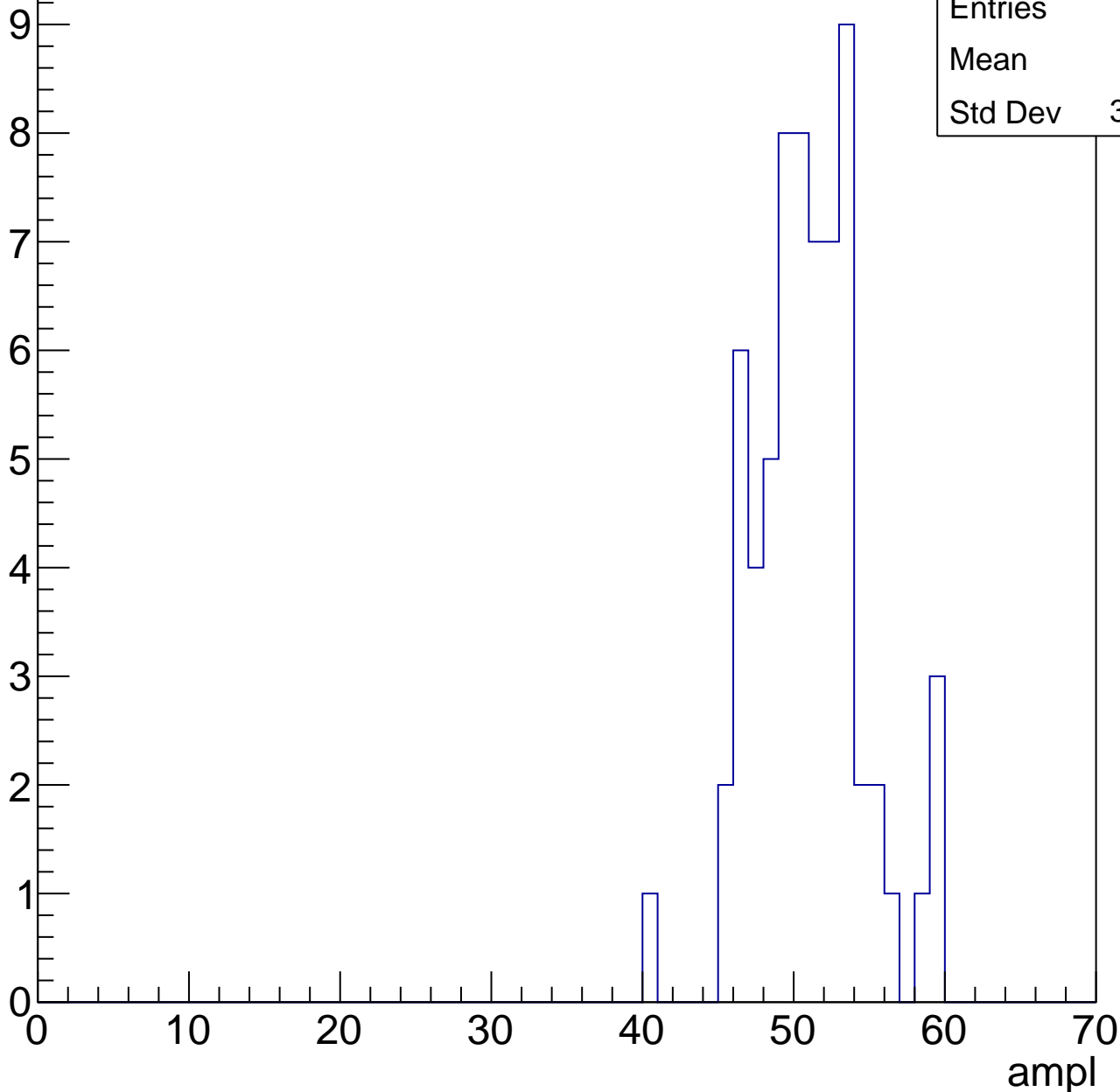


B1L100S, U7-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

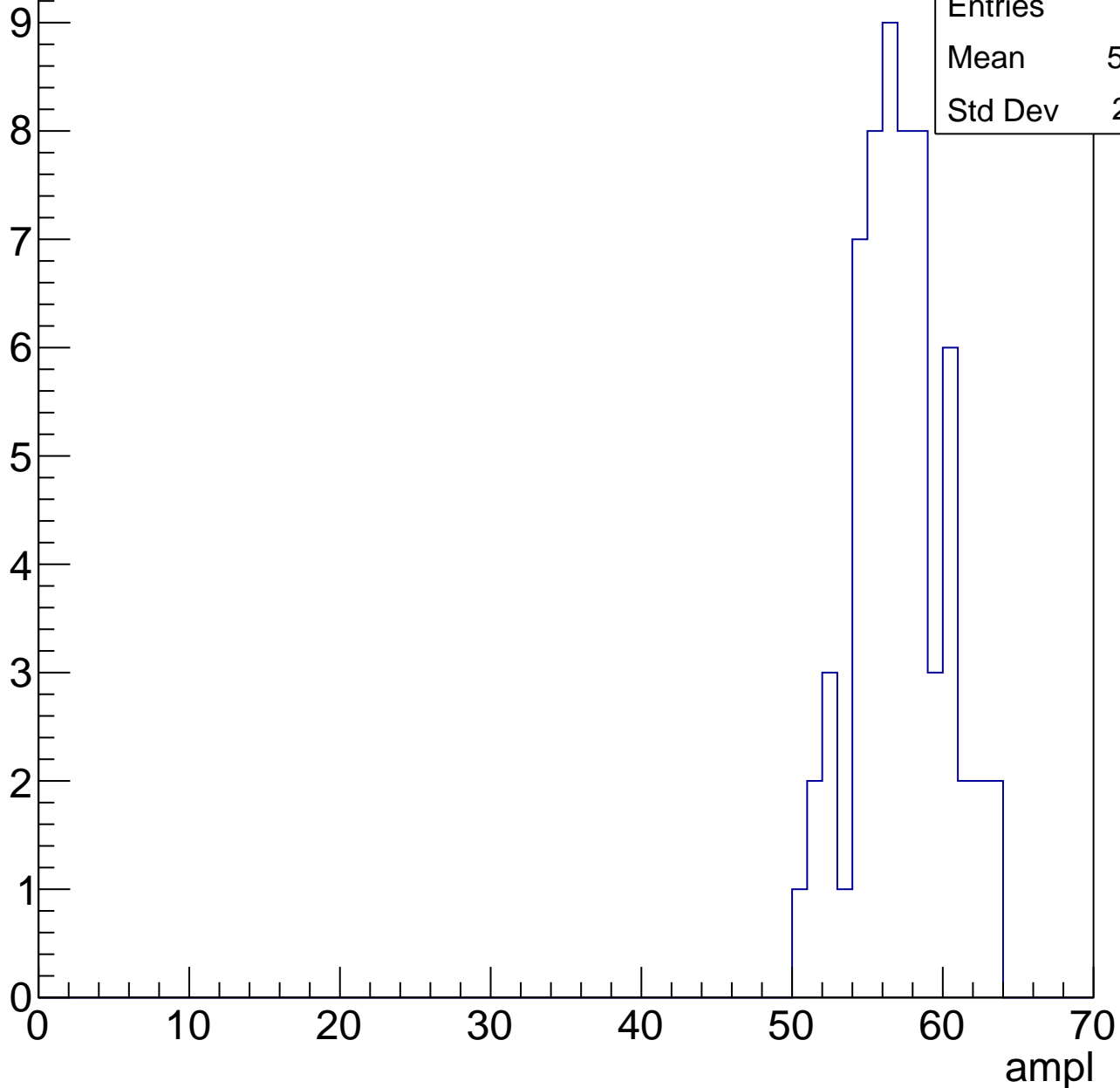
| | |
|---------|-------|
| Entries | 66 |
| Mean | 50.5 |
| Std Dev | 3.556 |



B1L100S, U7-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

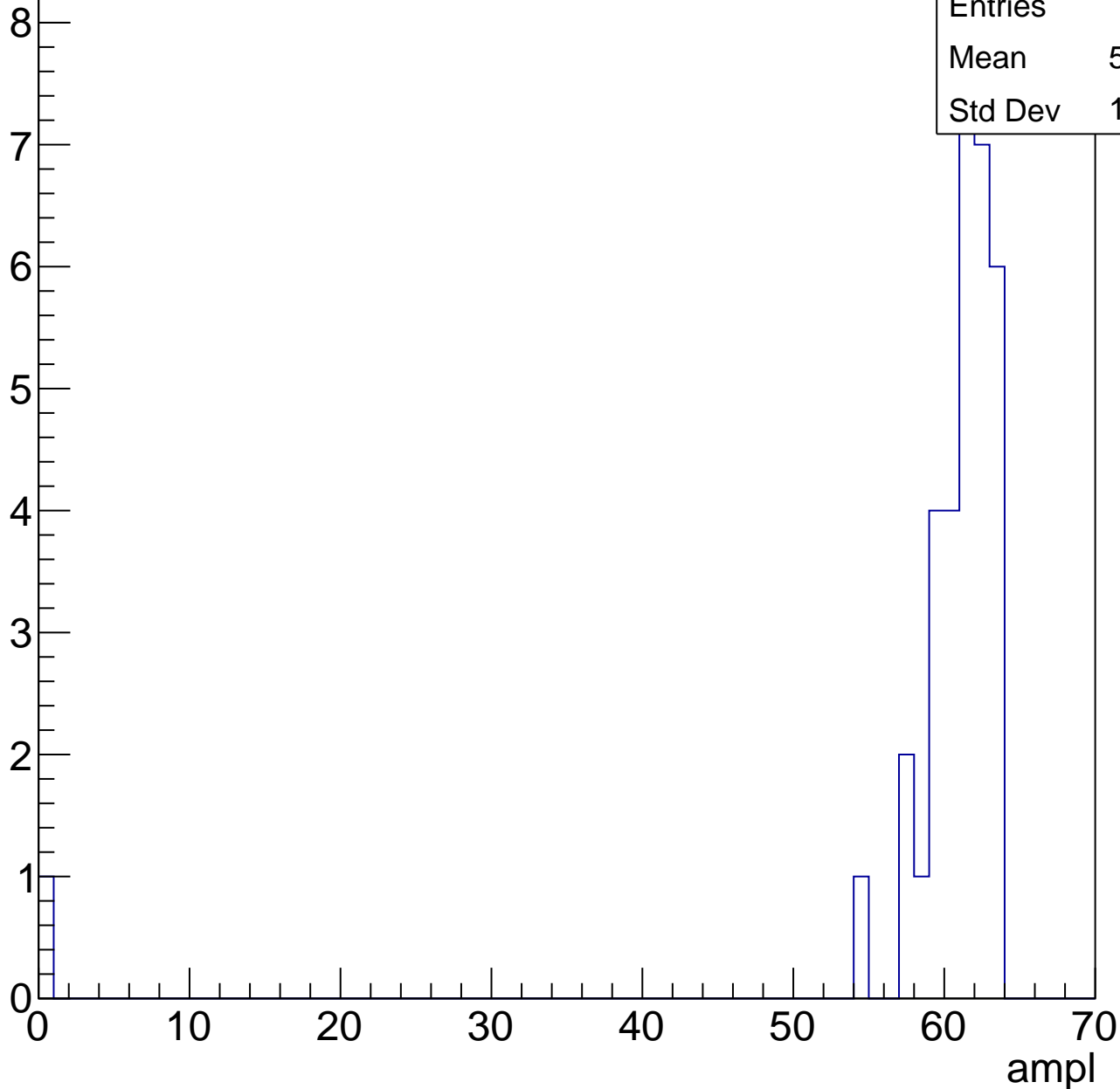


B1L100S, U7-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 58.88 |
| Std Dev | 10.45 |



B1L100S, U7-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B1L100S, U7-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch120, adc0

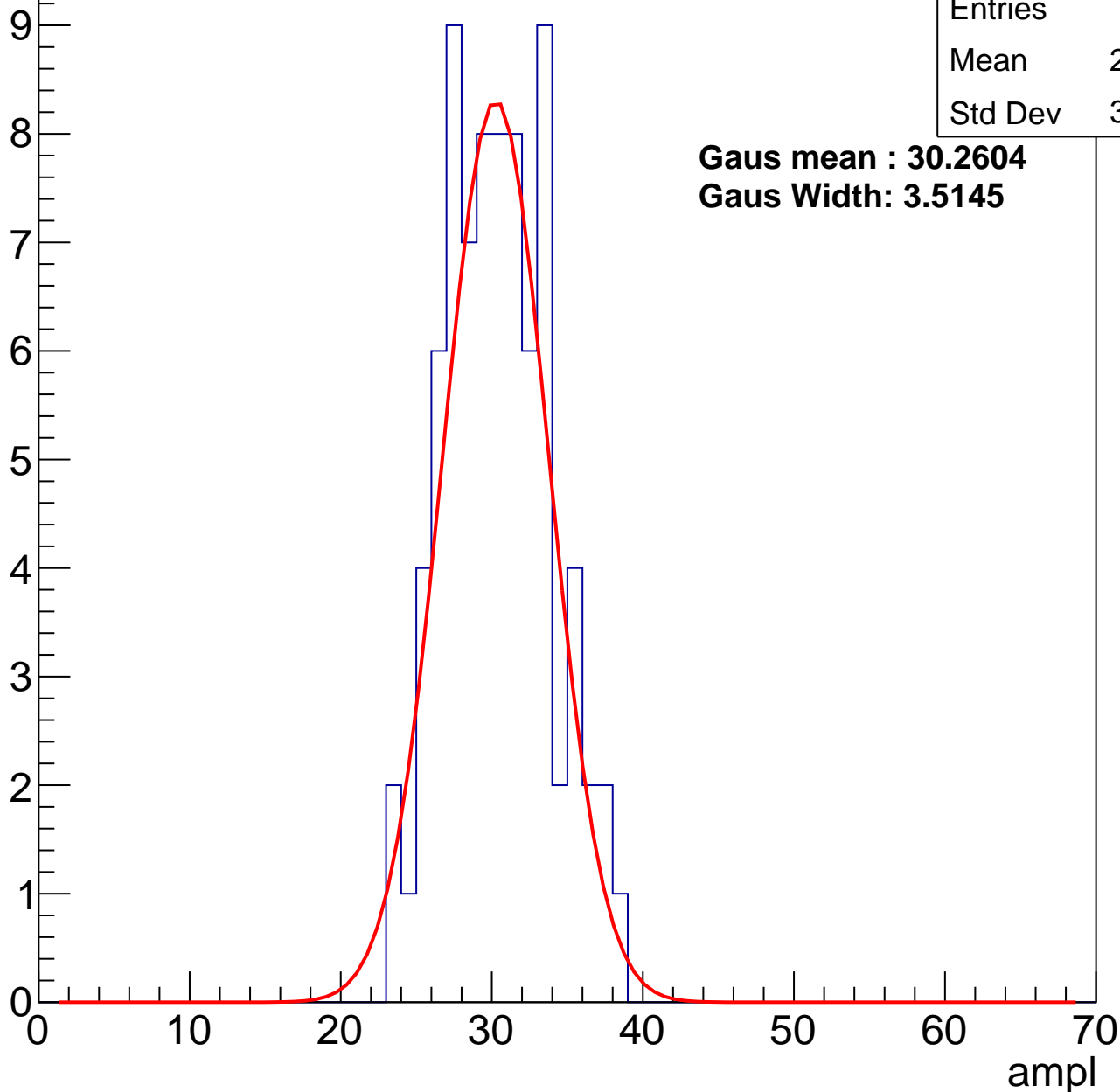
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.95 |
| Std Dev | 3.423 |

Gaus mean : 30.2604

Gaus Width: 3.5145



B1L100S, U7-ch120, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.17 |
| Std Dev | 3.572 |

Gaus mean : 38.0864

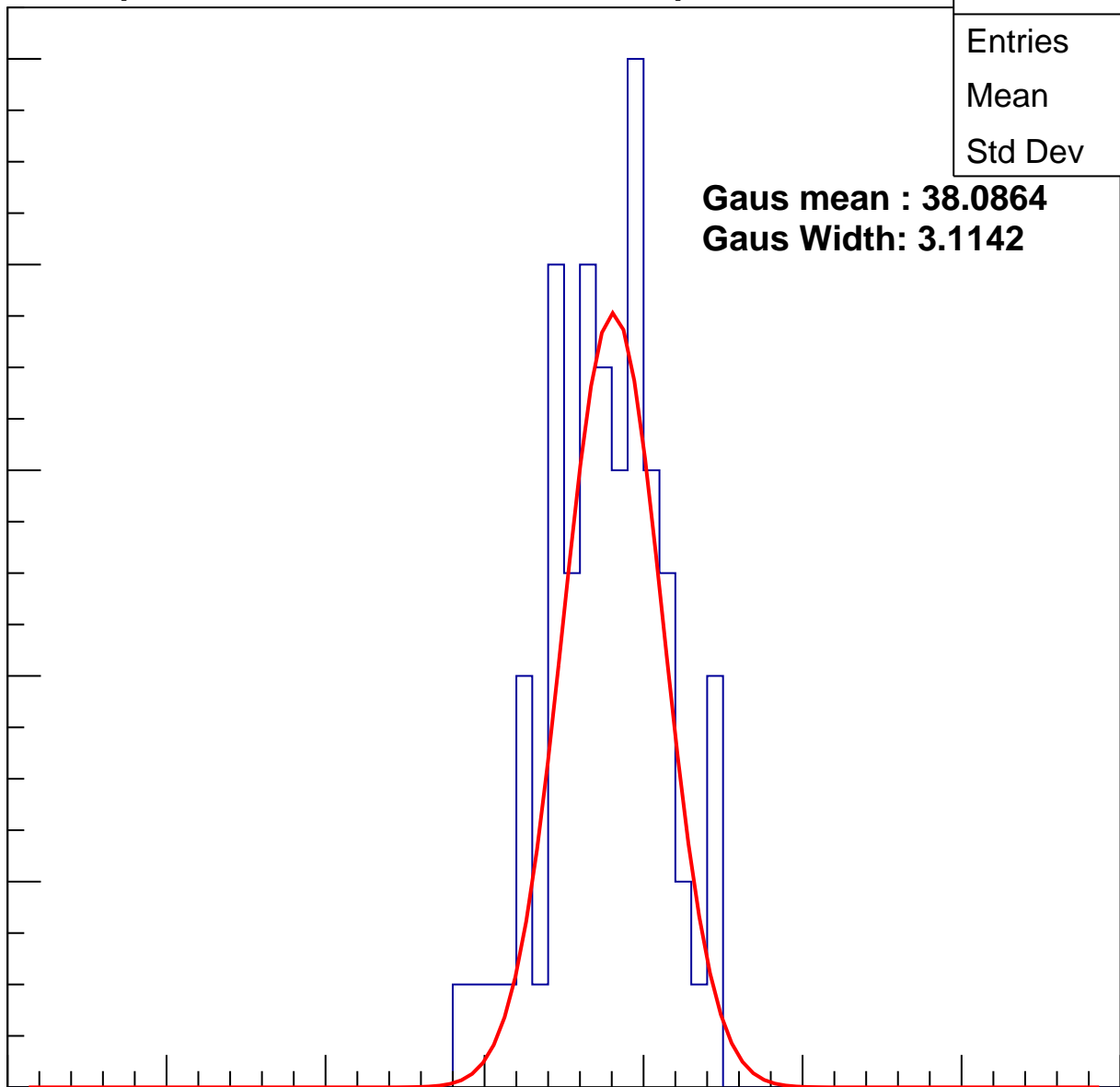
Gaus Width: 3.1142

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch120, adc2

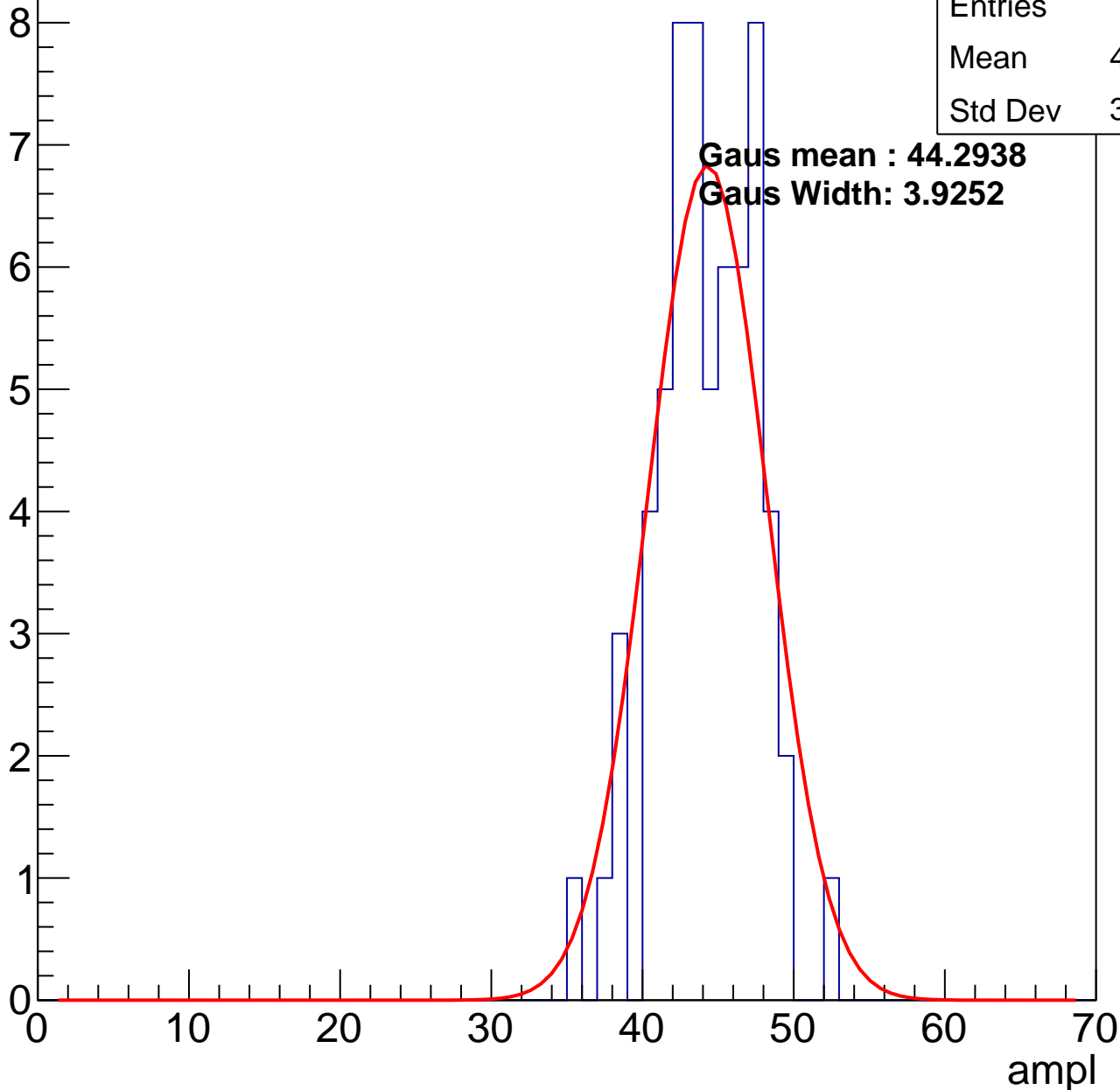
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.79 |
| Std Dev | 3.273 |

Gaus mean : 44.2938

Gaus Width: 3.9252



B1L100S, U7-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

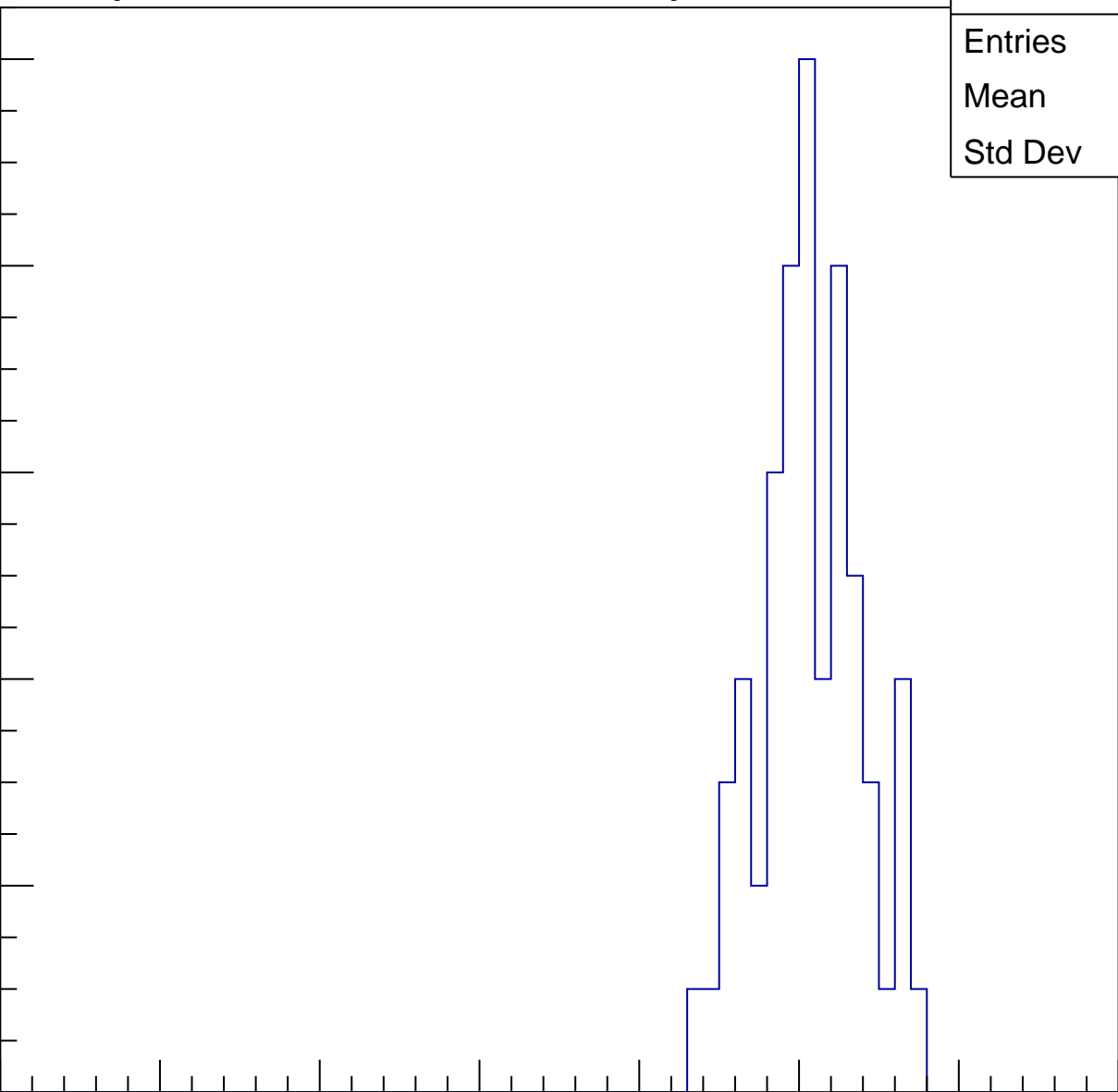
| | |
|---------|-------|
| Entries | 61 |
| Mean | 50.21 |
| Std Dev | 3.184 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

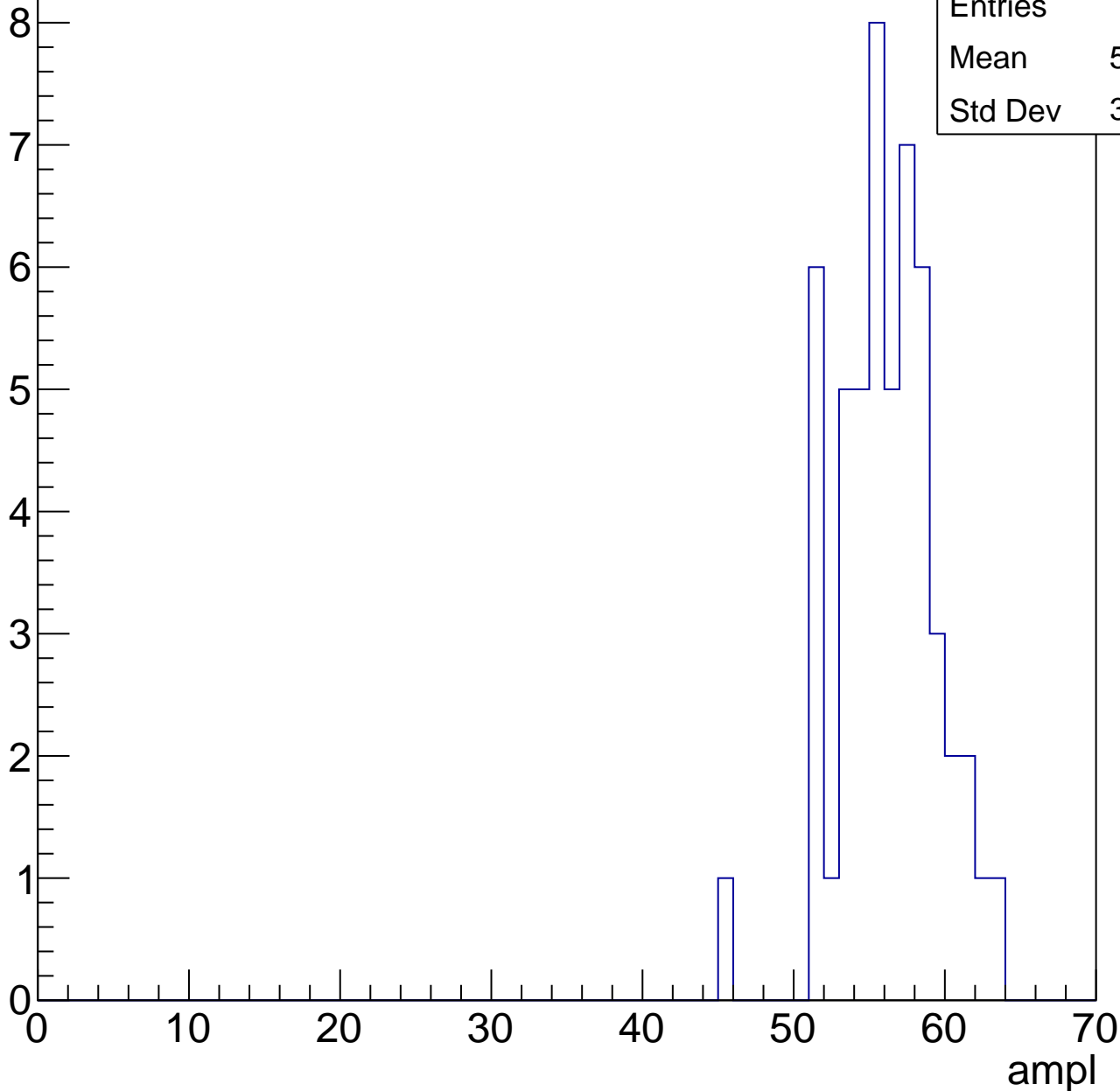


B1L100S, U7-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

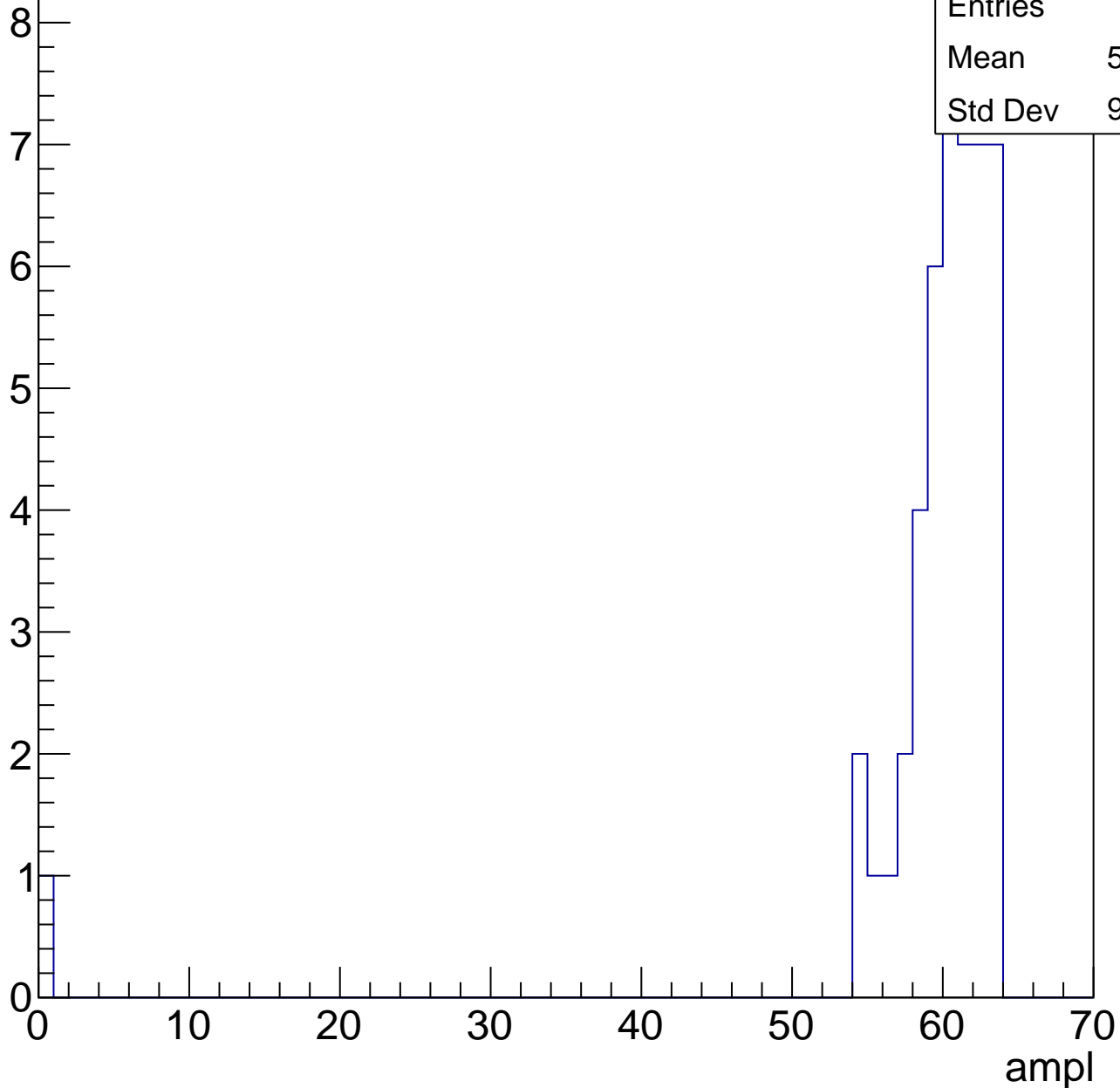
| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.64 |
| Std Dev | 3.303 |



B1L100S, U7-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

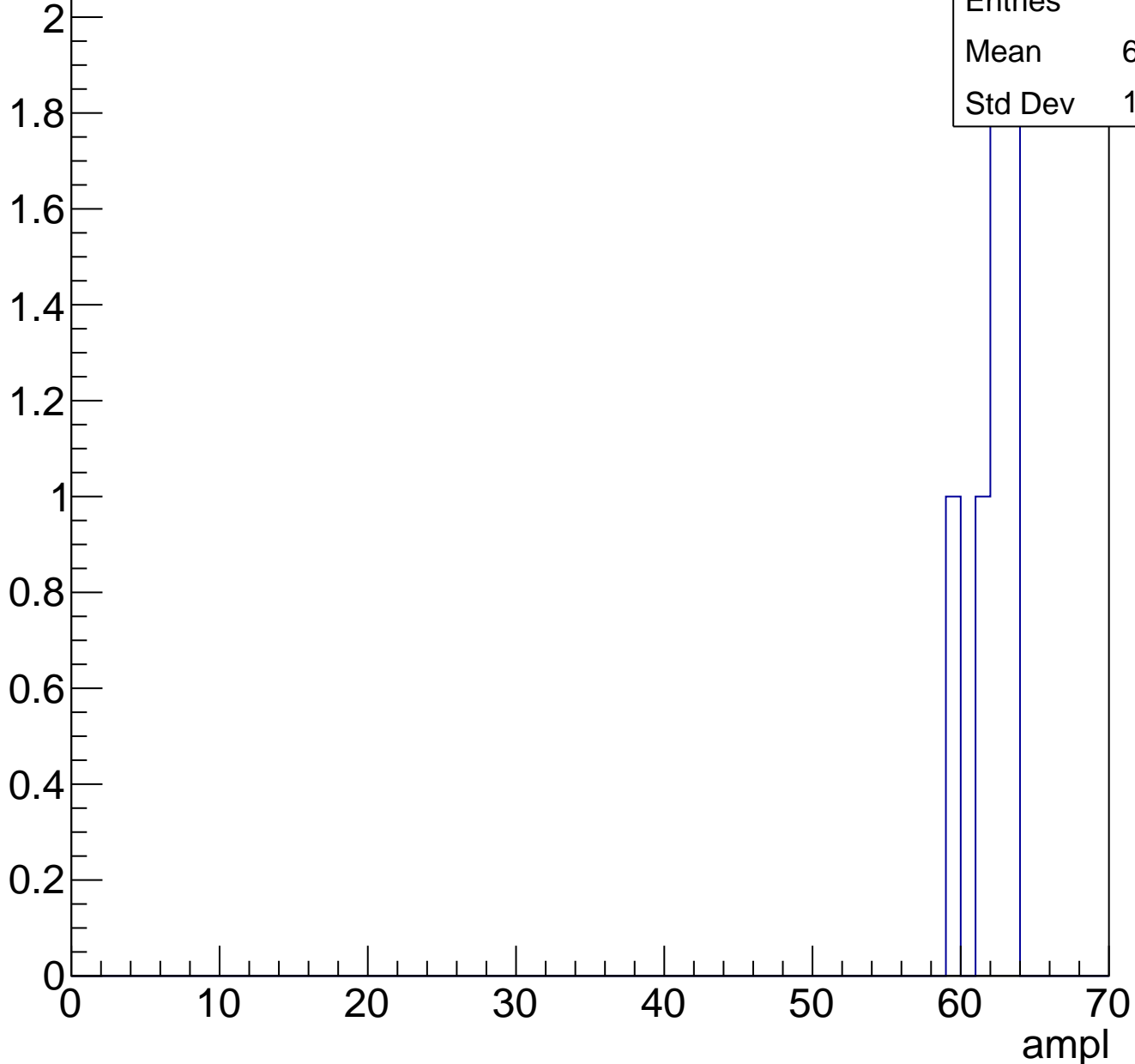
Entry



B1L100S, U7-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch121, adc0

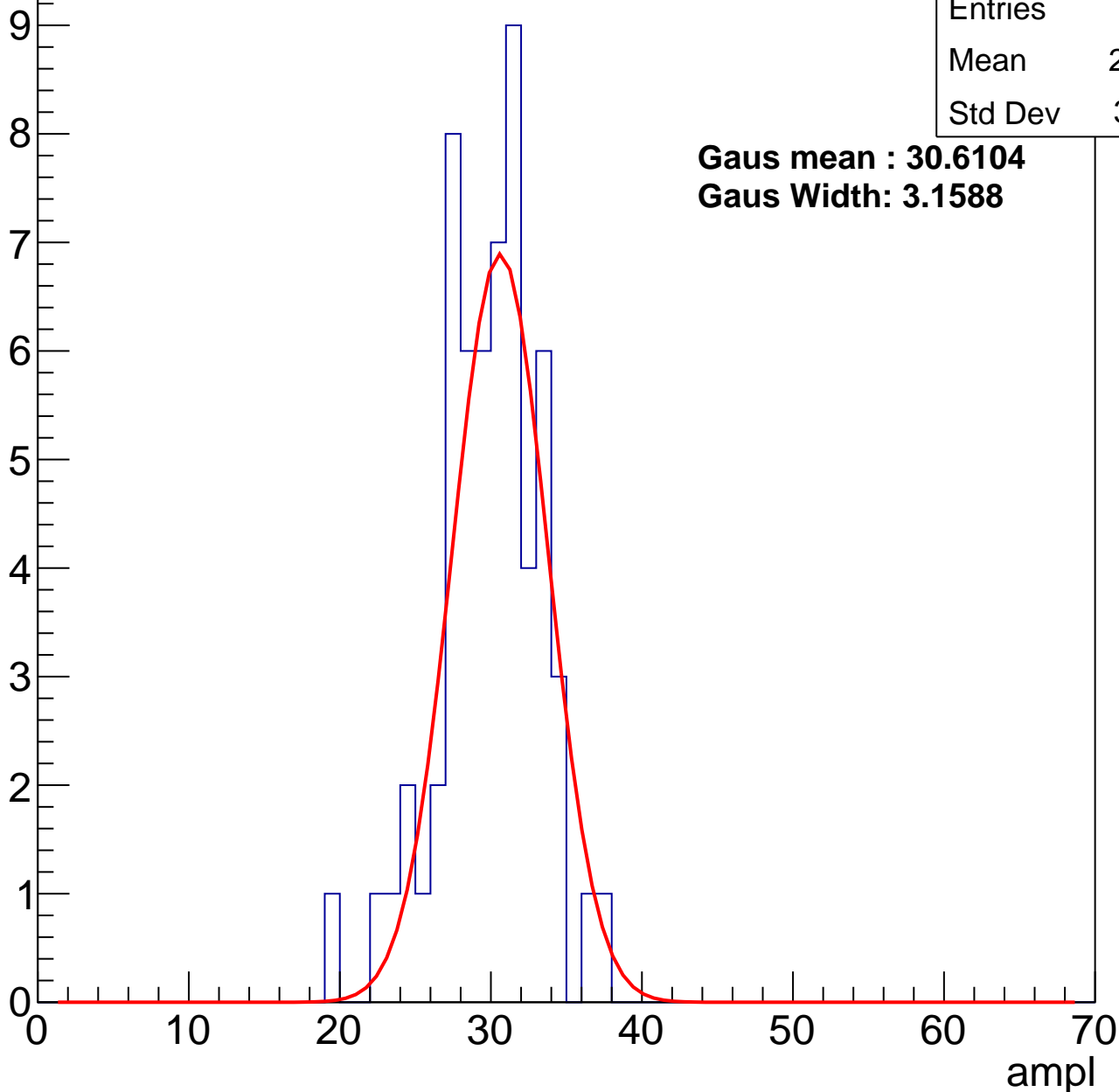
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 29.44 |
| Std Dev | 3.341 |

Gaus mean : 30.6104

Gaus Width: 3.1588



B1L100S, U7-ch121, adc1

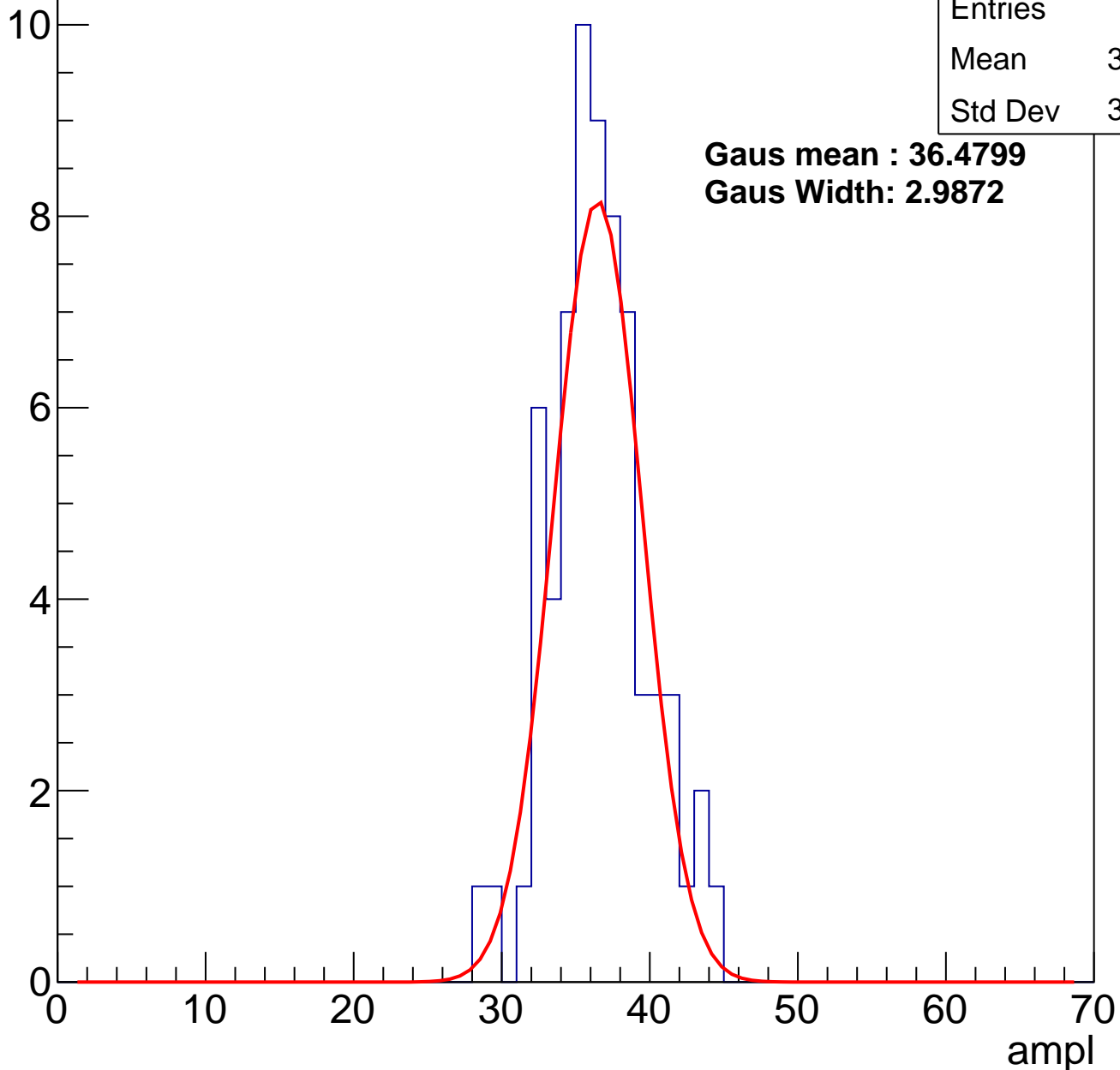
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.09 |
| Std Dev | 3.213 |

Gaus mean : 36.4799

Gaus Width: 2.9872

Entry



B1L100S, U7-ch121, adc2

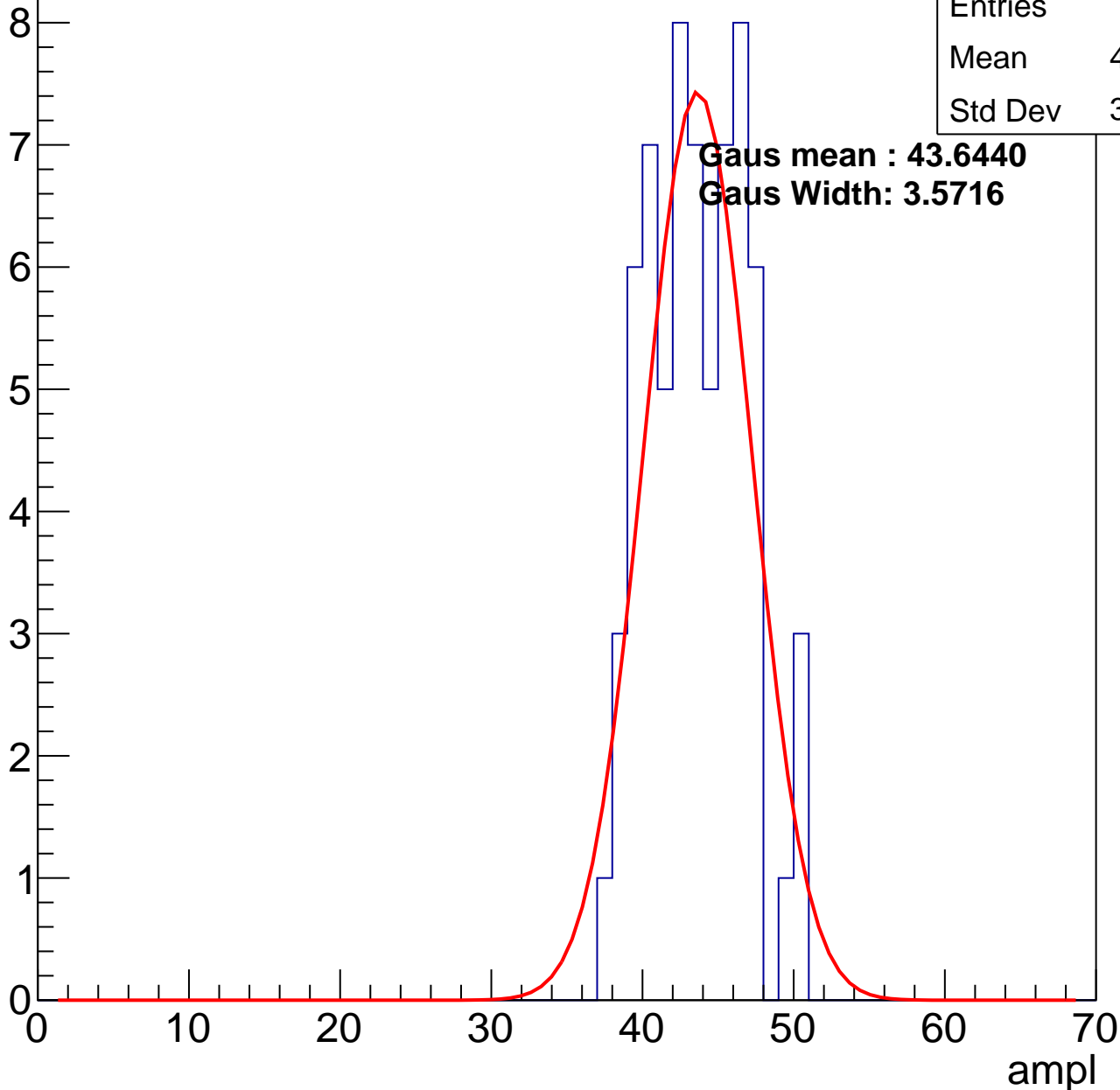
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.15 |
| Std Dev | 3.187 |

Gaus mean : 43.6440

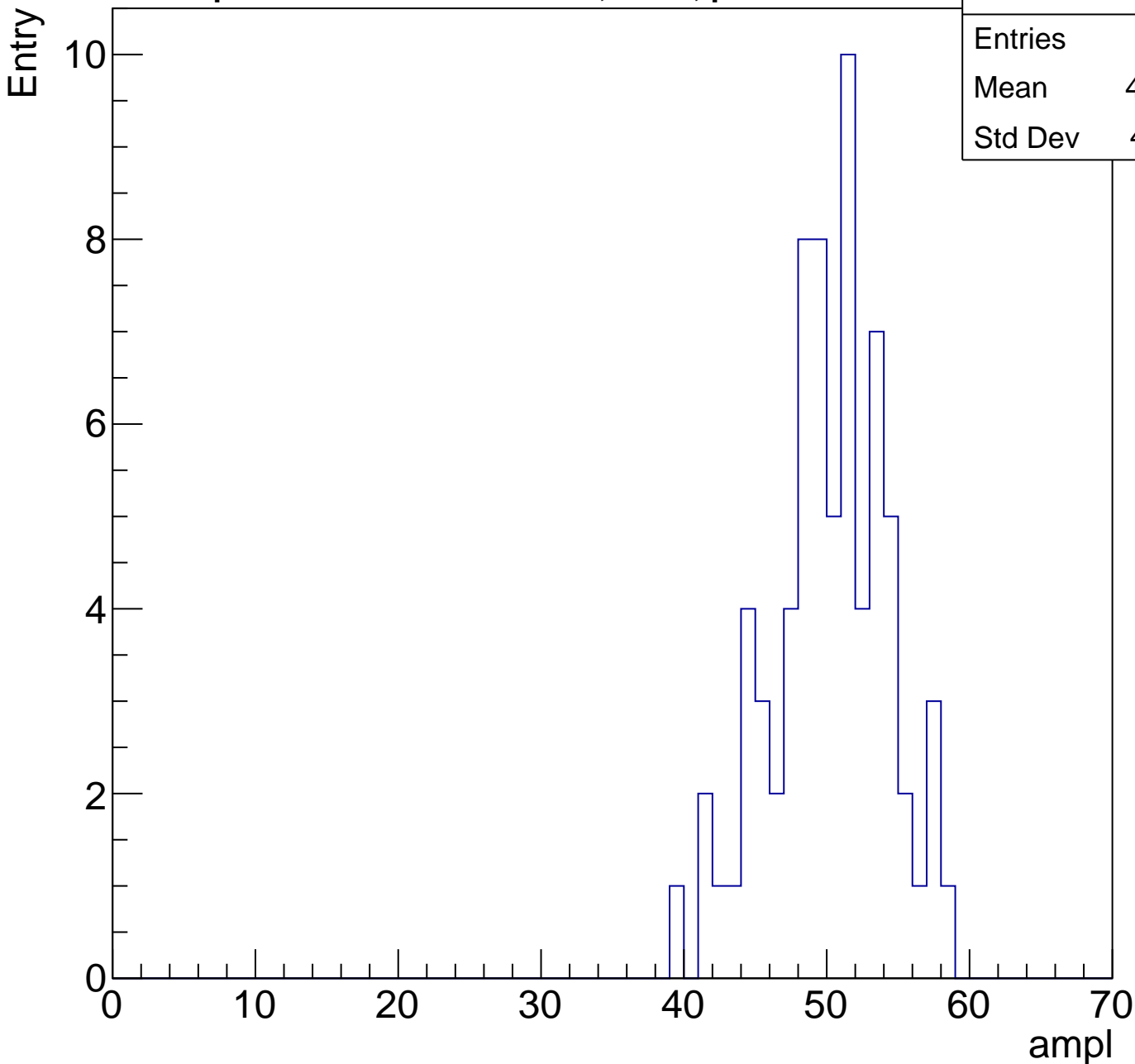
Gaus Width: 3.5716



B1L100S, U7-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 49.68 |
| Std Dev | 4.051 |

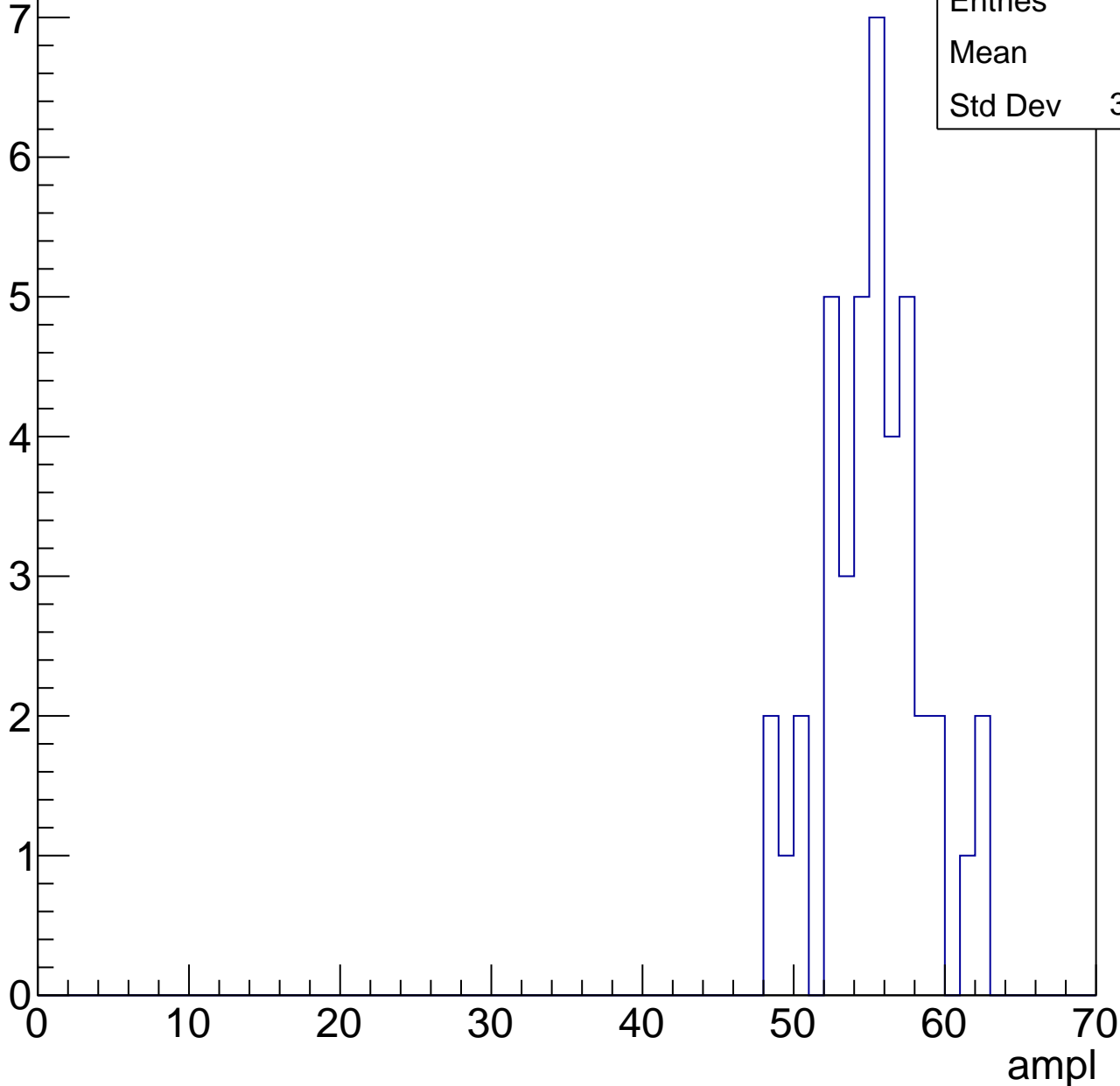


B1L100S, U7-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 54.8 |
| Std Dev | 3.322 |



B1L100S, U7-ch121, adc5

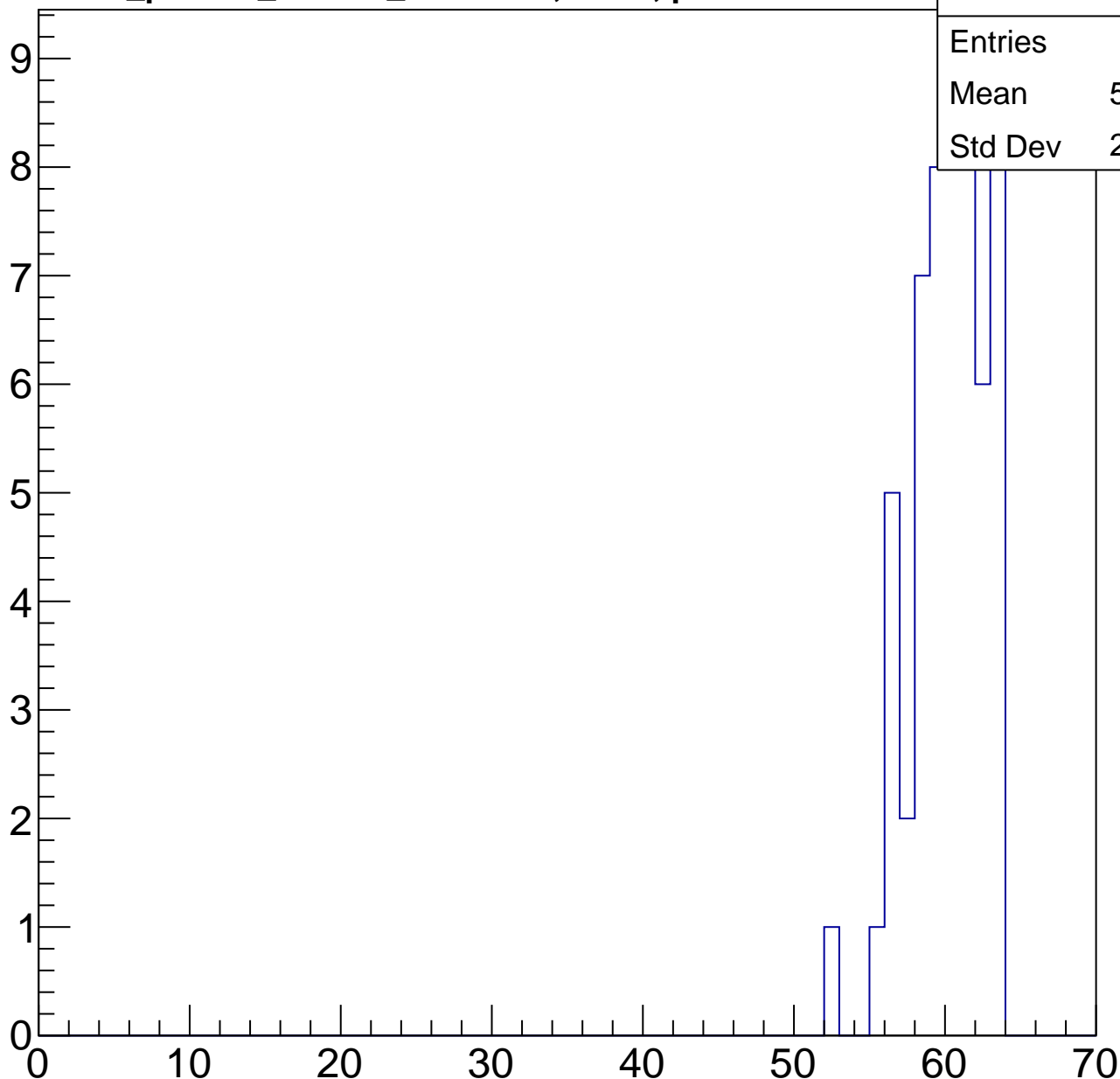
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.75 |
| Std Dev | 2.437 |

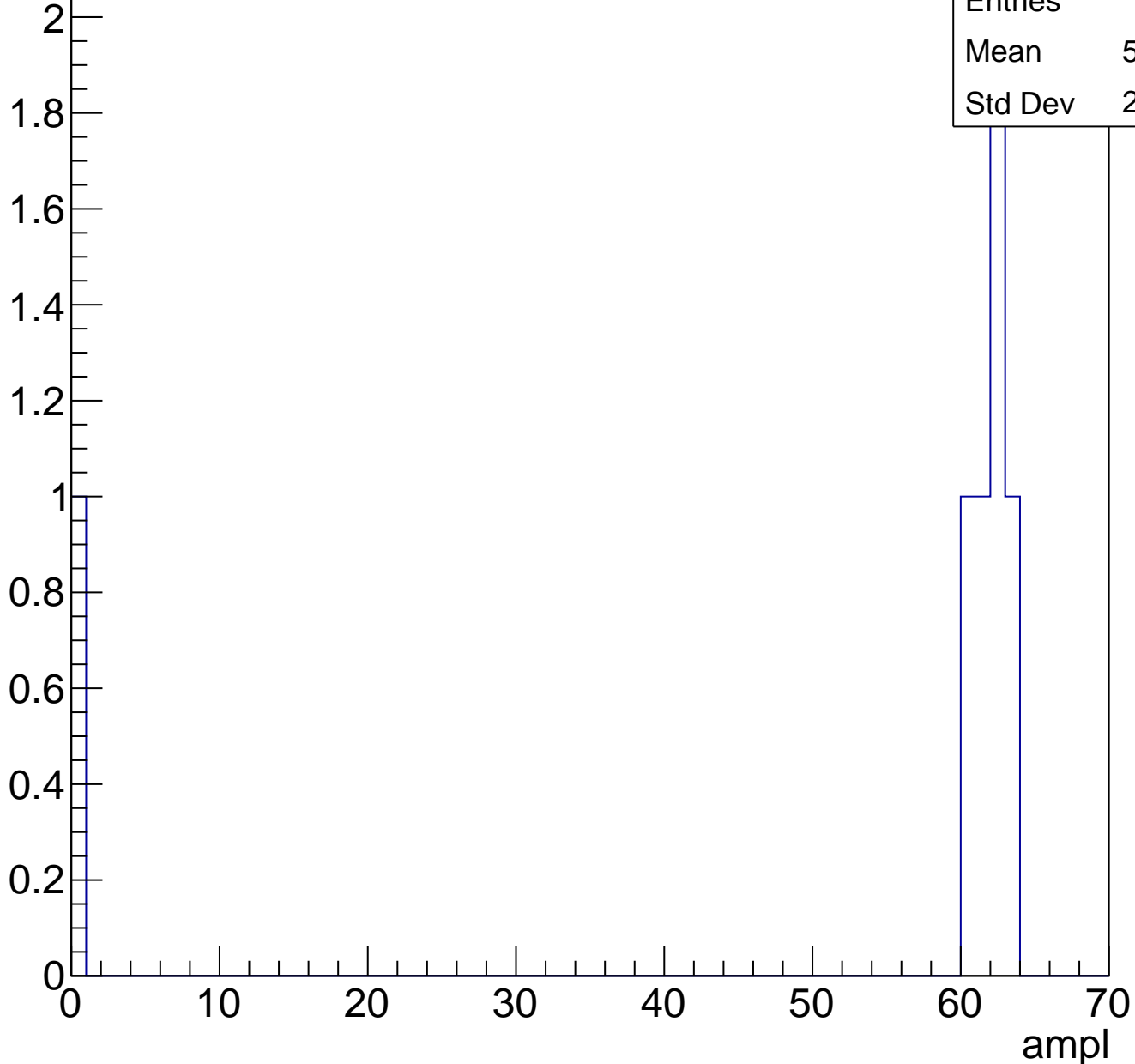
ampl



B1L100S, U7-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.33 |
| Std Dev | 22.98 |

B1L100S, U7-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch122, adc0

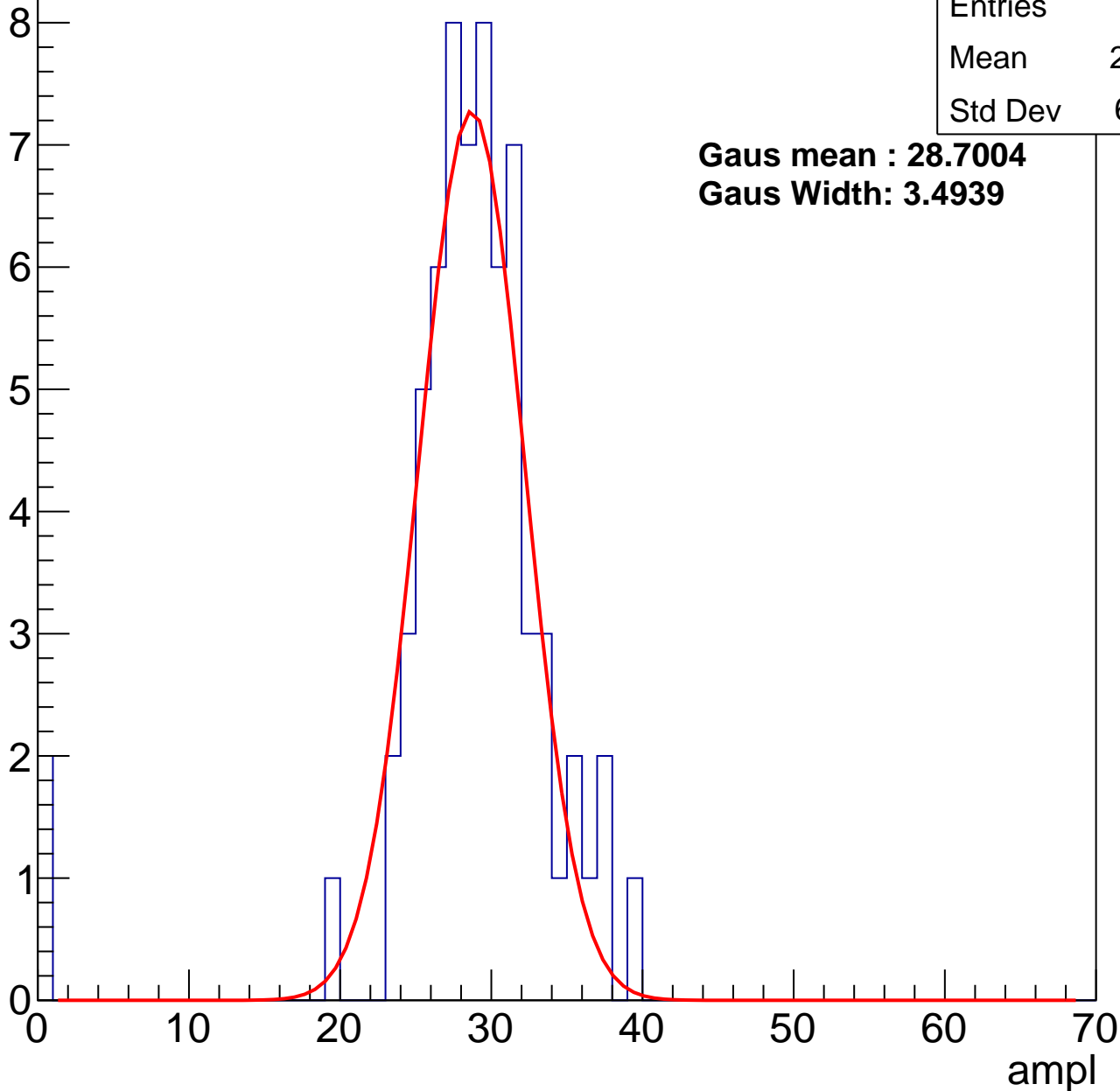
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.04 |
| Std Dev | 6.091 |

Gaus mean : 28.7004

Gaus Width: 3.4939



B1L100S, U7-ch122, adc1

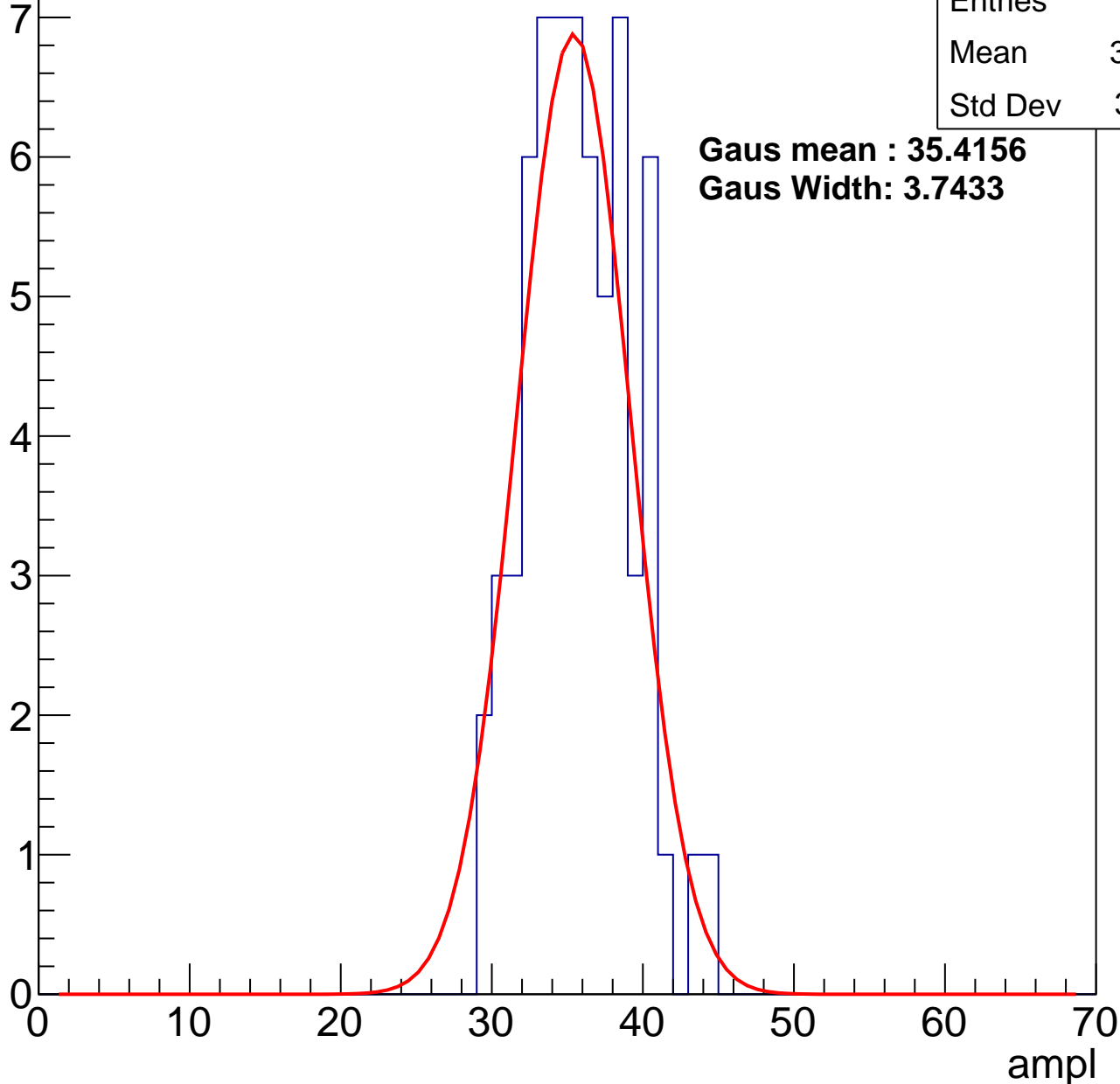
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.37 |
| Std Dev | 3.381 |

Gaus mean : 35.4156

Gaus Width: 3.7433



B1L100S, U7-ch122, adc2

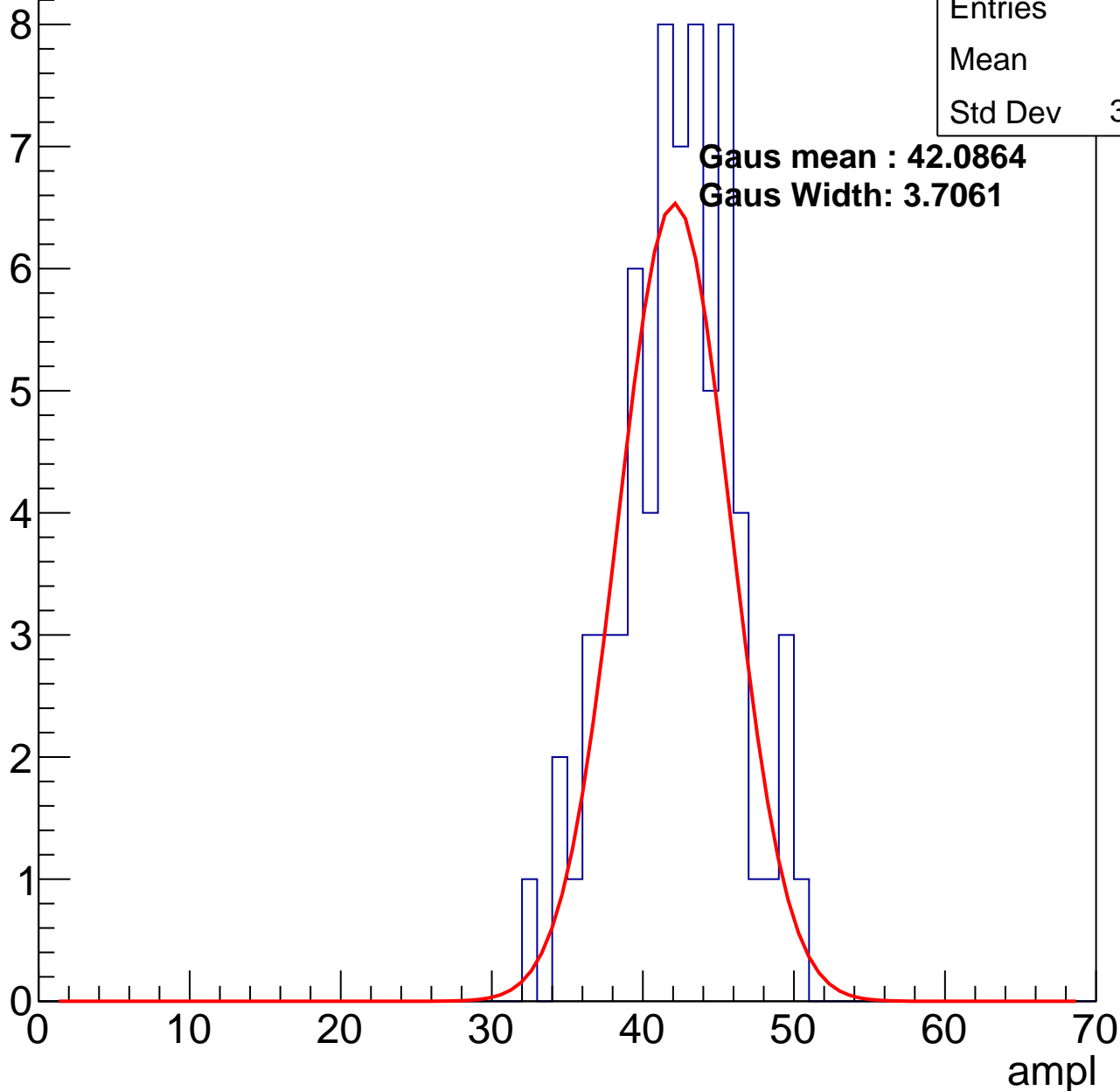
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 41.8 |
| Std Dev | 3.843 |

Gaus mean : 42.0864

Gaus Width: 3.7061

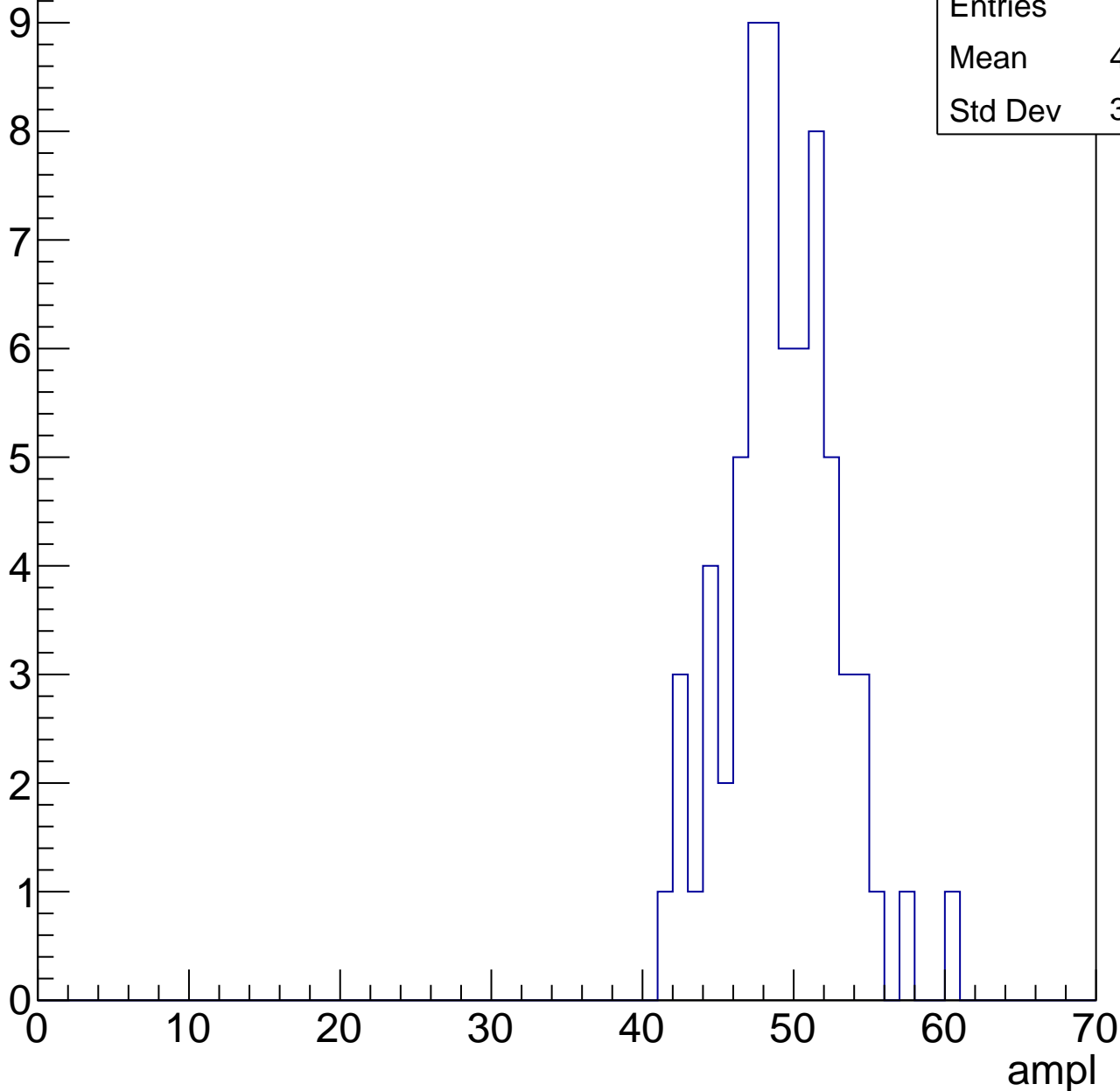


B1L100S, U7-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

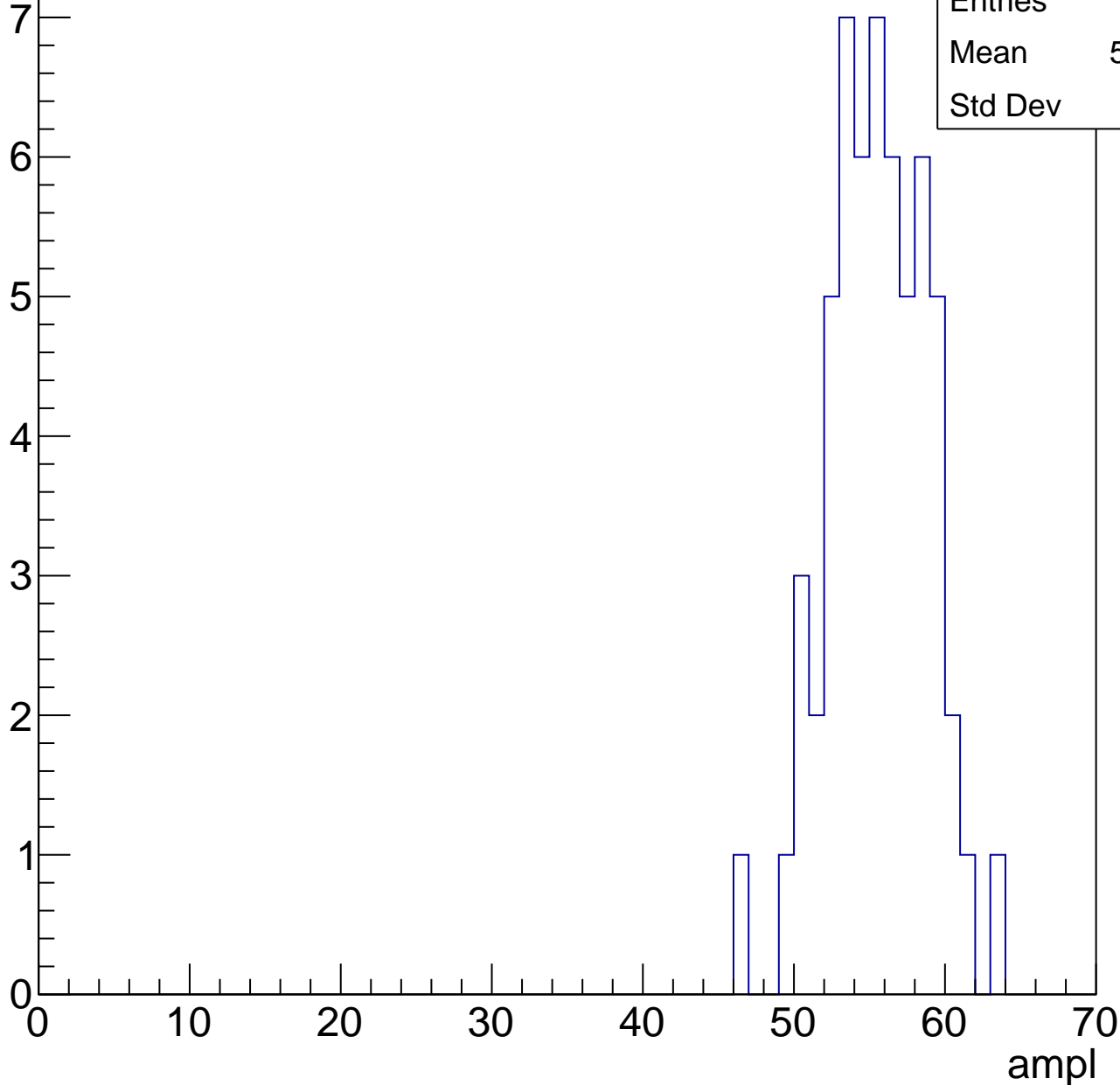
| | |
|---------|-------|
| Entries | 68 |
| Mean | 48.76 |
| Std Dev | 3.622 |



B1L100S, U7-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

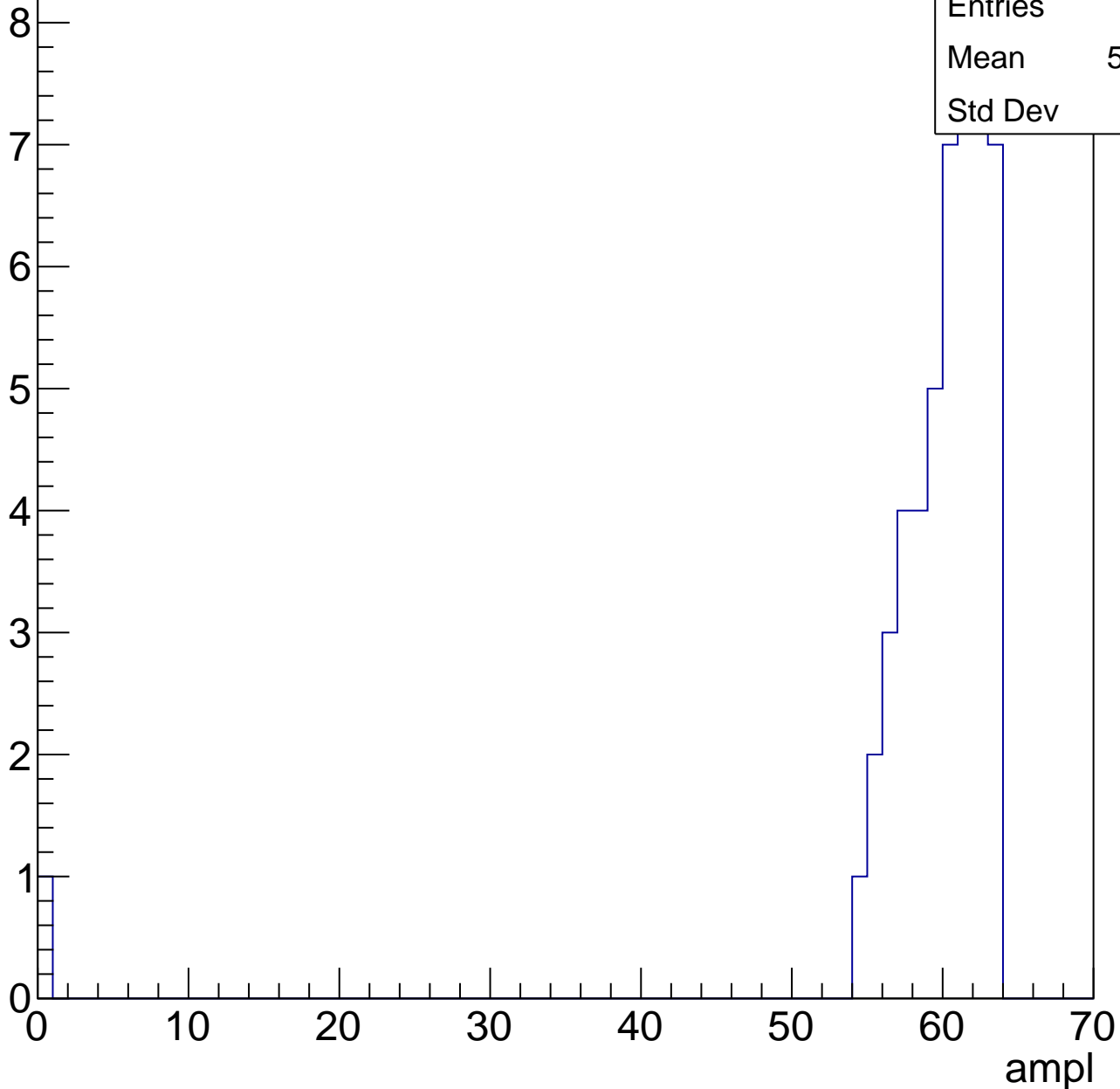


B1L100S, U7-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

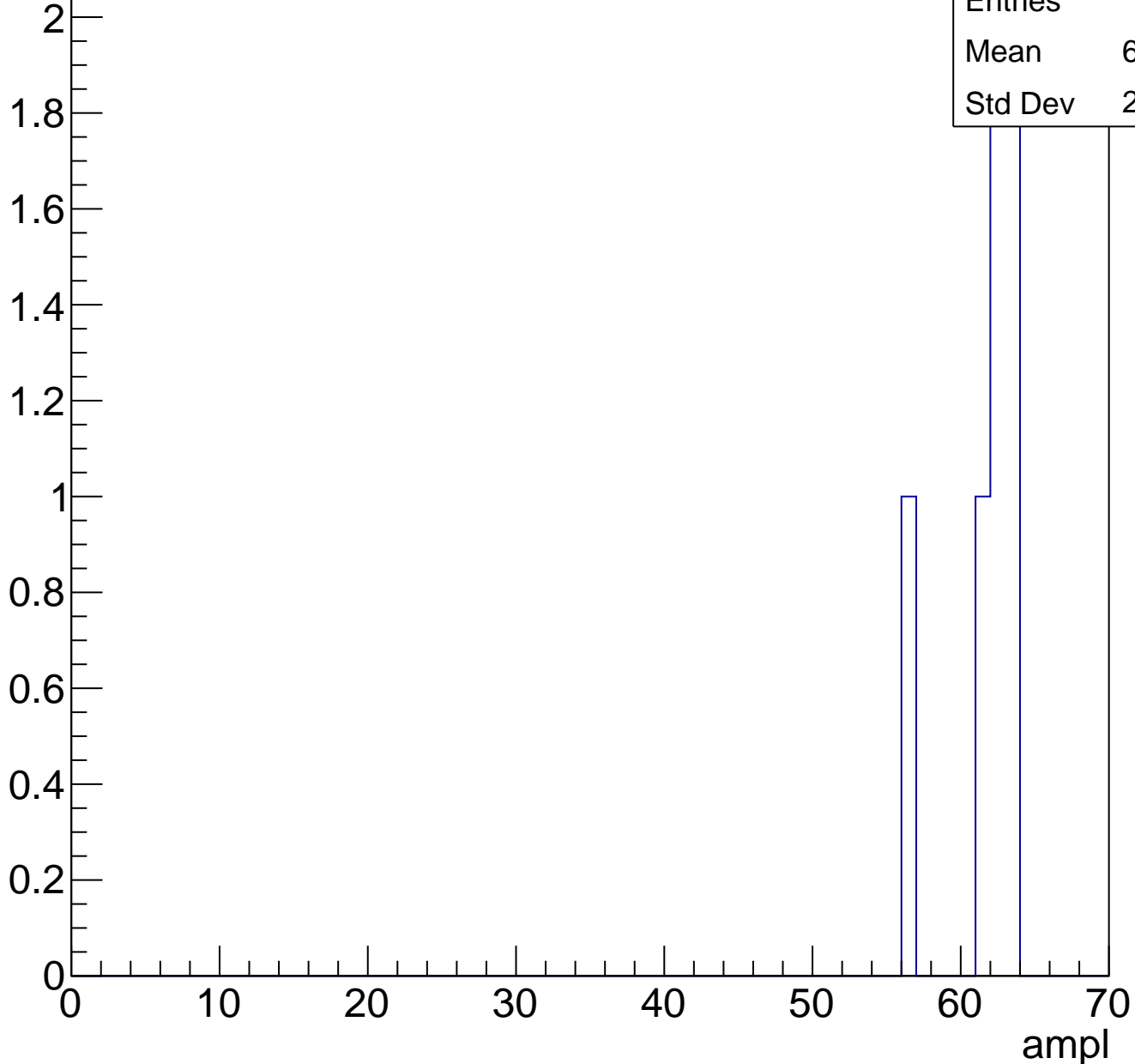
| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.64 |
| Std Dev | 8.72 |



B1L100S, U7-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch123, adc0

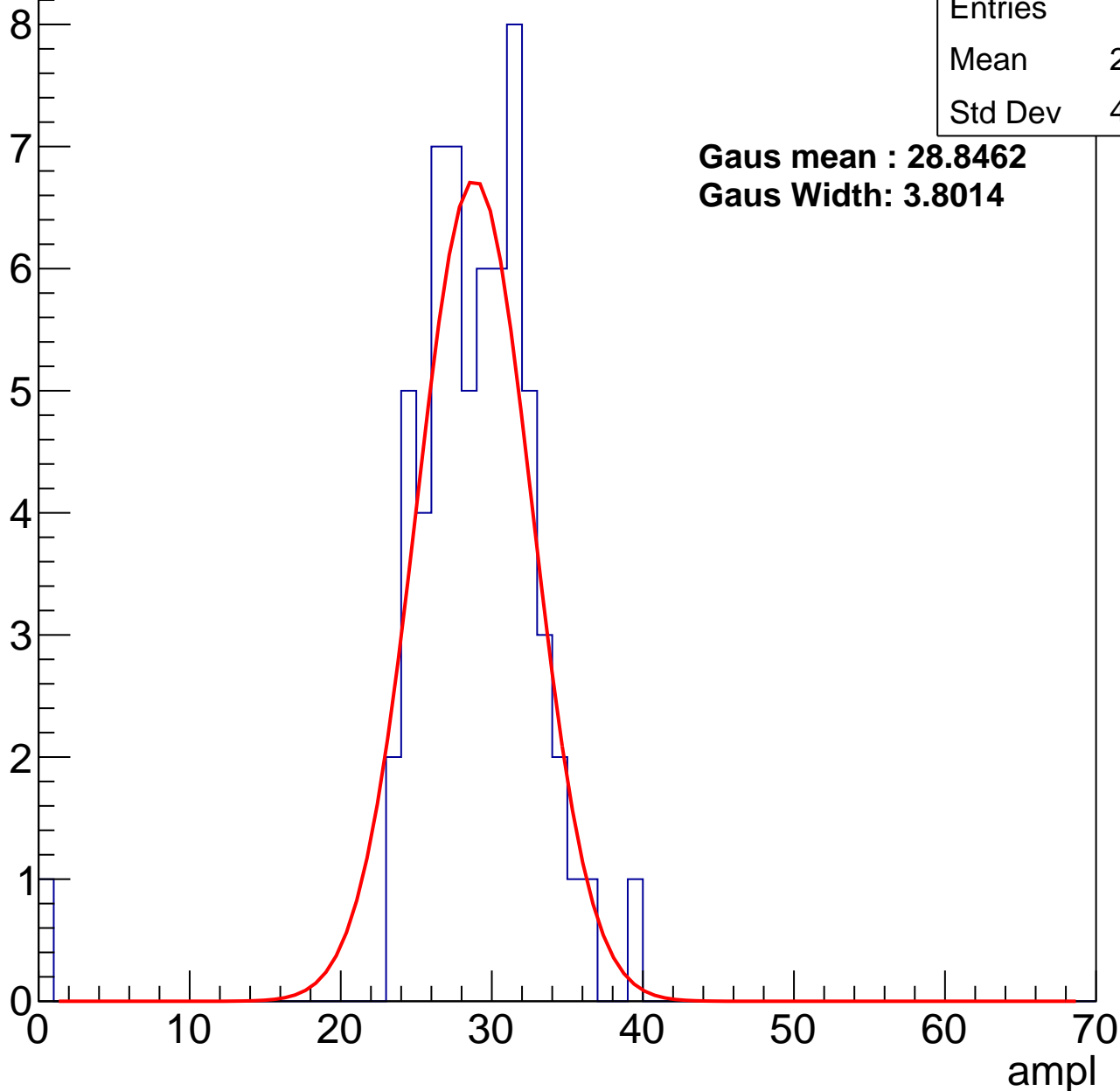
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 28.38 |
| Std Dev | 4.904 |

Gaus mean : 28.8462

Gaus Width: 3.8014



B1L100S, U7-ch123, adc1

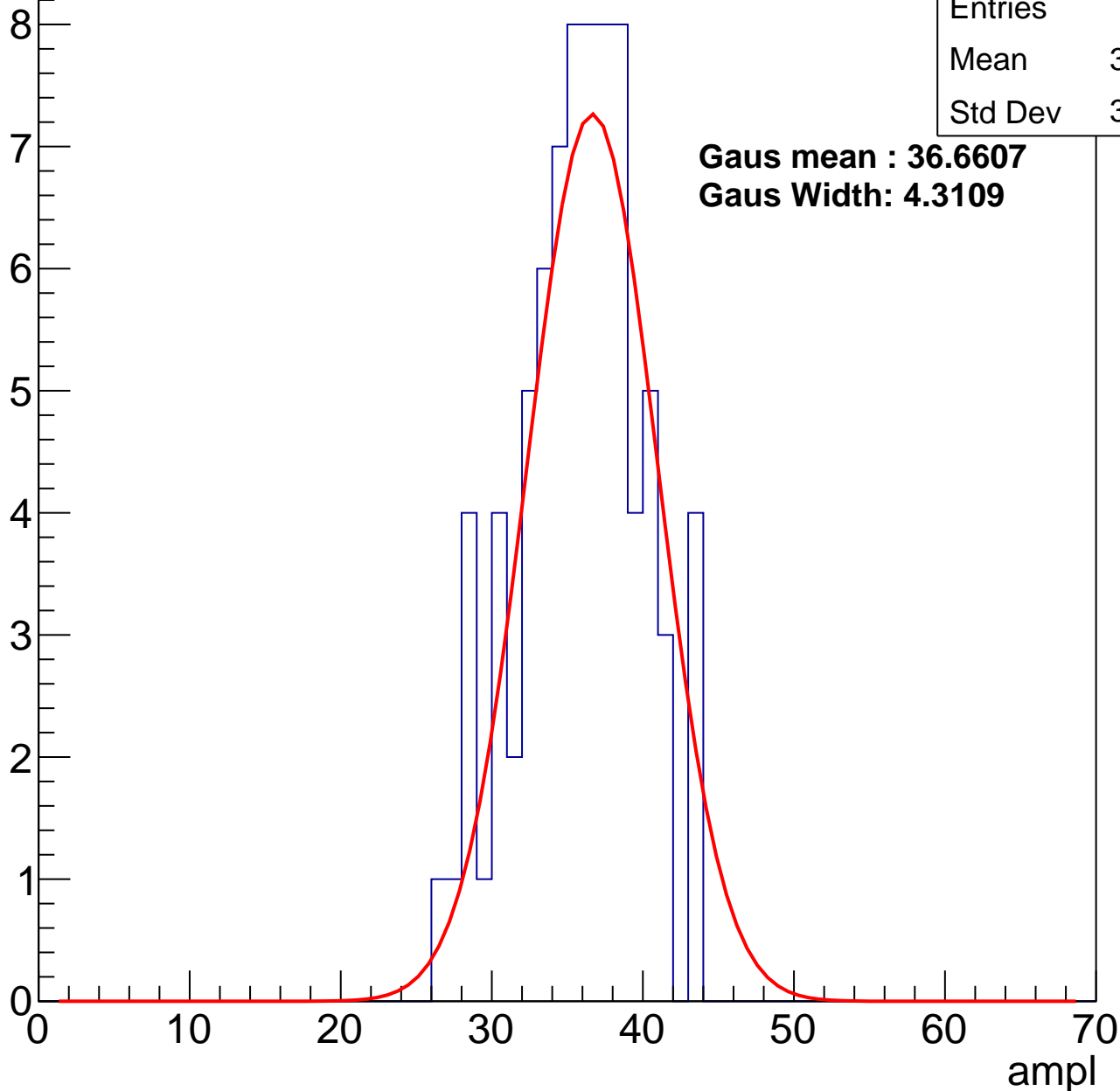
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 35.33 |
| Std Dev | 3.964 |

Gaus mean : 36.6607

Gaus Width: 4.3109



B1L100S, U7-ch123, adc2

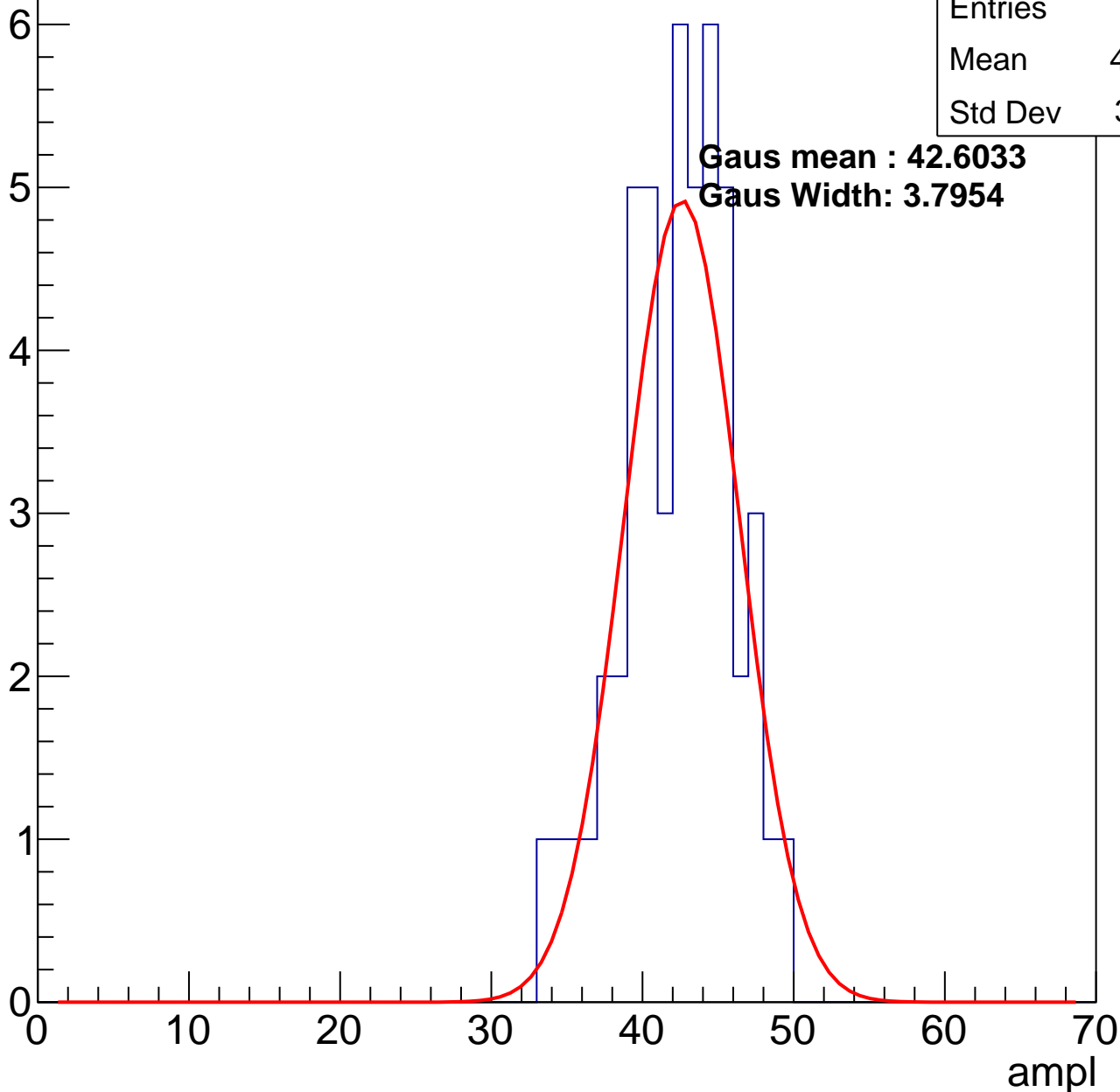
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 41.84 |
| Std Dev | 3.591 |

Gaus mean : 42.6033

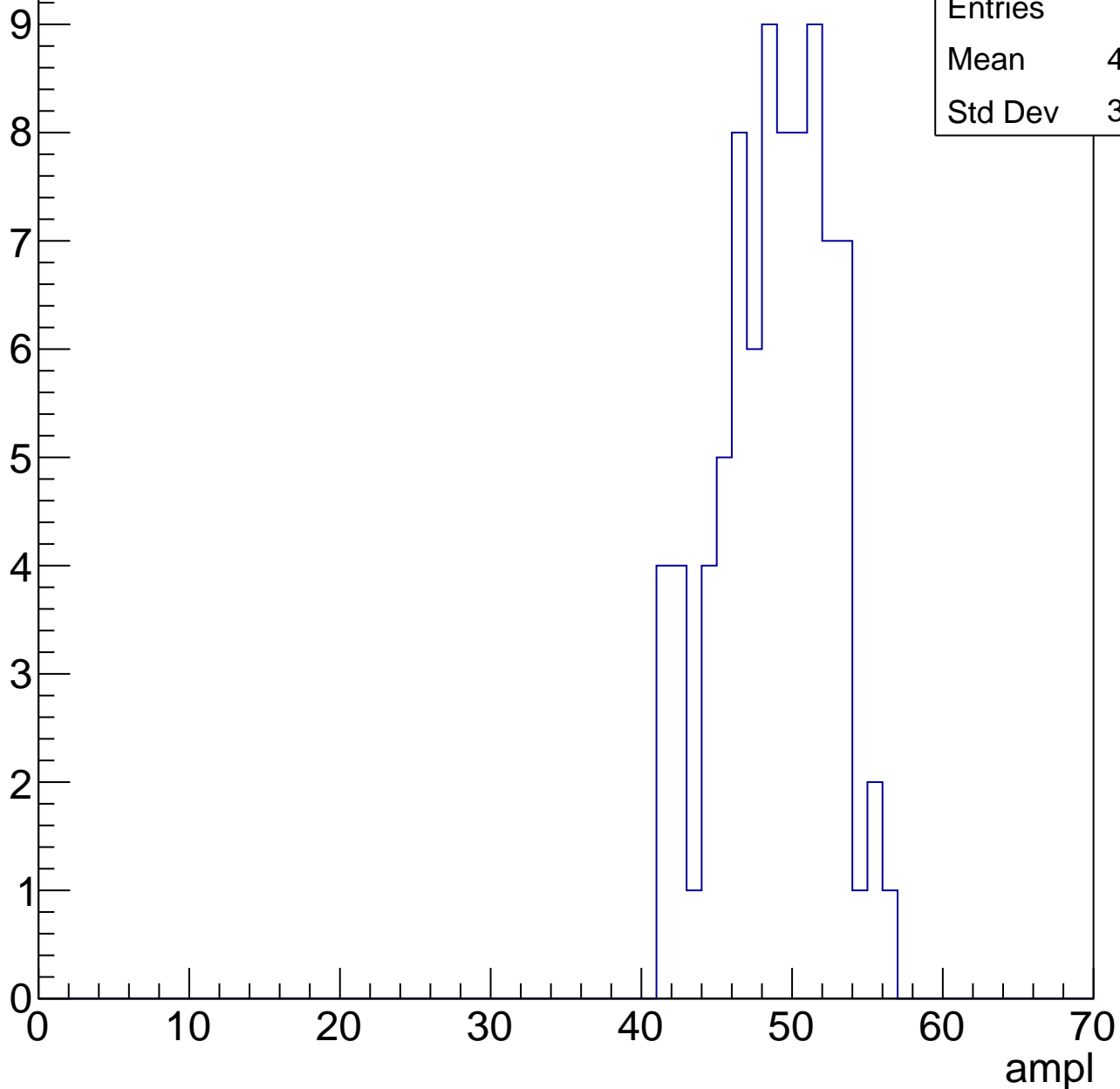
Gaus Width: 3.7954



B1L100S, U7-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



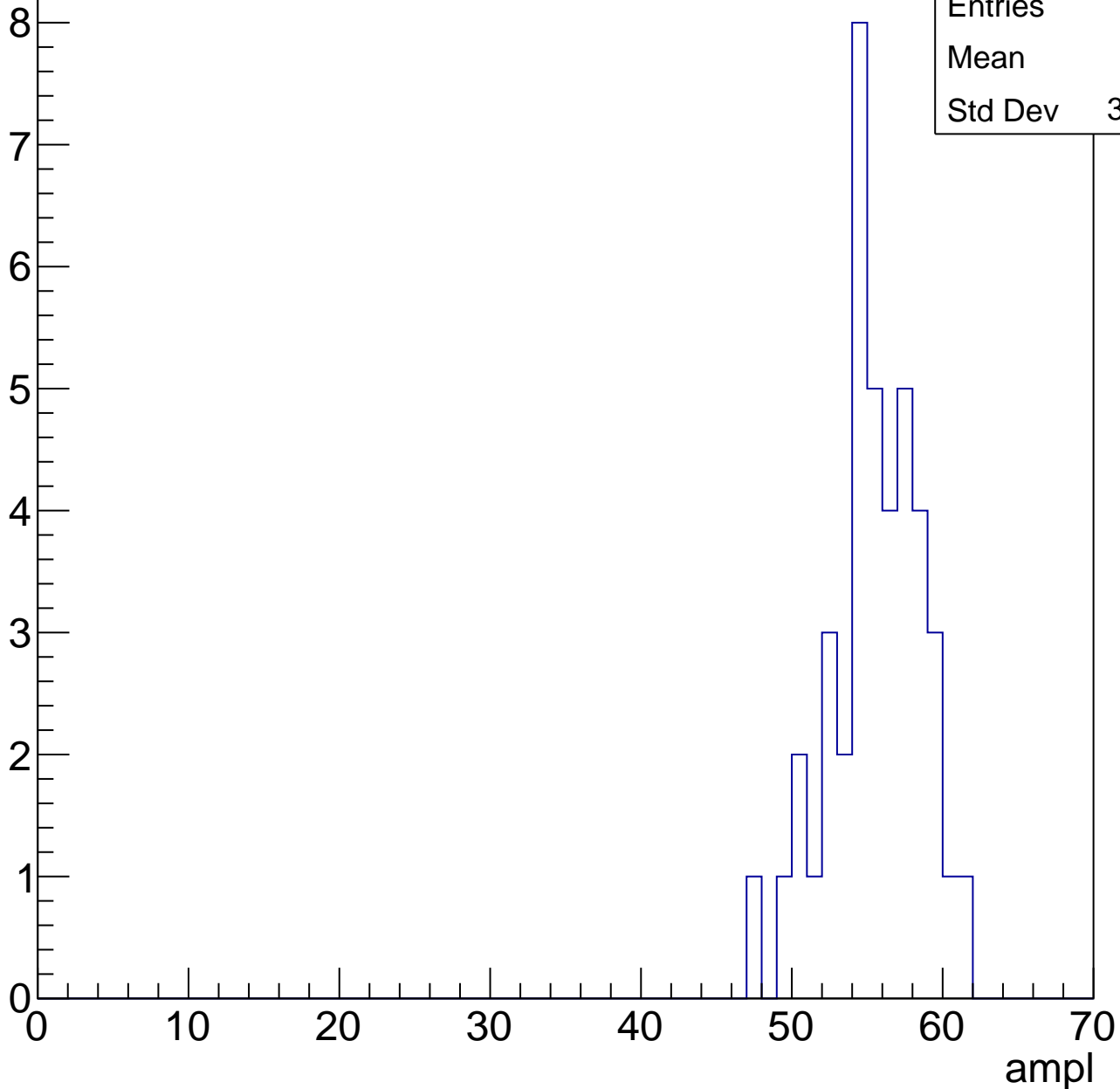
| | |
|---------|-------|
| Entries | 84 |
| Mean | 48.38 |
| Std Dev | 3.628 |

B1L100S, U7-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 55 |
| Std Dev | 3.036 |



B1L100S, U7-ch123, adc5

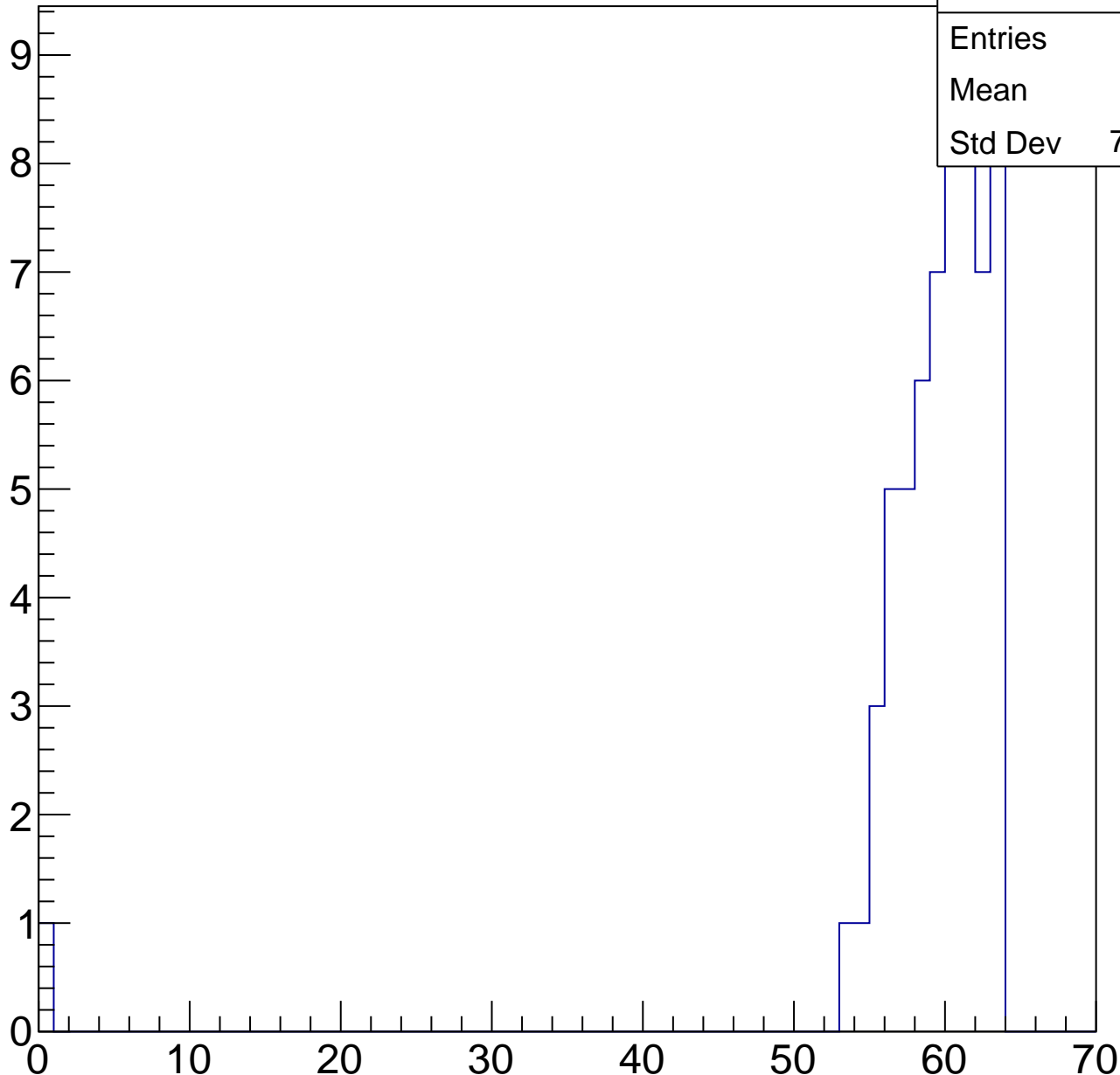
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 58.5 |
| Std Dev | 7.925 |

ampl



B1L100S, U7-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

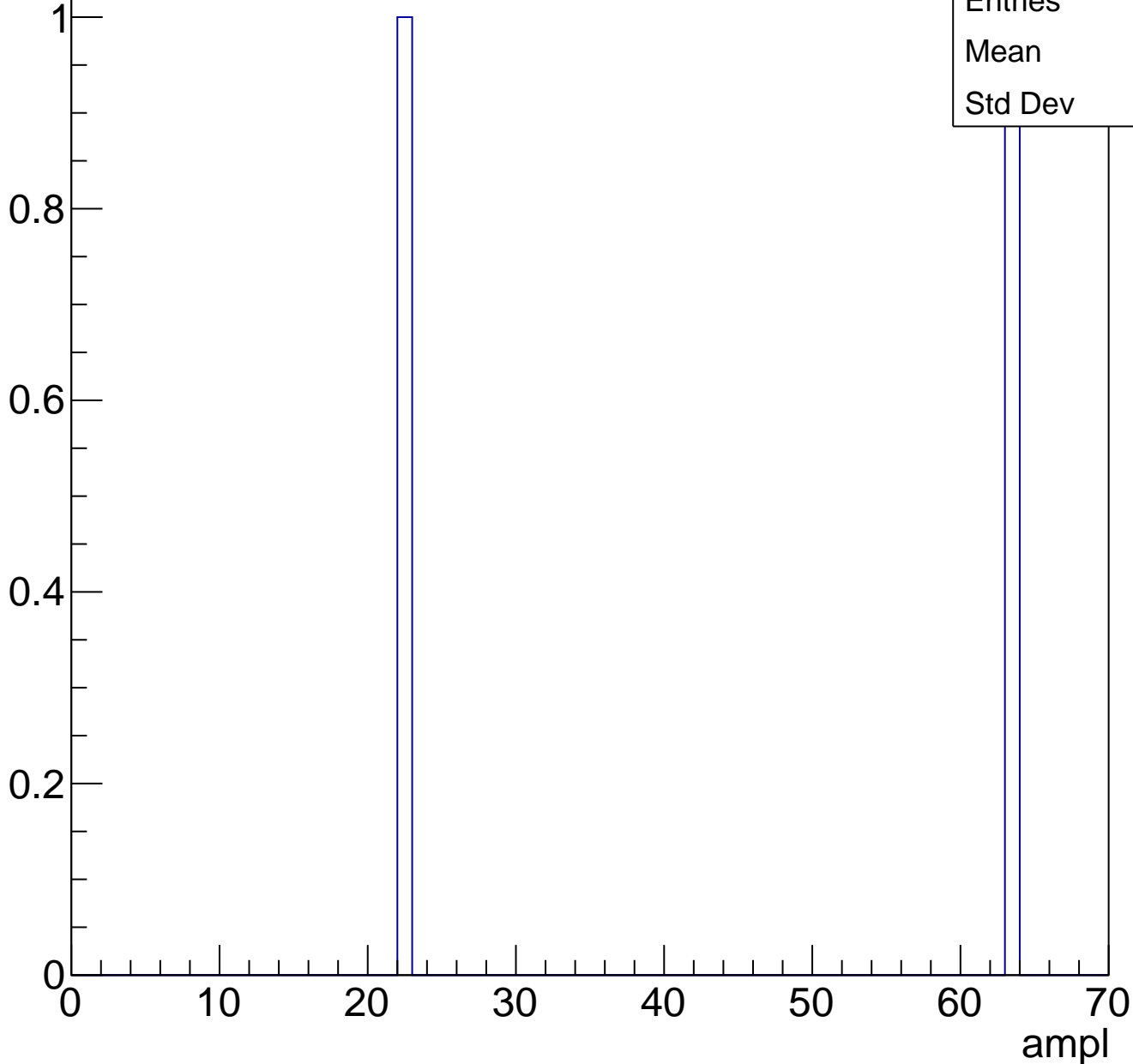
Entry



B1L100S, U7-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch124, adc0

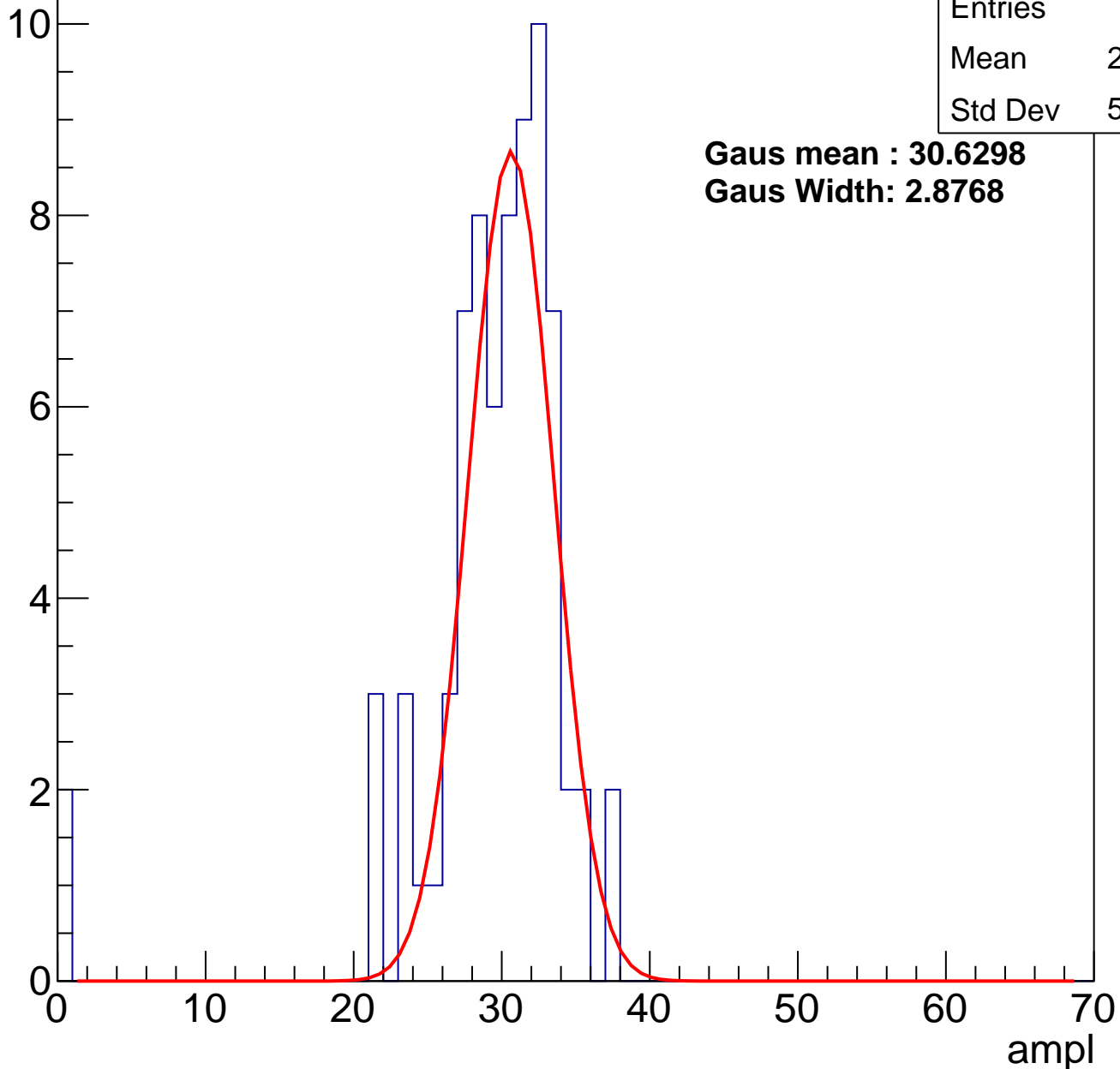
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.76 |
| Std Dev | 5.902 |

Gaus mean : 30.6298

Gaus Width: 2.8768

Entry



B1L100S, U7-ch124, adc1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.87 |
| Std Dev | 3.583 |

Gaus mean : 37.2425

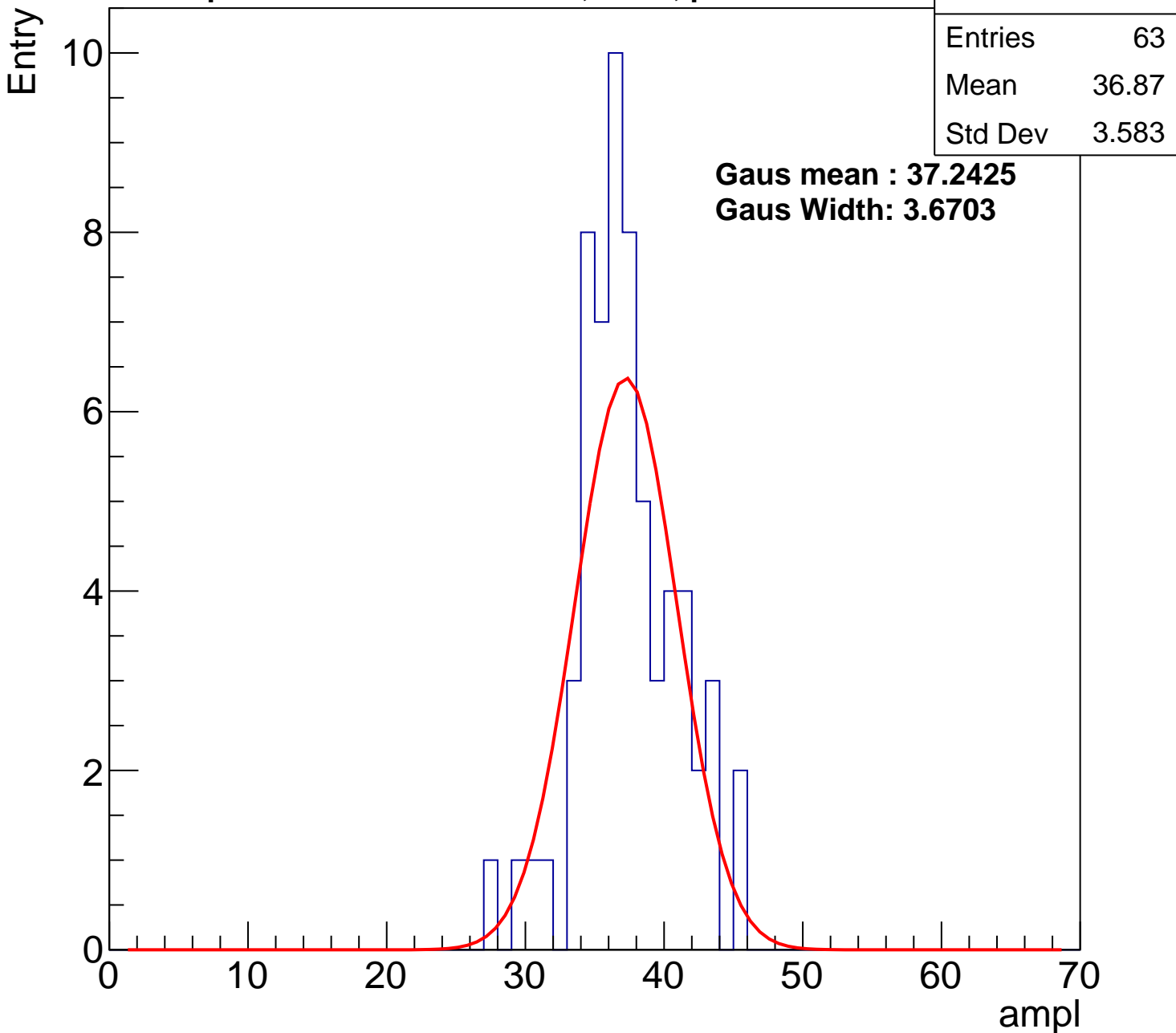
Gaus Width: 3.6703

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch124, adc2

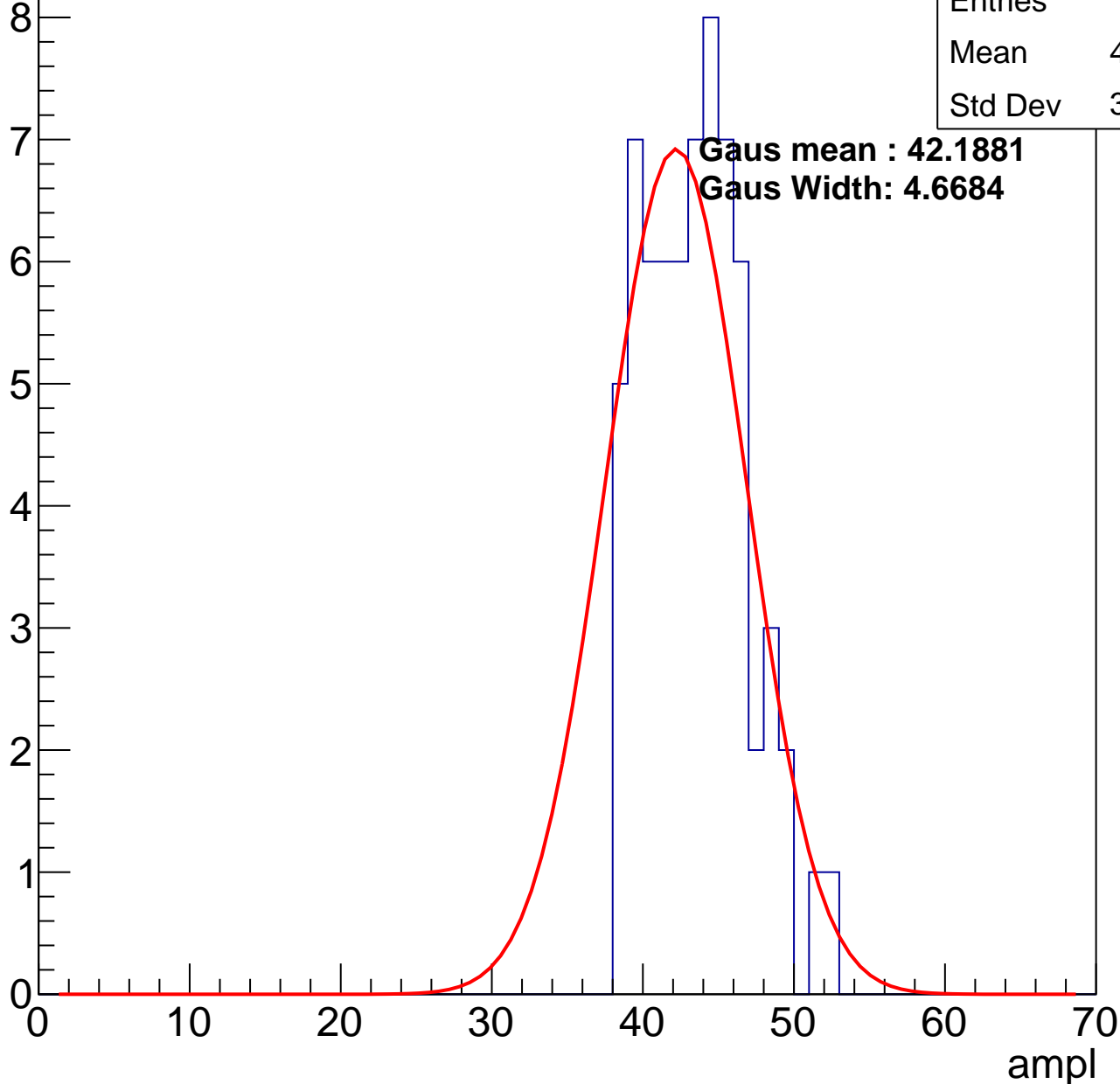
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.04 |
| Std Dev | 3.312 |

Gaus mean : 42.1881

Gaus Width: 4.6684

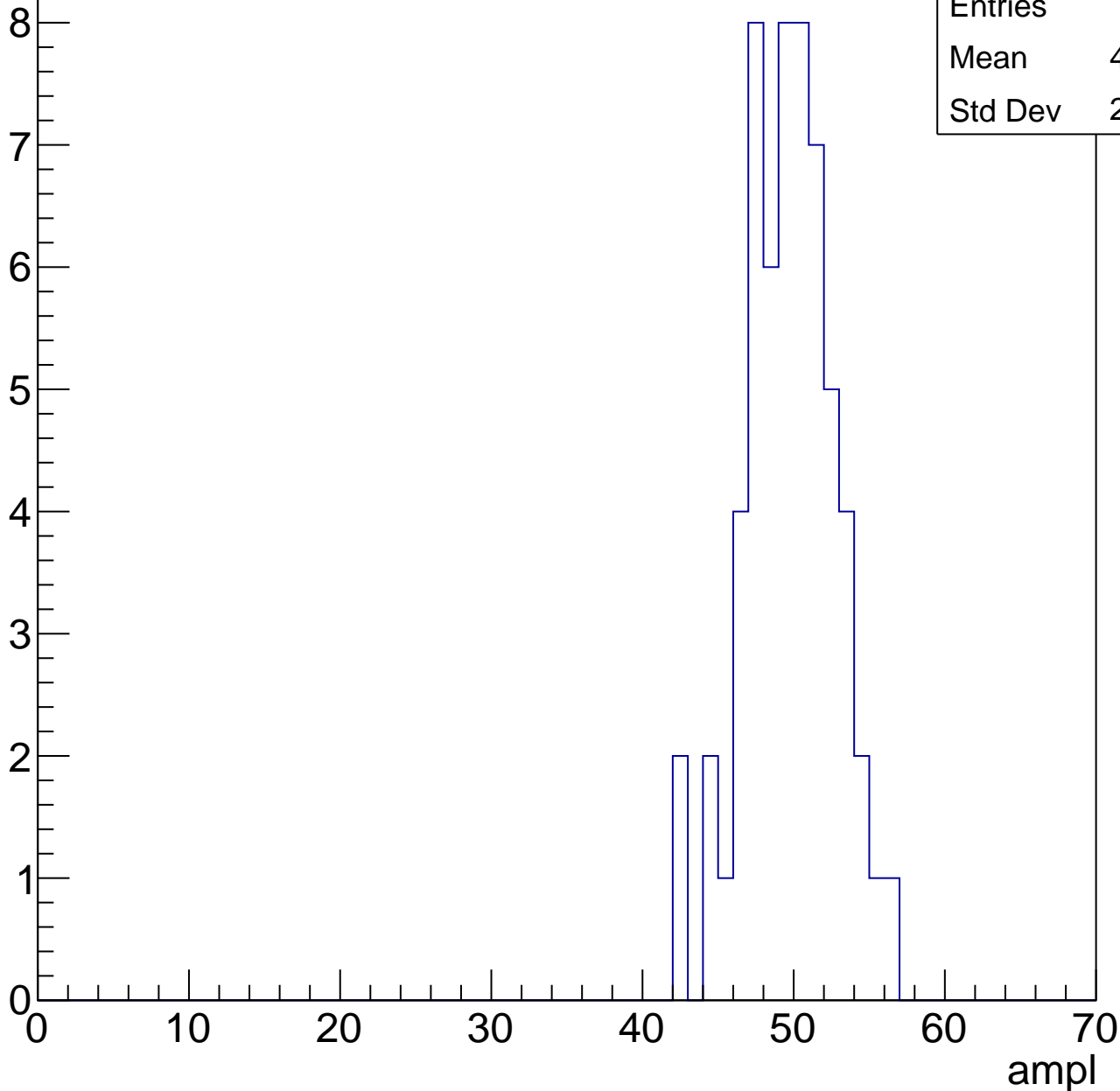


B1L100S, U7-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.24 |
| Std Dev | 2.954 |

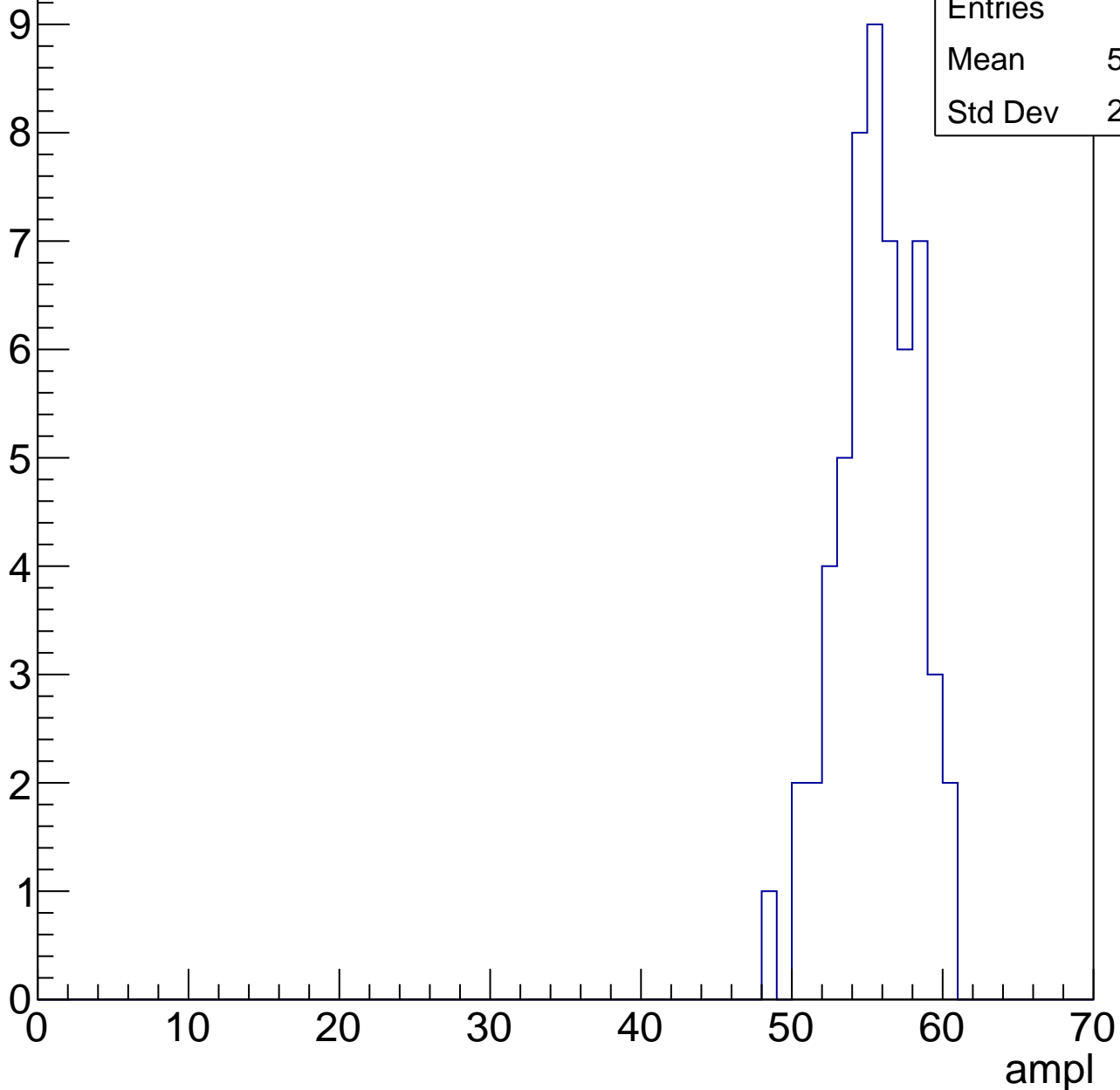


B1L100S, U7-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.12 |
| Std Dev | 2.626 |

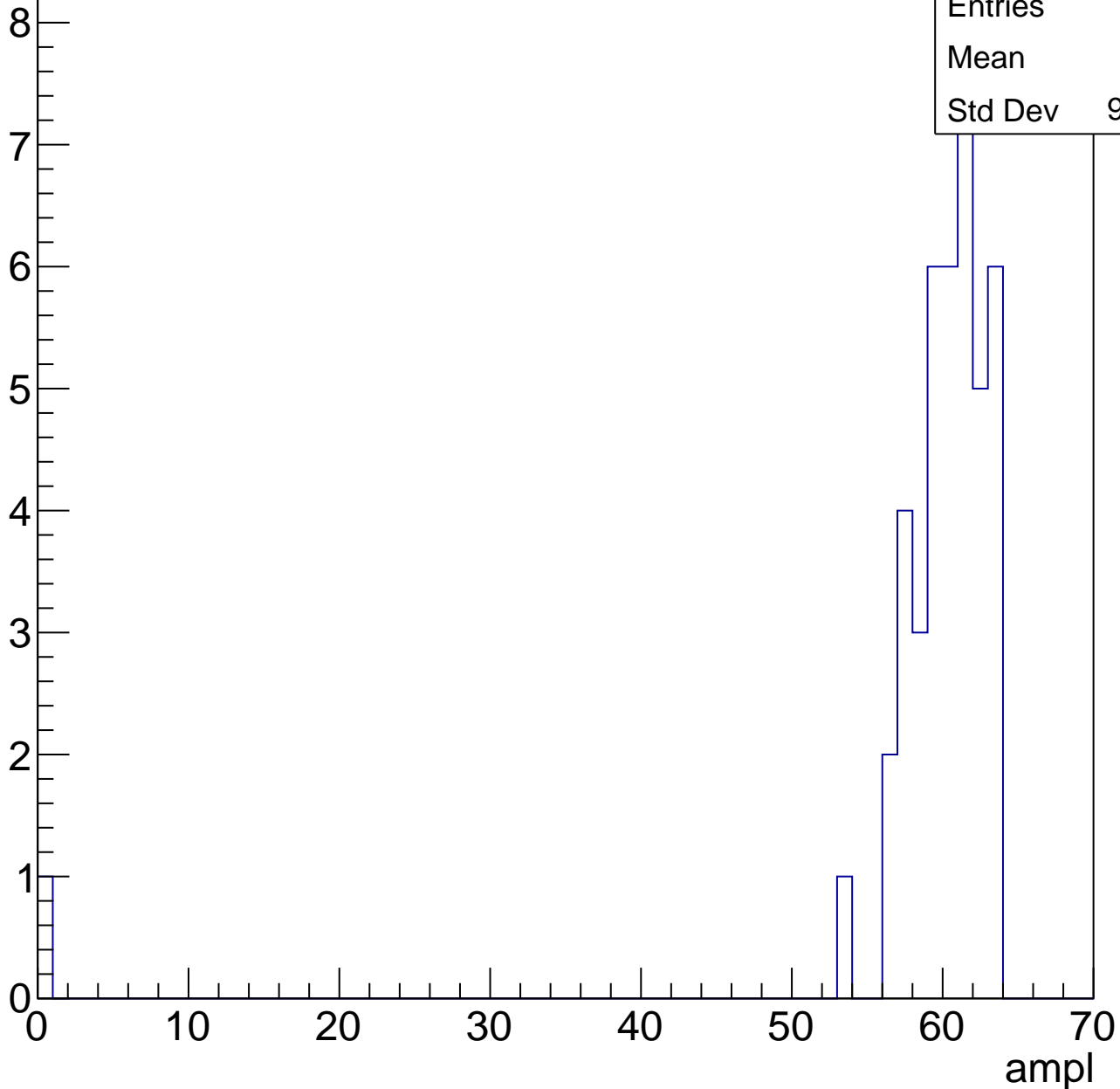


B1L100S, U7-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 58.5 |
| Std Dev | 9.414 |

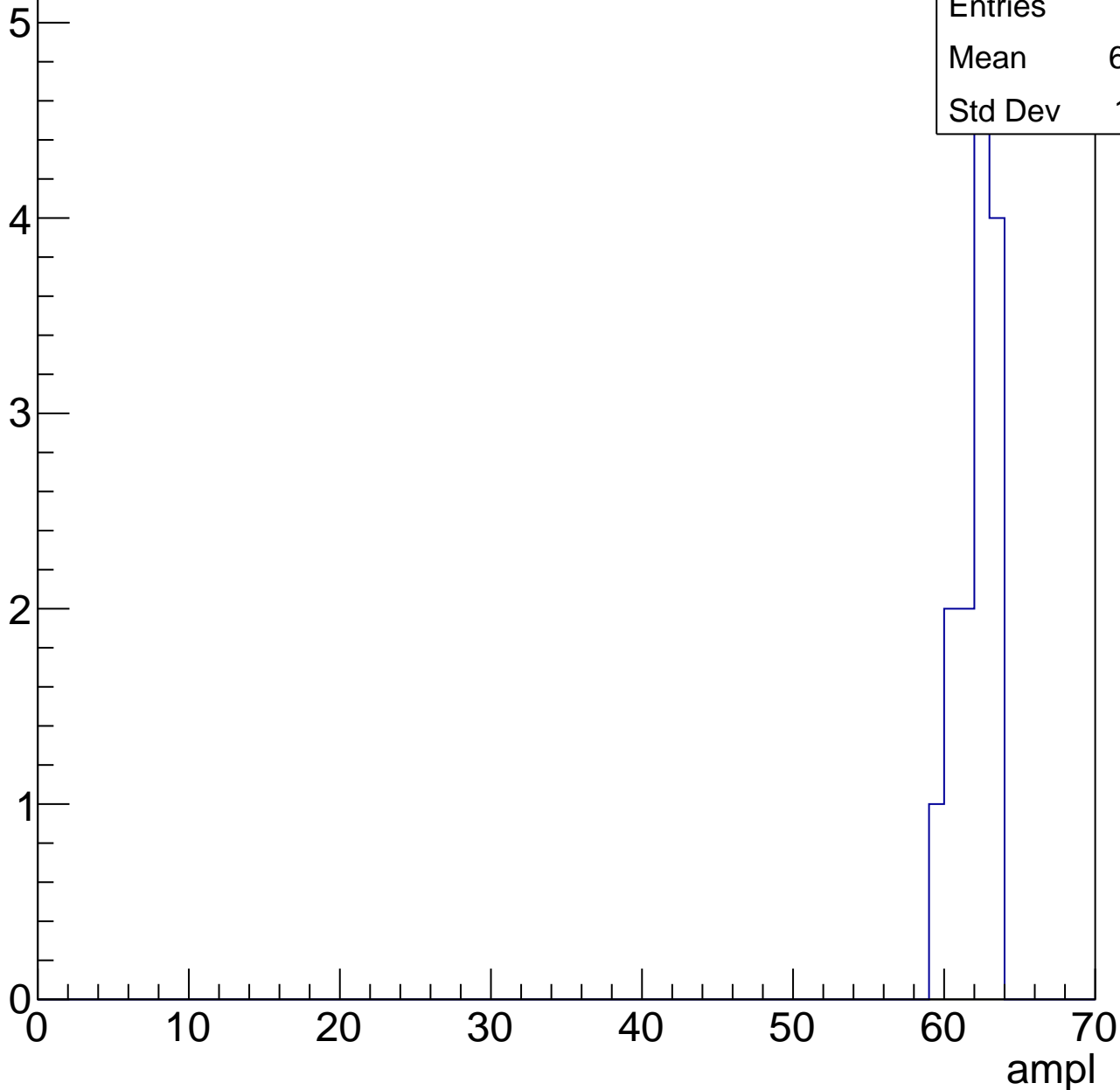


B1L100S, U7-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.64 |
| Std Dev | 1.231 |



B1L100S, U7-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L100S, U7-ch125, adc0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 28.64 |
| Std Dev | 5.846 |

Gaus mean : 29.5429

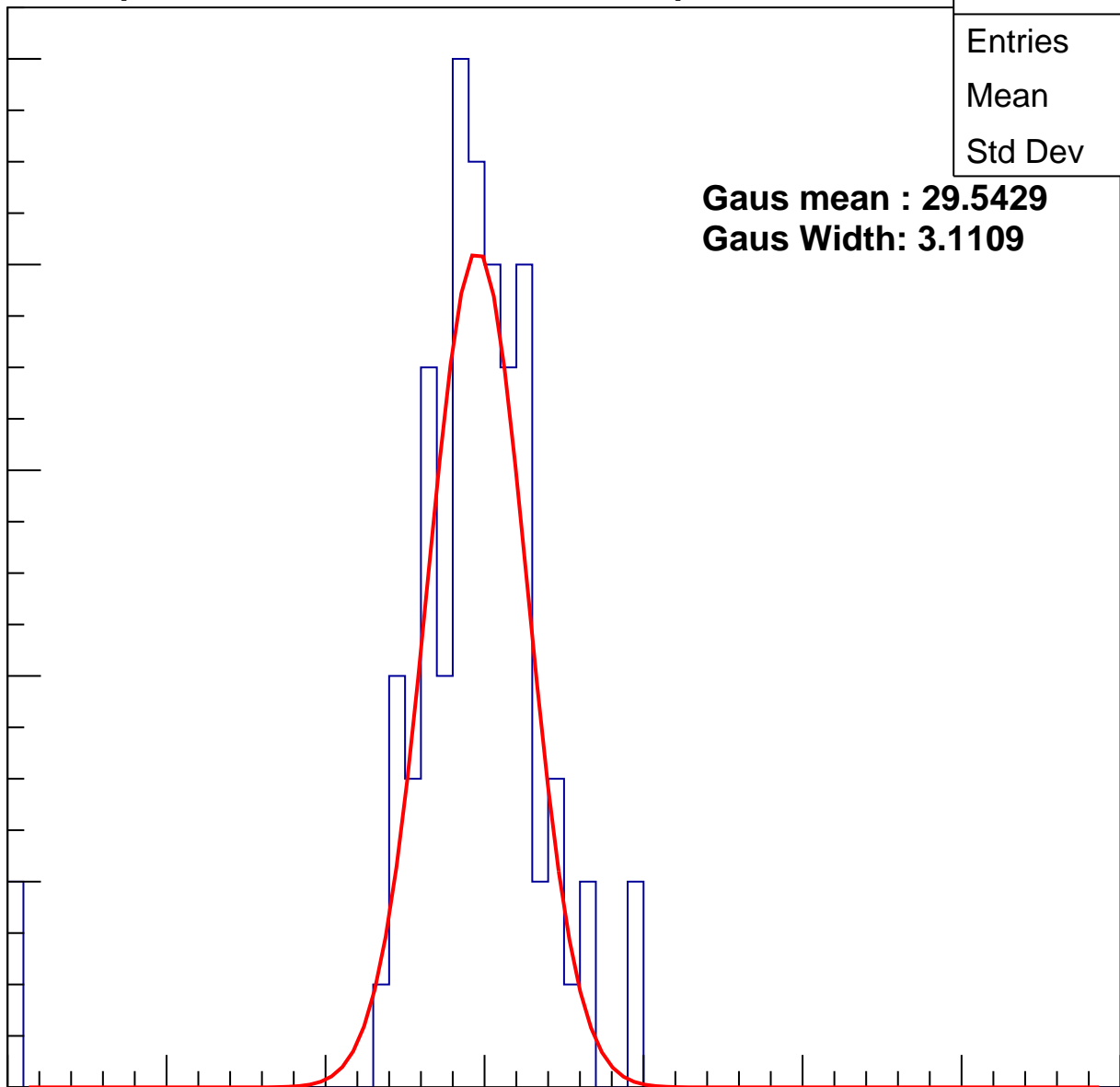
Gaus Width: 3.1109

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U7-ch125, adc1

calib_packv5_042523_0143.root, FC#4, port A2

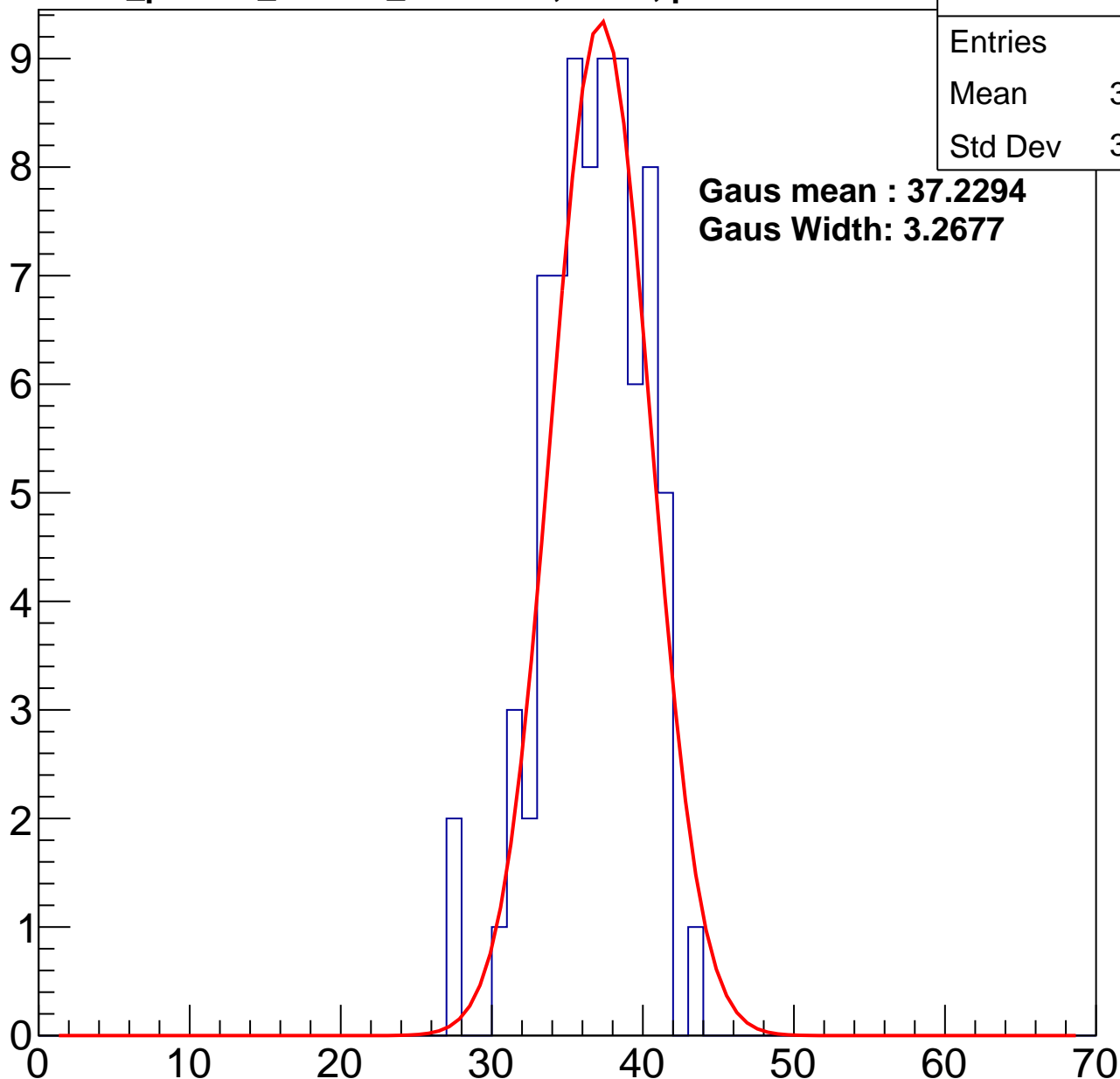
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 77 |
| Mean | 36.23 |
| Std Dev | 3.227 |

Gaus mean : 37.2294
Gaus Width: 3.2677

ampl



B1L100S, U7-ch125, adc2

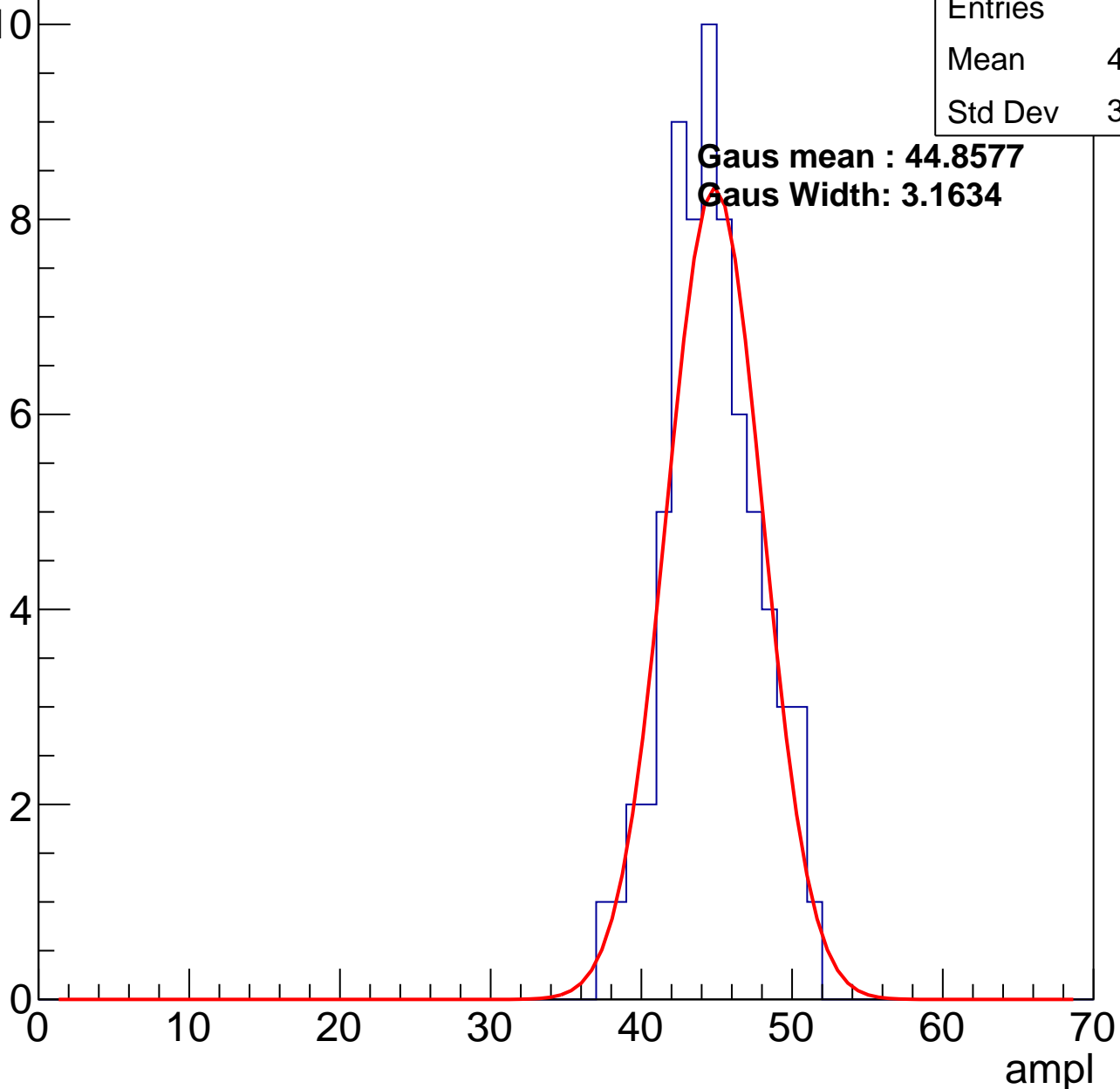
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 44.28 |
| Std Dev | 3.029 |

Gaus mean : 44.8577

Gaus Width: 3.1634

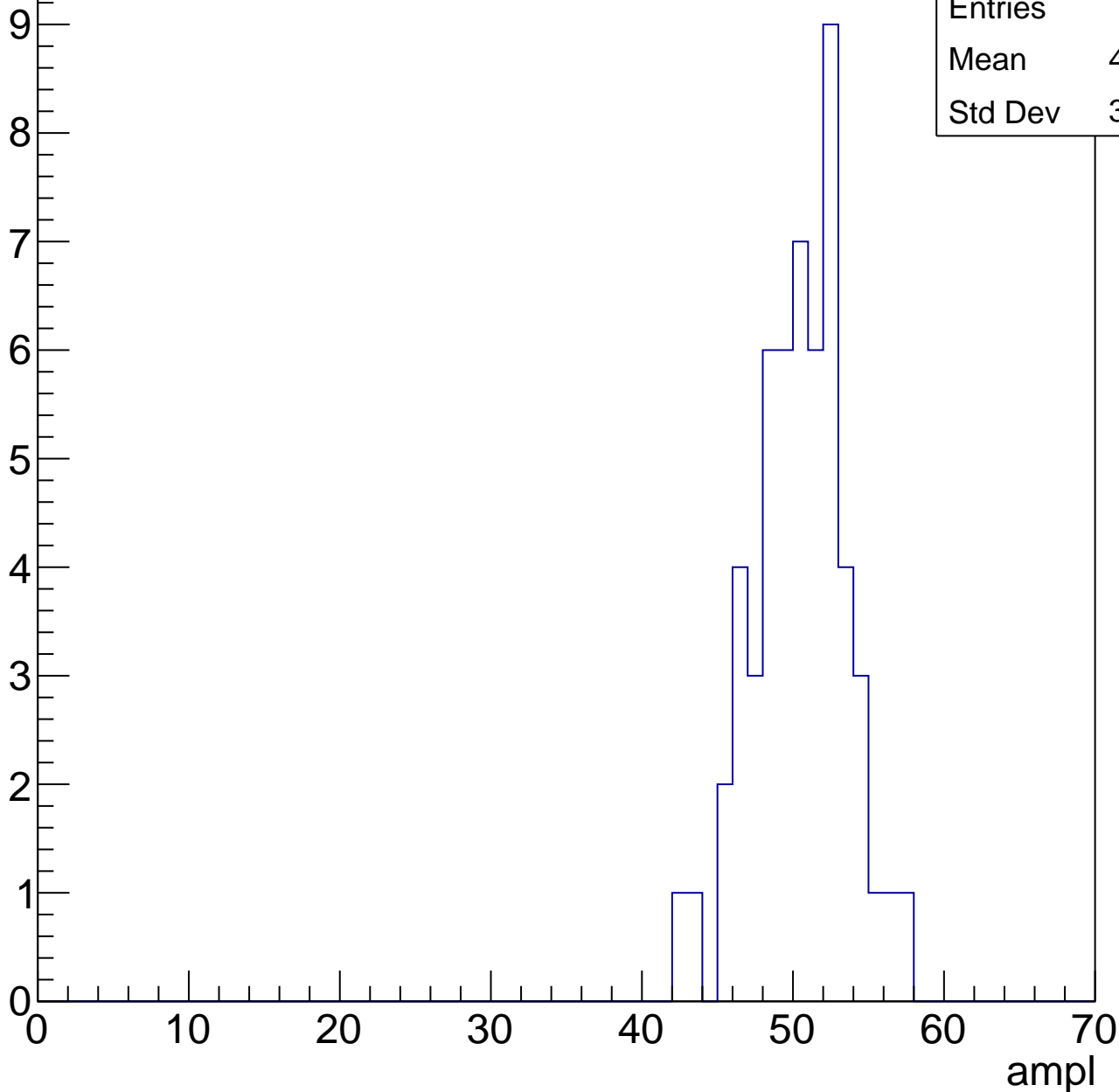


B1L100S, U7-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 49.96 |
| Std Dev | 3.075 |



B1L100S, U7-ch125, adc4

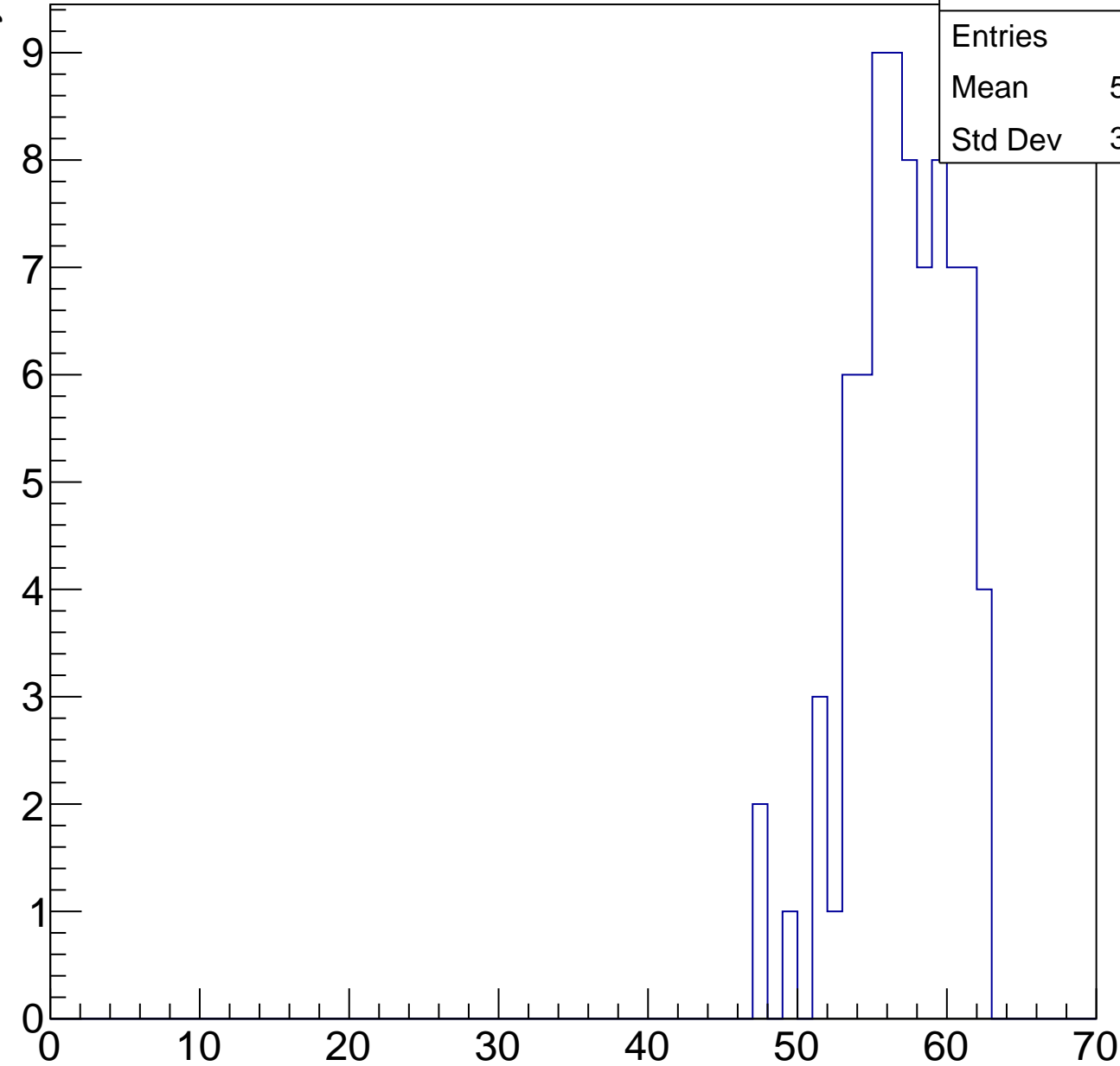
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 78 |
| Mean | 56.64 |
| Std Dev | 3.389 |

ampl

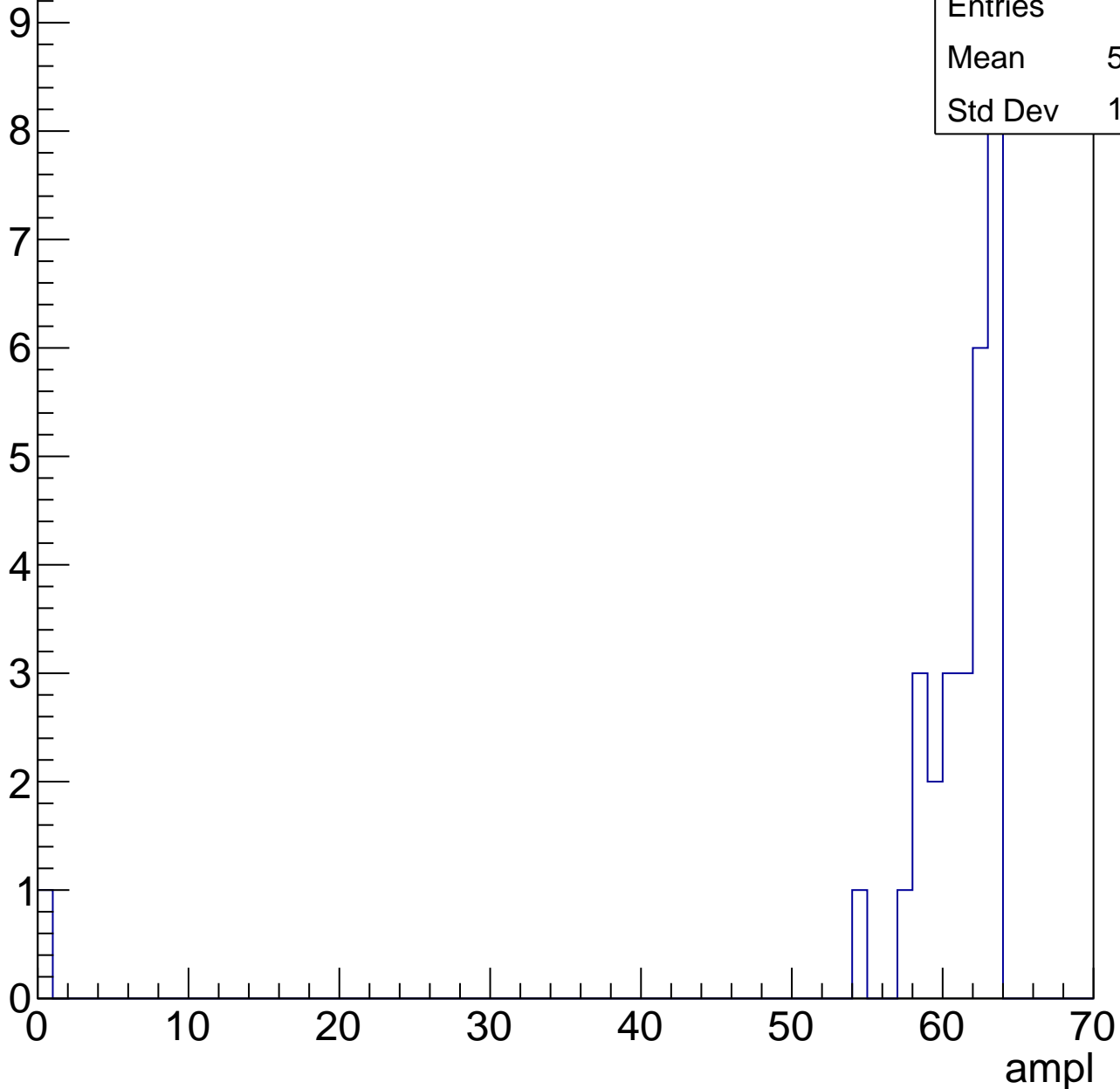


B1L100S, U7-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.79 |
| Std Dev | 11.33 |



B1L100S, U7-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.333 |
| Std Dev | 10.37 |

B1L100S, U7-ch126, adc0

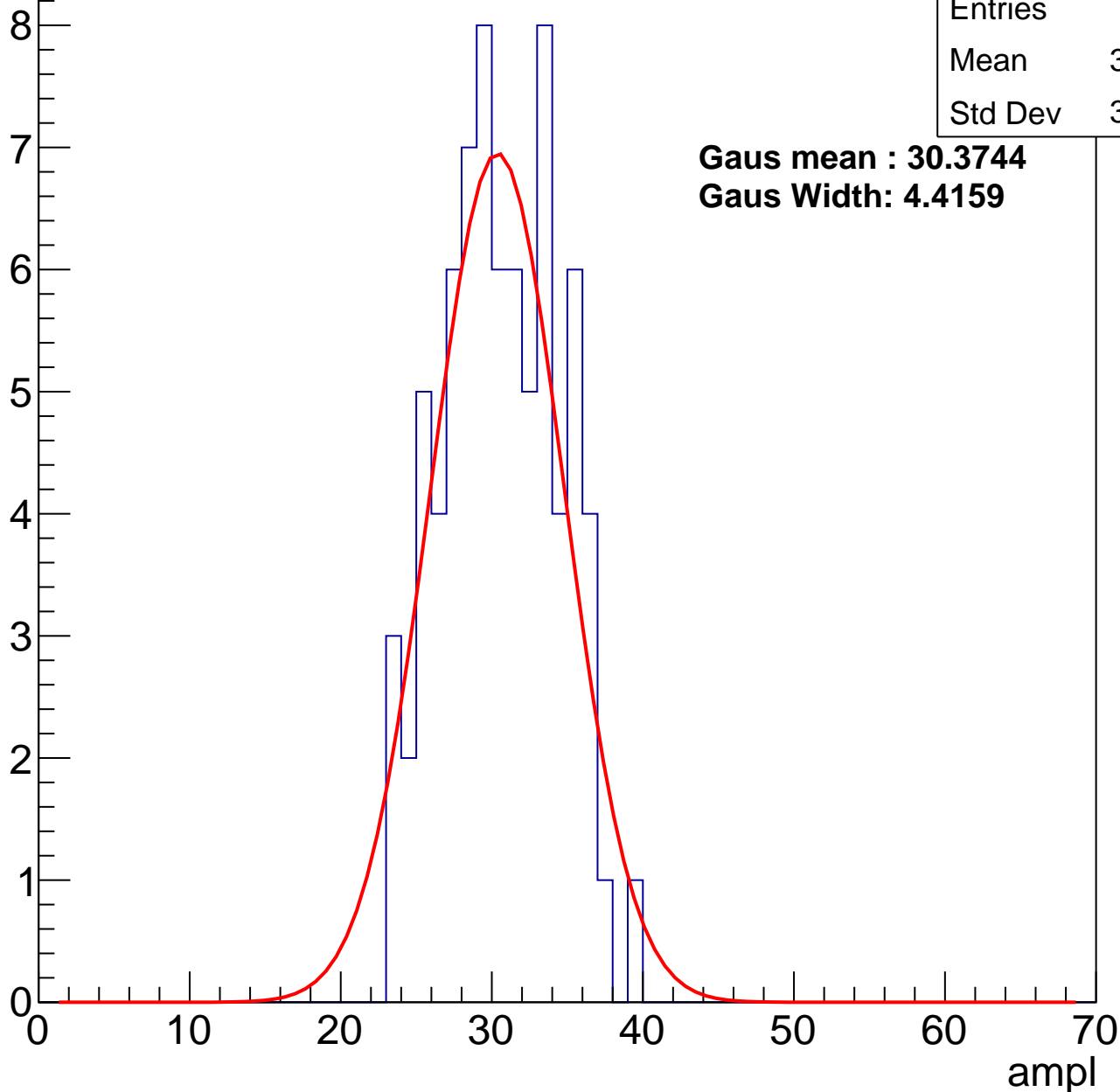
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 30.16 |
| Std Dev | 3.773 |

Gaus mean : 30.3744

Gaus Width: 4.4159



B1L100S, U7-ch126, adc1

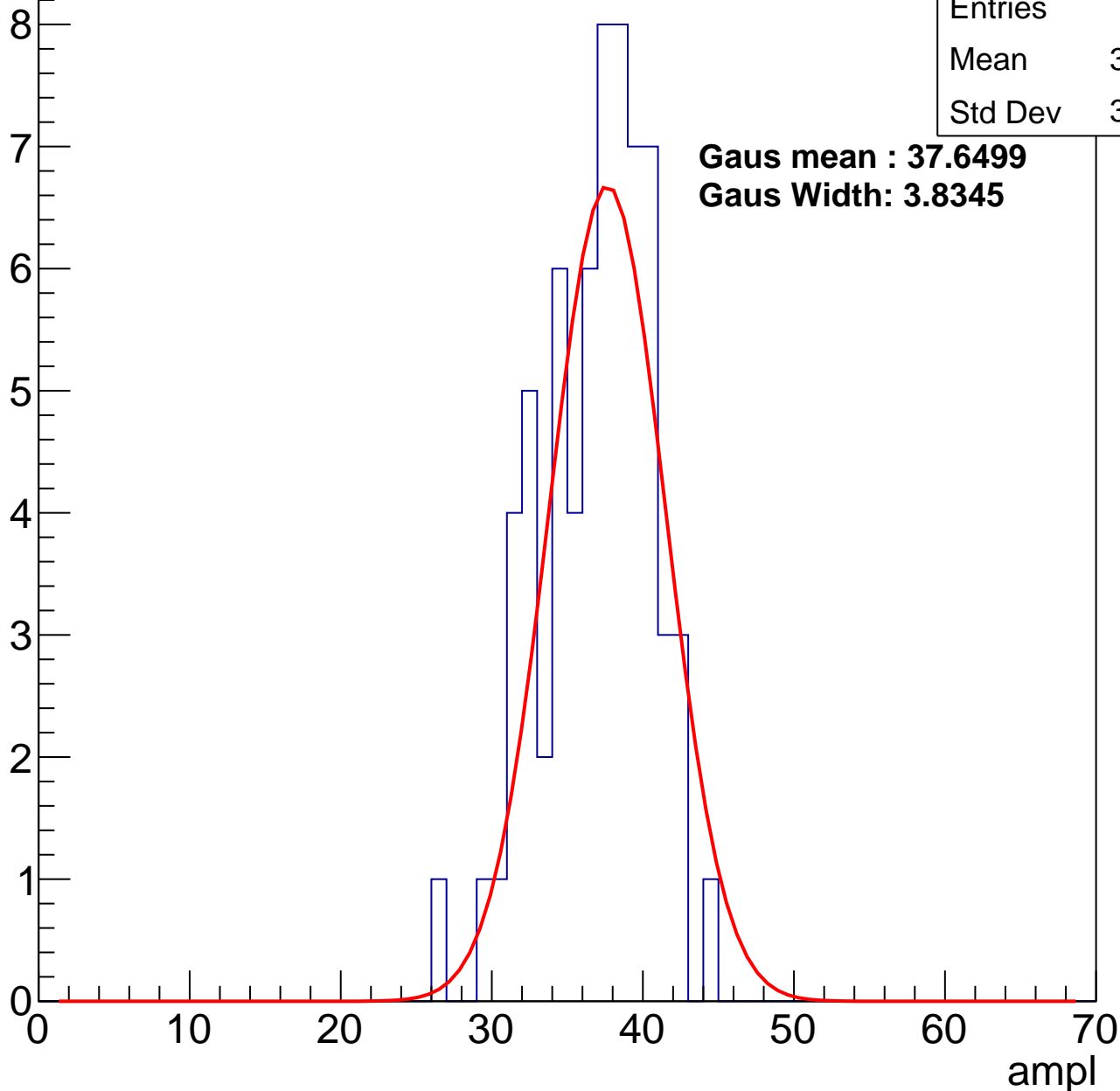
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.43 |
| Std Dev | 3.584 |

Gaus mean : 37.6499

Gaus Width: 3.8345



B1L100S, U7-ch126, adc2

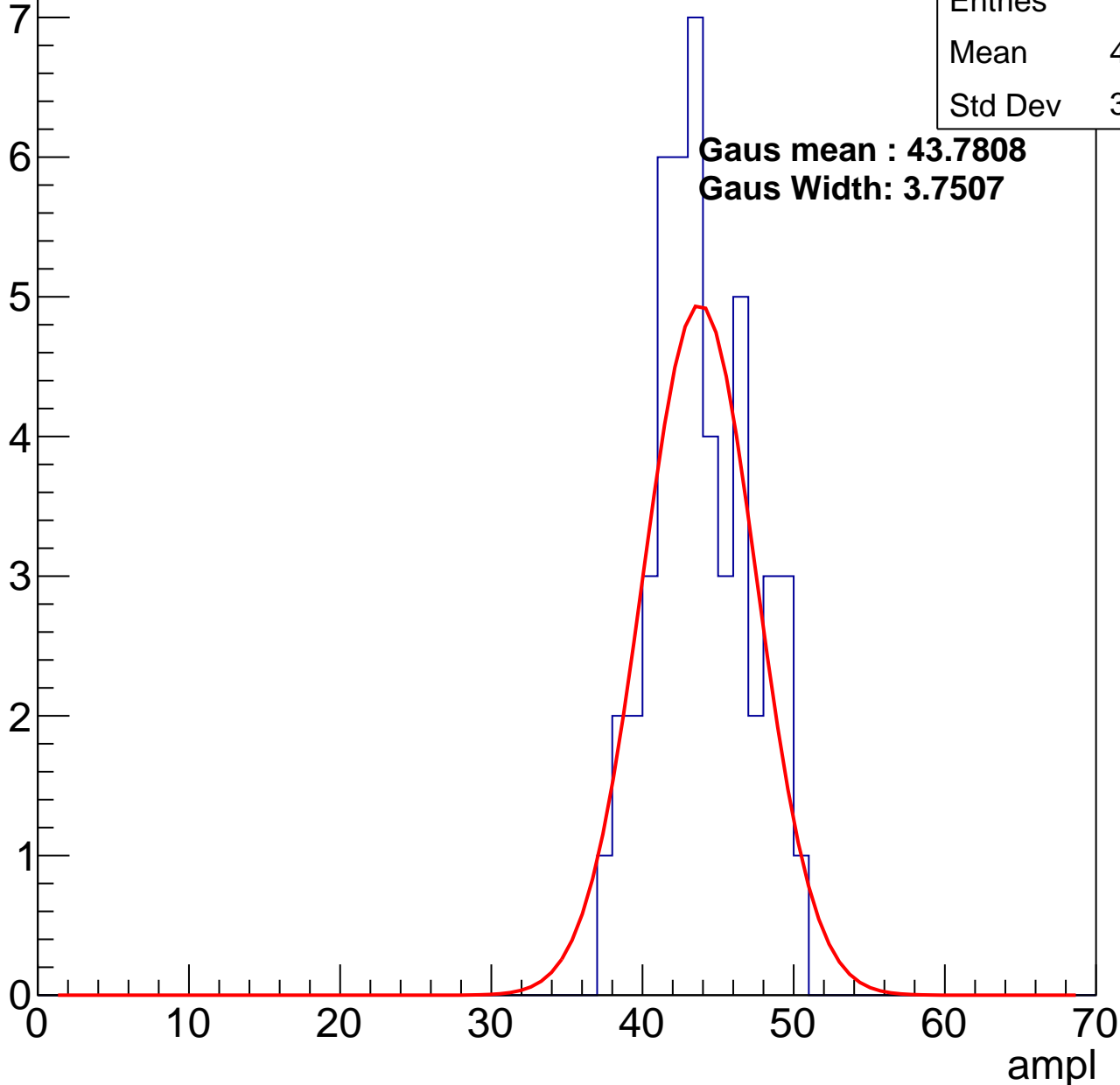
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 43.46 |
| Std Dev | 3.195 |

Gaus mean : 43.7808

Gaus Width: 3.7507

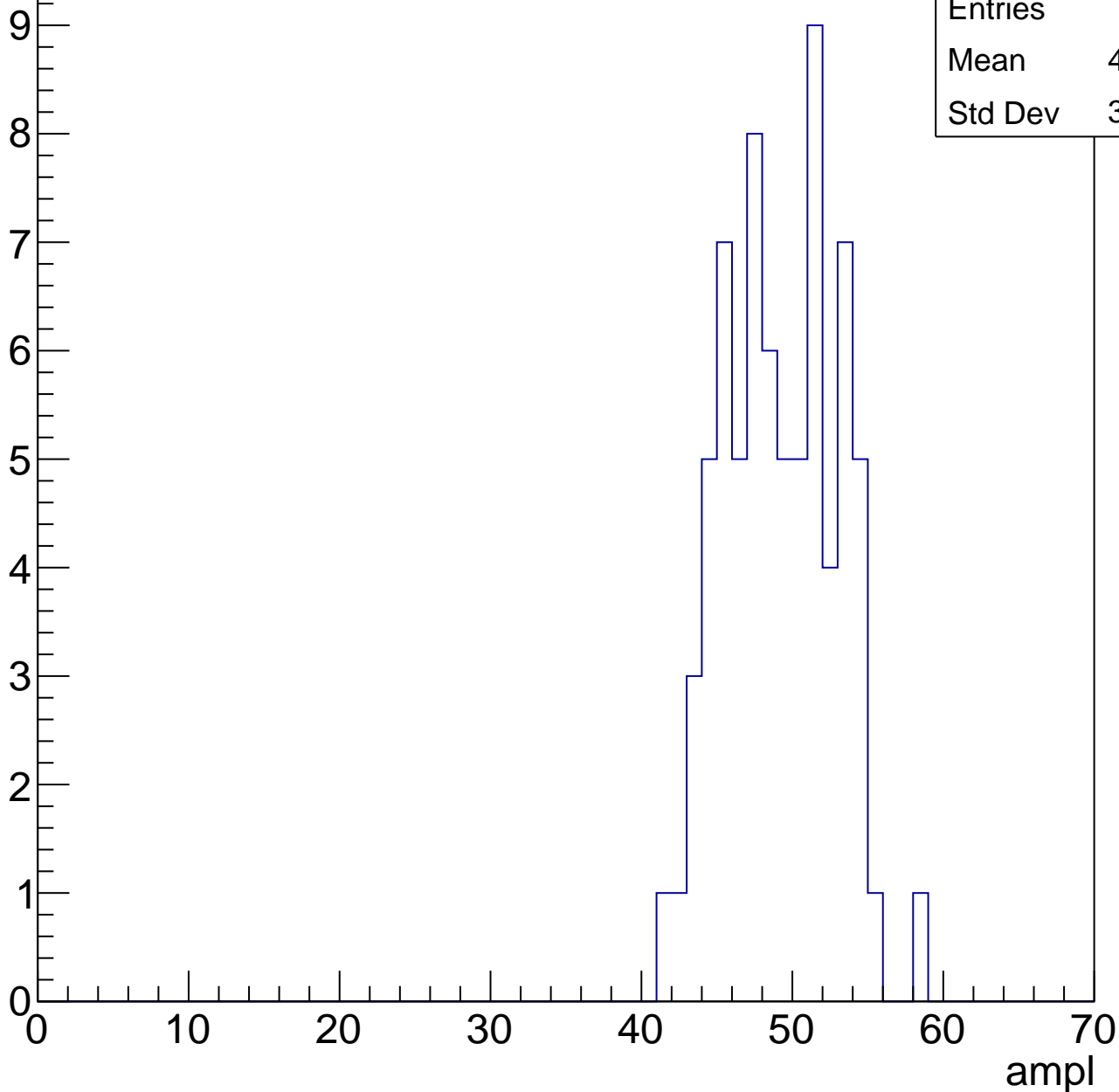


B1L100S, U7-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

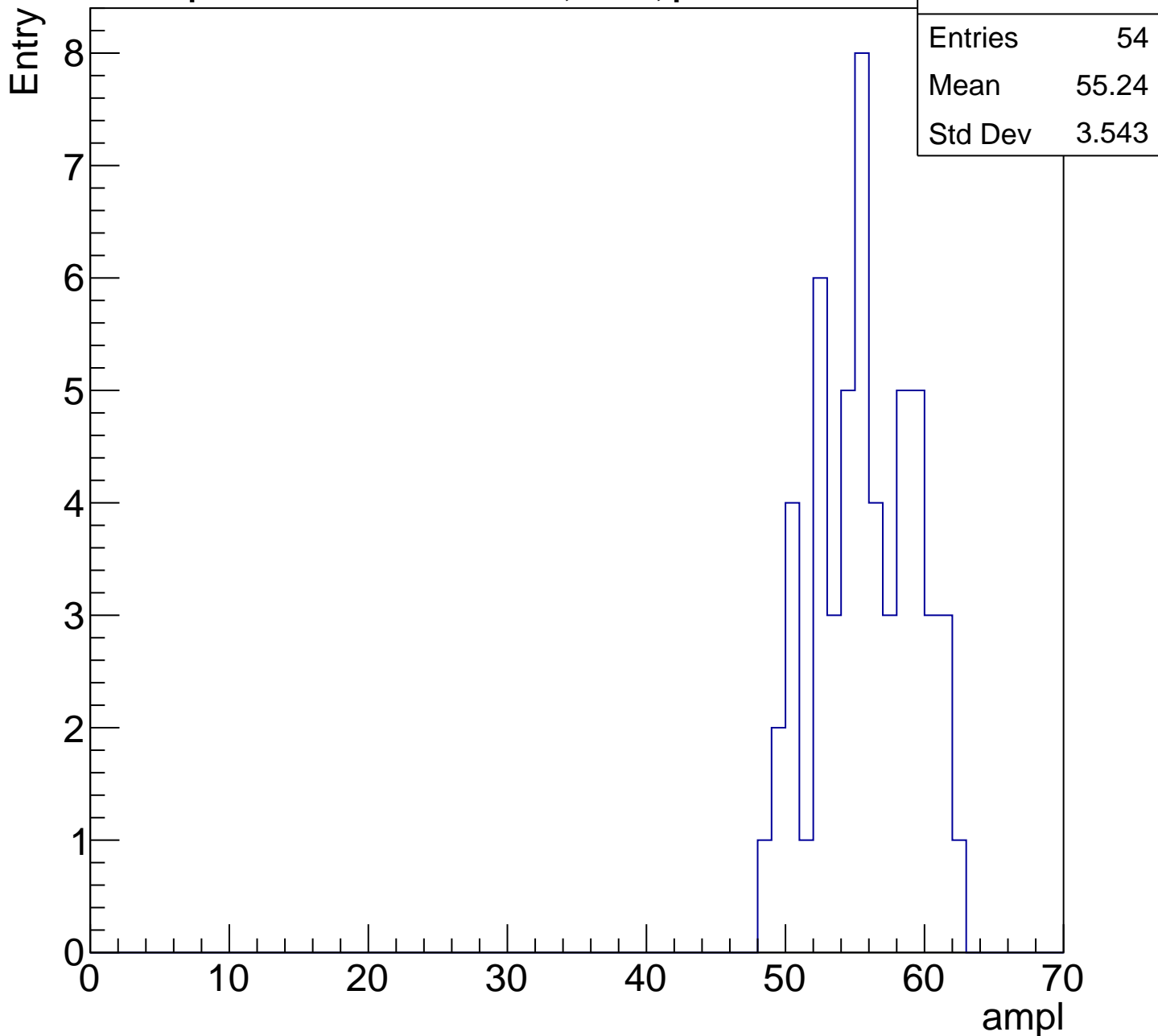
Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 48.73 |
| Std Dev | 3.639 |



B1L100S, U7-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

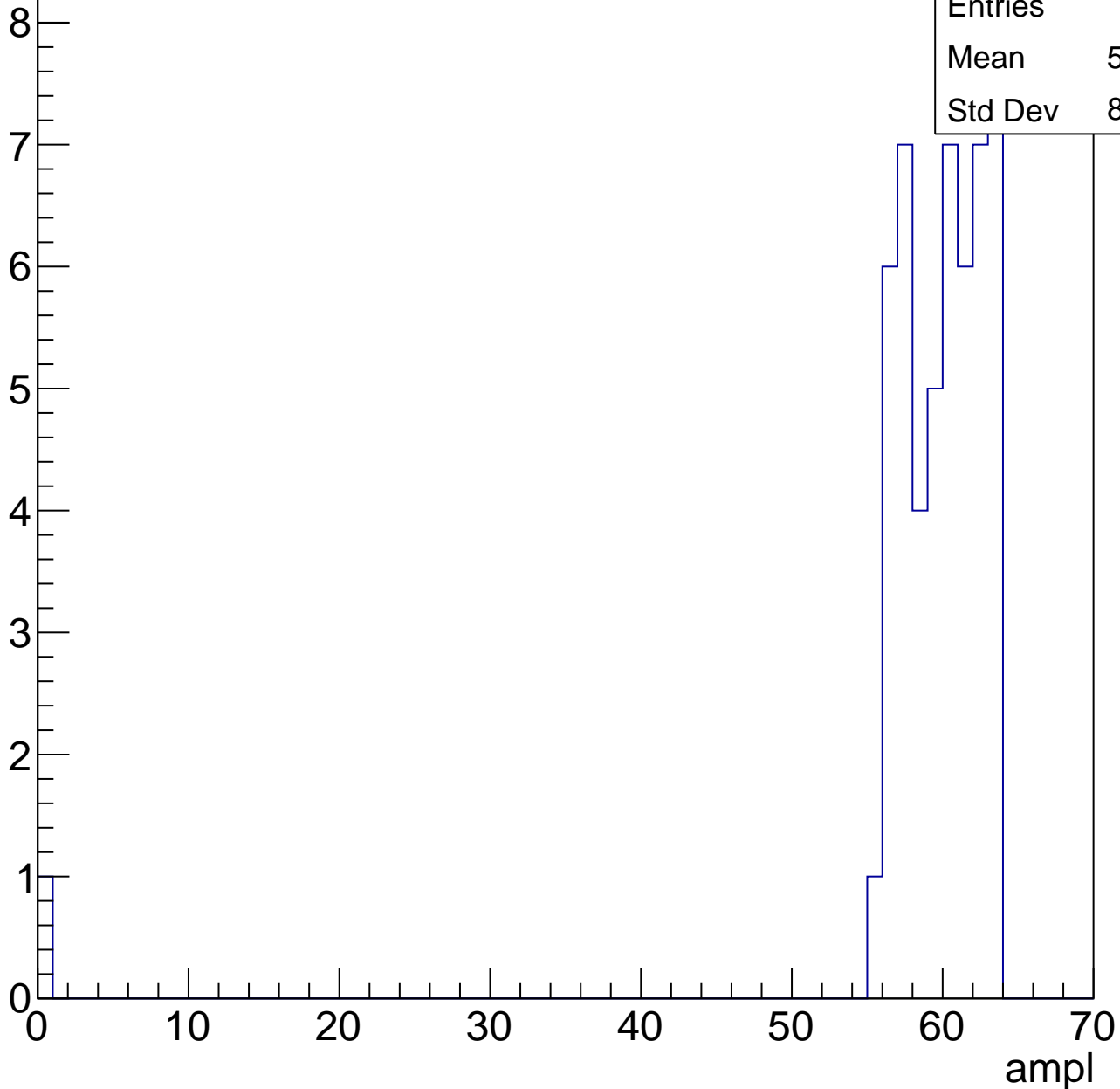


B1L100S, U7-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.48 |
| Std Dev | 8.538 |



B1L100S, U7-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 20.67 |
| Std Dev | 29.23 |

B1L100S, U7-ch127, adc0

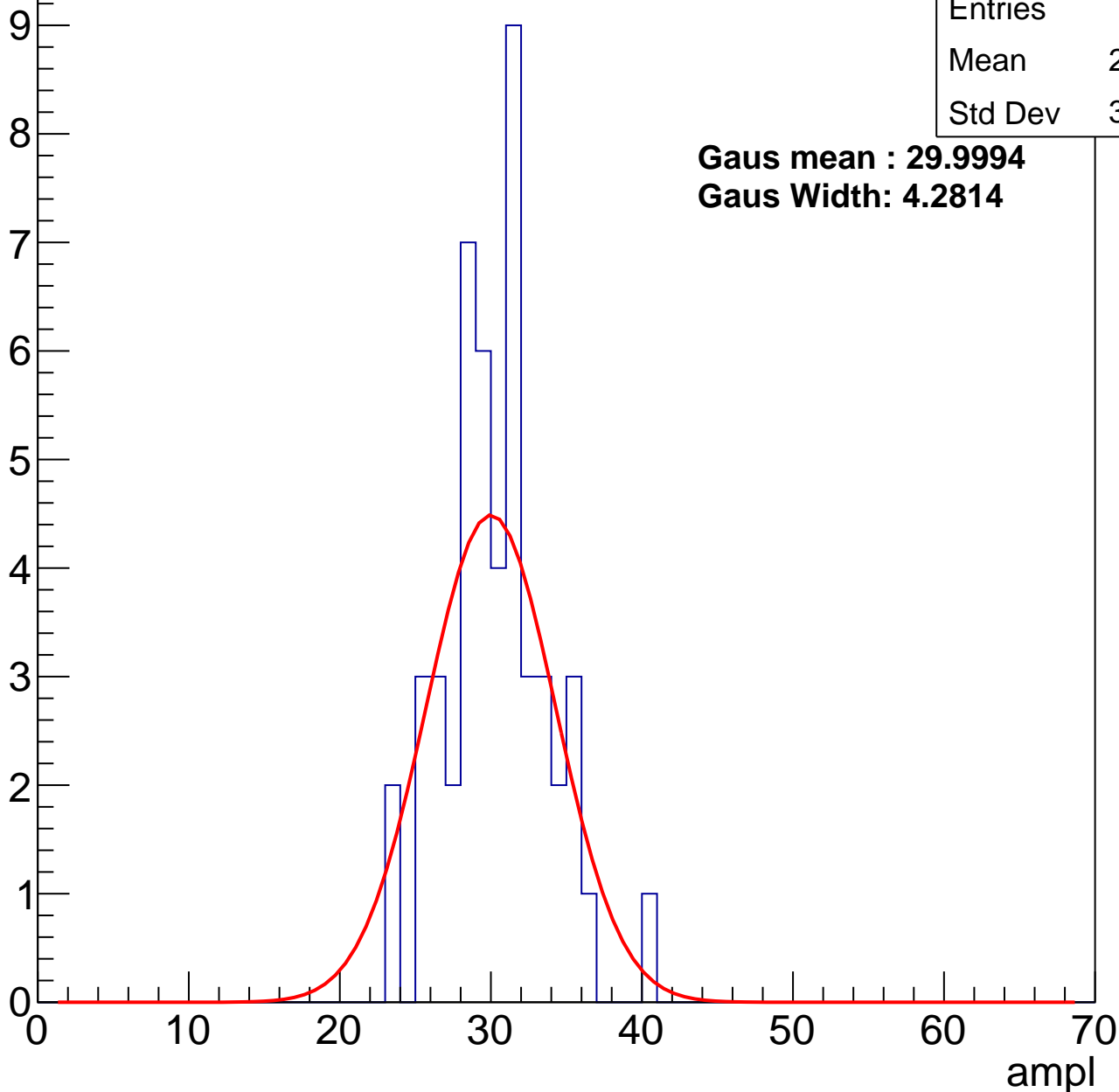
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 29.92 |
| Std Dev | 3.392 |

Gaus mean : 29.9994

Gaus Width: 4.2814



B1L100S, U7-ch127, adc1

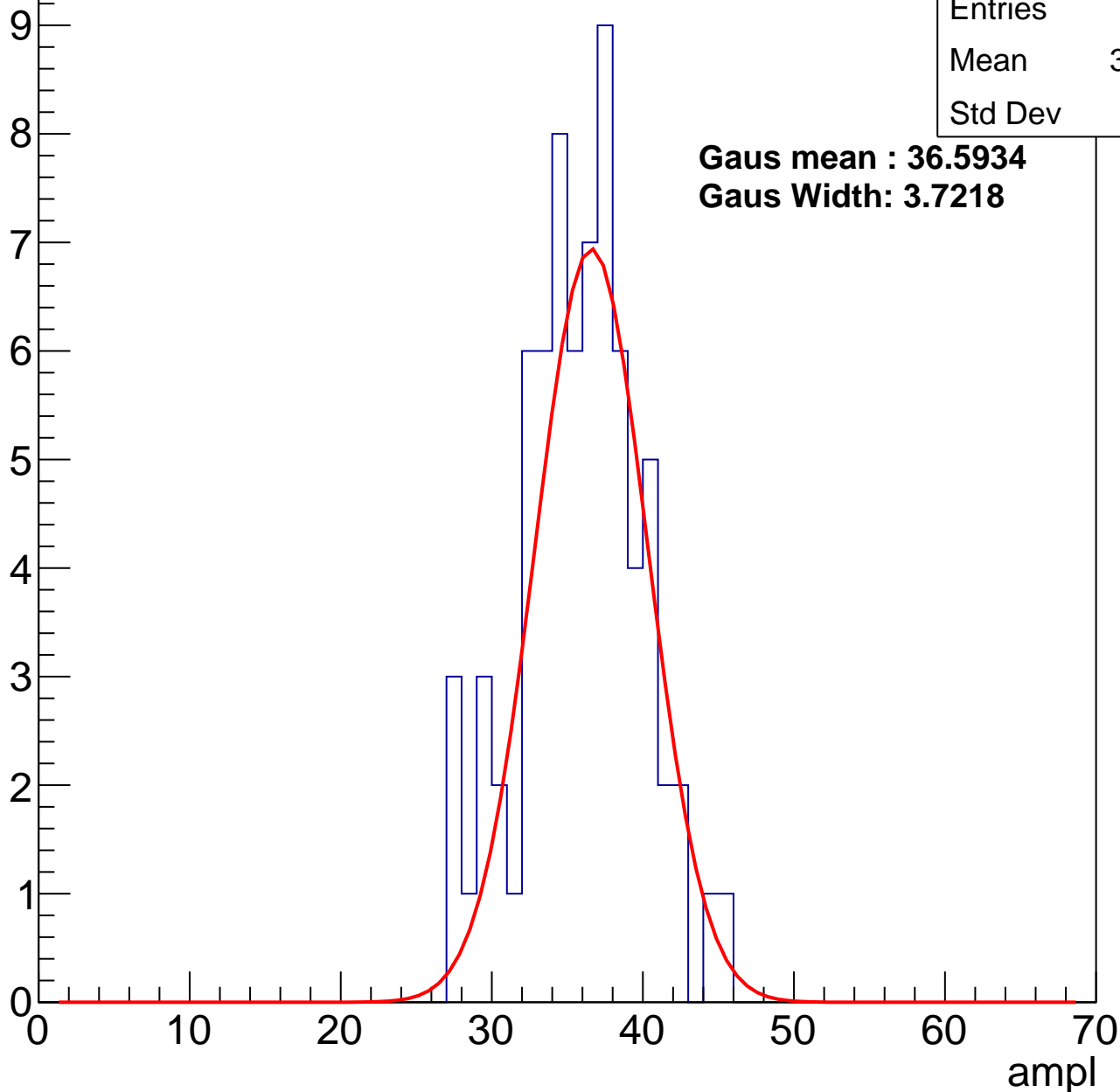
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 35.38 |
| Std Dev | 3.93 |

Gaus mean : 36.5934

Gaus Width: 3.7218



B1L100S, U7-ch127, adc2

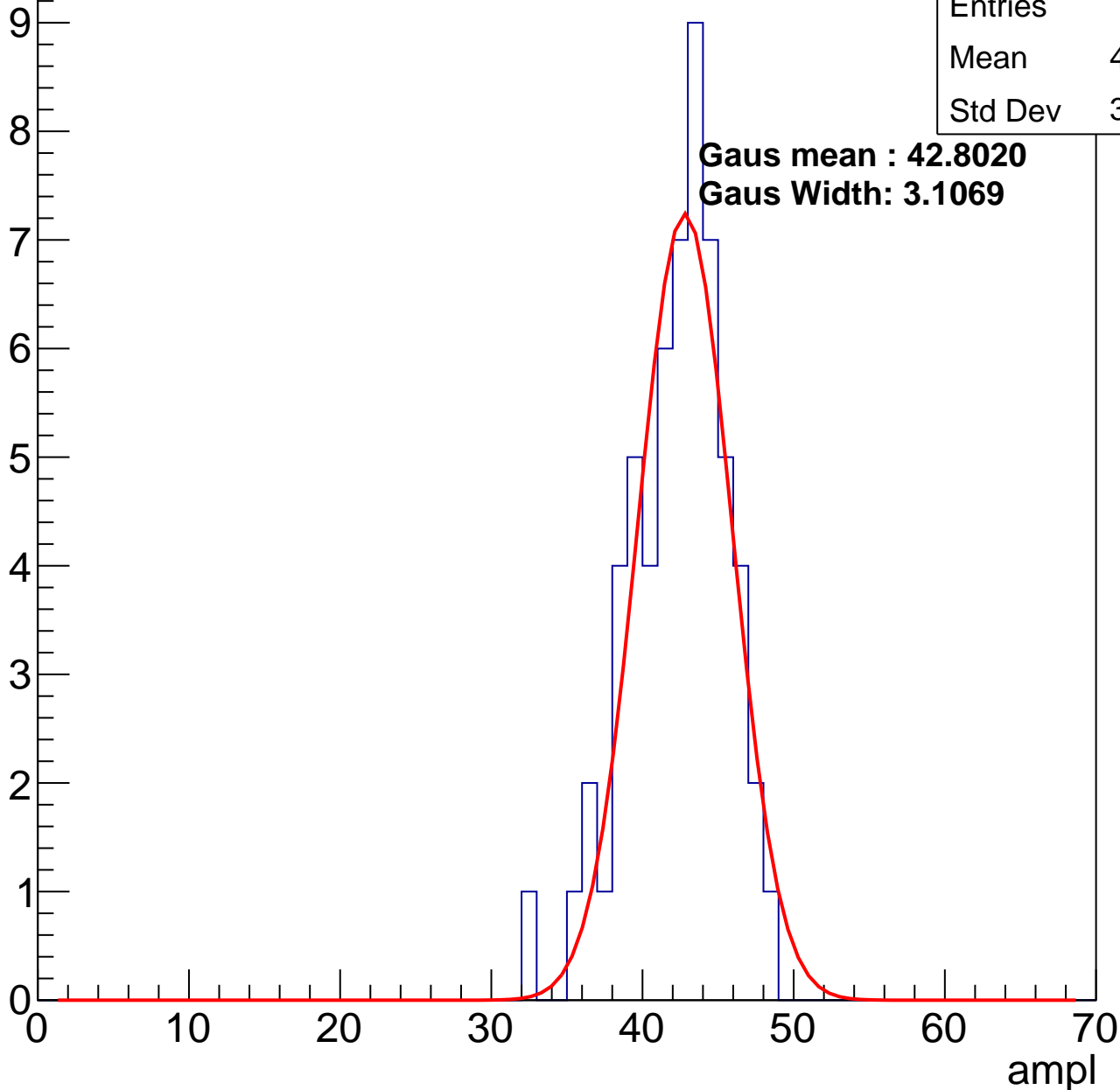
calib_packv5_042523_0143.root, FC#4, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 41.85 |
| Std Dev | 3.209 |

Gaus mean : 42.8020

Gaus Width: 3.1069



B1L100S, U7-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

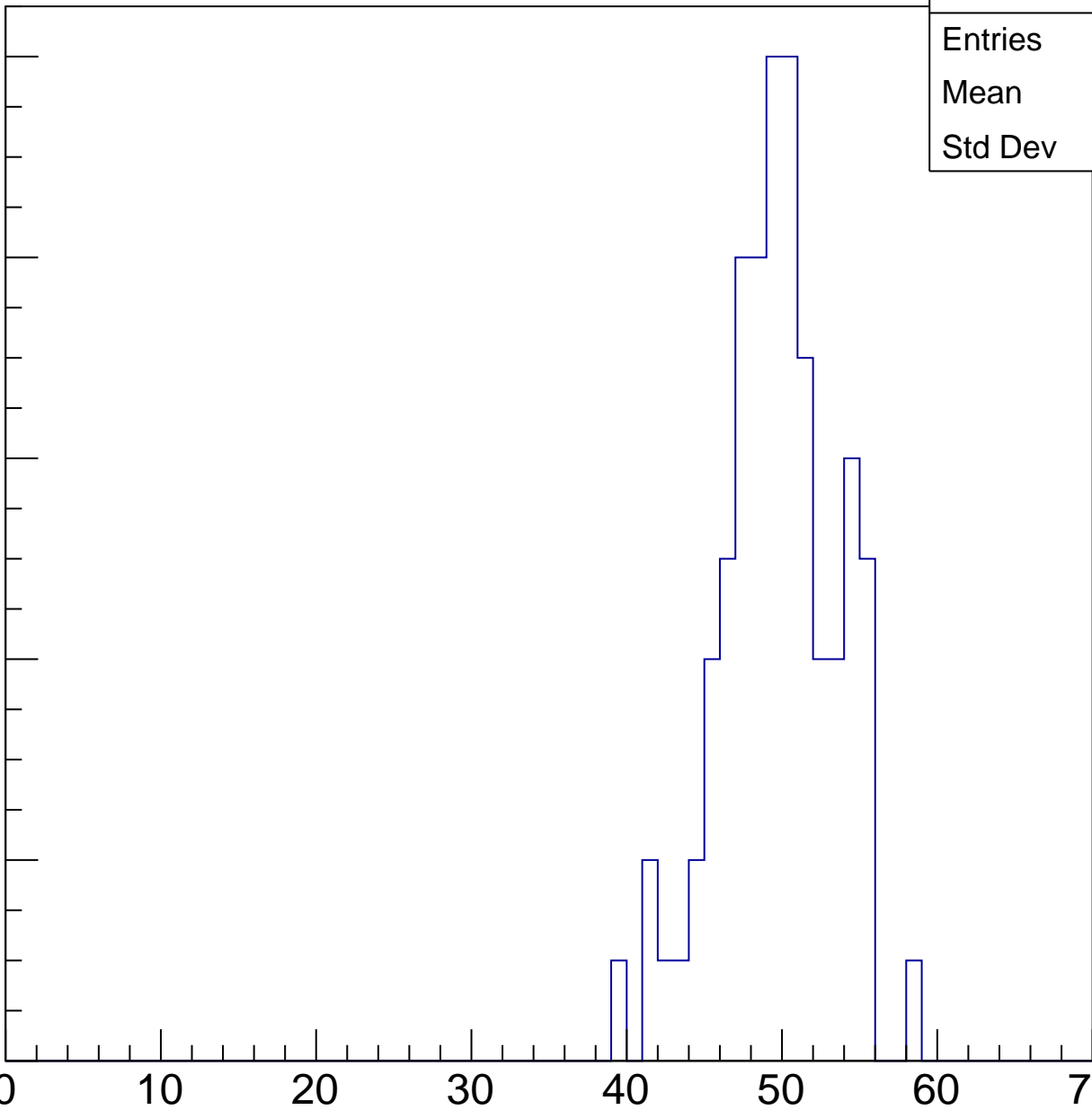
| | |
|---------|-------|
| Entries | 79 |
| Mean | 49.22 |
| Std Dev | 3.658 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

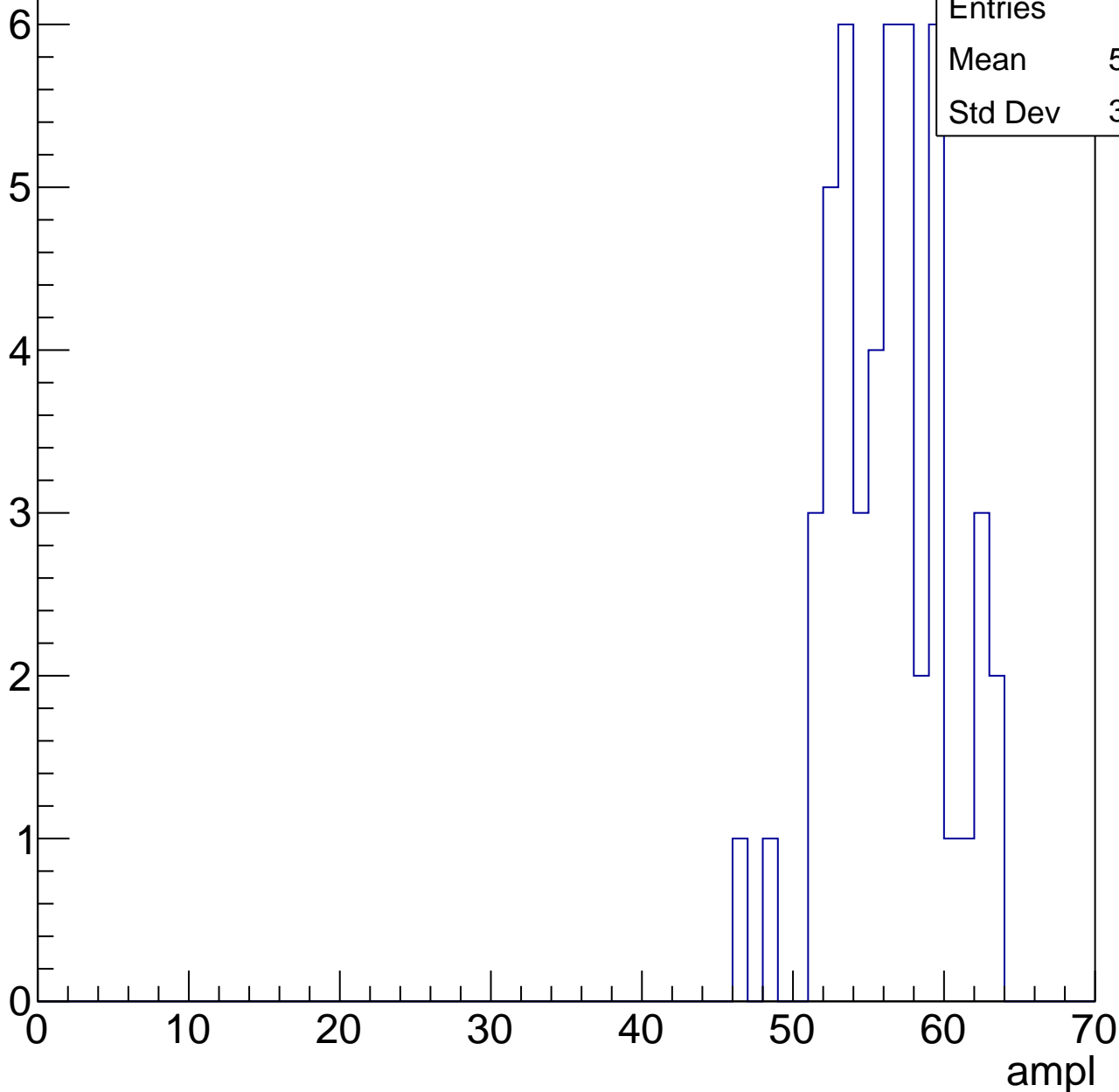


B1L100S, U7-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

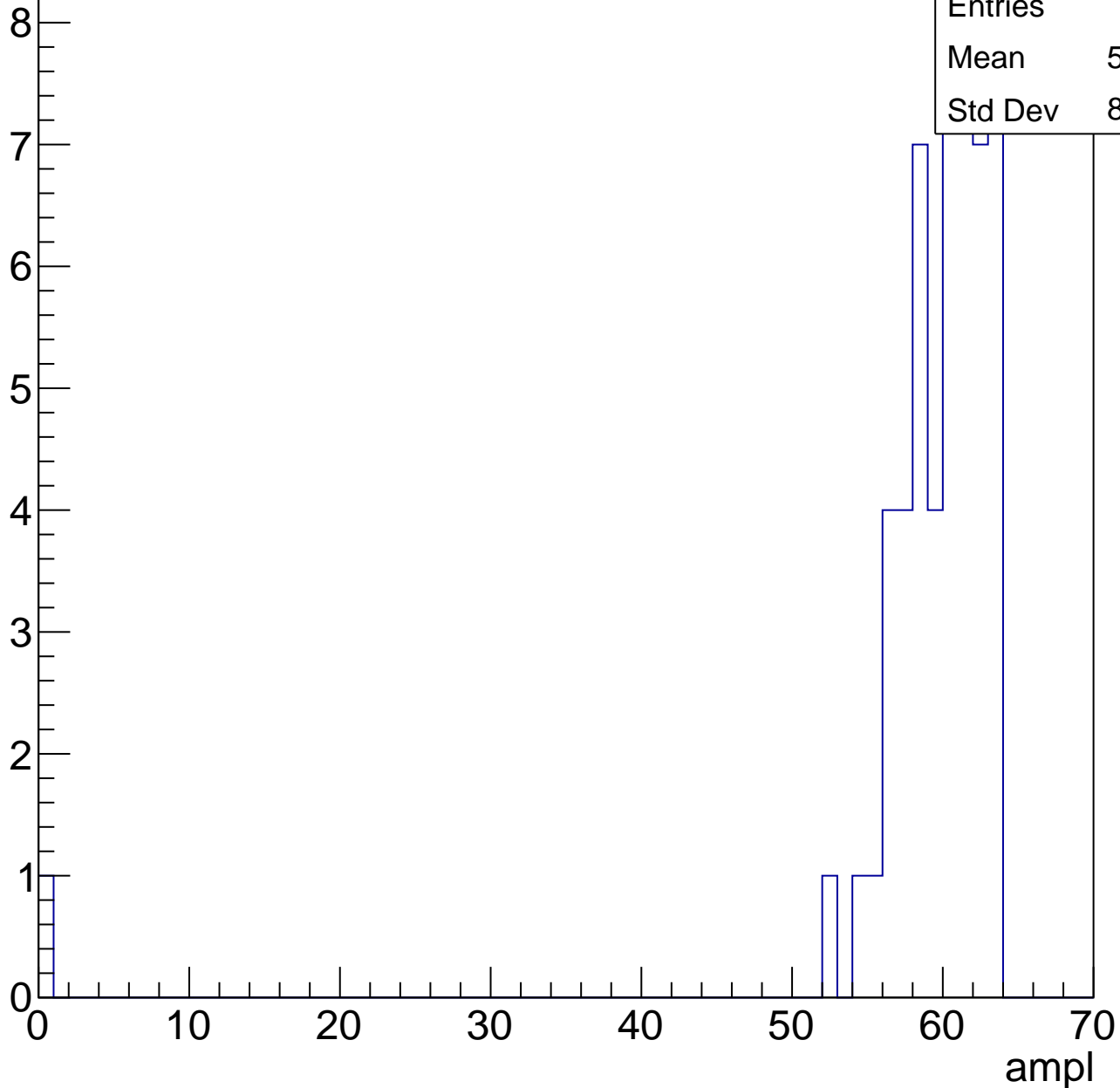
| | |
|---------|-------|
| Entries | 50 |
| Mean | 55.76 |
| Std Dev | 3.745 |



B1L100S, U7-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

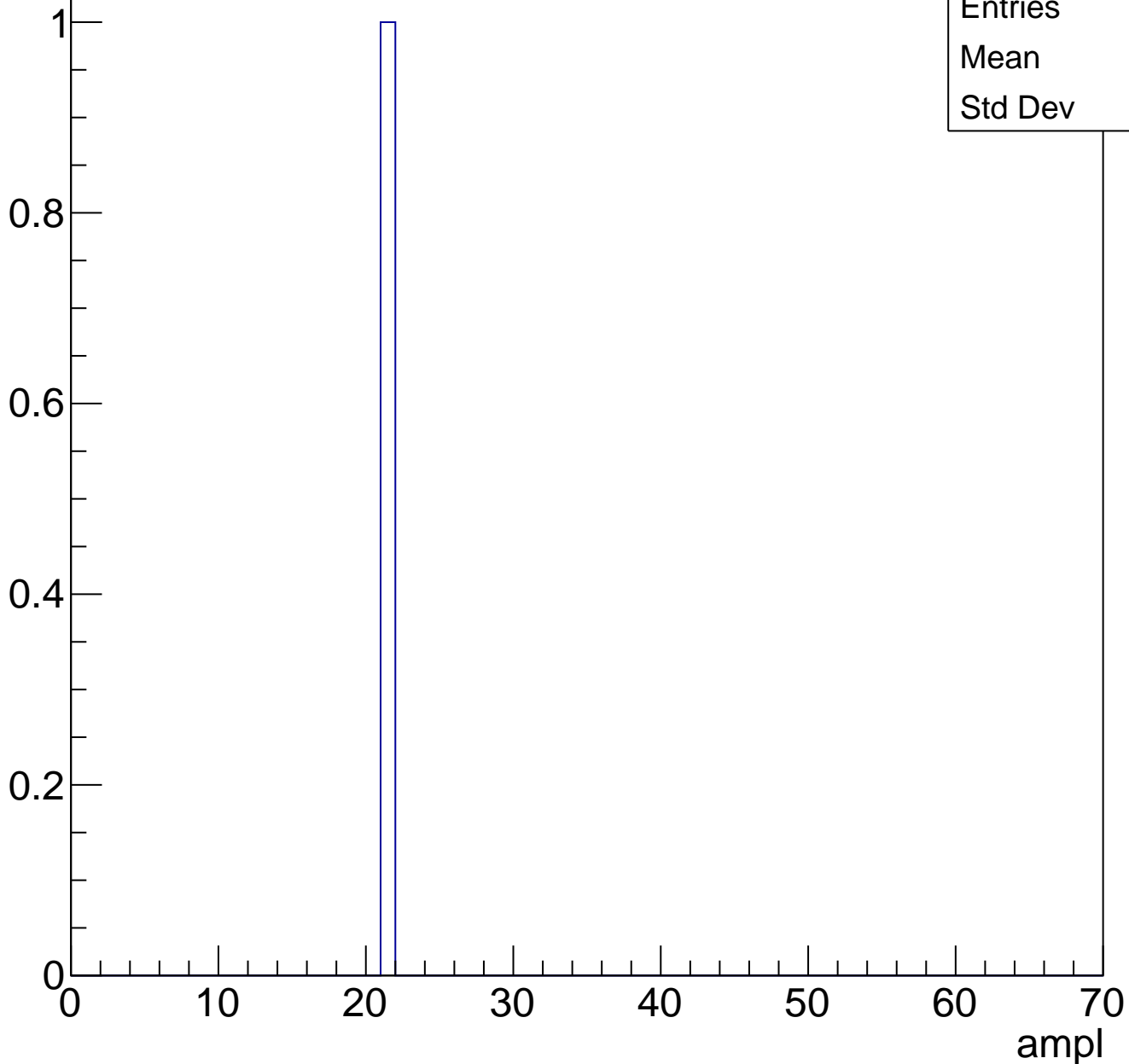
Entry



B1L100S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

