



# B0L000S, U10-ch0, adc0

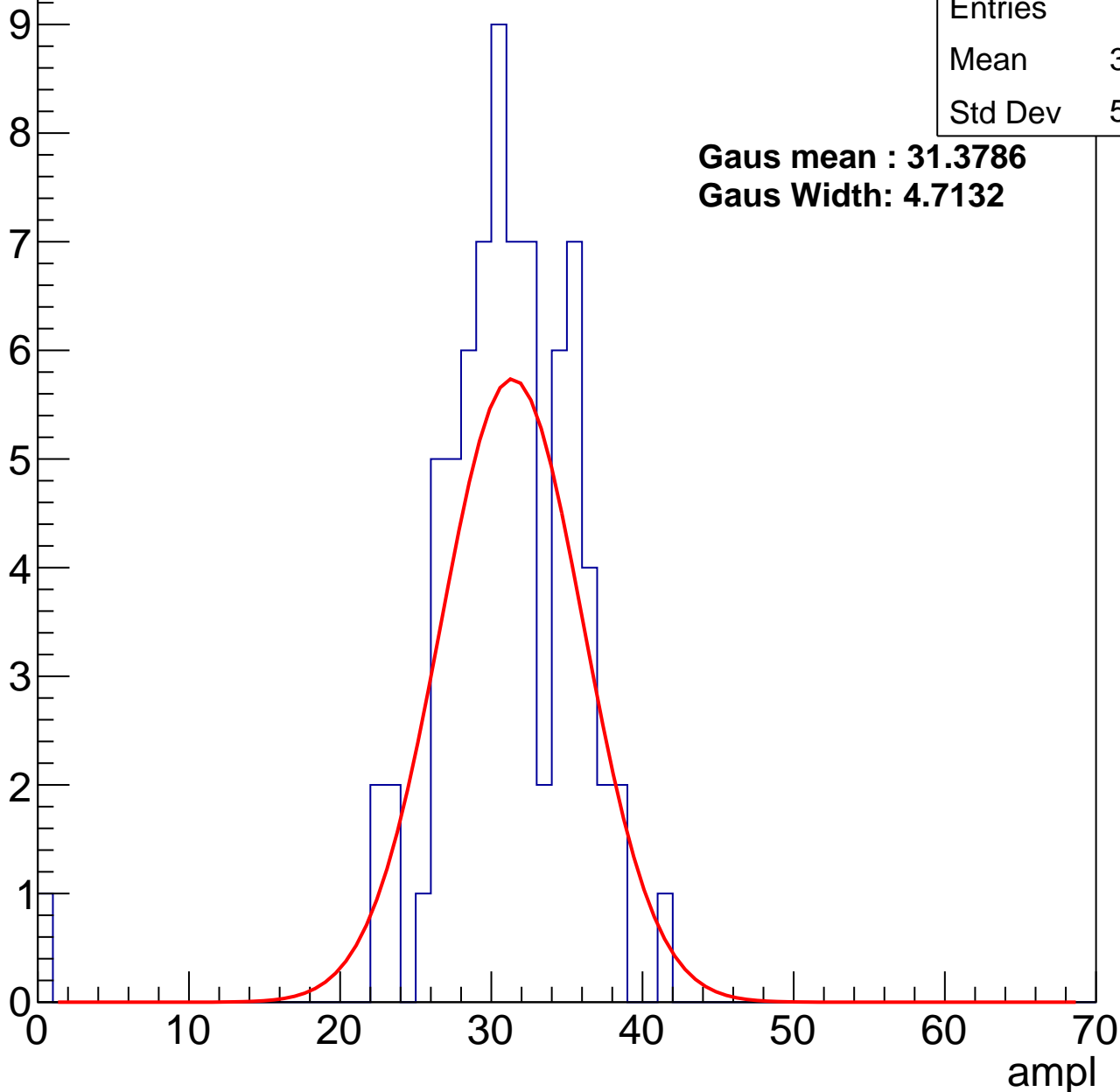
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.42
Std Dev	5.267

**Gaus mean : 31.3786**

**Gaus Width: 4.7132**



# B0L000S, U10-ch0, adc1

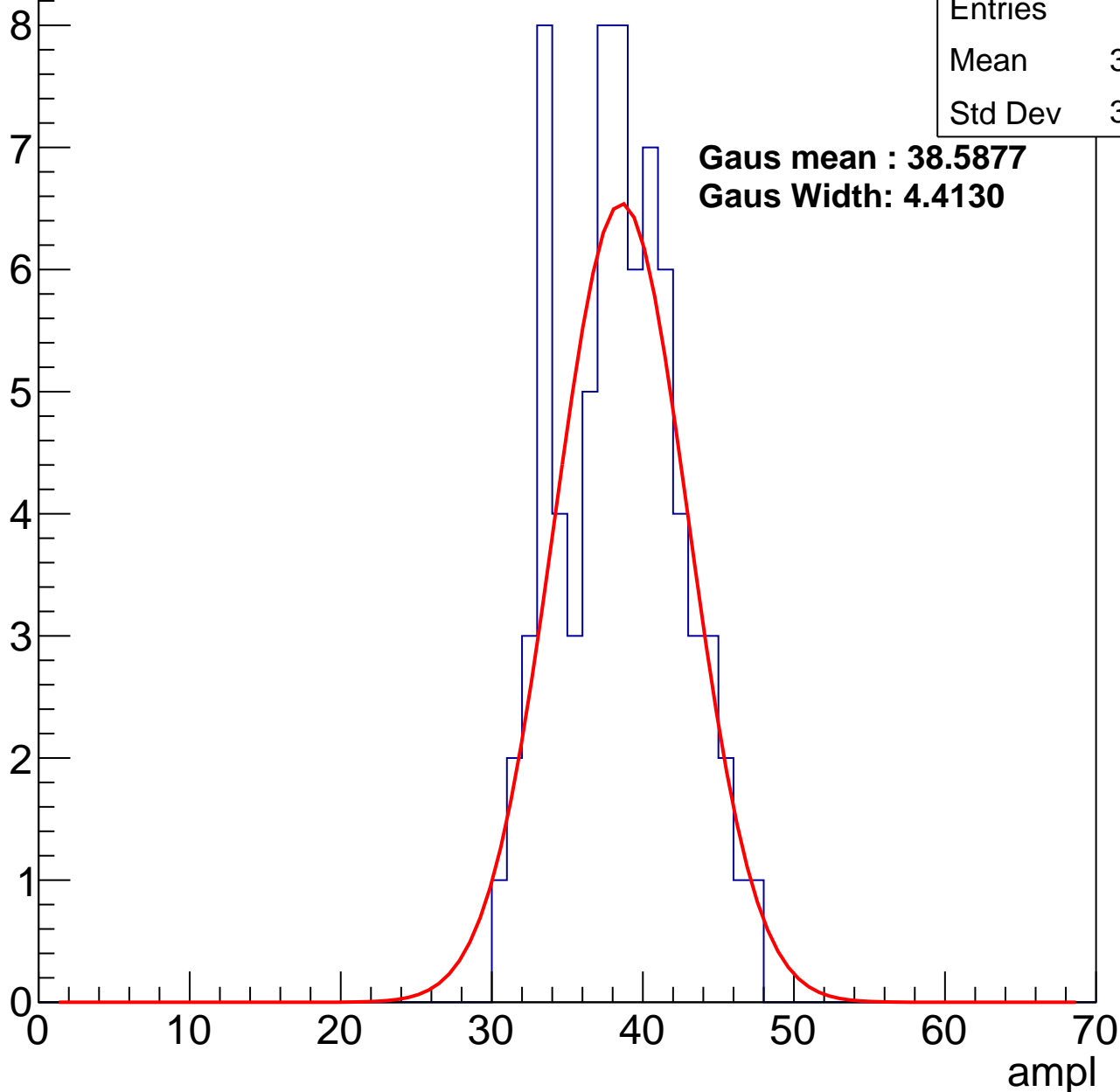
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.93
Std Dev	3.944

**Gaus mean : 38.5877**

**Gaus Width: 4.4130**



# B0L000S, U10-ch0, adc2

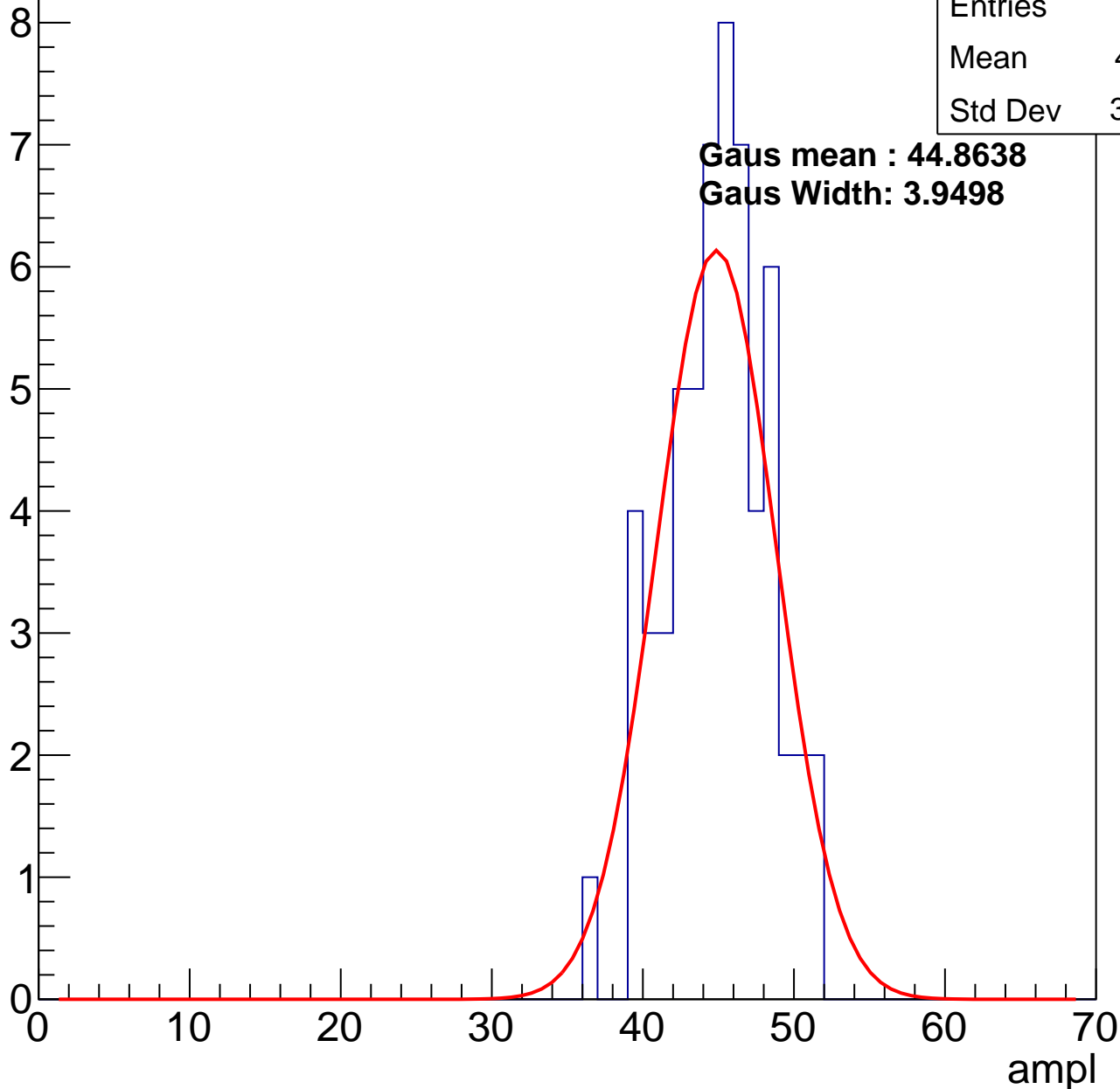
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	44.51
Std Dev	3.285

**Gaus mean : 44.8638**

**Gaus Width: 3.9498**

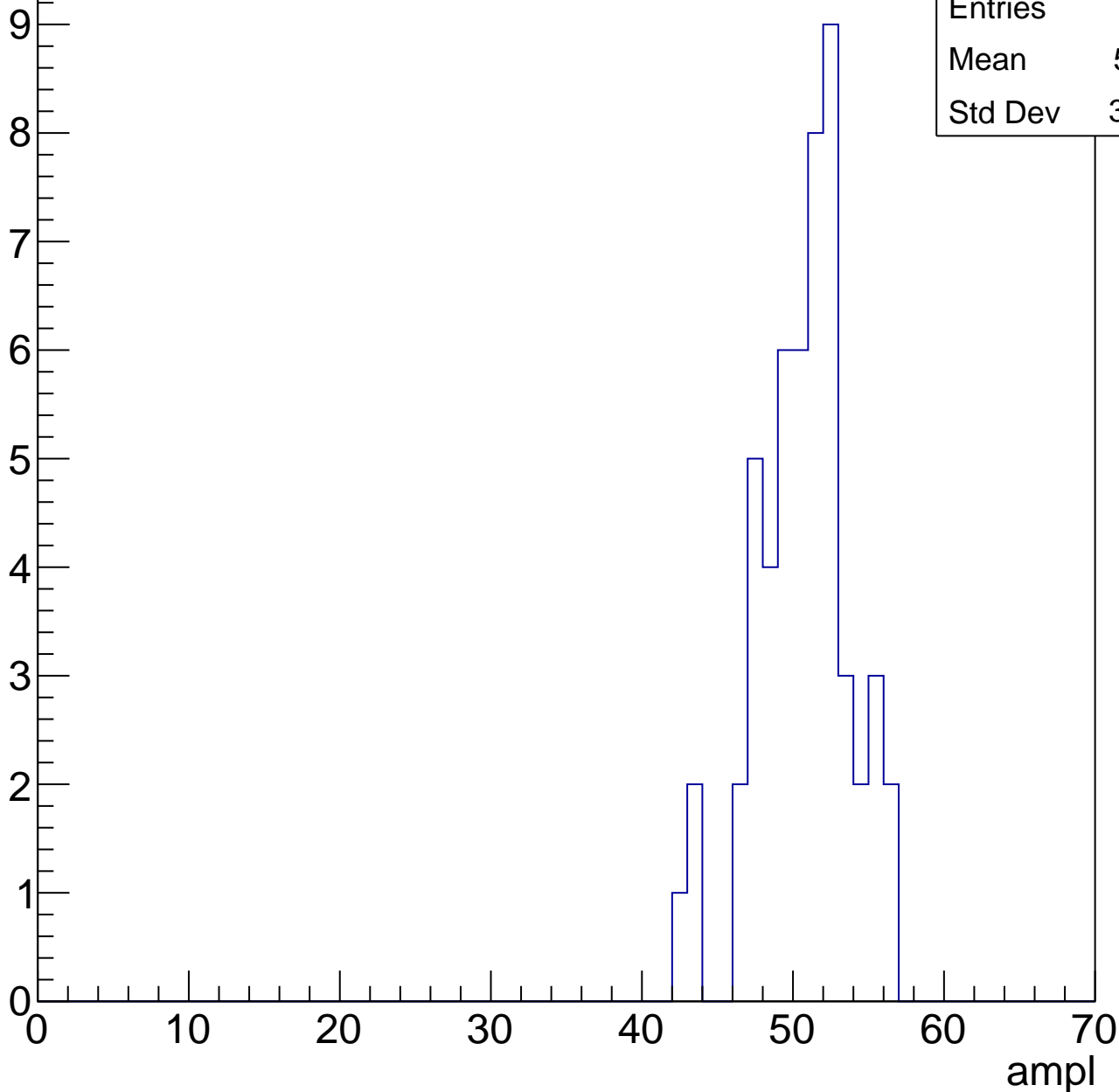


# B0L000S, U10-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	50.21
Std Dev	3.098

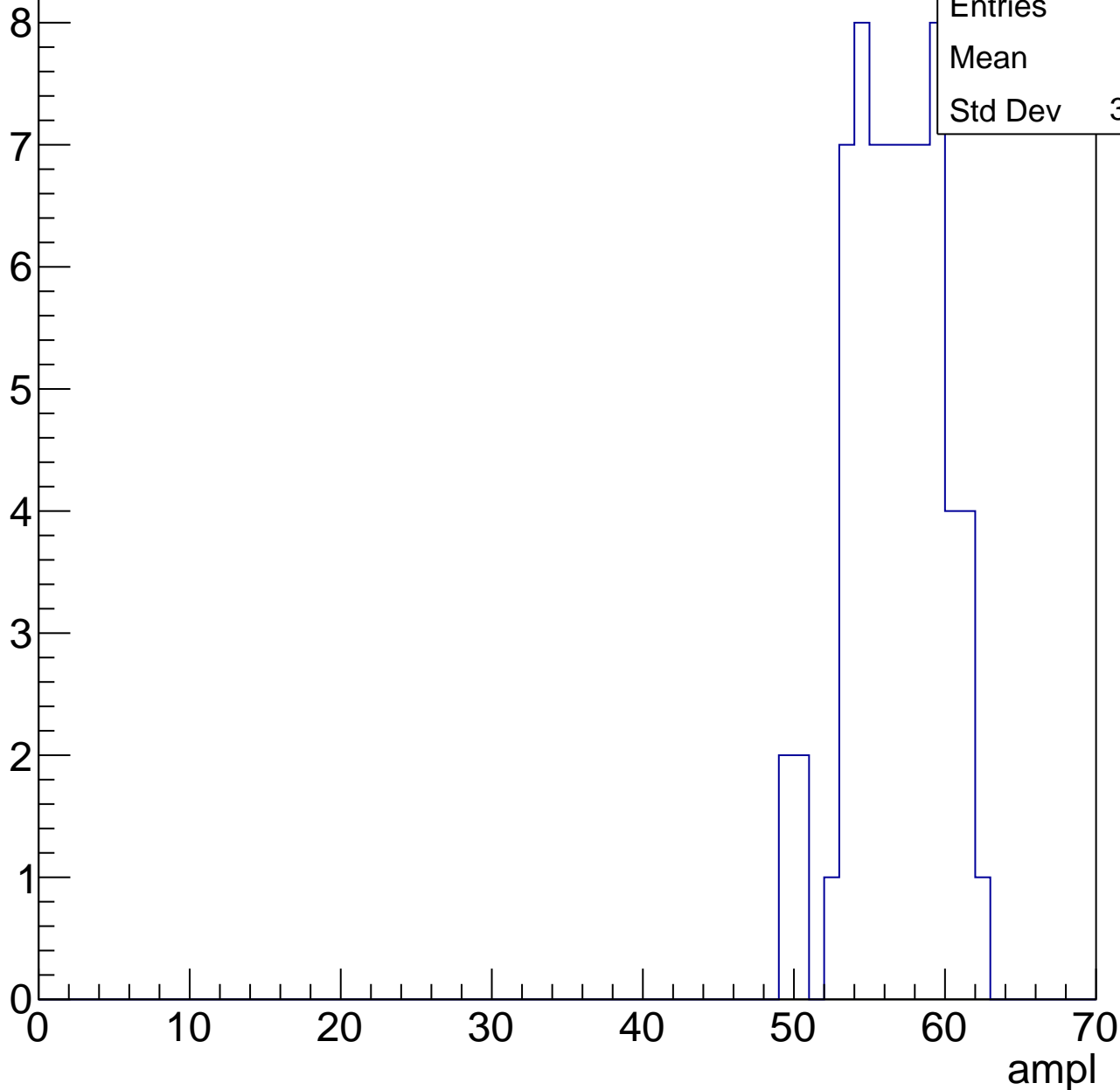


# B0L000S, U10-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.2
Std Dev	3.019

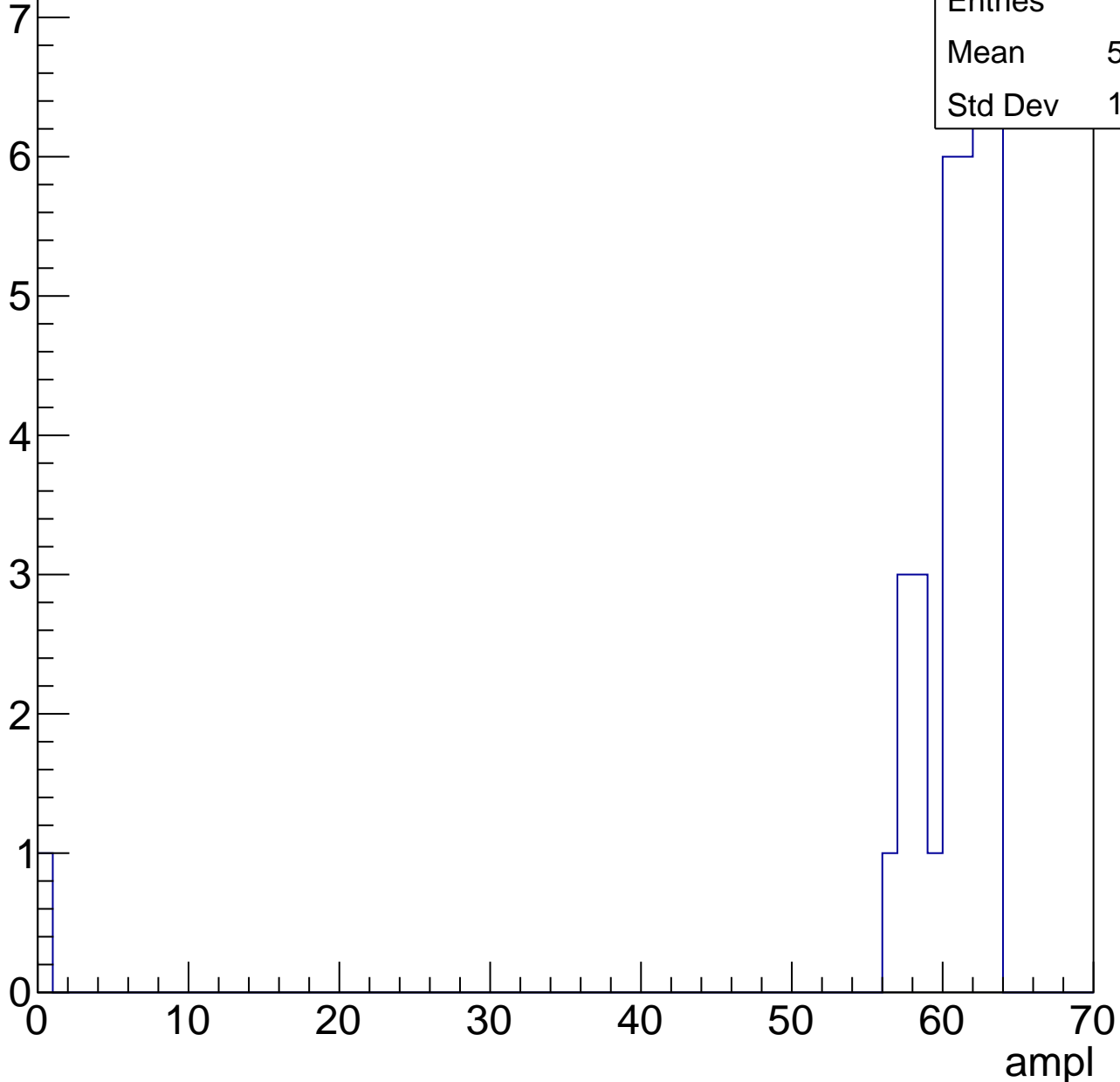


# B0L000S, U10-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.89
Std Dev	10.29



# B0L000S, U10-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch1, adc0

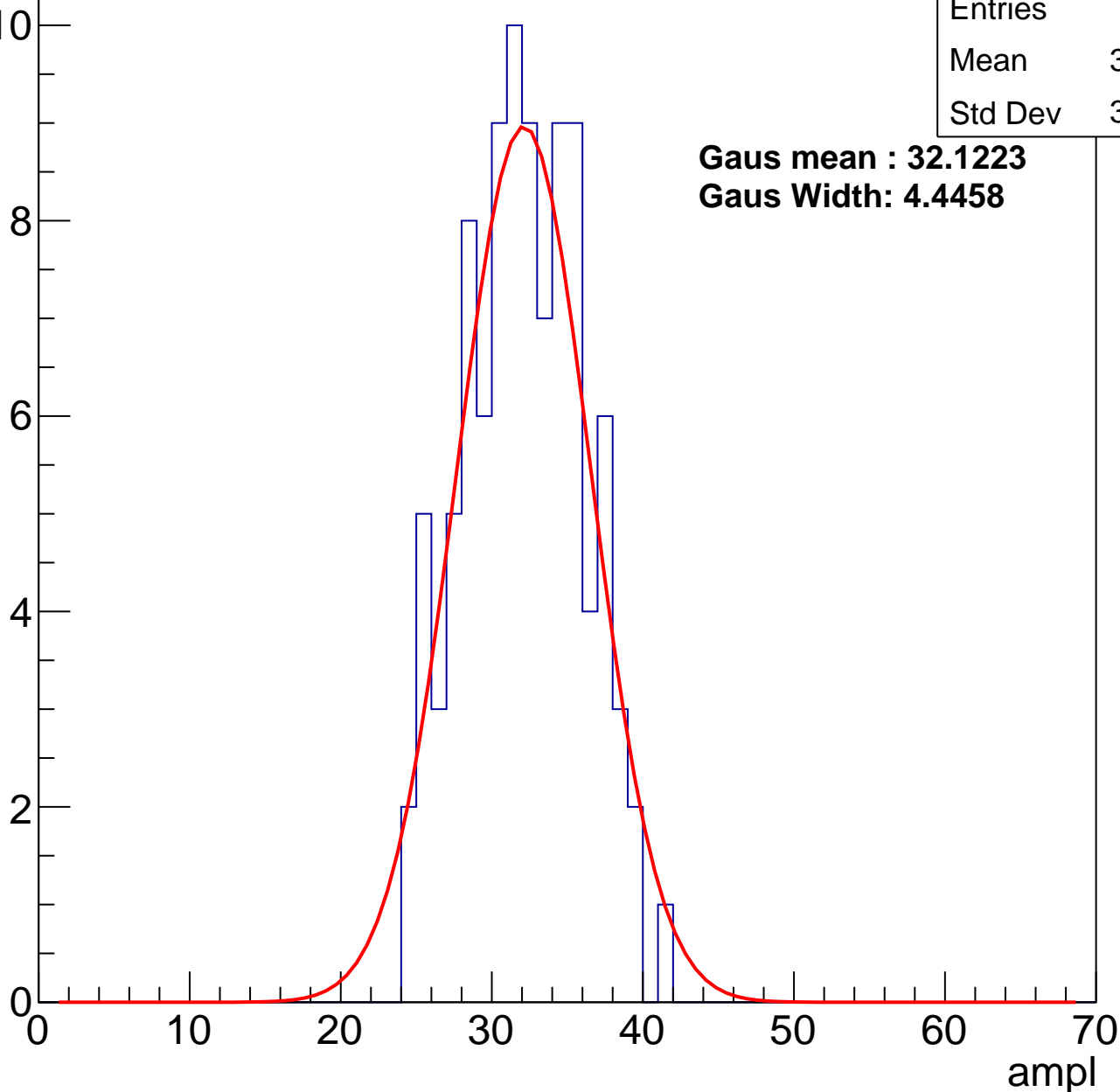
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	98
Mean	31.66
Std Dev	3.844

**Gaus mean : 32.1223**

**Gaus Width: 4.4458**



# B0L000S, U10-ch1, adc1

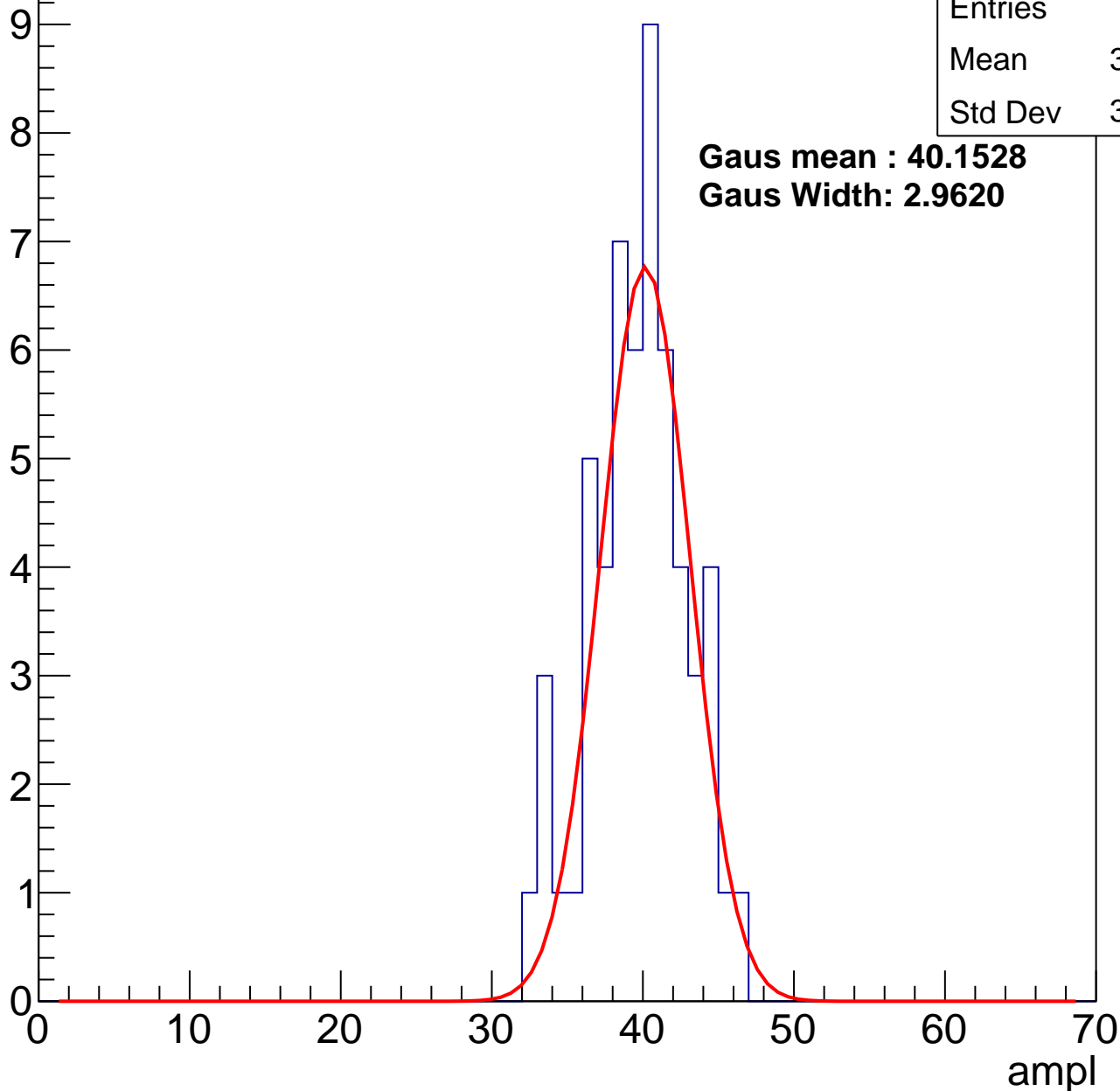
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	39.25
Std Dev	3.175

**Gaus mean : 40.1528**

**Gaus Width: 2.9620**



# B0L000S, U10-ch1, adc2

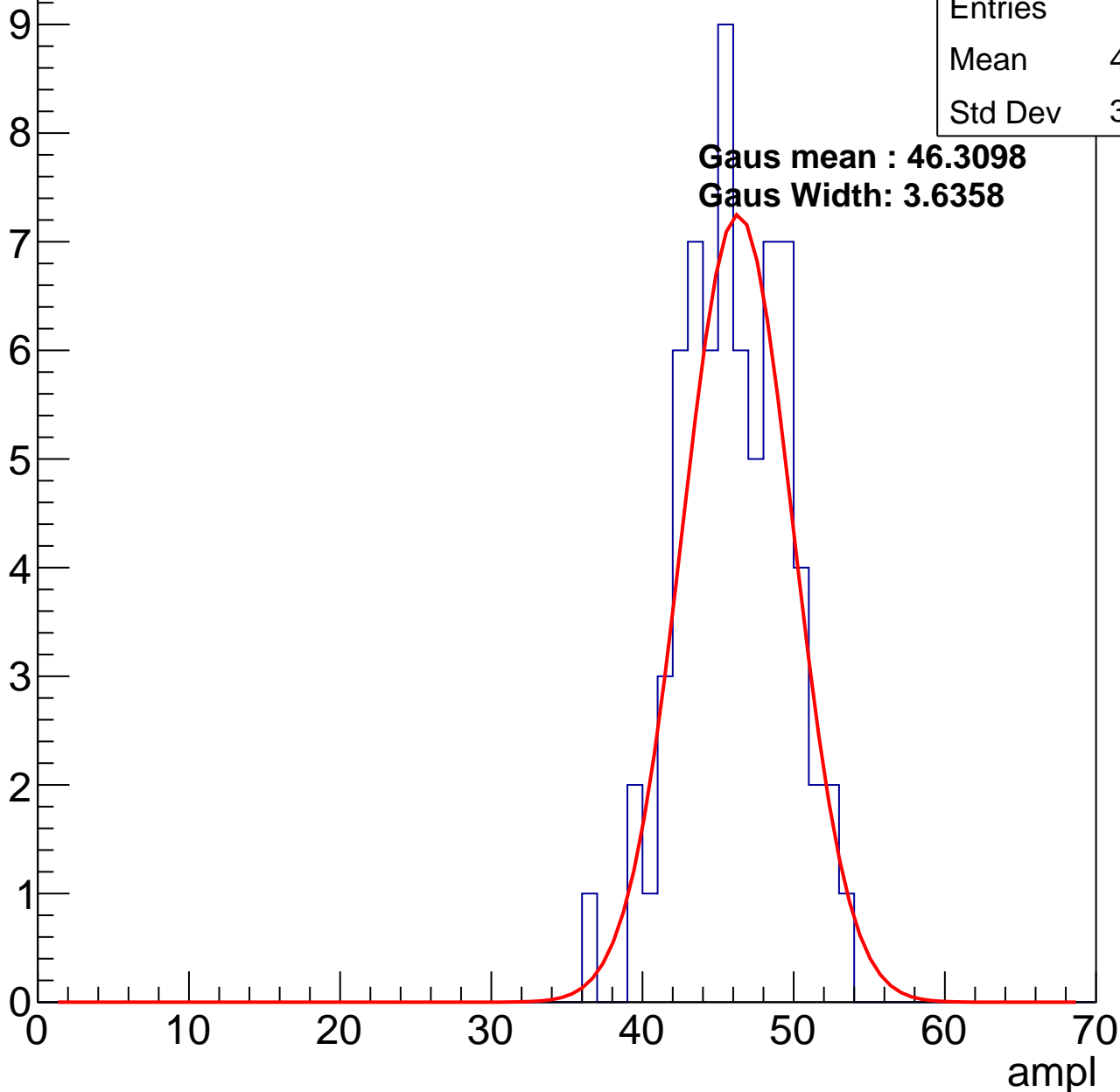
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	45.62
Std Dev	3.452

**Gaus mean : 46.3098**

**Gaus Width: 3.6358**

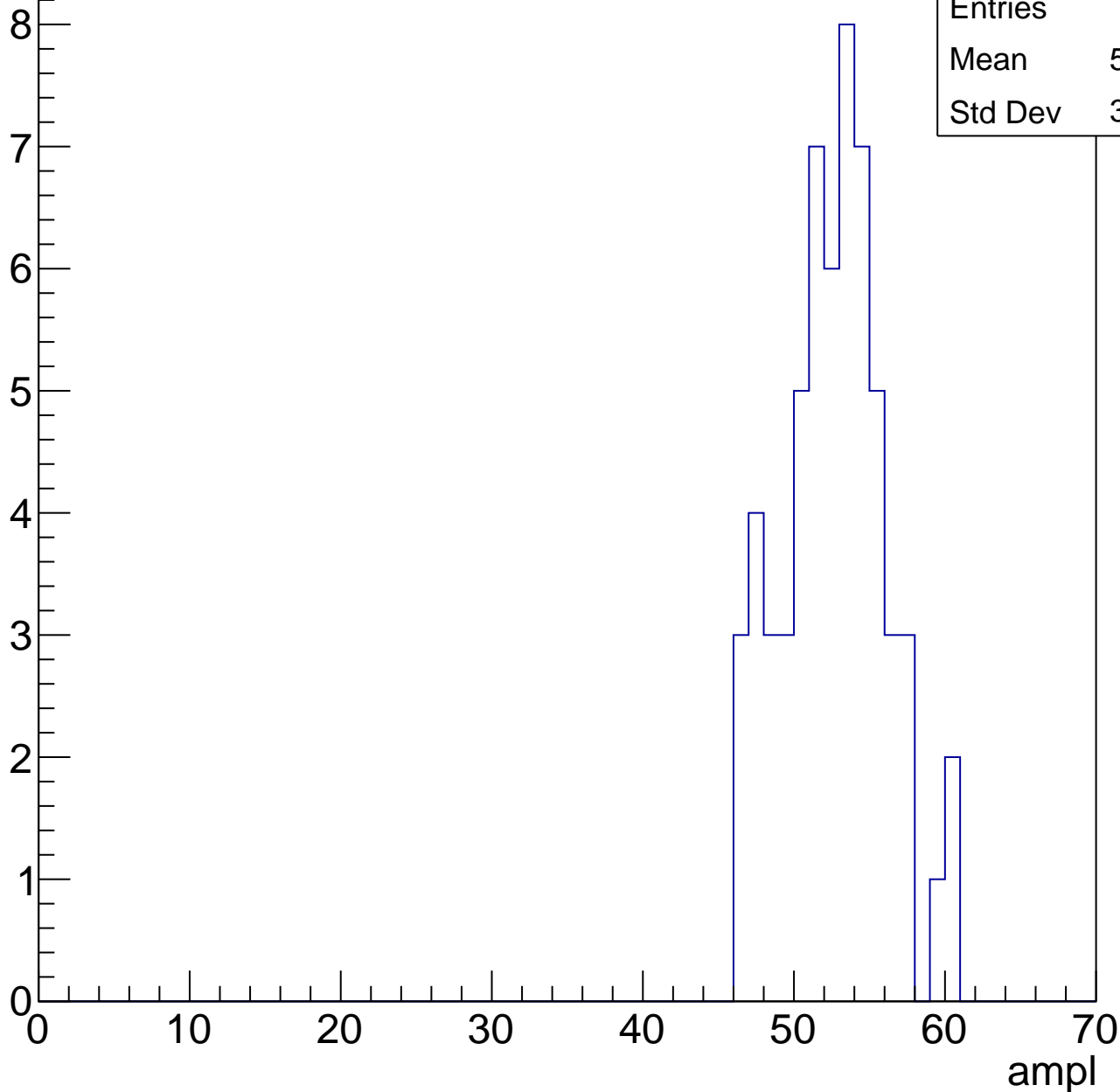


# B0L000S, U10-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

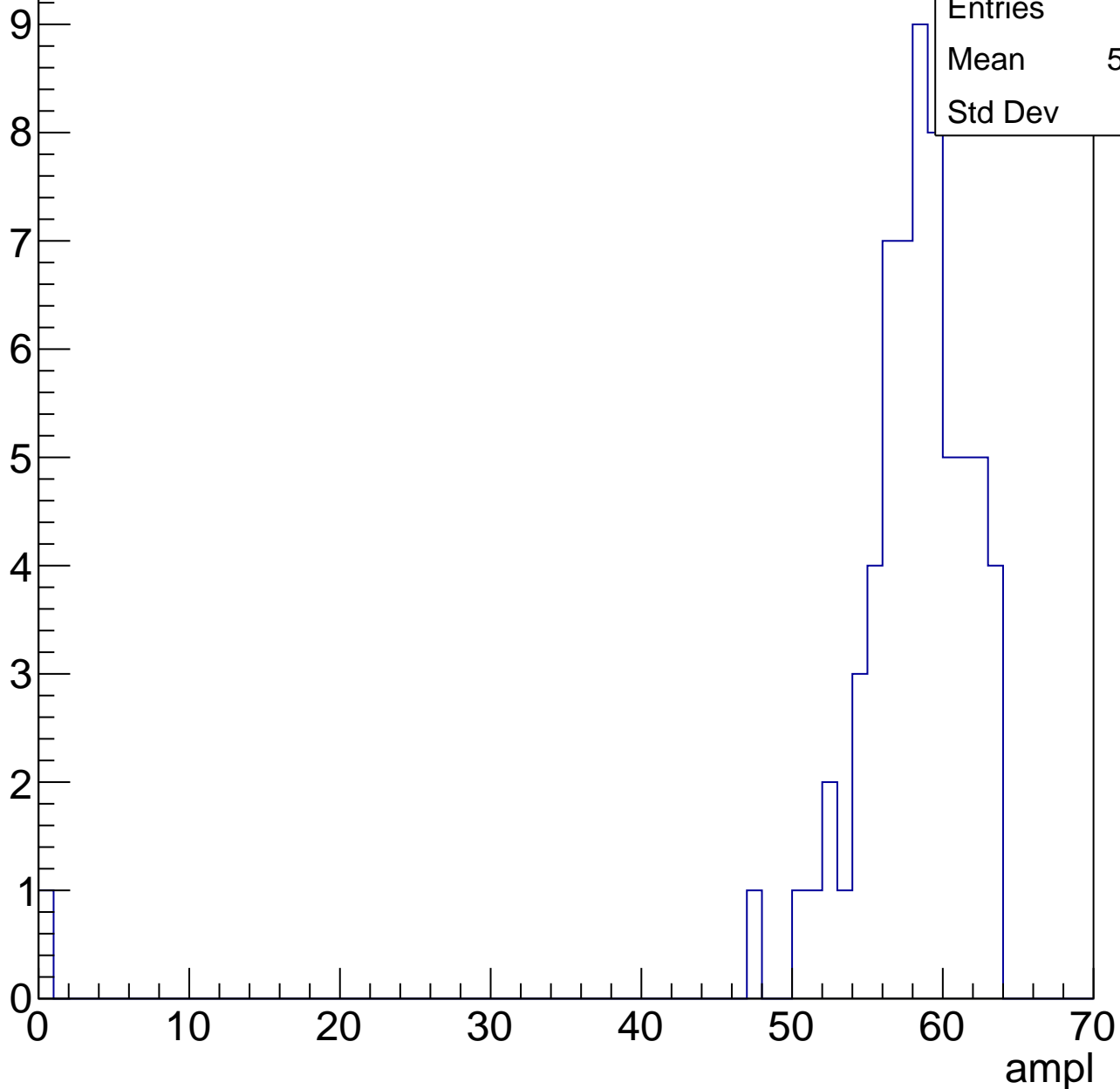
Entries	60
Mean	52.18
Std Dev	3.389



# B0L000S, U10-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

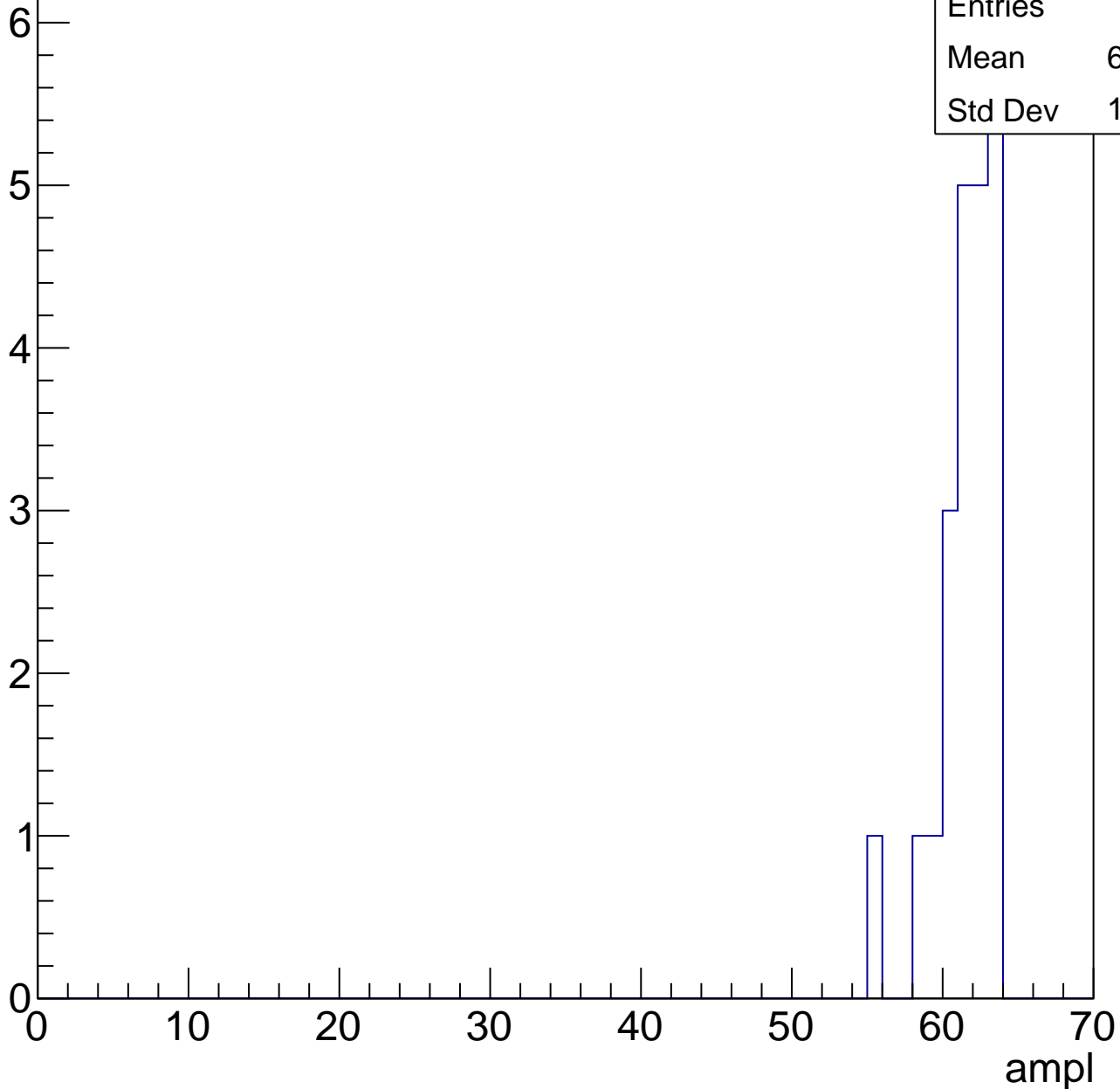


# B0L000S, U10-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	61.14
Std Dev	1.914



# B0L000S, U10-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U10-ch2, adc0

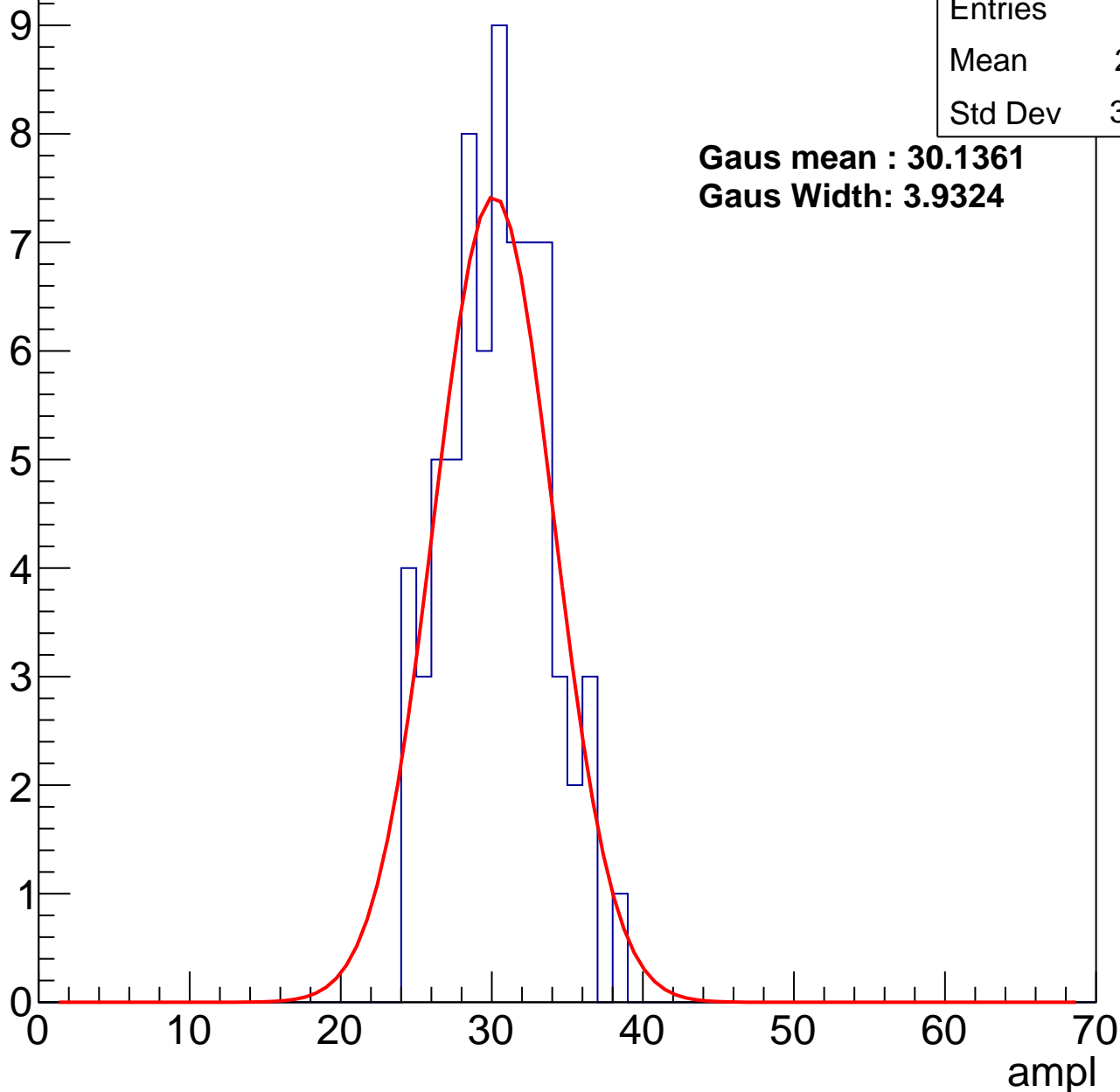
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.91
Std Dev	3.272

**Gaus mean : 30.1361**

**Gaus Width: 3.9324**



# B0L000S, U10-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.87
Std Dev	3.528

**Gaus mean : 37.9015**

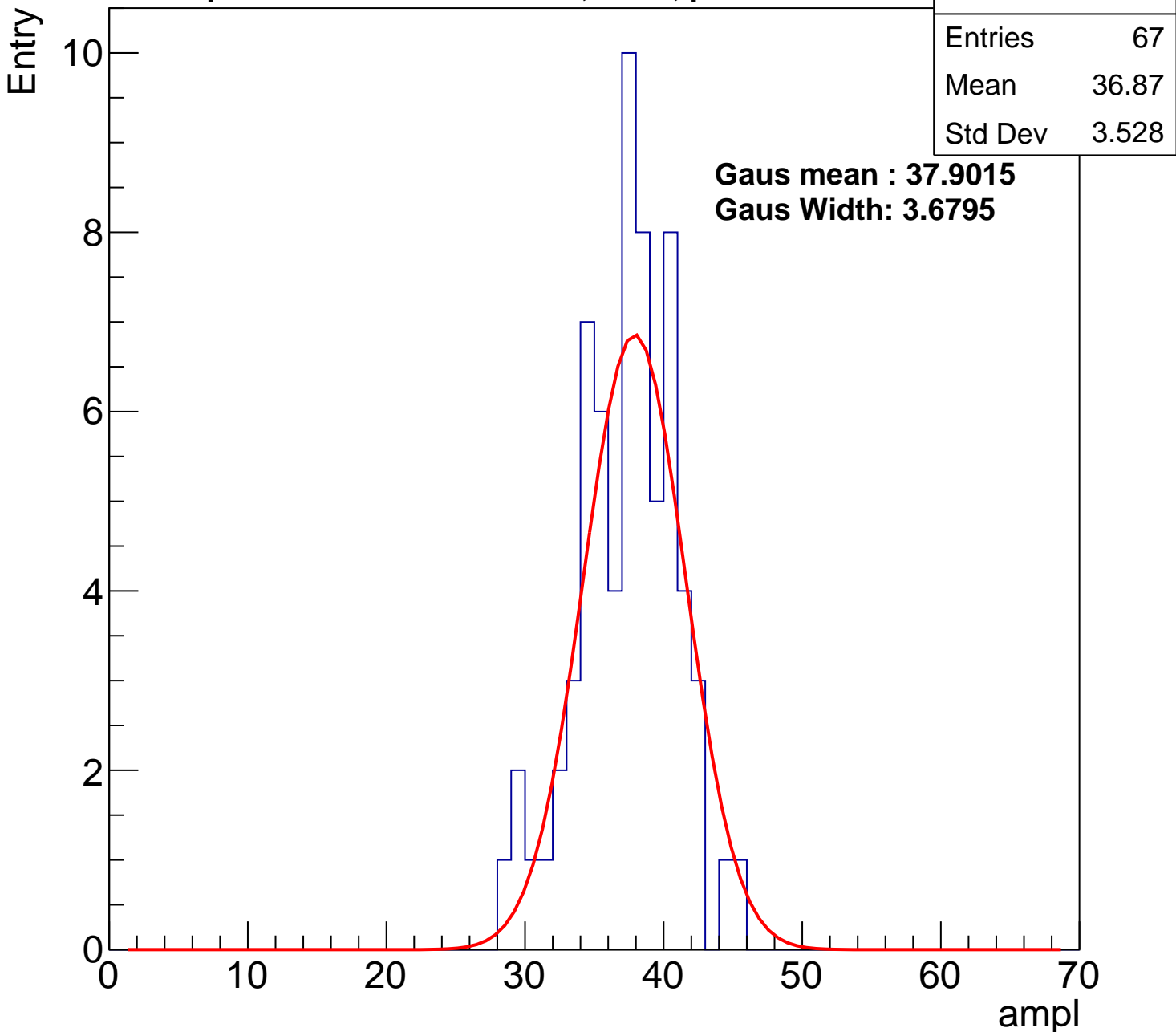
**Gaus Width: 3.6795**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch2, adc2

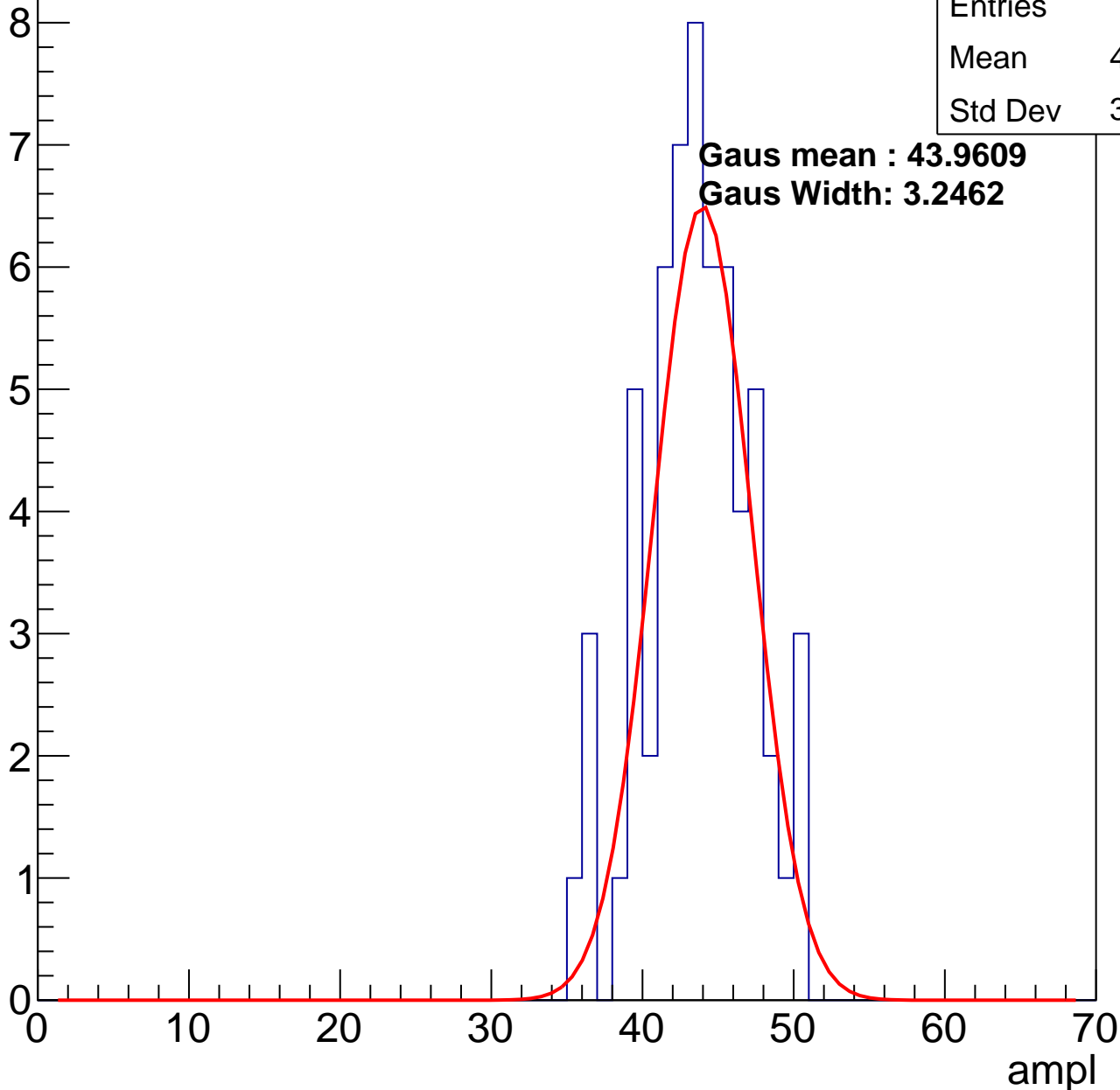
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.13
Std Dev	3.519

**Gaus mean : 43.9609**

**Gaus Width: 3.2462**

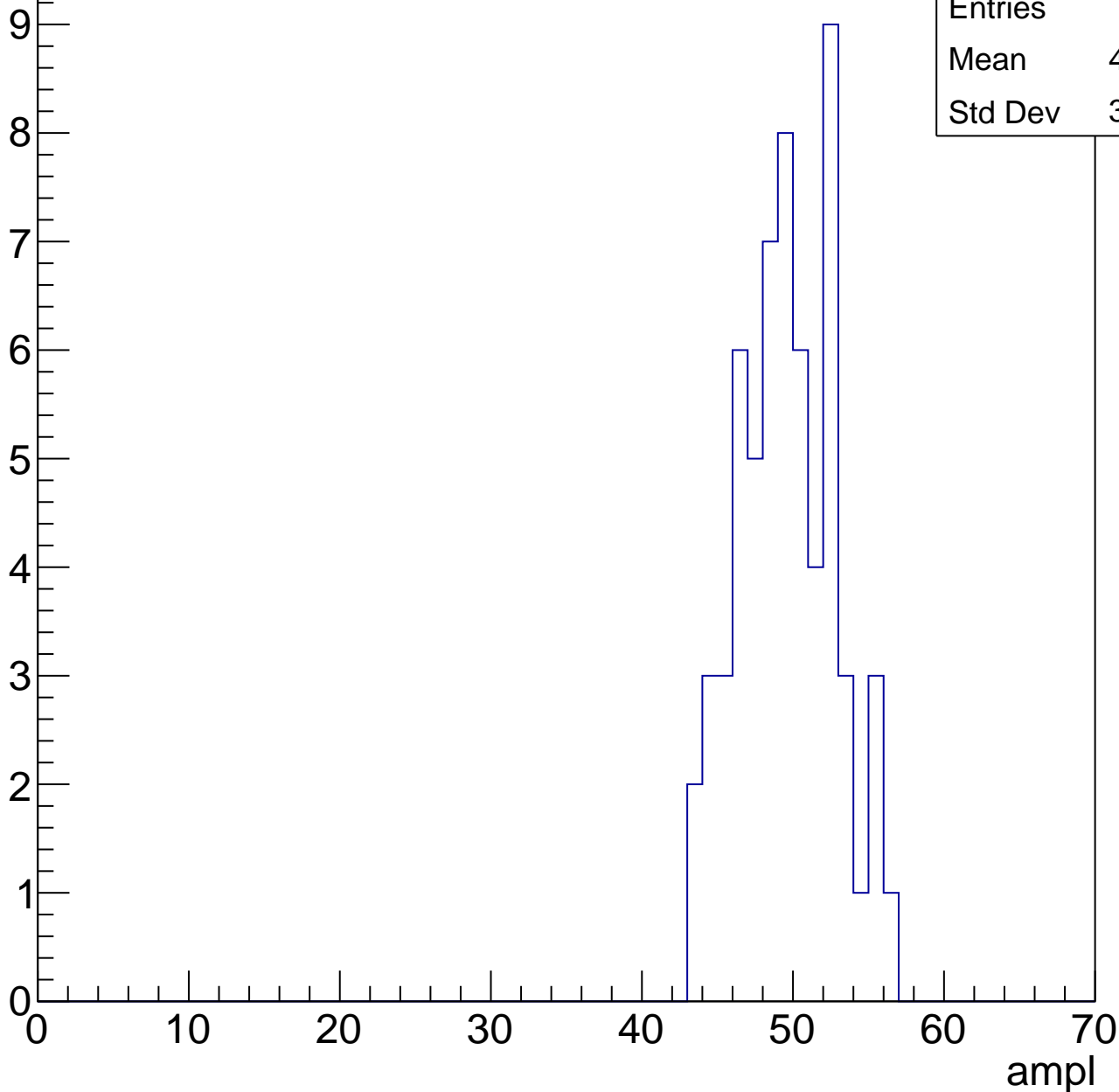


# B0L000S, U10-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	49.15
Std Dev	3.156

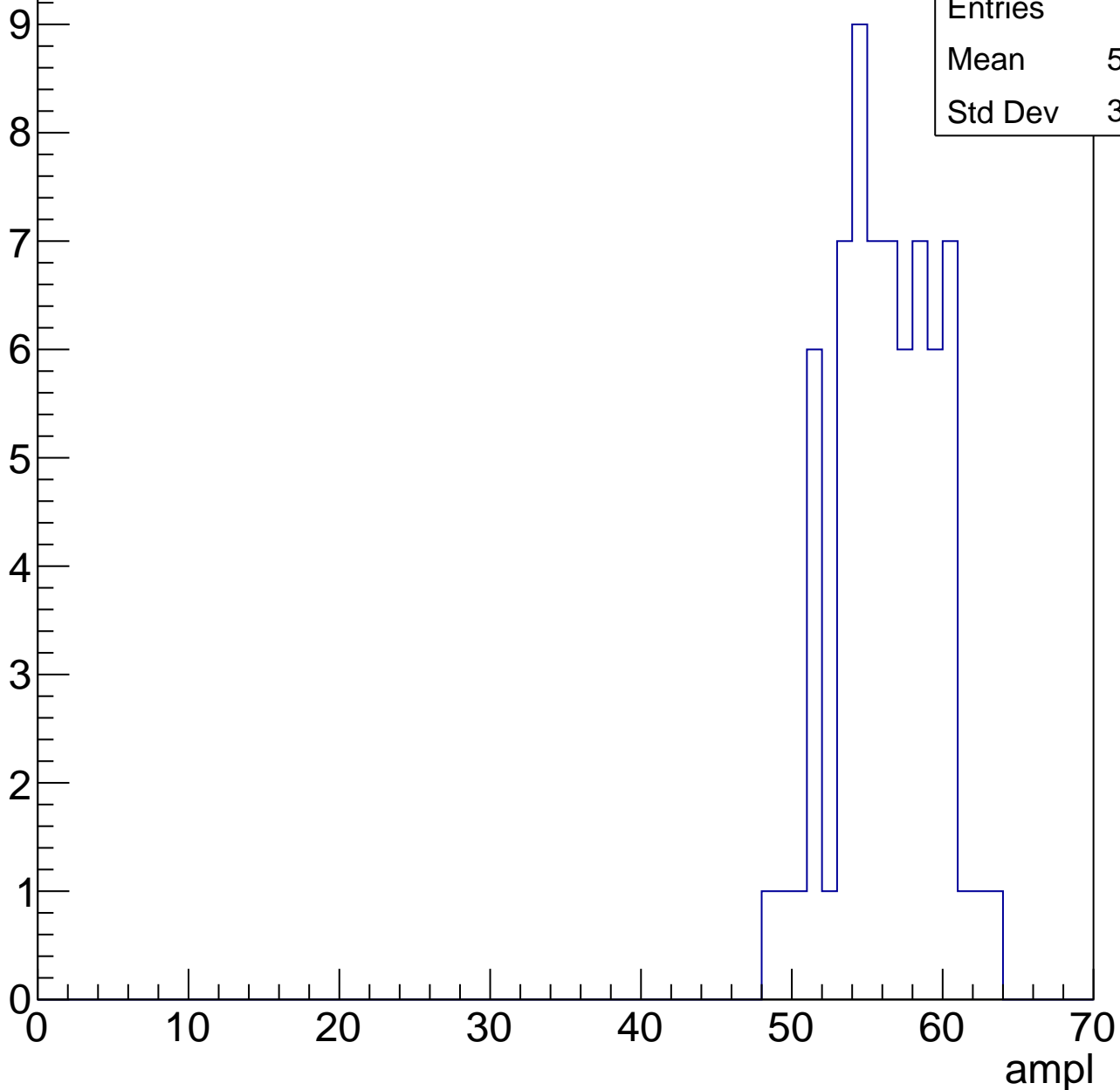


# B0L000S, U10-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

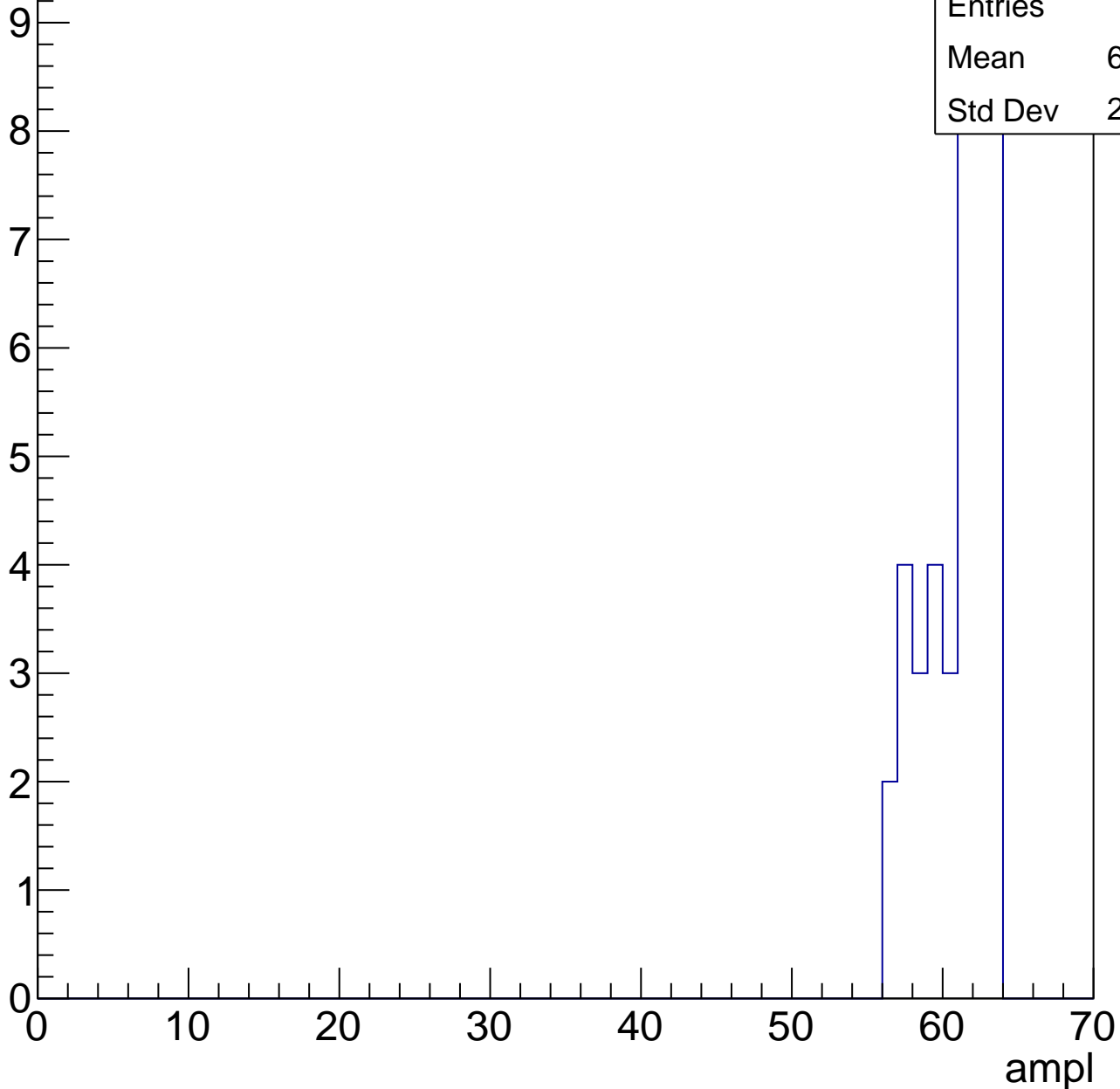
Entries	69
Mean	55.75
Std Dev	3.246



# B0L000S, U10-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch3, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	31.16
Std Dev	3.095

**Gaus mean : 31.3417**

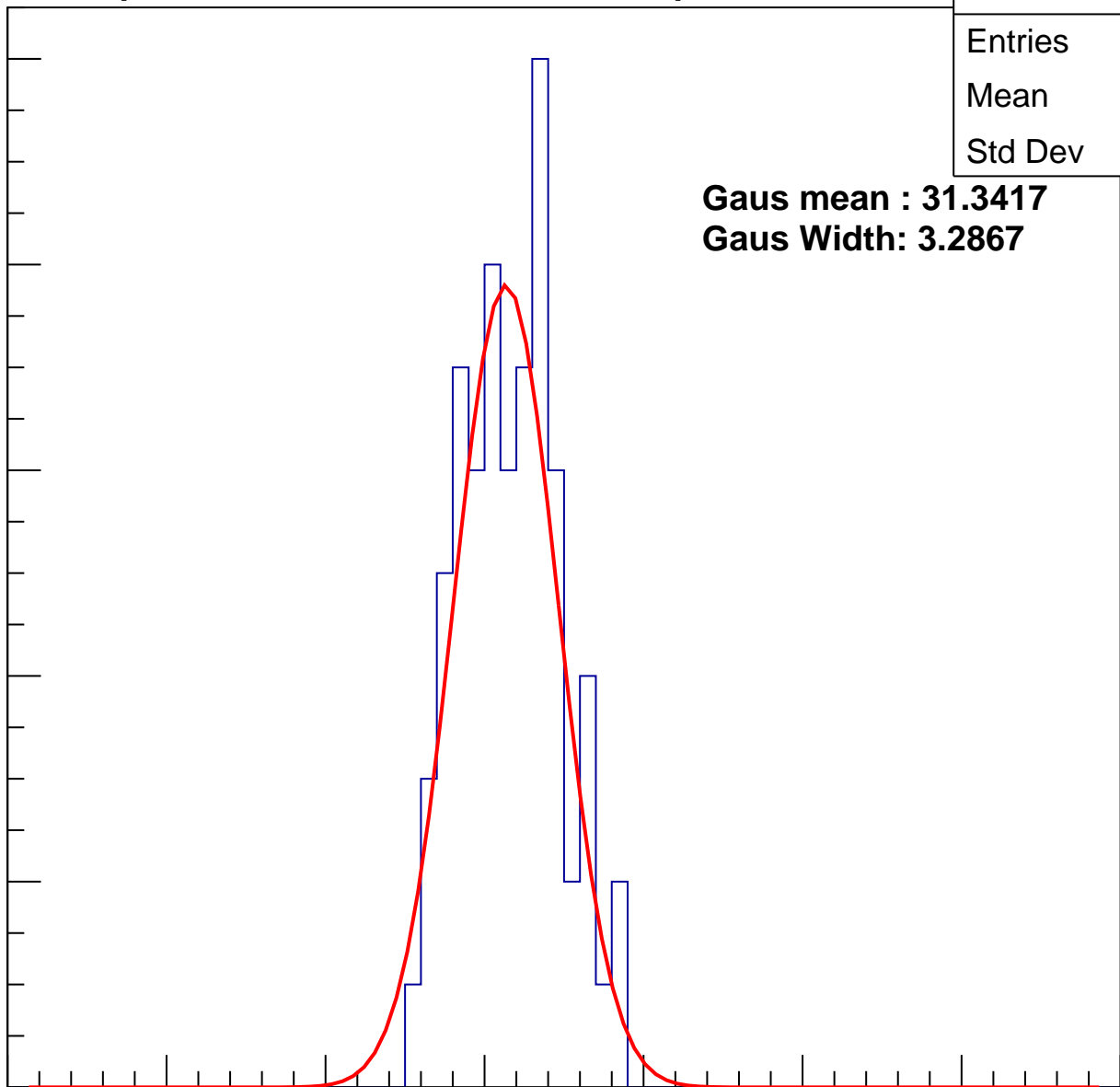
**Gaus Width: 3.2867**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch3, adc1

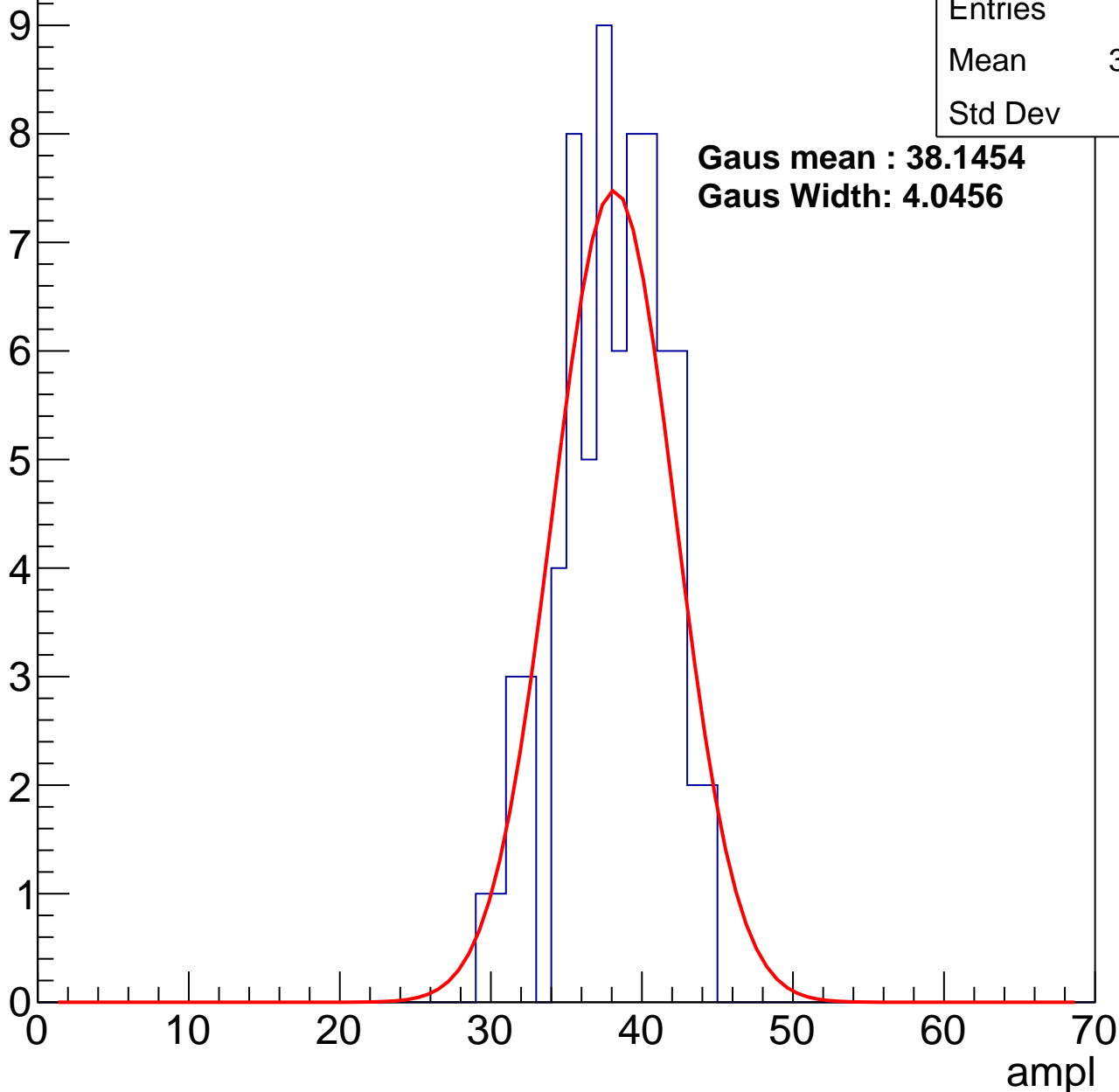
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	37.62
Std Dev	3.47

**Gaus mean : 38.1454**

**Gaus Width: 4.0456**



# B0L000S, U10-ch3, adc2

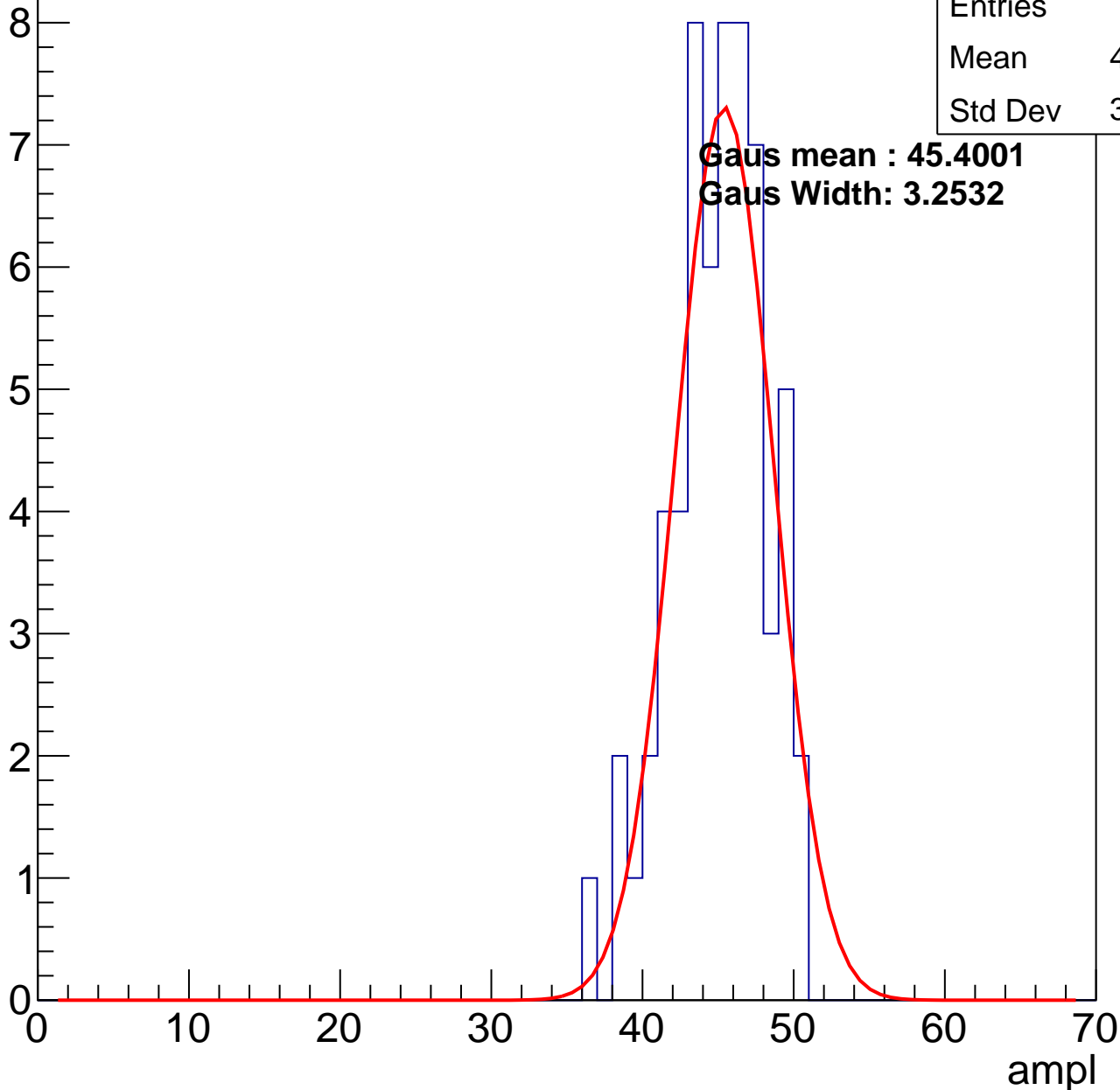
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.54
Std Dev	3.092

**Gaus mean : 45.4001**

**Gaus Width: 3.2532**



# B0L000S, U10-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

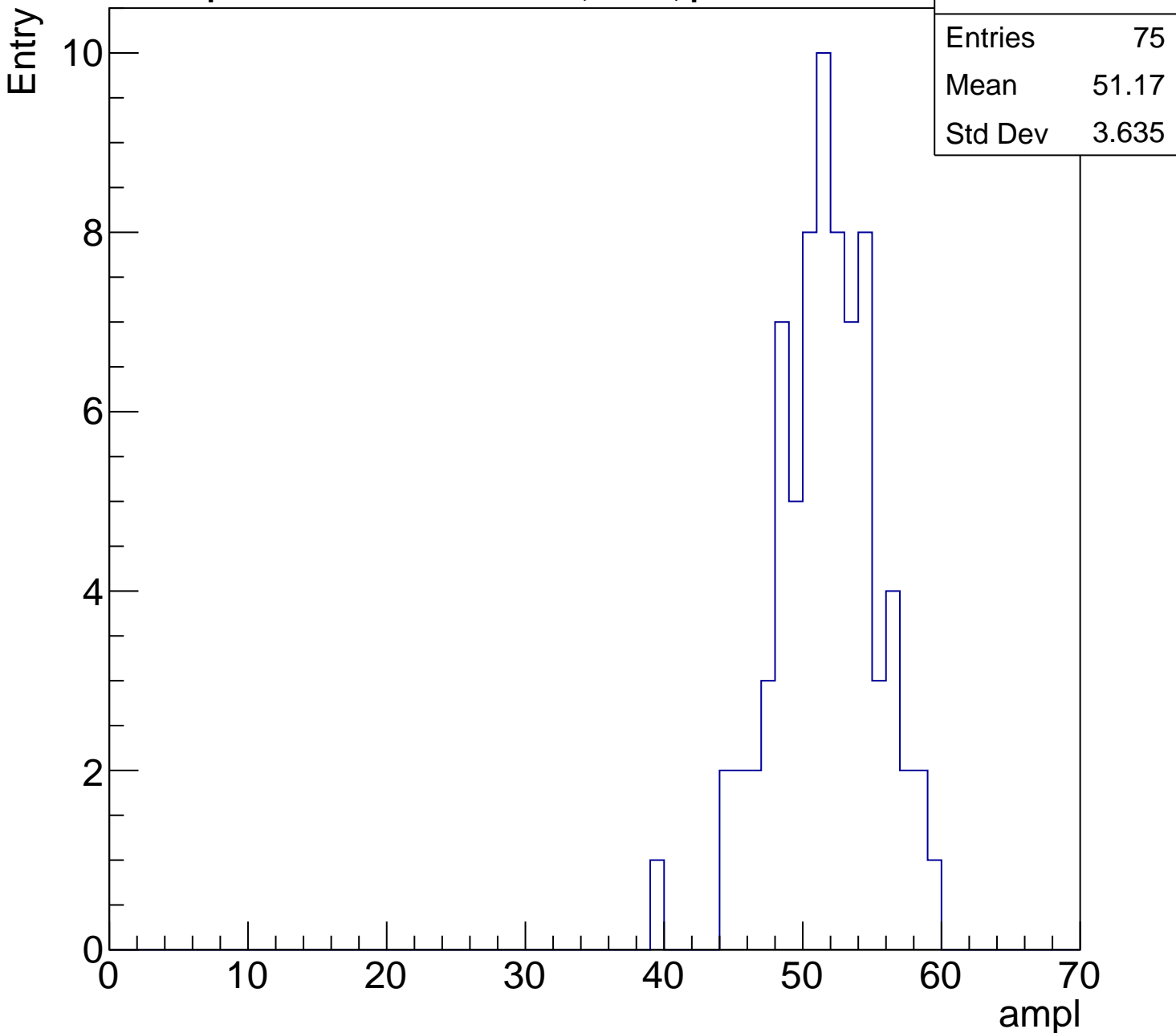
Entries	75
Mean	51.17
Std Dev	3.635

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

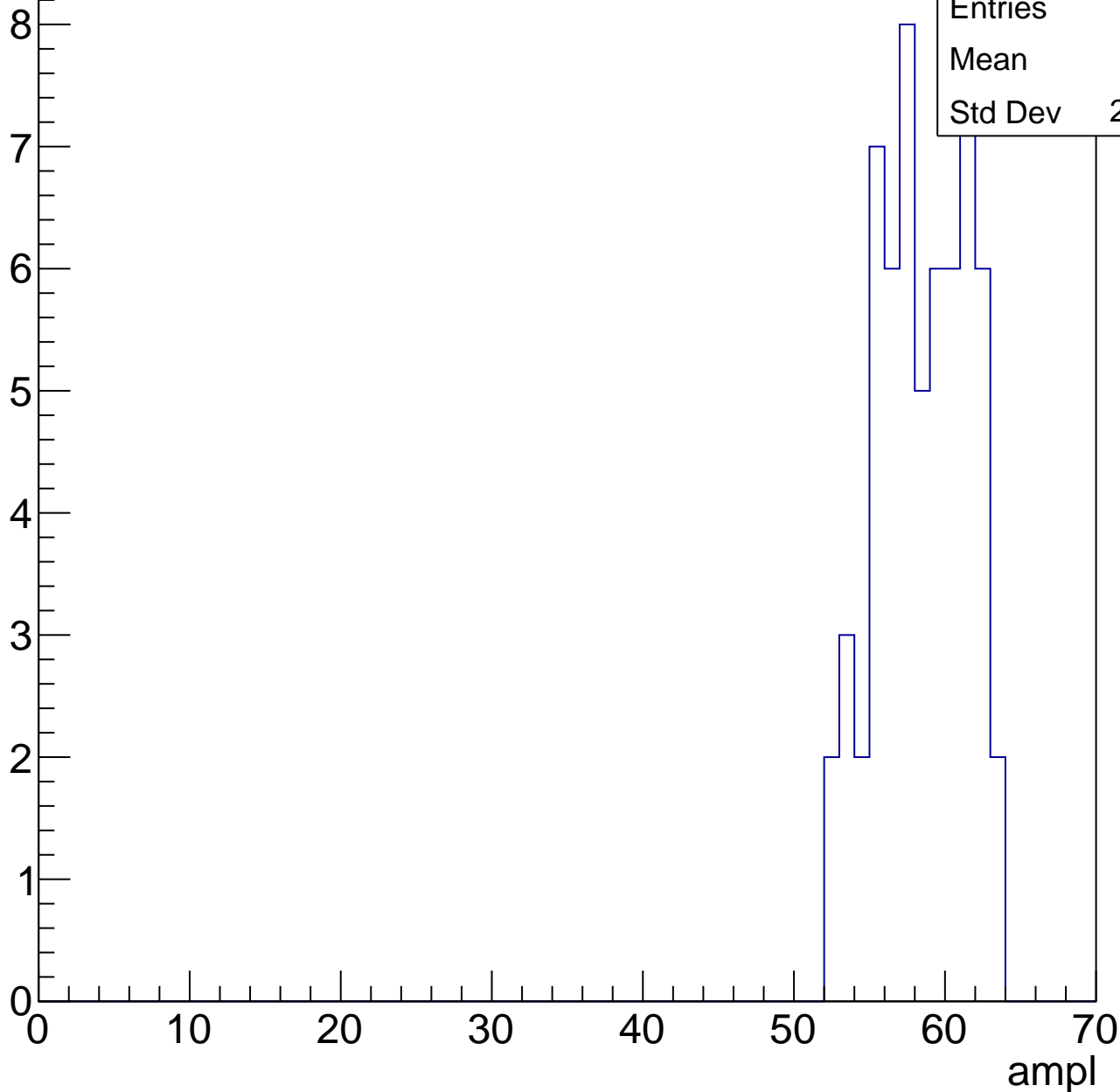


# B0L000S, U10-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	58
Std Dev	2.925

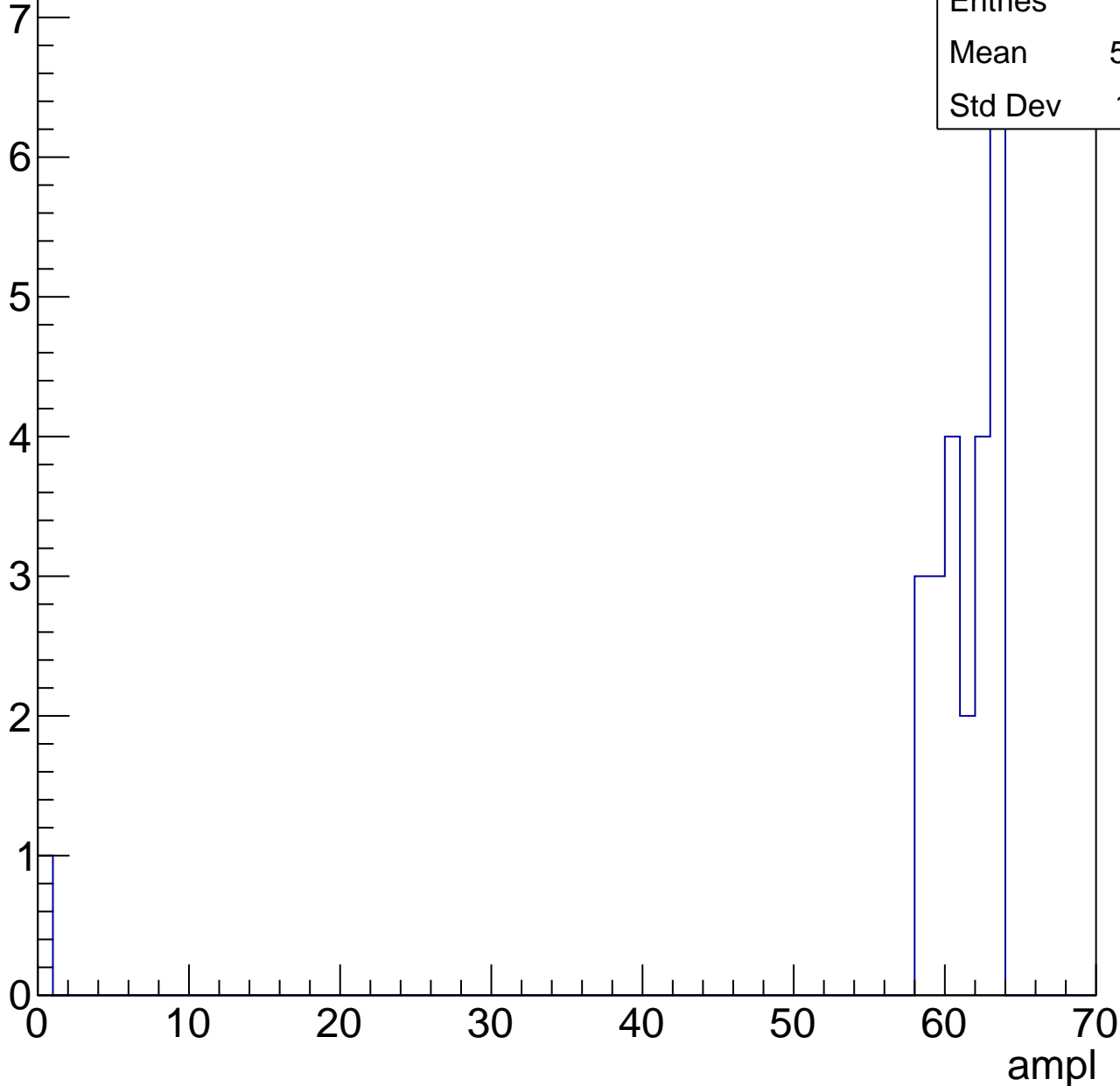


# B0L000S, U10-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	24
Mean	58.42
Std Dev	12.31



# B0L000S, U10-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

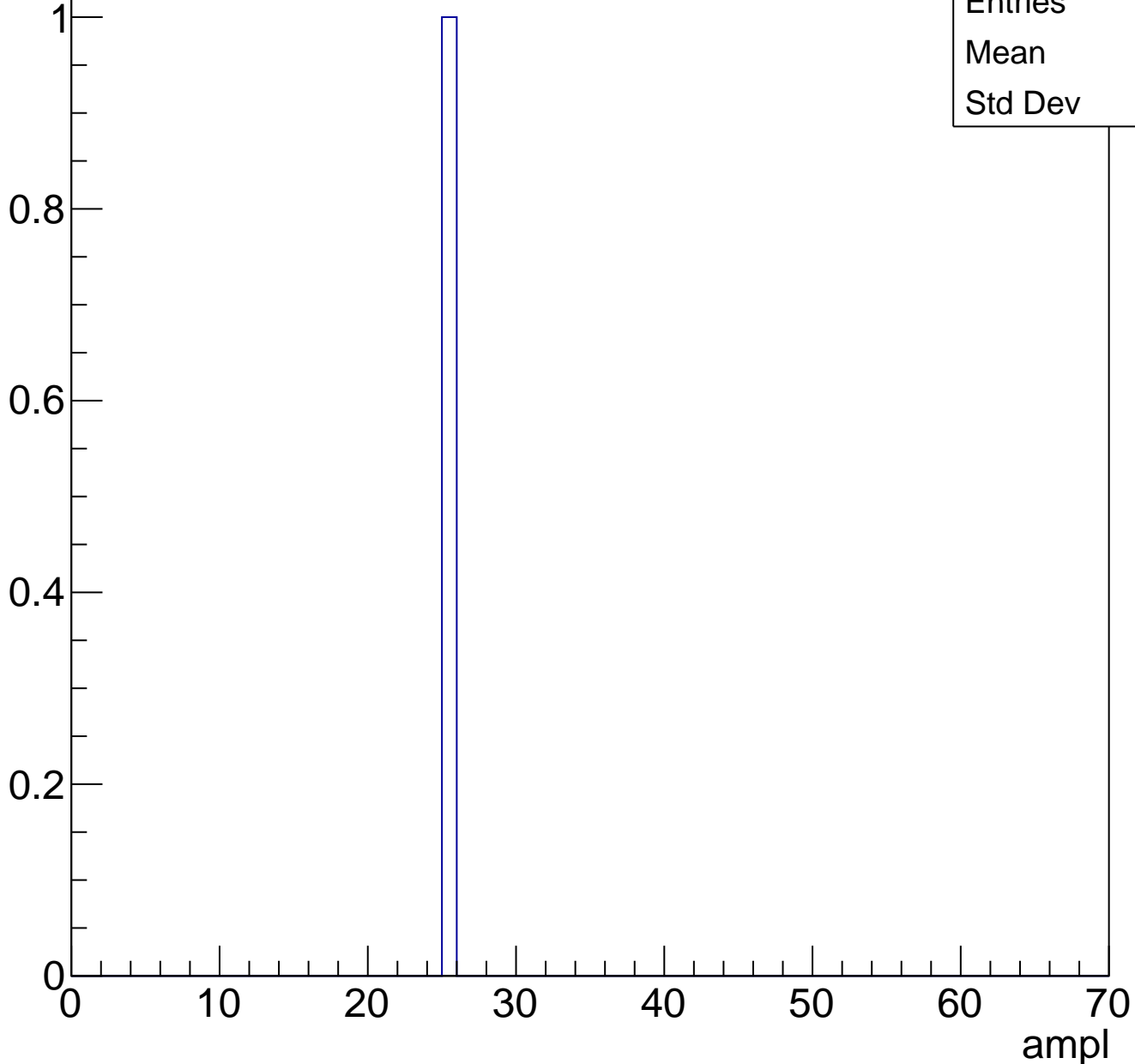




# B0L000S, U10-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch4, adc0

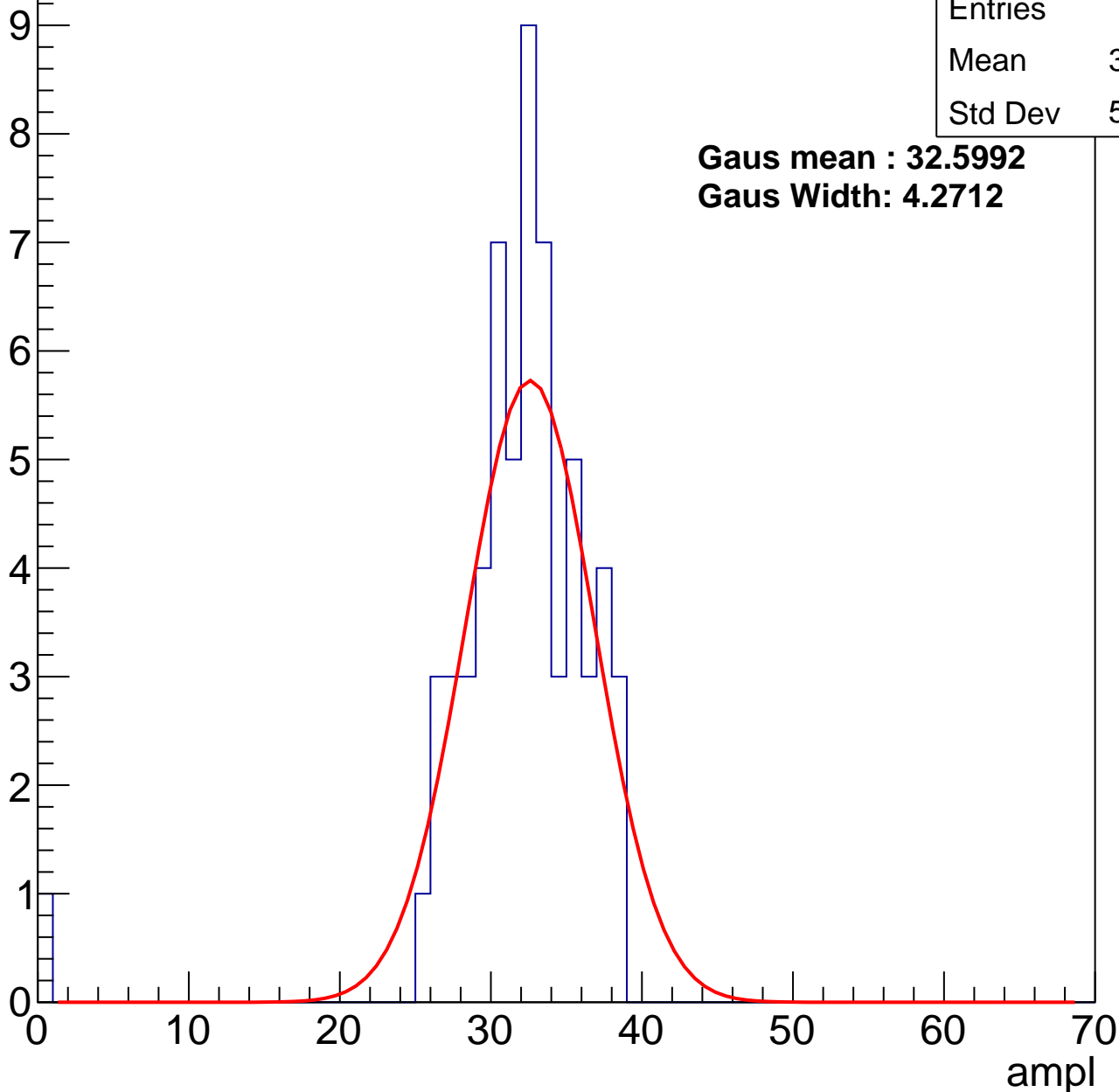
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	31.39
Std Dev	5.233

**Gaus mean : 32.5992**

**Gaus Width: 4.2712**



# B0L000S, U10-ch4, adc1

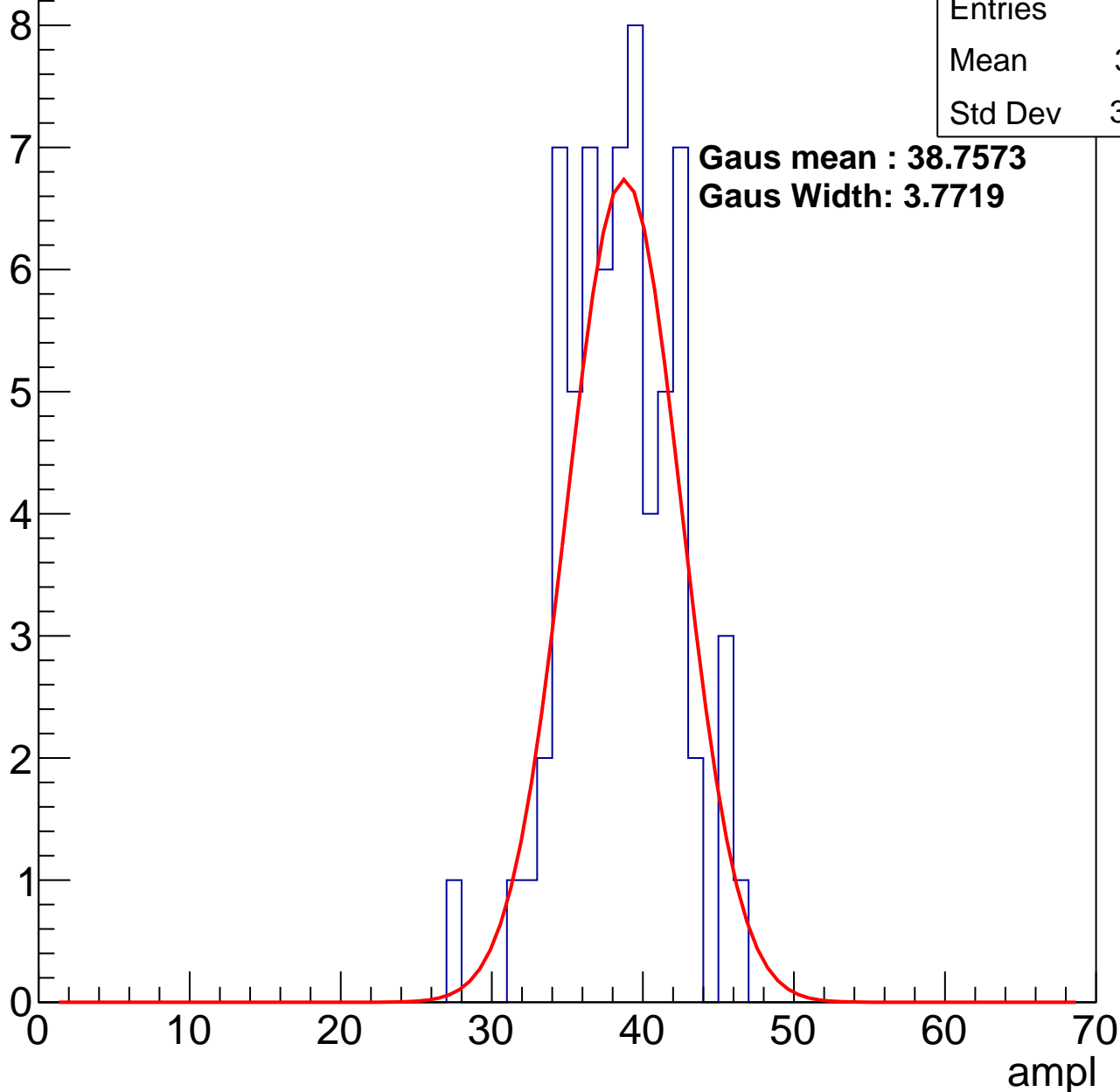
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	38.01
Std Dev	3.643

**Gaus mean : 38.7573**

**Gaus Width: 3.7719**



# B0L000S, U10-ch4, adc2

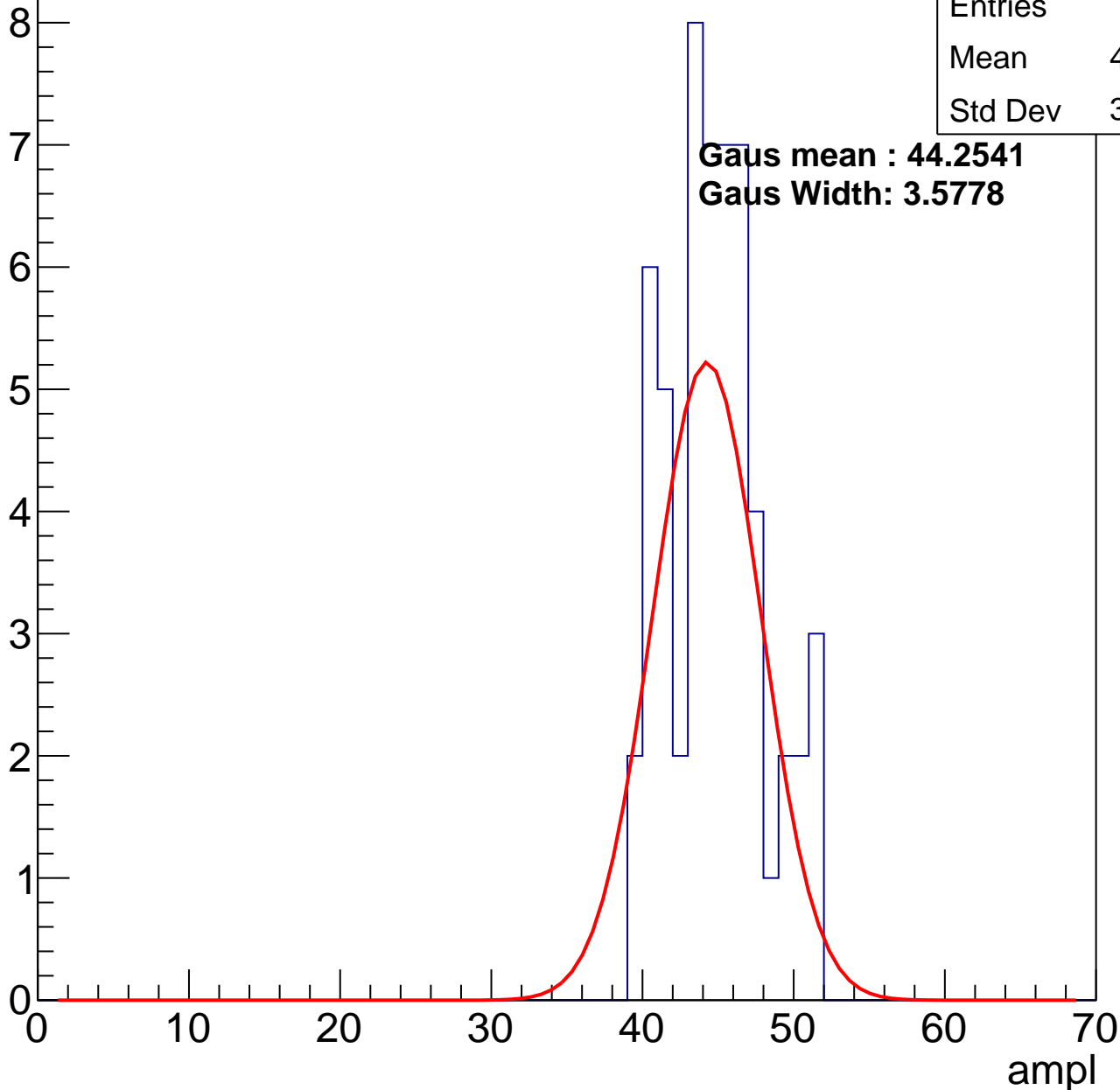
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	44.34
Std Dev	3.153

**Gaus mean : 44.2541**

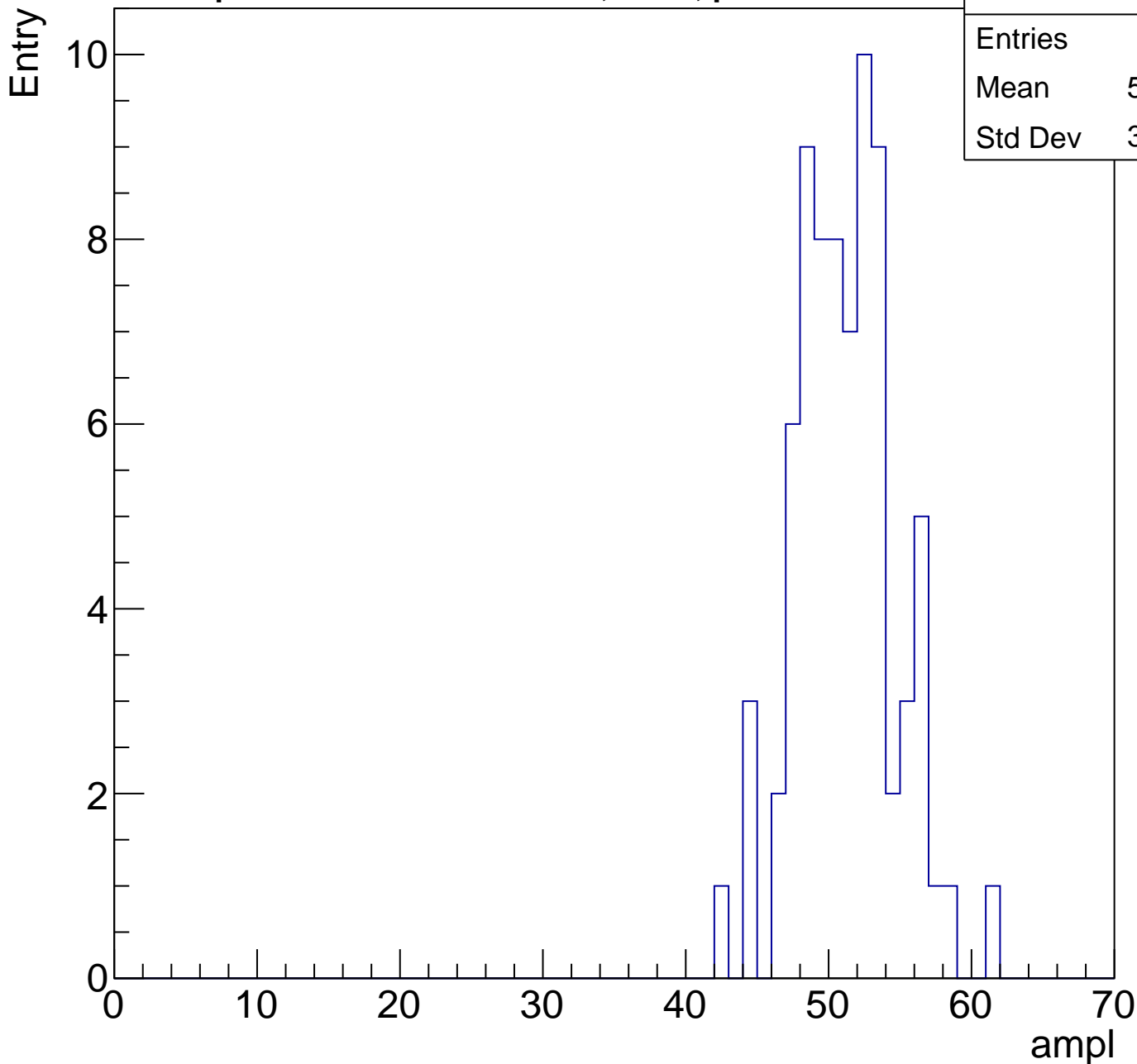
**Gaus Width: 3.5778**



# B0L000S, U10-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	50.72
Std Dev	3.463



# B0L000S, U10-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 56.93

Std Dev 2.29

0

10

20

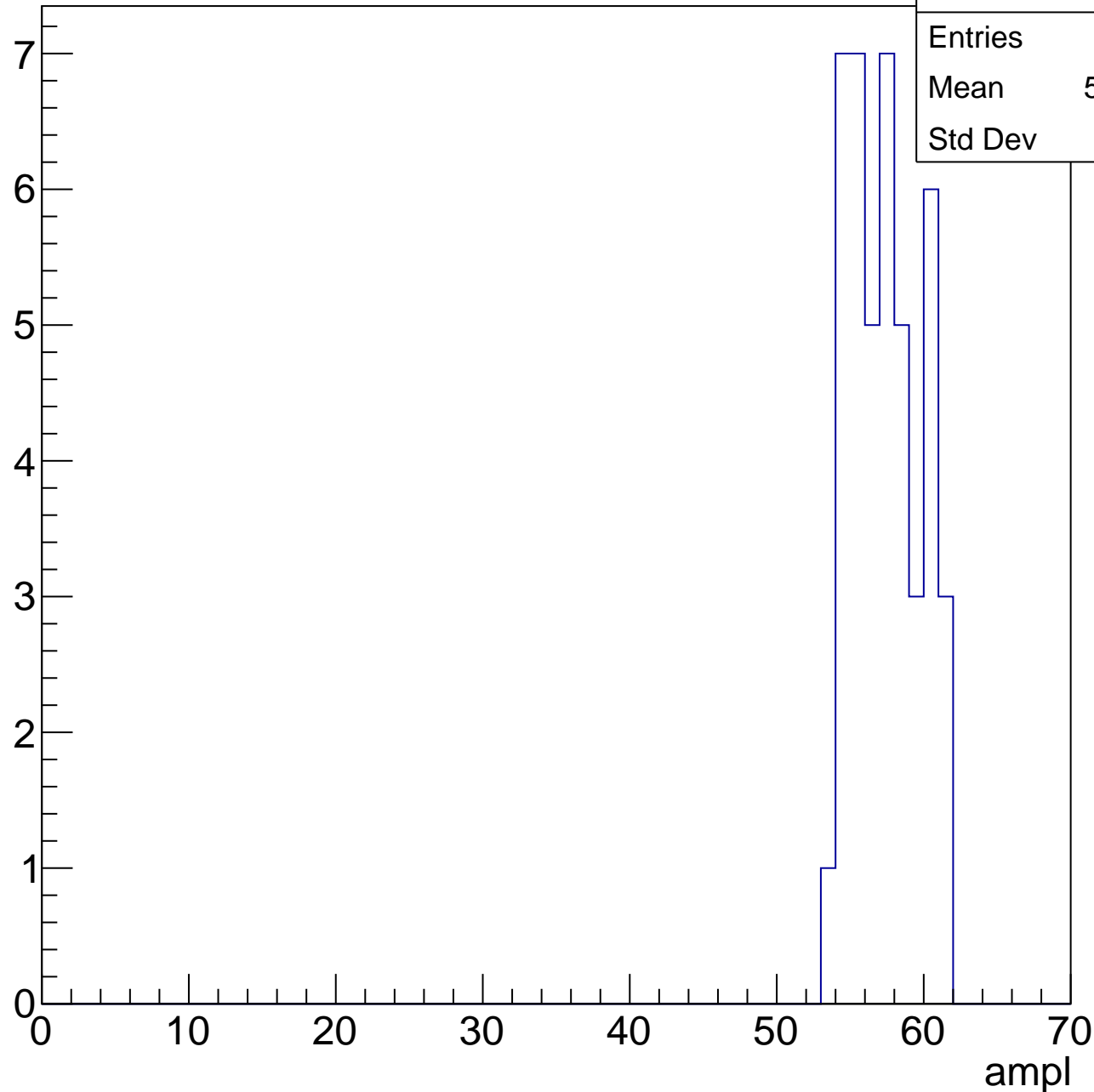
30

40

50

60

ampl

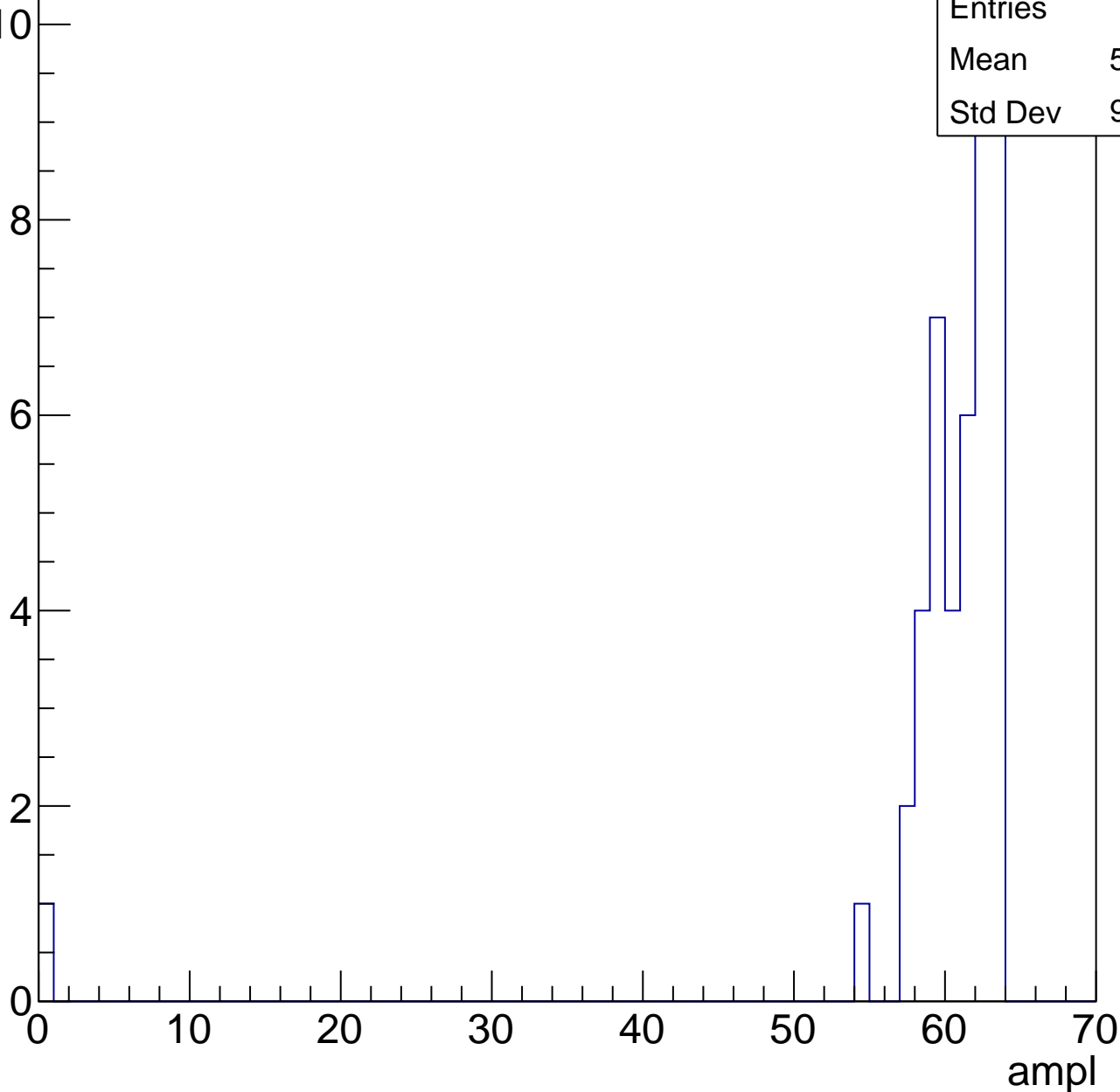


# B0L000S, U10-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

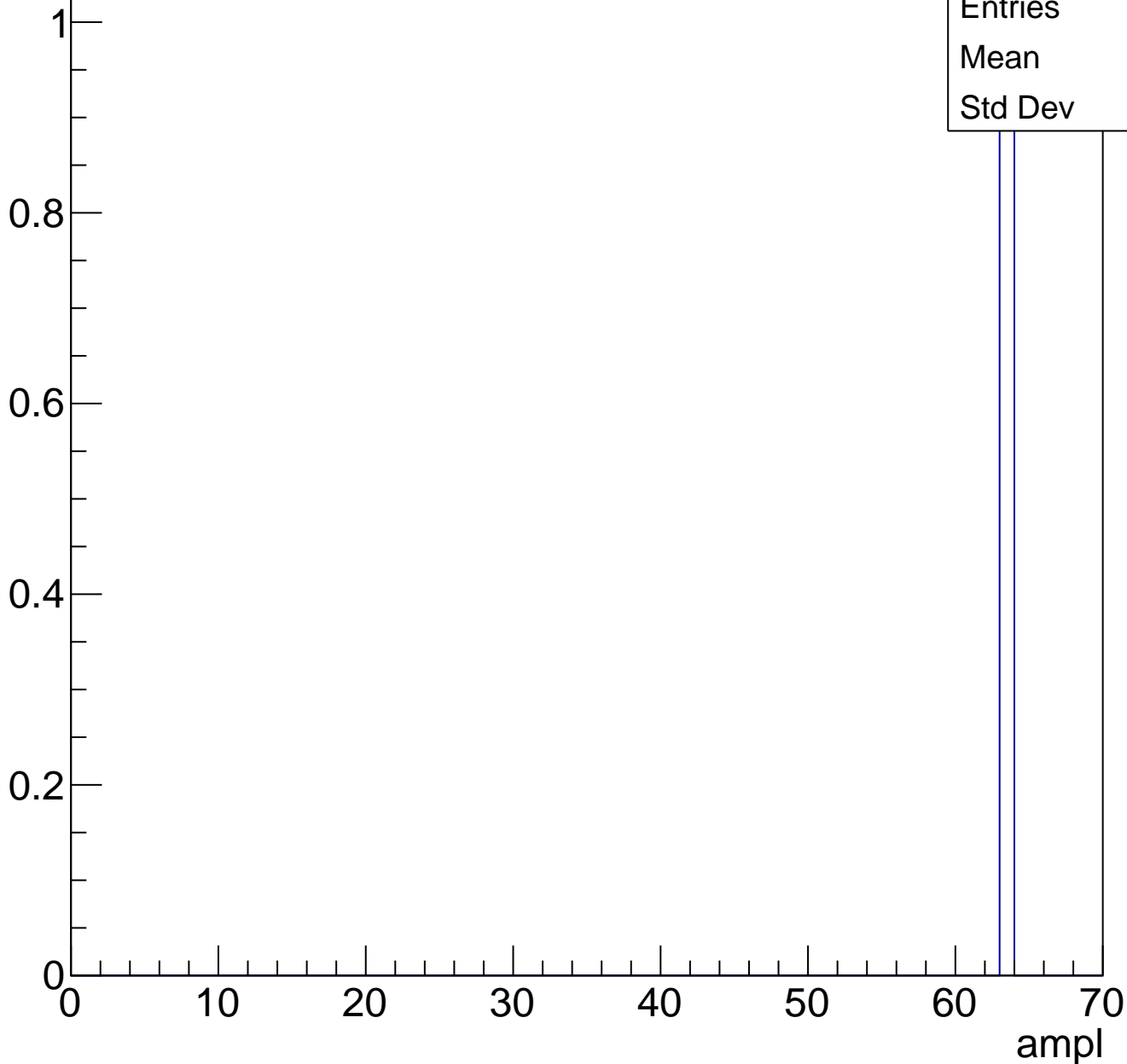
Entries	44
Mean	59.23
Std Dev	9.266



# B0L000S, U10-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U10-ch5, adc0

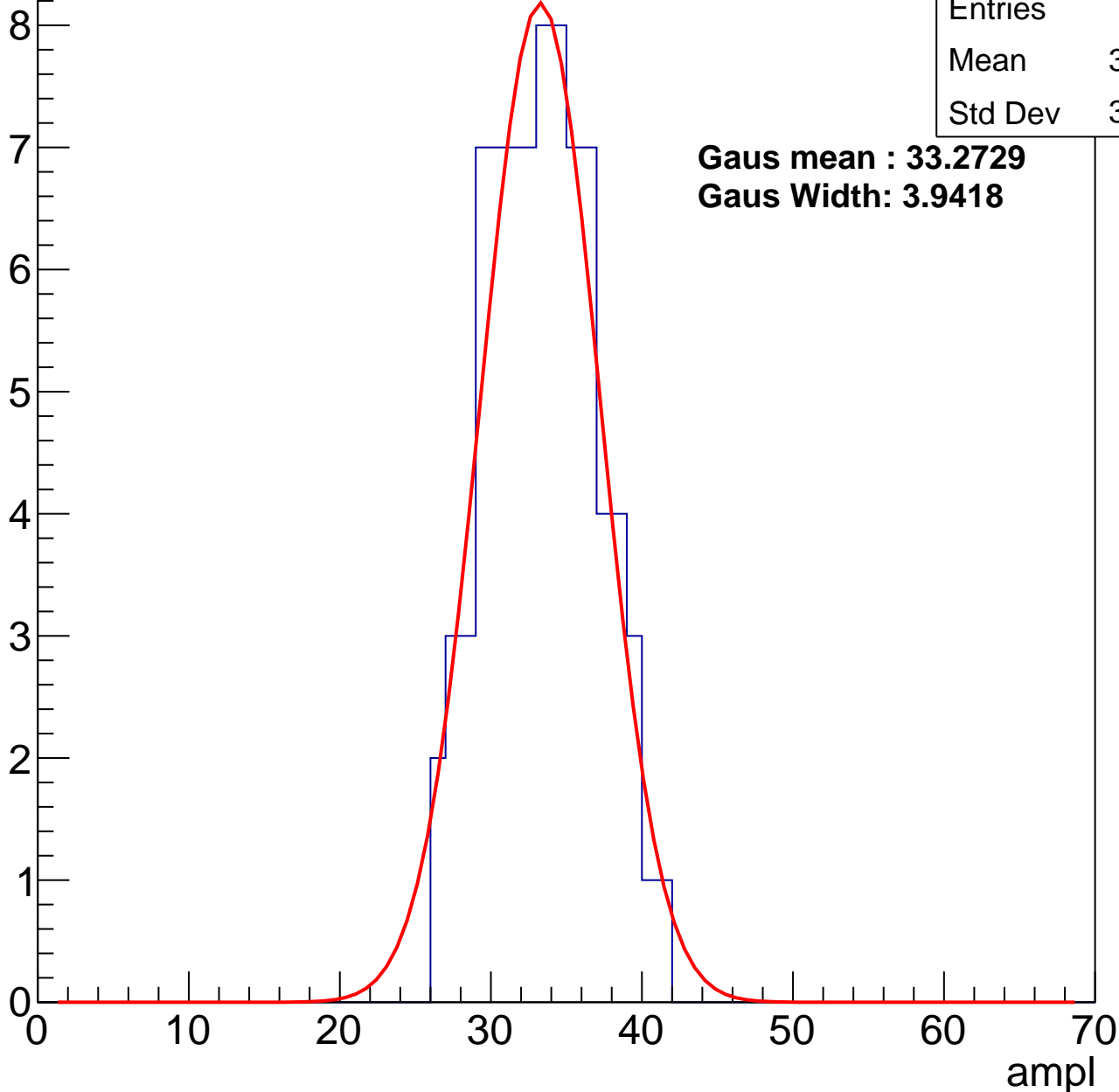
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	32.94
Std Dev	3.513

**Gaus mean : 33.2729**

**Gaus Width: 3.9418**



# B0L000S, U10-ch5, adc1

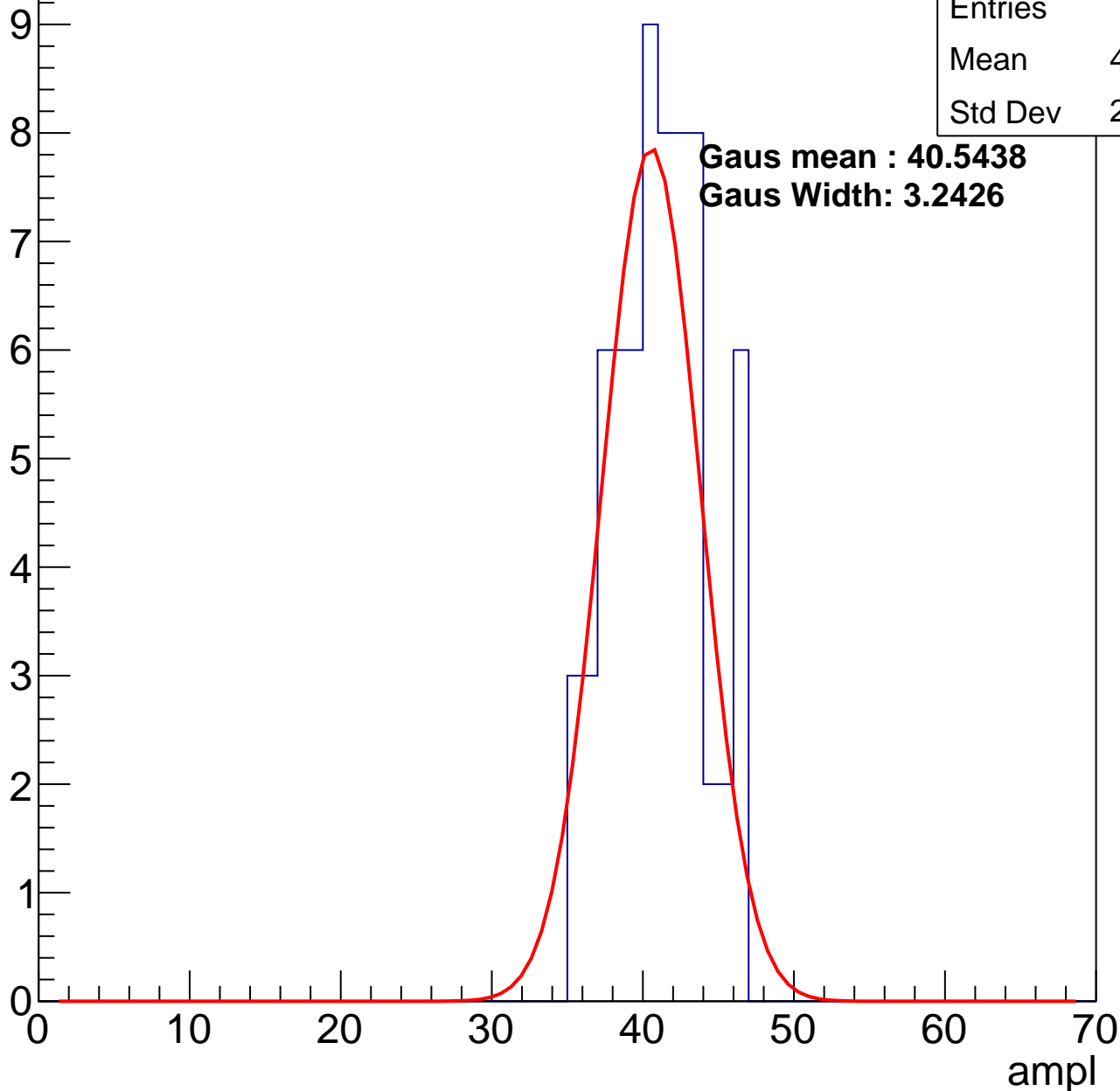
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	40.58
Std Dev	2.978

**Gaus mean : 40.5438**

**Gaus Width: 3.2426**



# B0L000S, U10-ch5, adc2

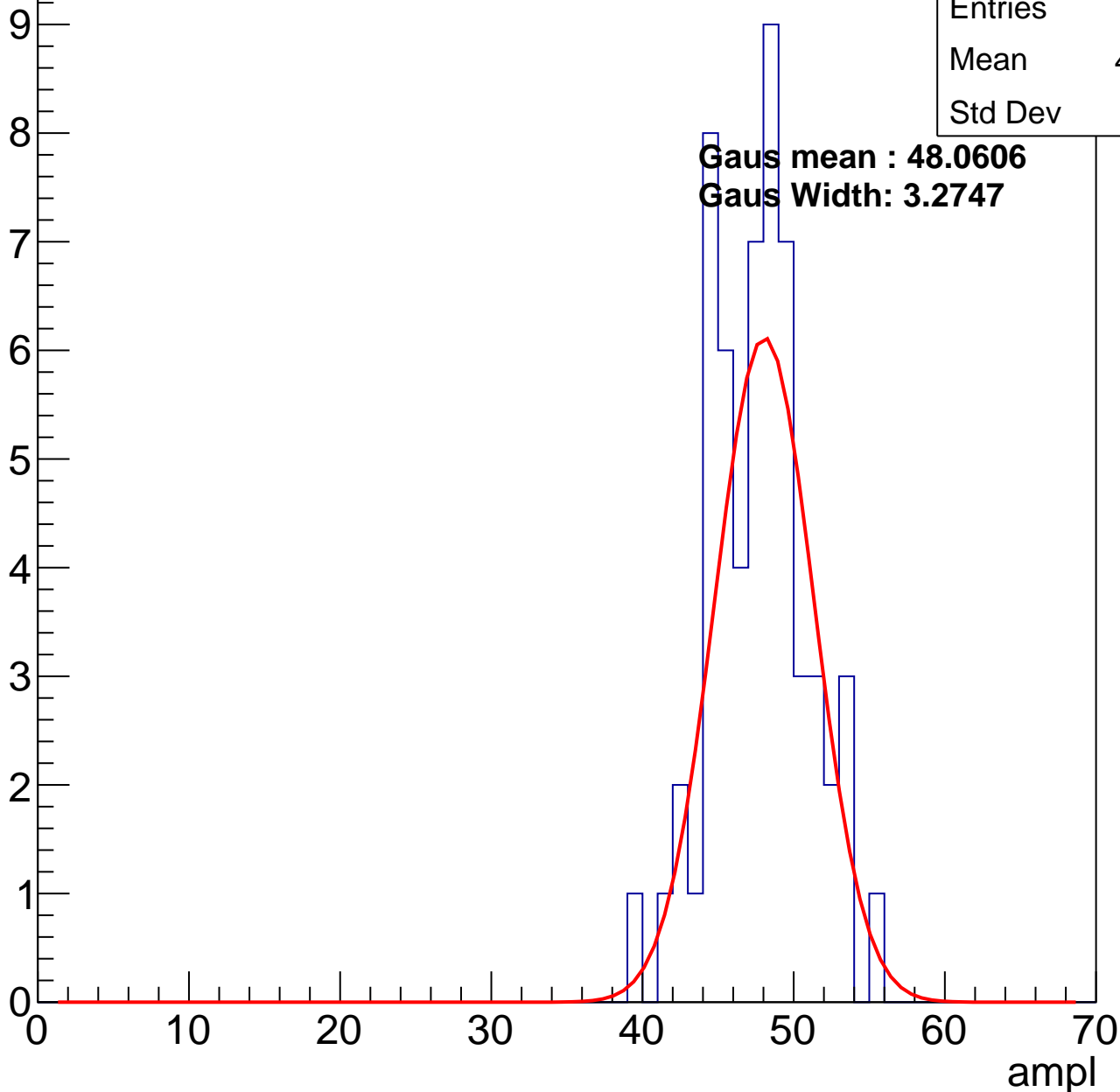
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	47.21
Std Dev	3.21

**Gaus mean : 48.0606**

**Gaus Width: 3.2747**

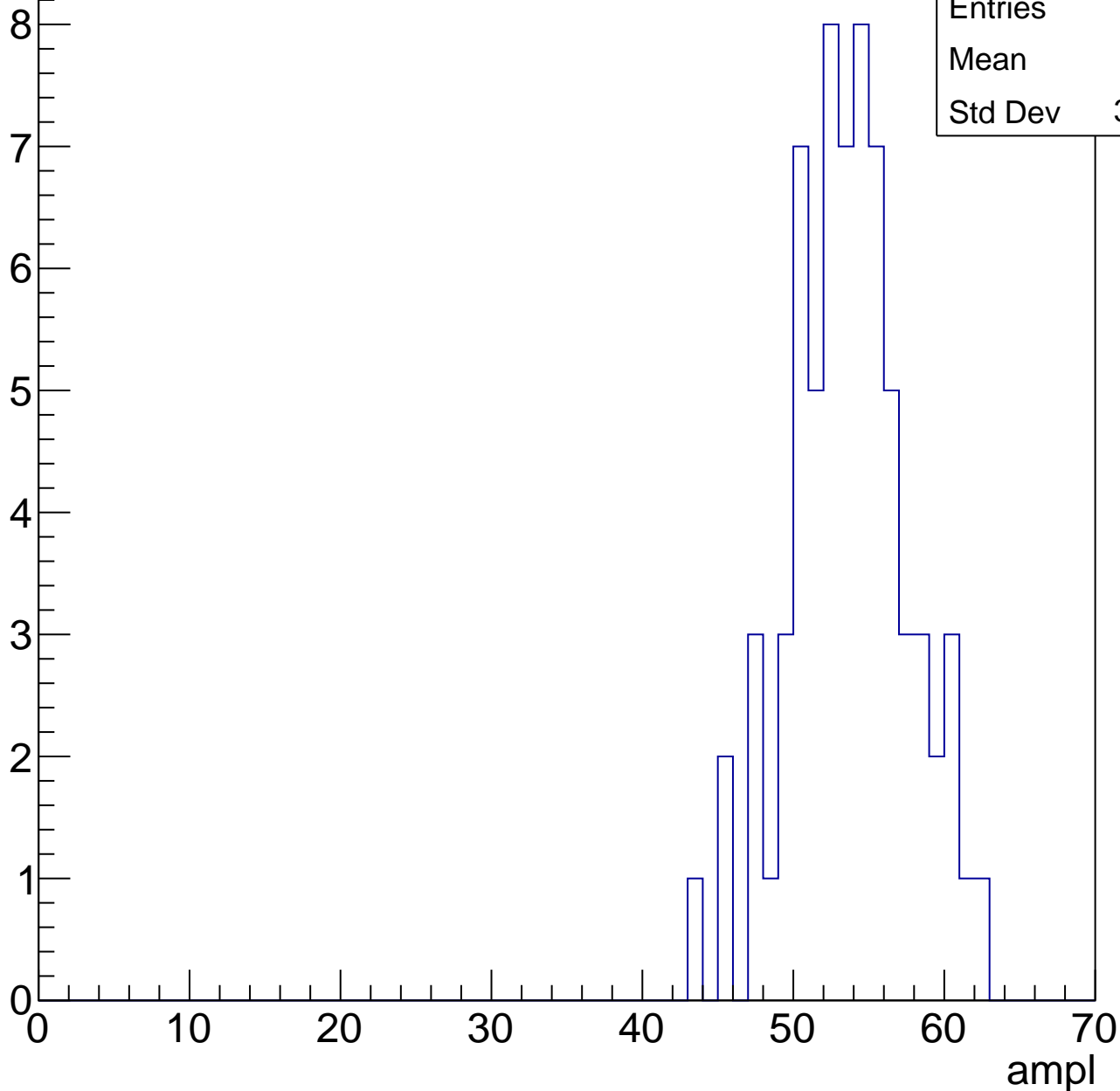


# B0L000S, U10-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	53.2
Std Dev	3.901

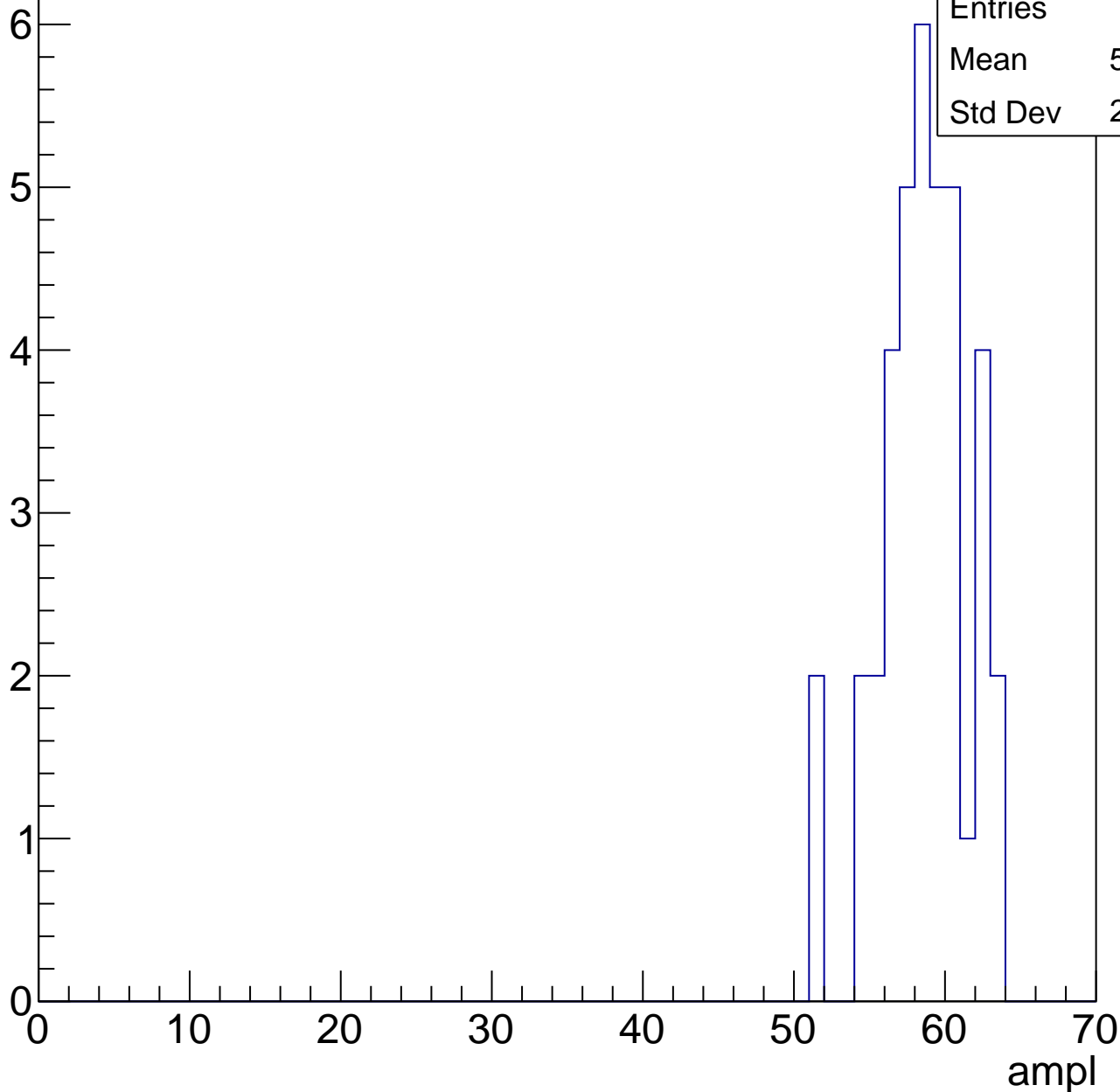


# B0L000S, U10-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.08
Std Dev	2.887



# B0L000S, U10-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

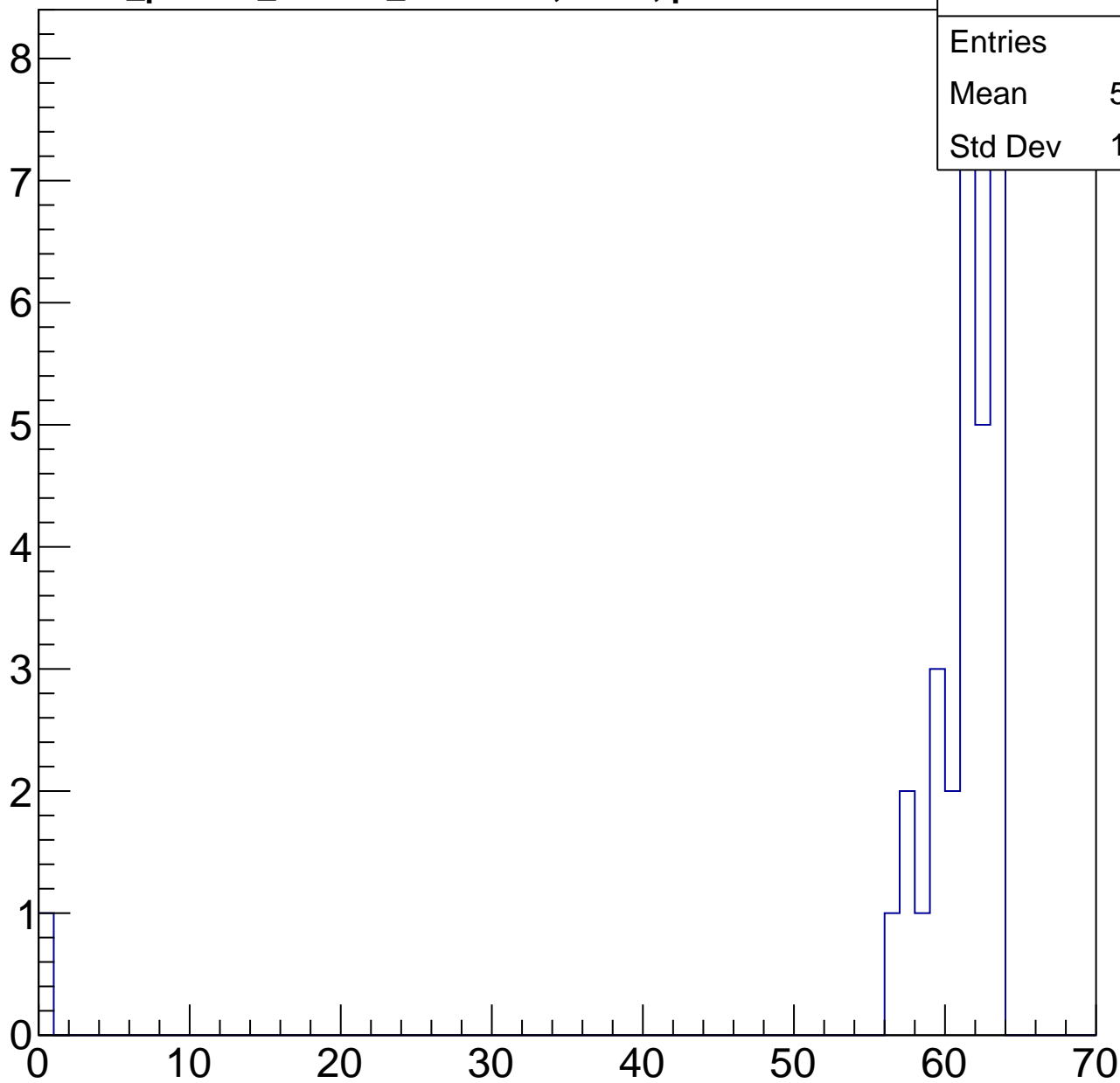
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	31
Mean	58.94
Std Dev	10.93

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch6, adc0

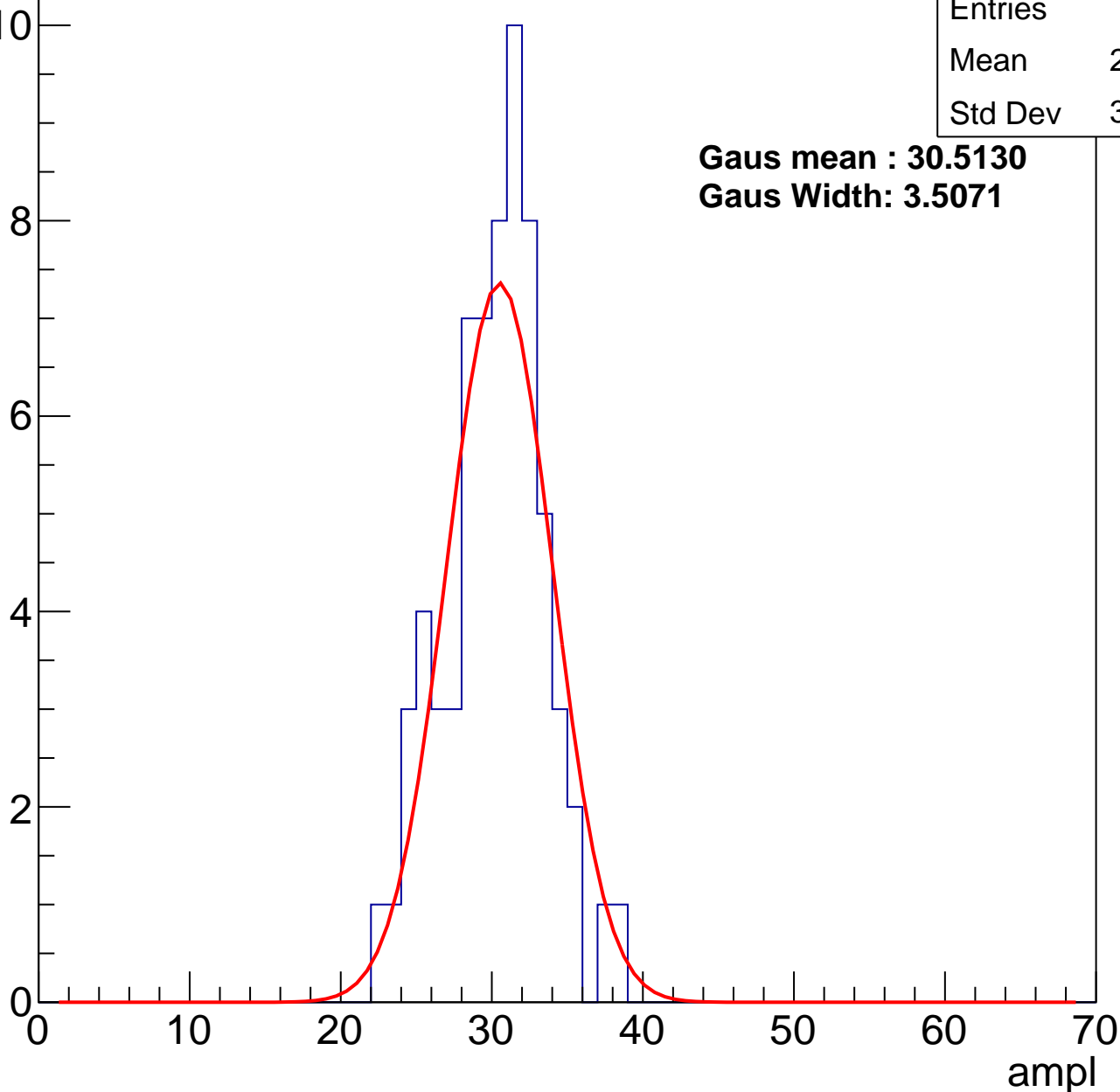
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.75
Std Dev	3.289

**Gaus mean : 30.5130**

**Gaus Width: 3.5071**



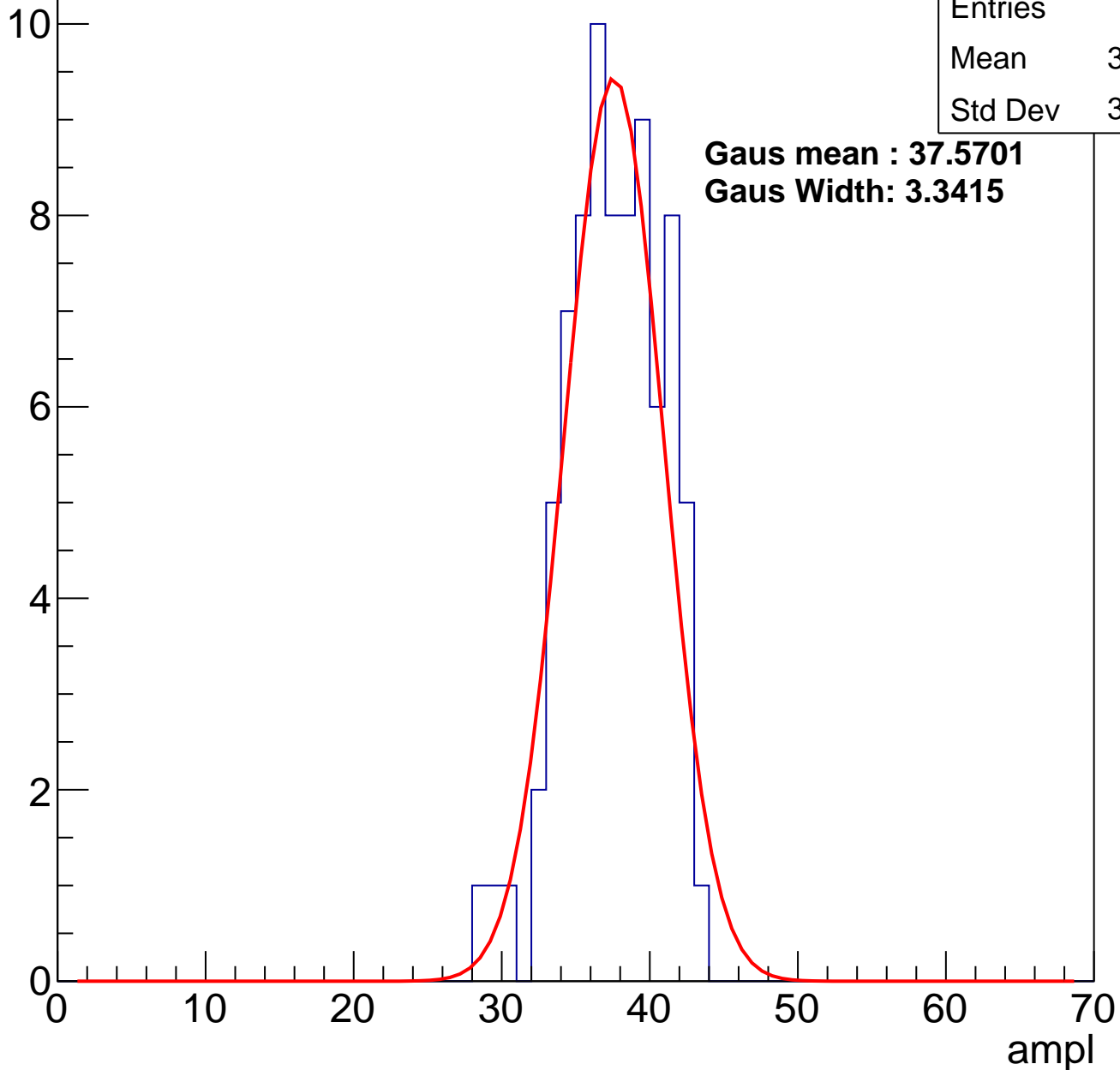
# B0L000S, U10-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	37.08
Std Dev	3.193

**Gaus mean : 37.5701**  
**Gaus Width: 3.3415**

Entry



# B0L000S, U10-ch6, adc2

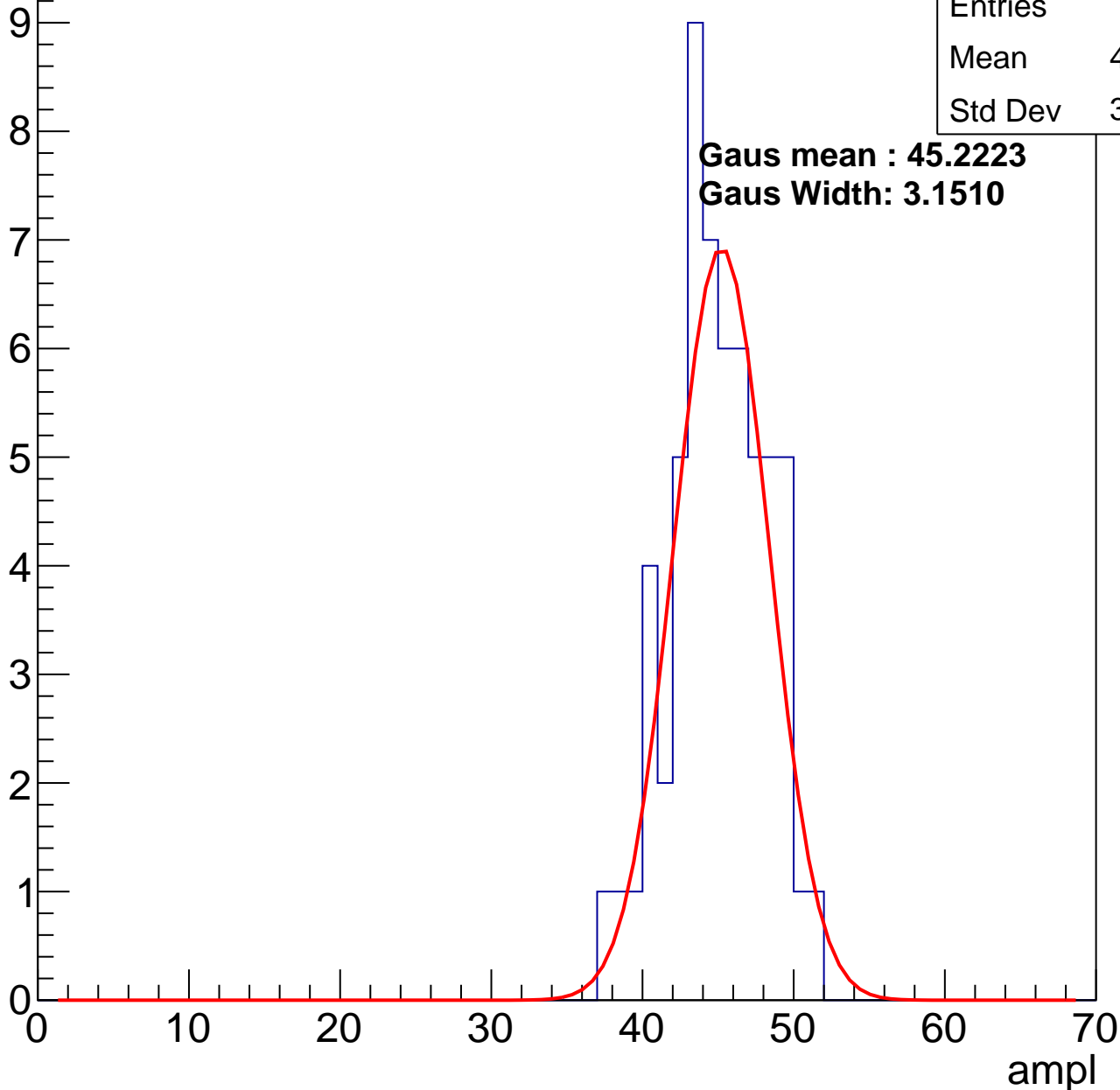
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	44.54
Std Dev	3.105

**Gaus mean : 45.2223**

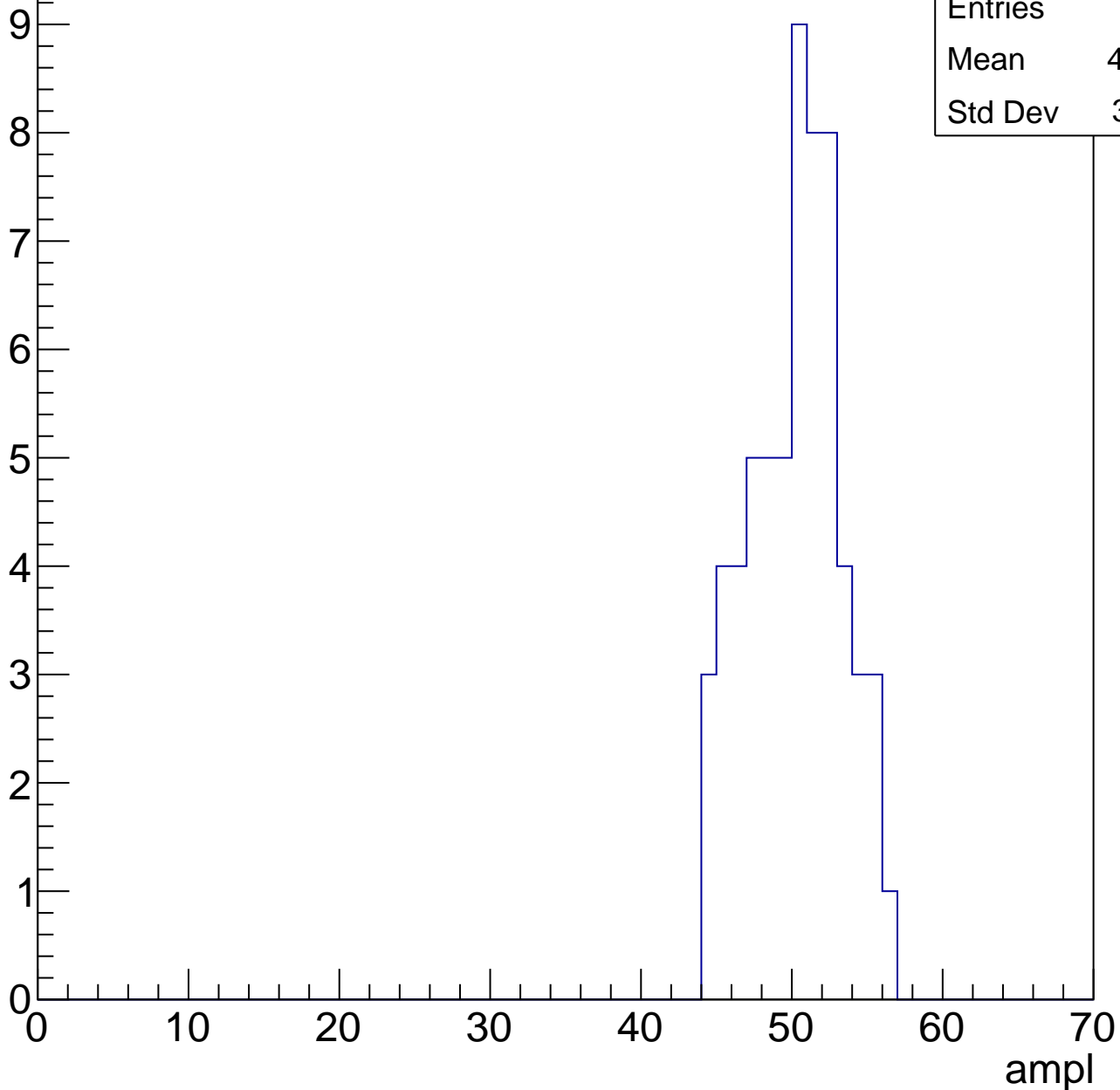
**Gaus Width: 3.1510**



# B0L000S, U10-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



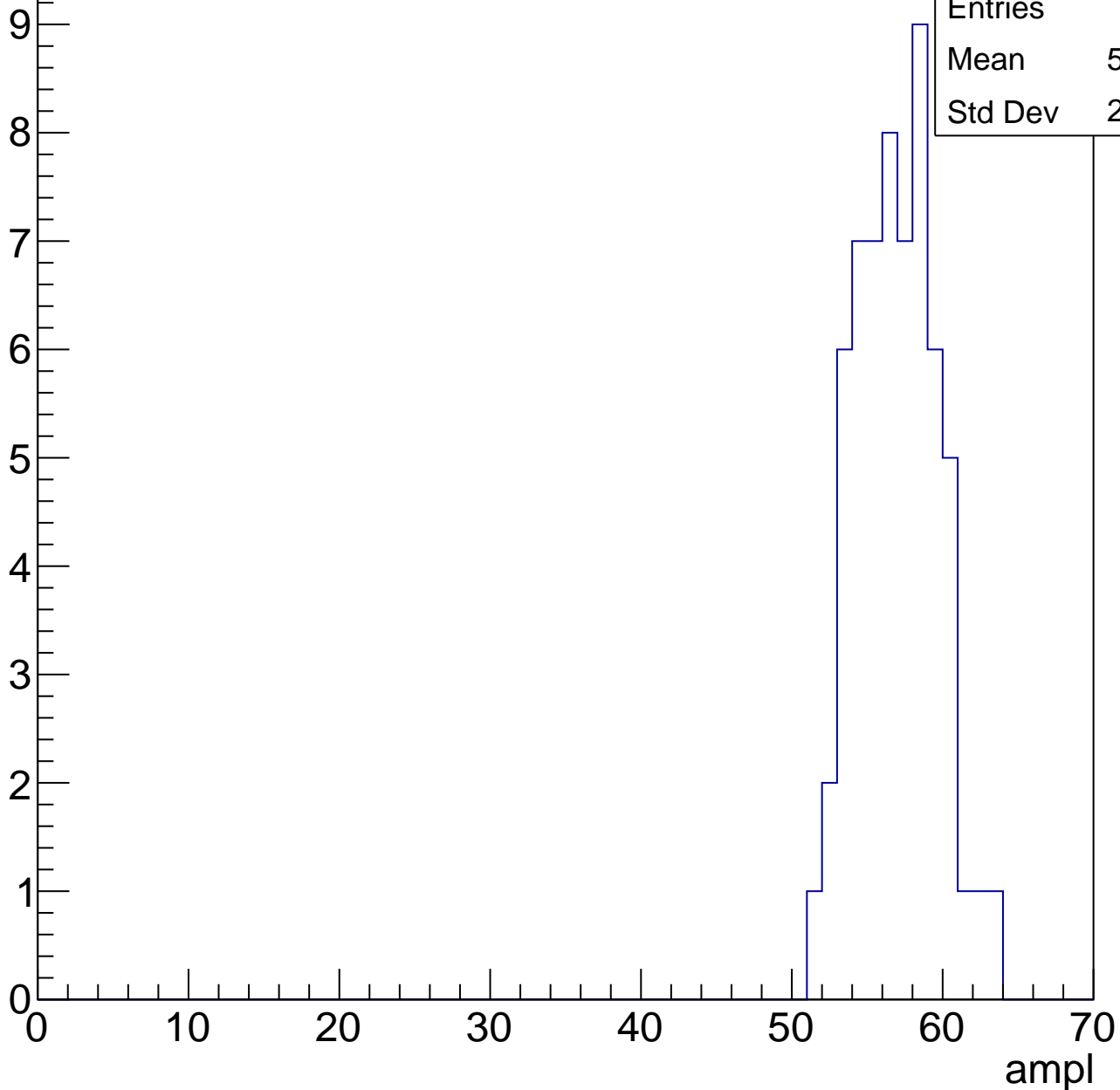
Entries	62
Mean	49.76
Std Dev	3.041

# B0L000S, U10-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.48
Std Dev	2.622

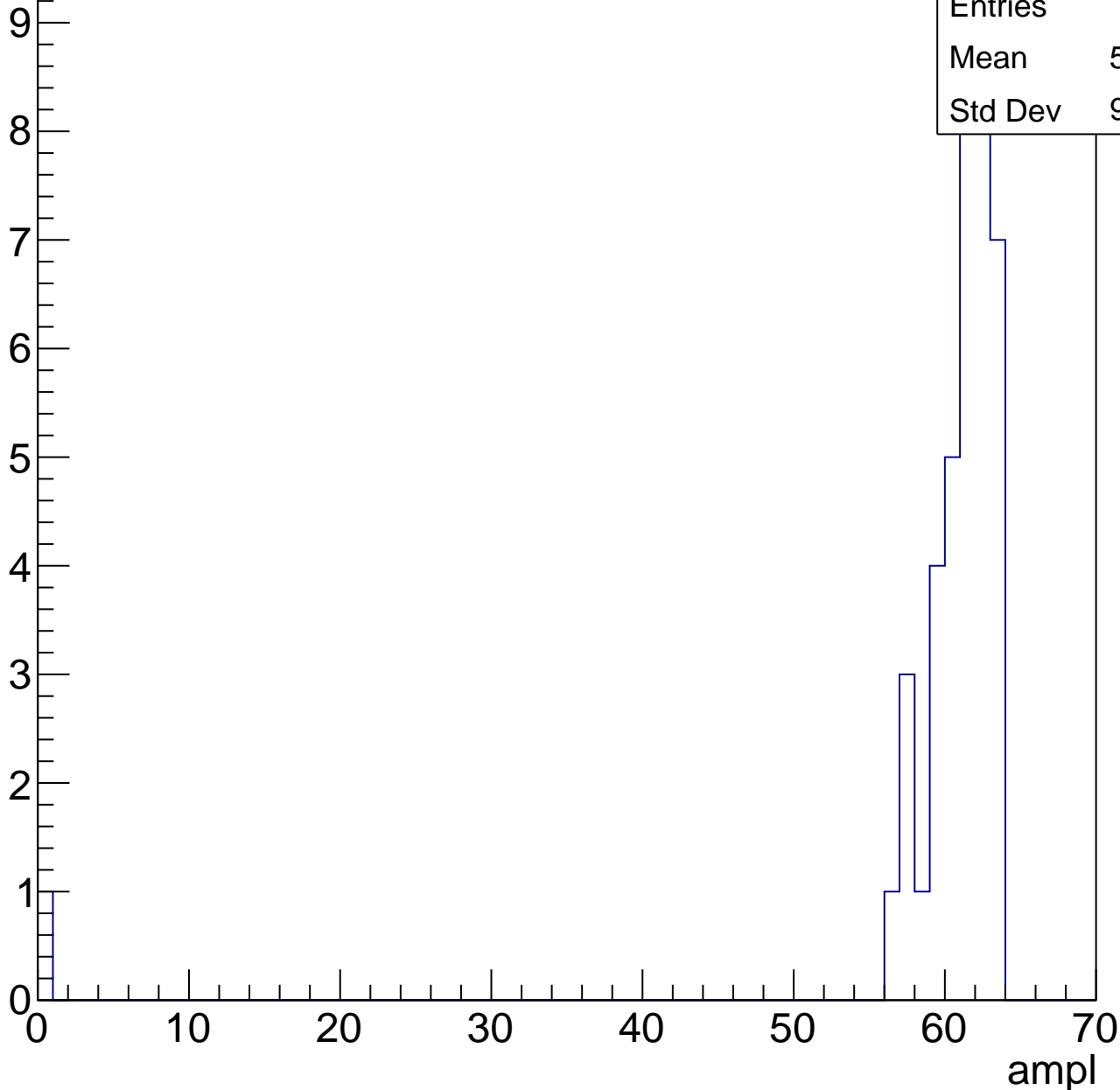


# B0L000S, U10-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	59.23
Std Dev	9.663



# B0L000S, U10-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	31.42
Std Dev	3.464

**Gaus mean : 31.3537**

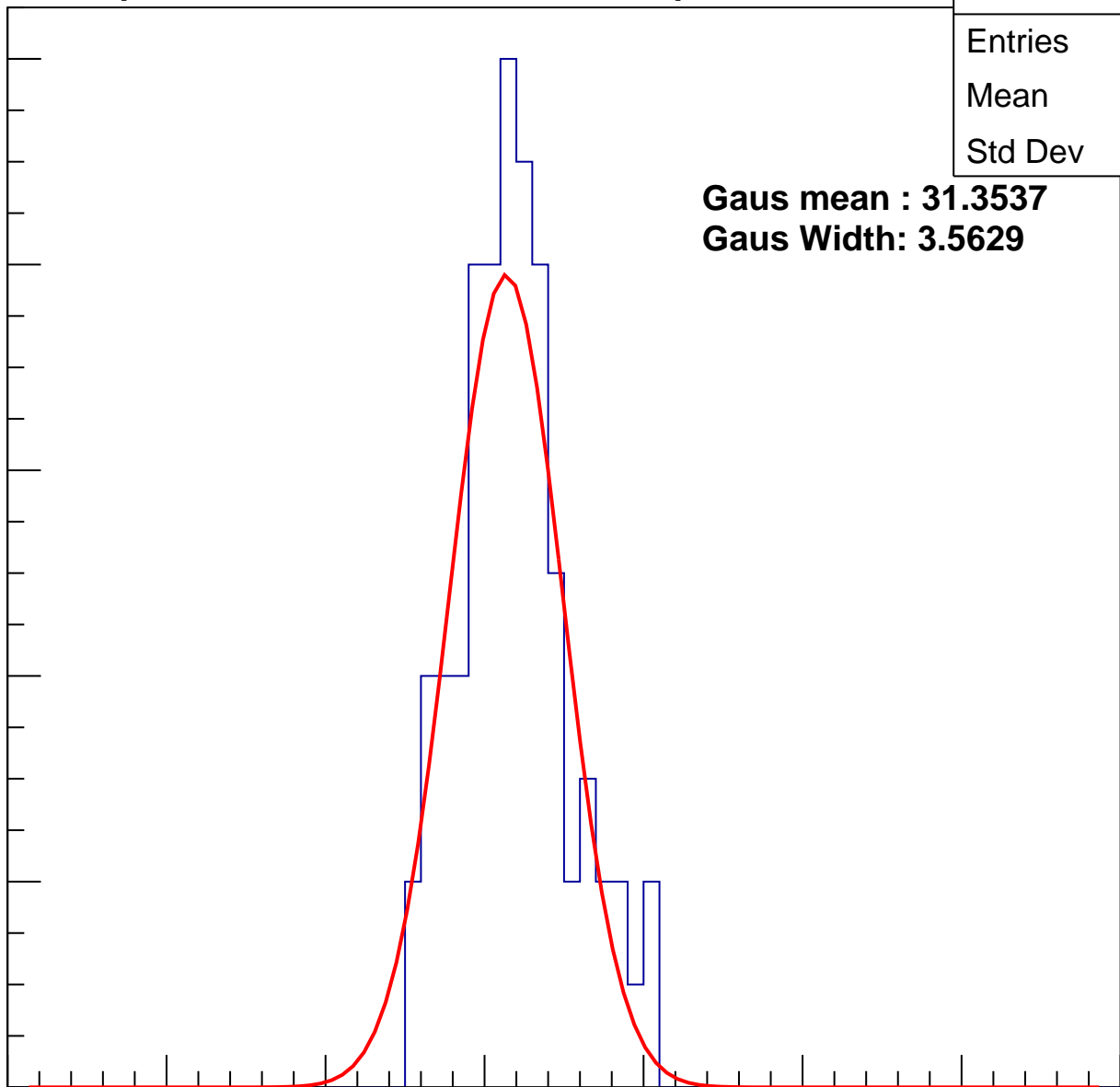
**Gaus Width: 3.5629**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch7, adc1

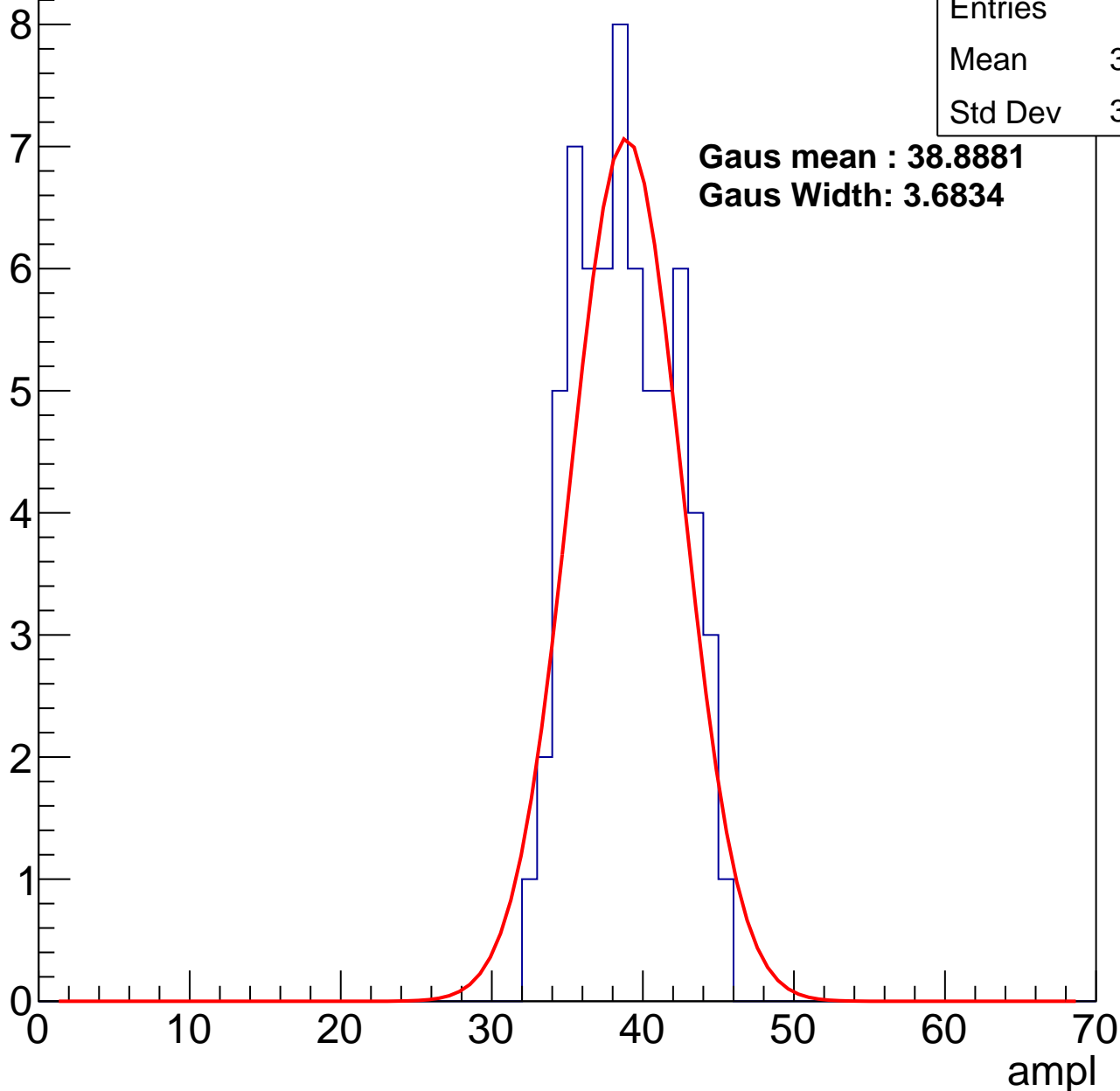
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	38.38
Std Dev	3.219

**Gaus mean : 38.8881**

**Gaus Width: 3.6834**



# B0L000S, U10-ch7, adc2

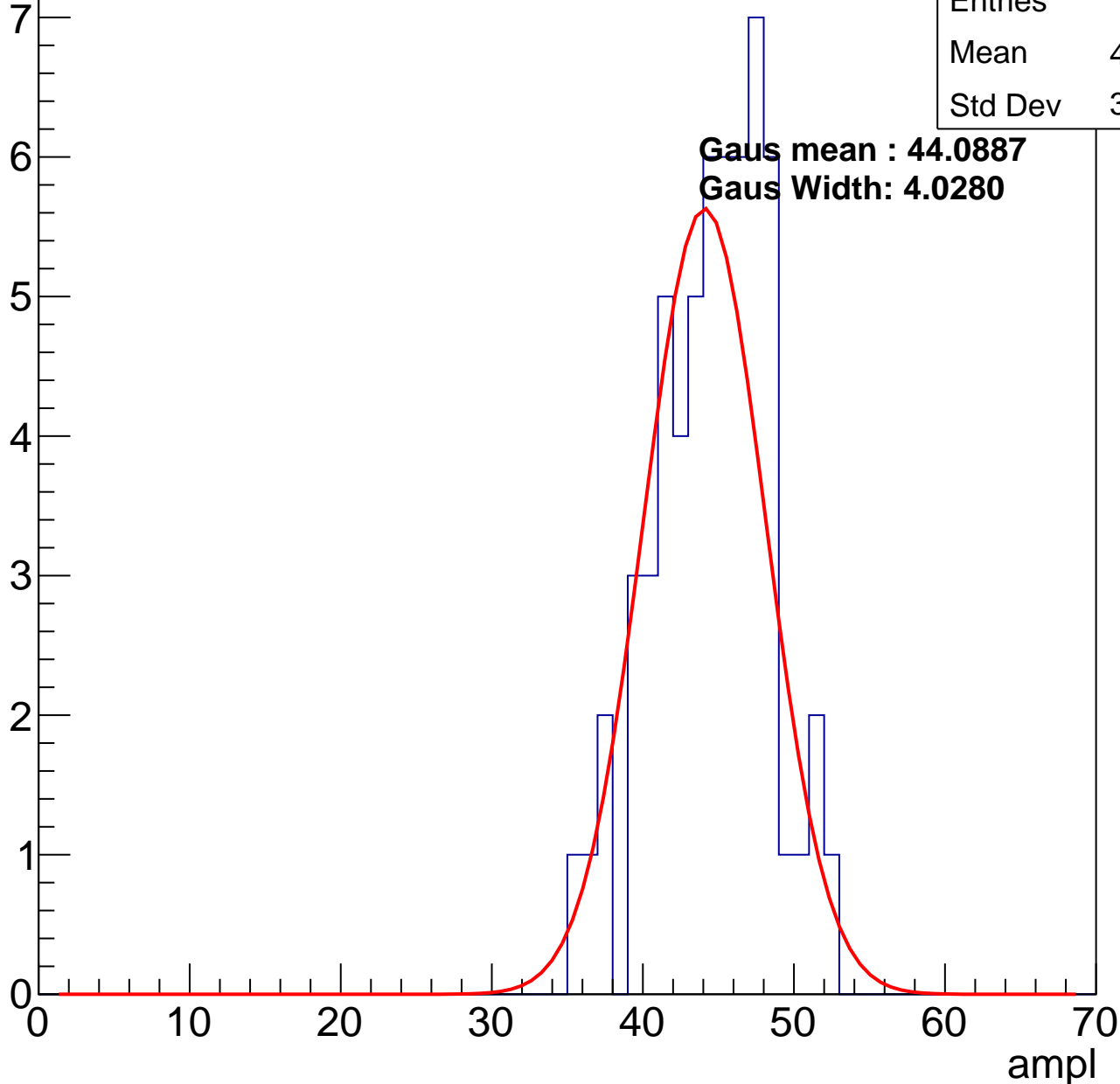
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.17
Std Dev	3.747

**Gaus mean : 44.0887**

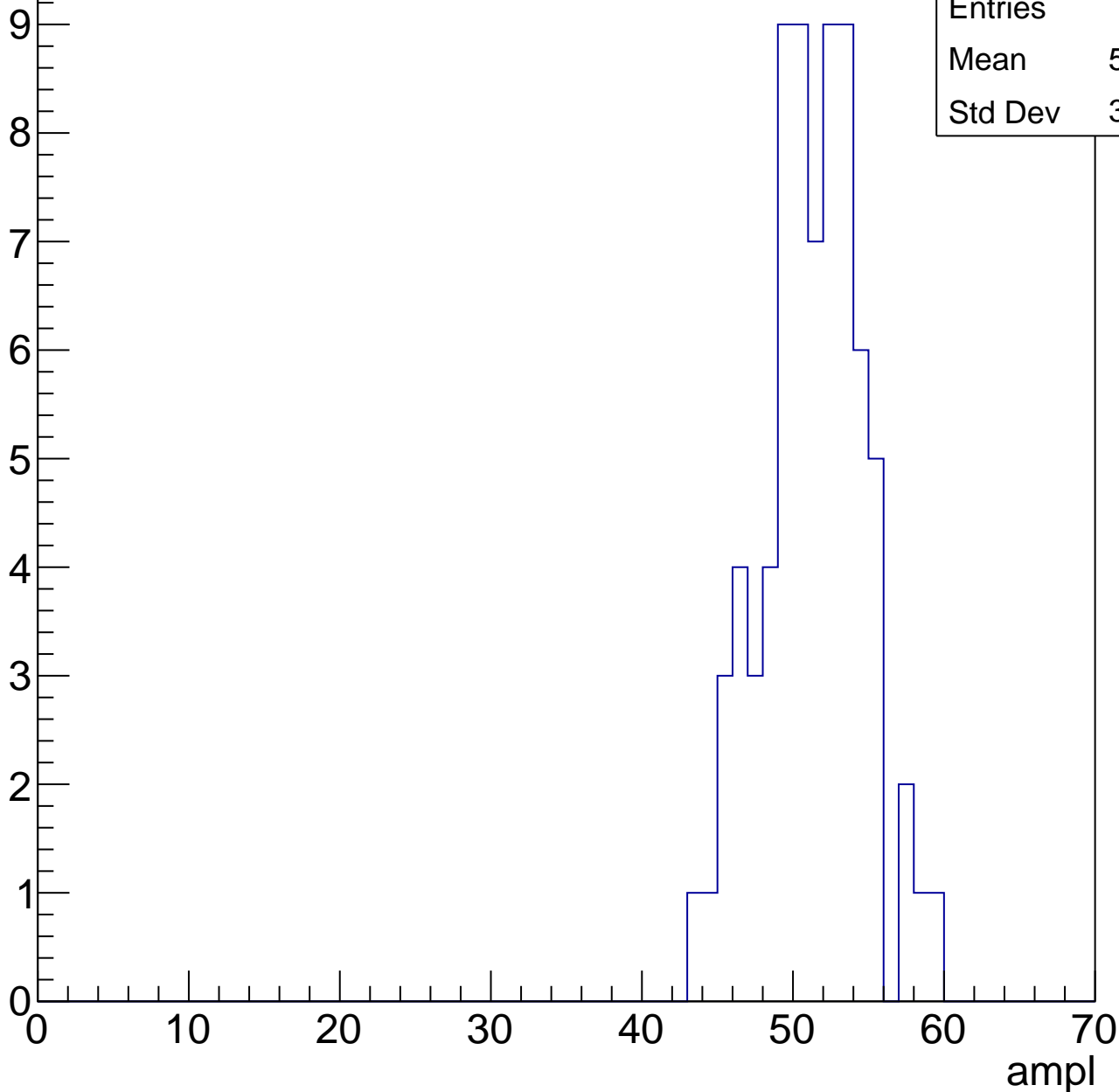
**Gaus Width: 4.0280**



# B0L000S, U10-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	74
Mean	50.84
Std Dev	3.313

# B0L000S, U10-ch7, adc4

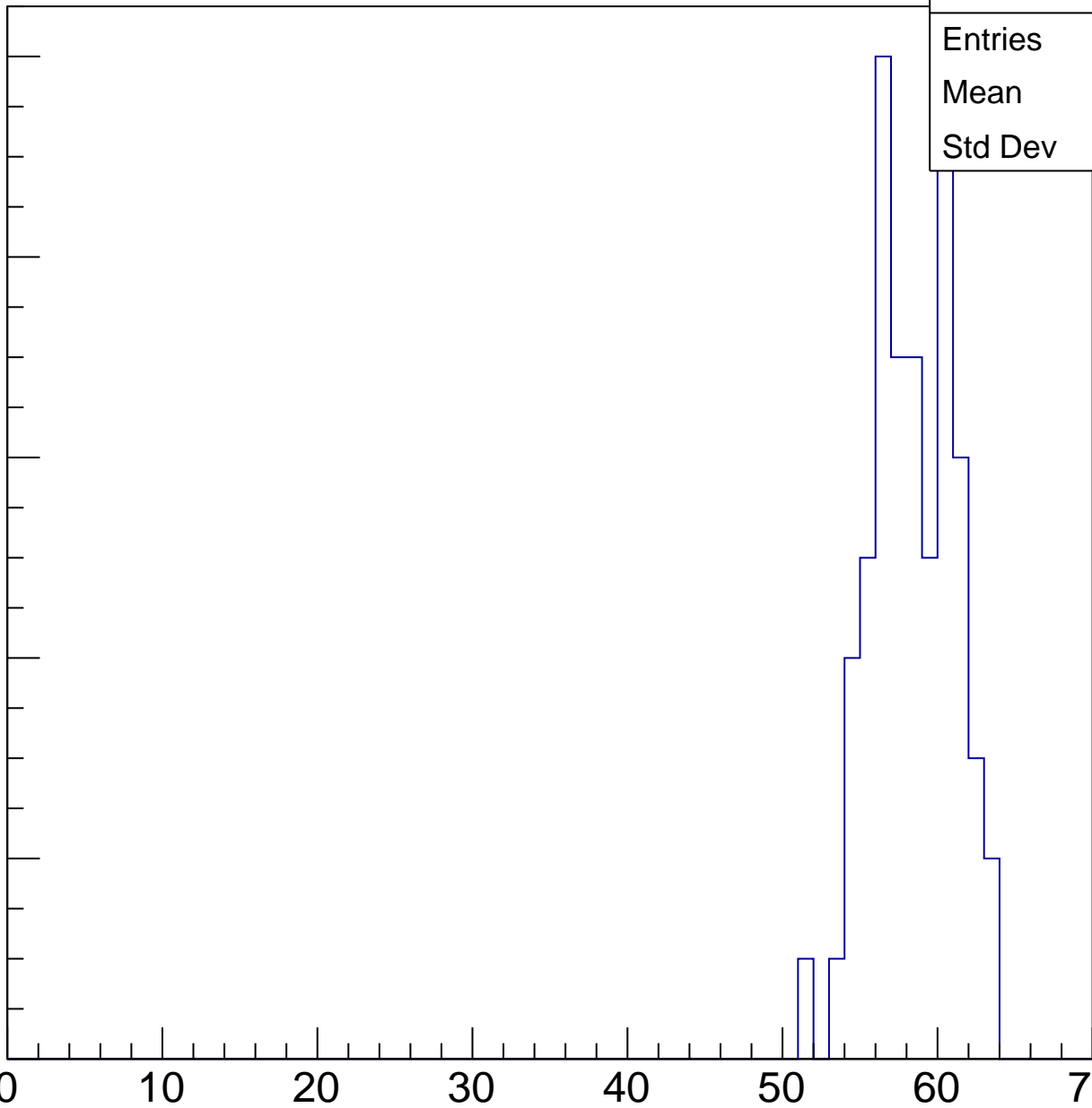
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10  
8  
6  
4  
2  
0

Entries	60
Mean	57.88
Std Dev	2.653

ampl

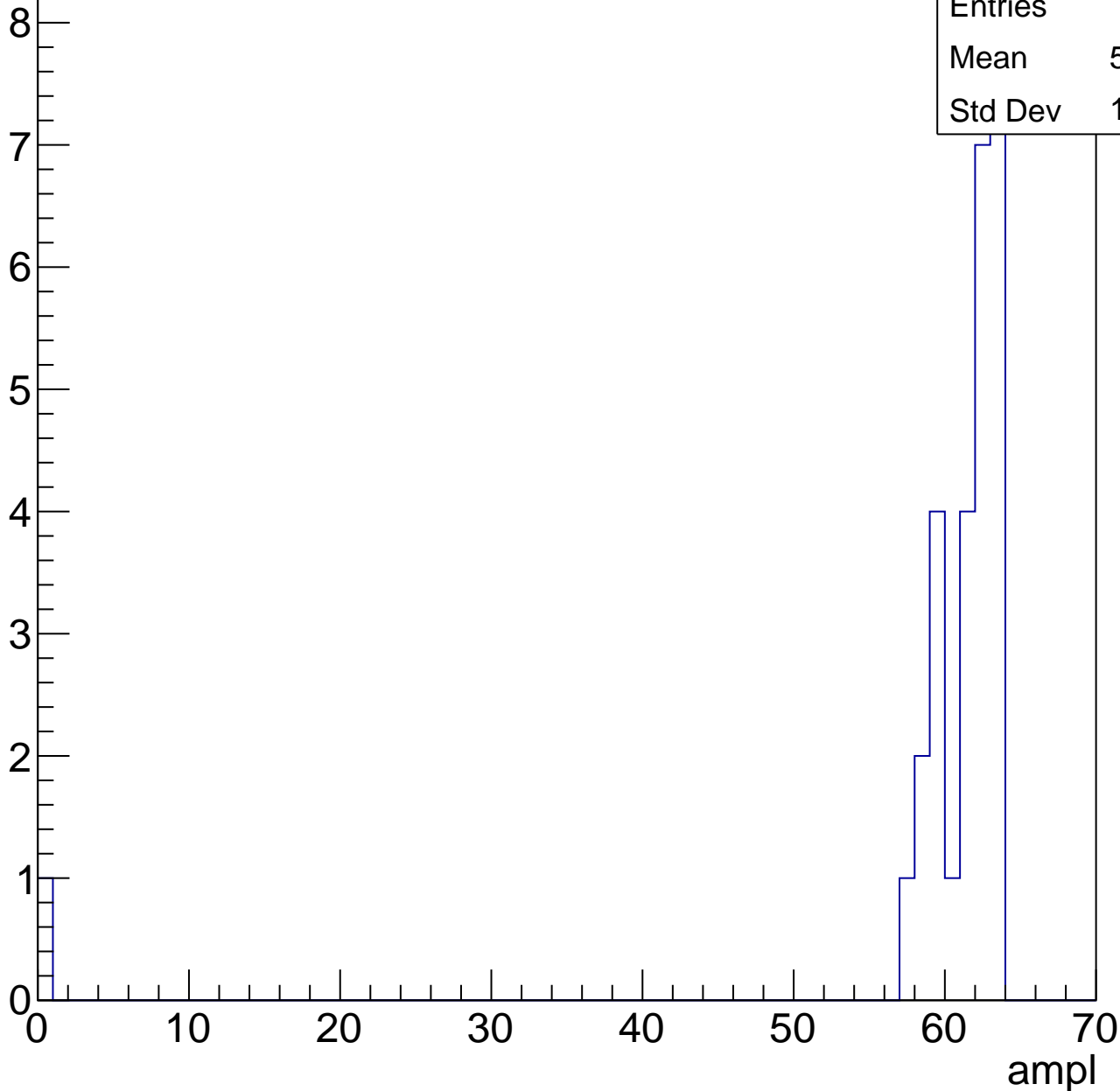


# B0L000S, U10-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.96
Std Dev	11.49



# B0L000S, U10-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch8, adc0

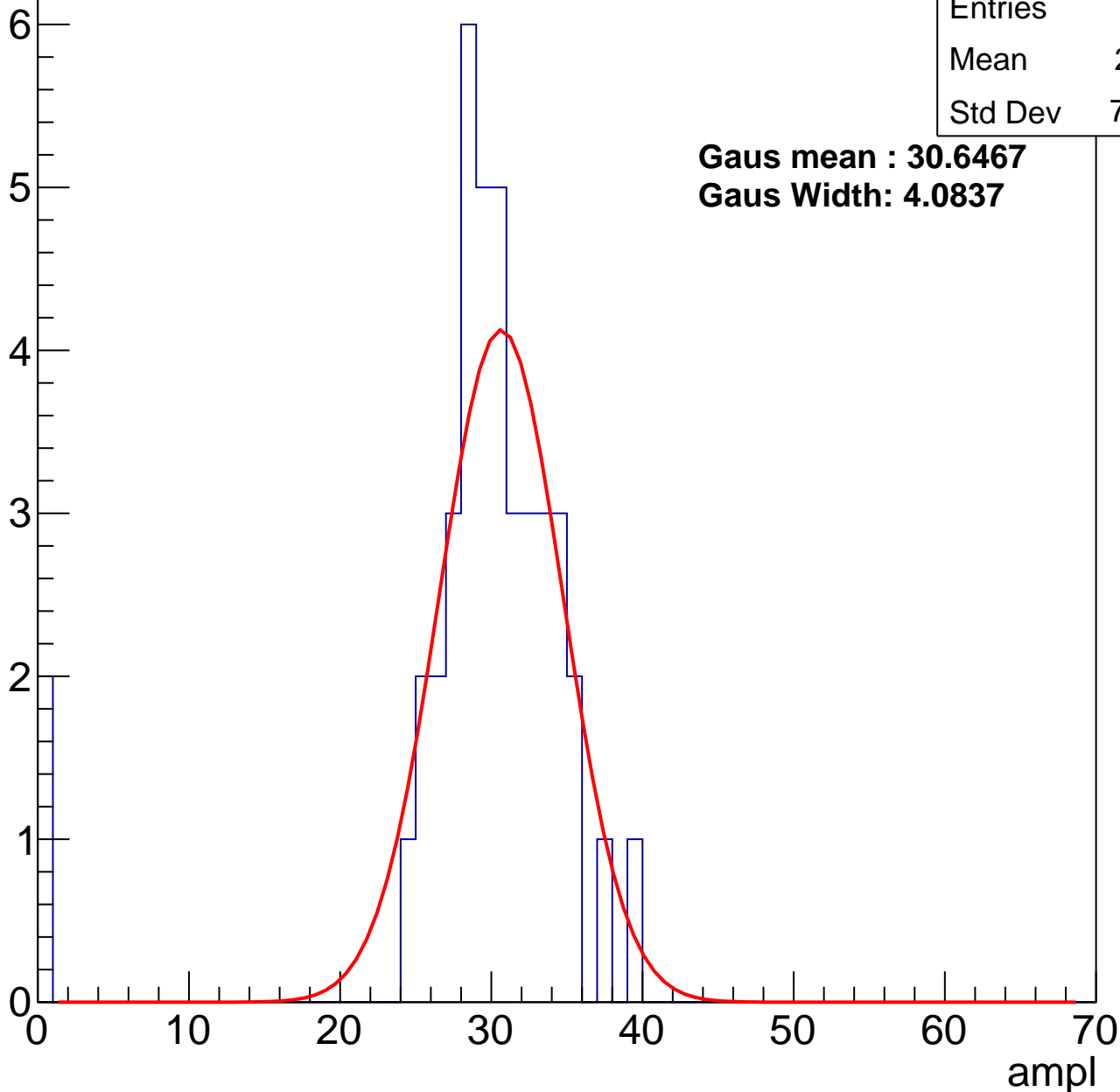
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	28.71
Std Dev	7.196

**Gaus mean : 30.6467**

**Gaus Width: 4.0837**



# B0L000S, U10-ch8, adc1

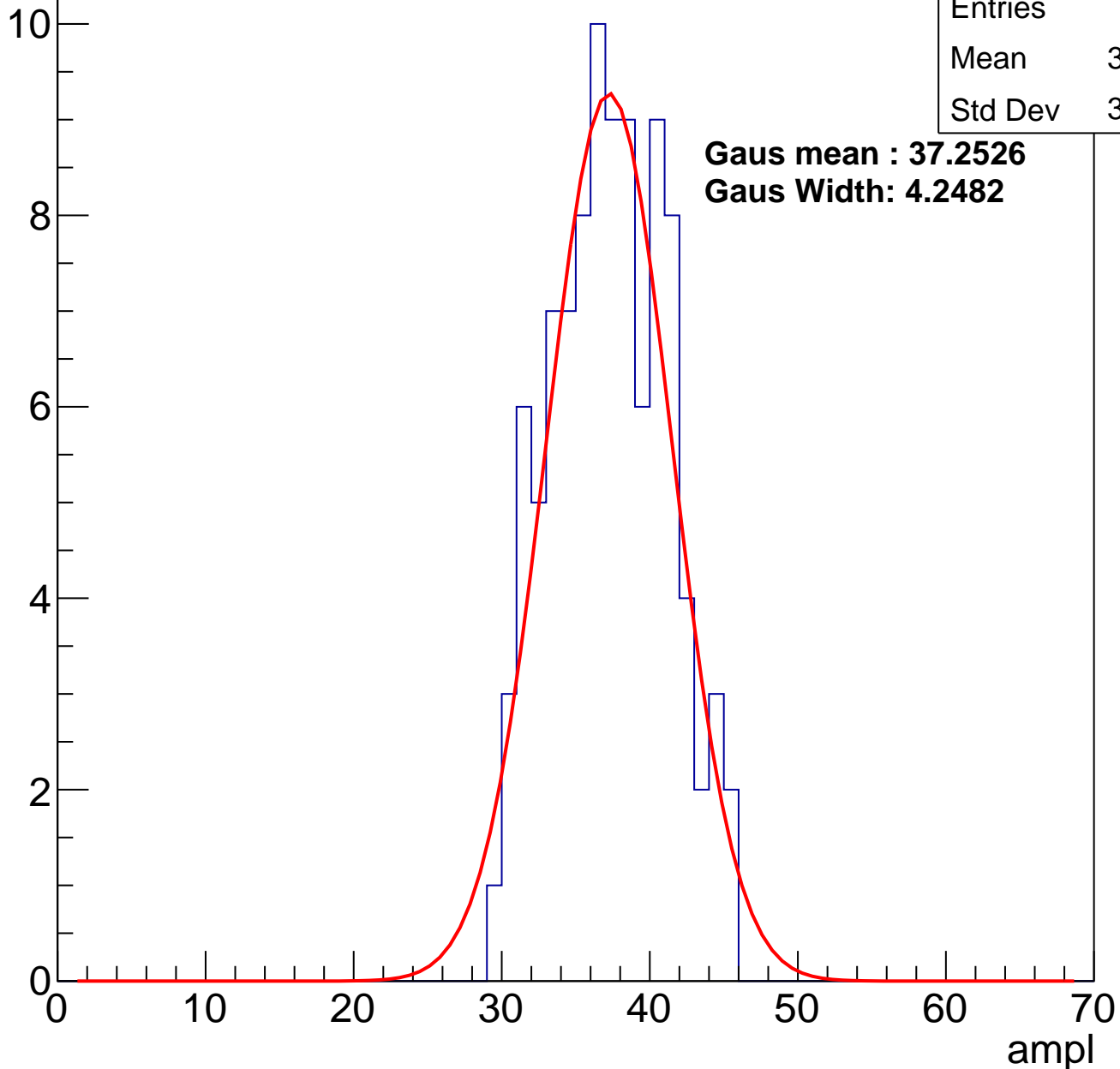
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	99
Mean	36.84
Std Dev	3.834

**Gaus mean : 37.2526**

**Gaus Width: 4.2482**

Entry



# B0L000S, U10-ch8, adc2

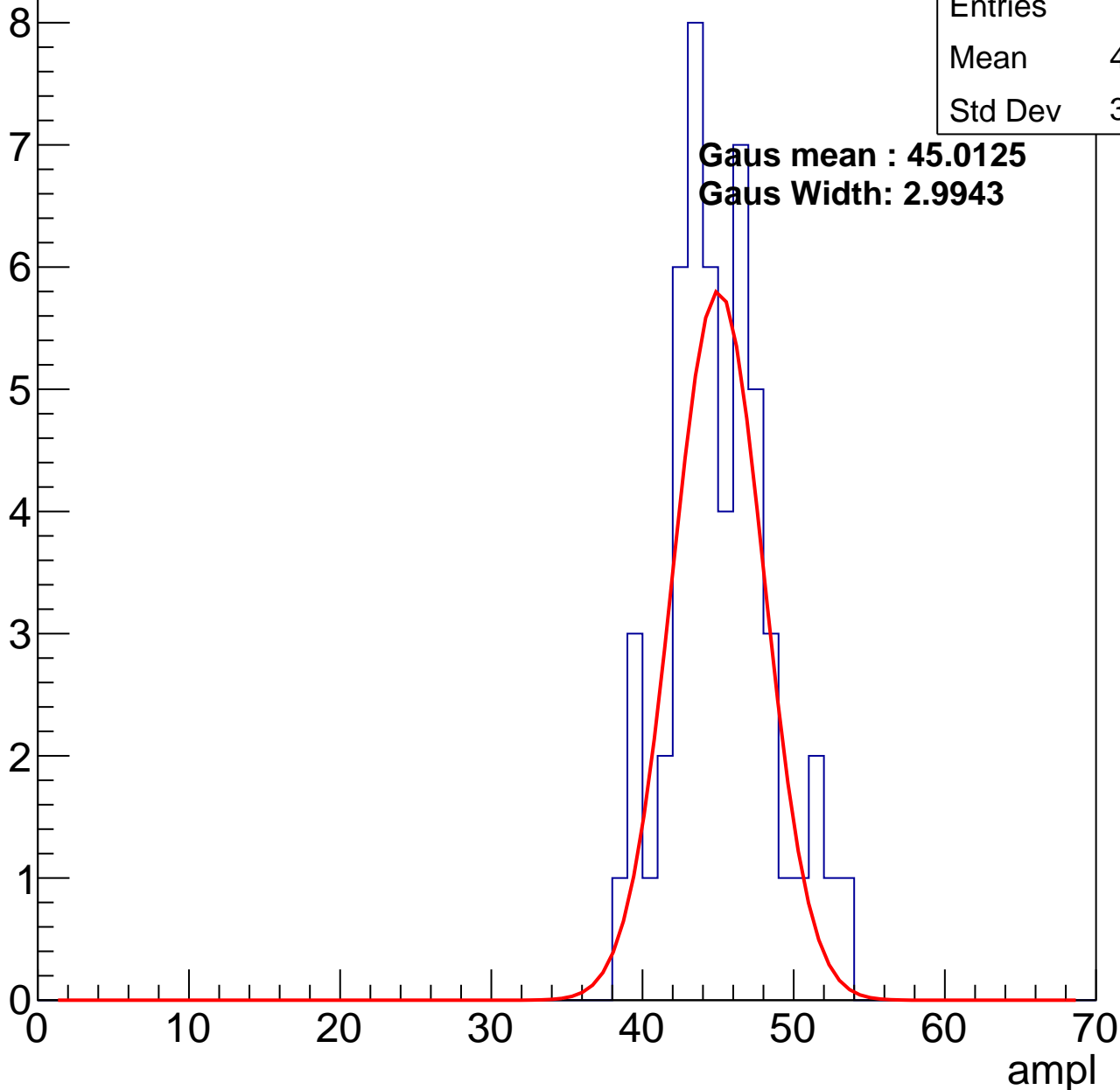
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.69
Std Dev	3.343

**Gaus mean : 45.0125**

**Gaus Width: 2.9943**

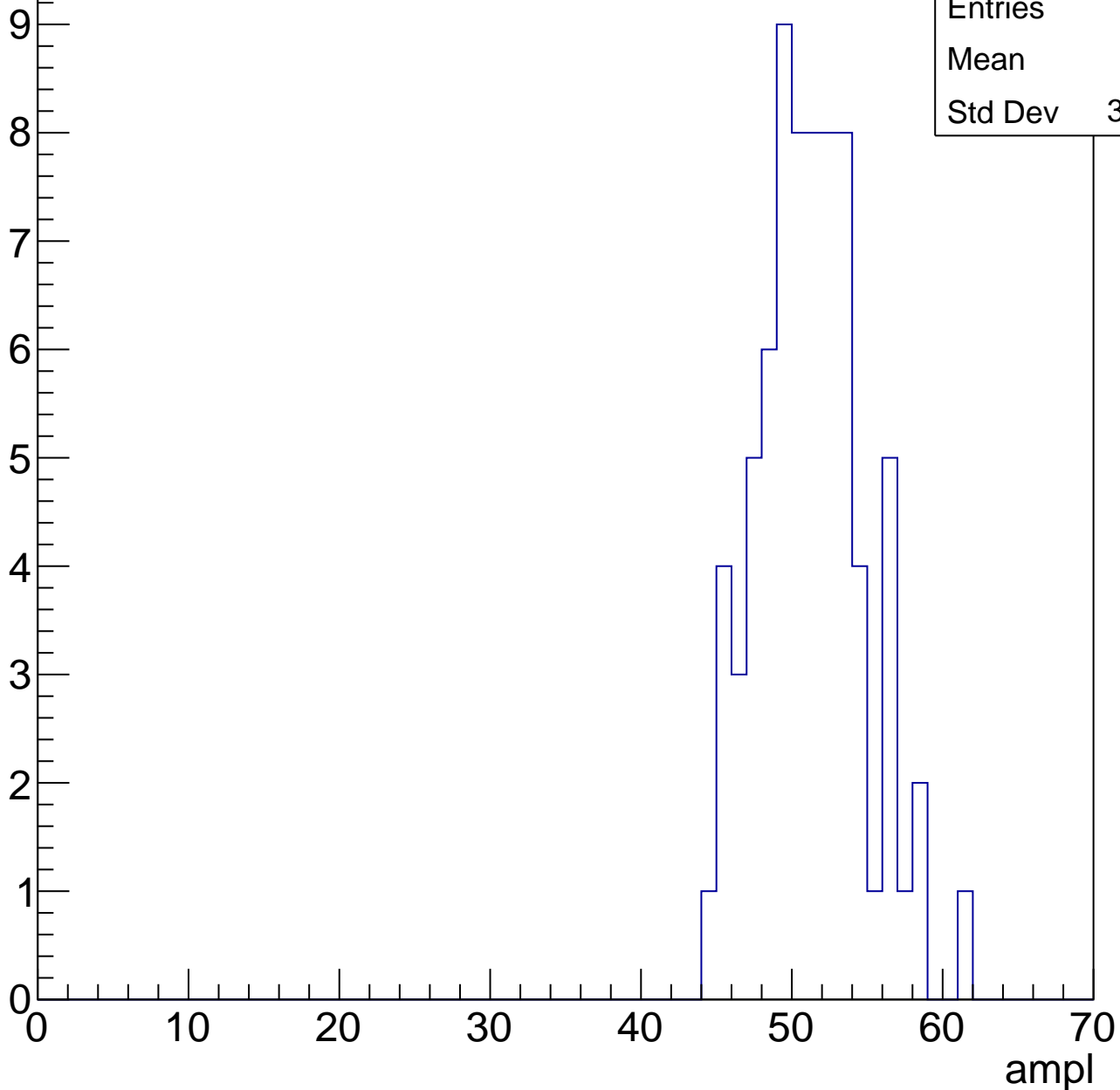


# B0L000S, U10-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

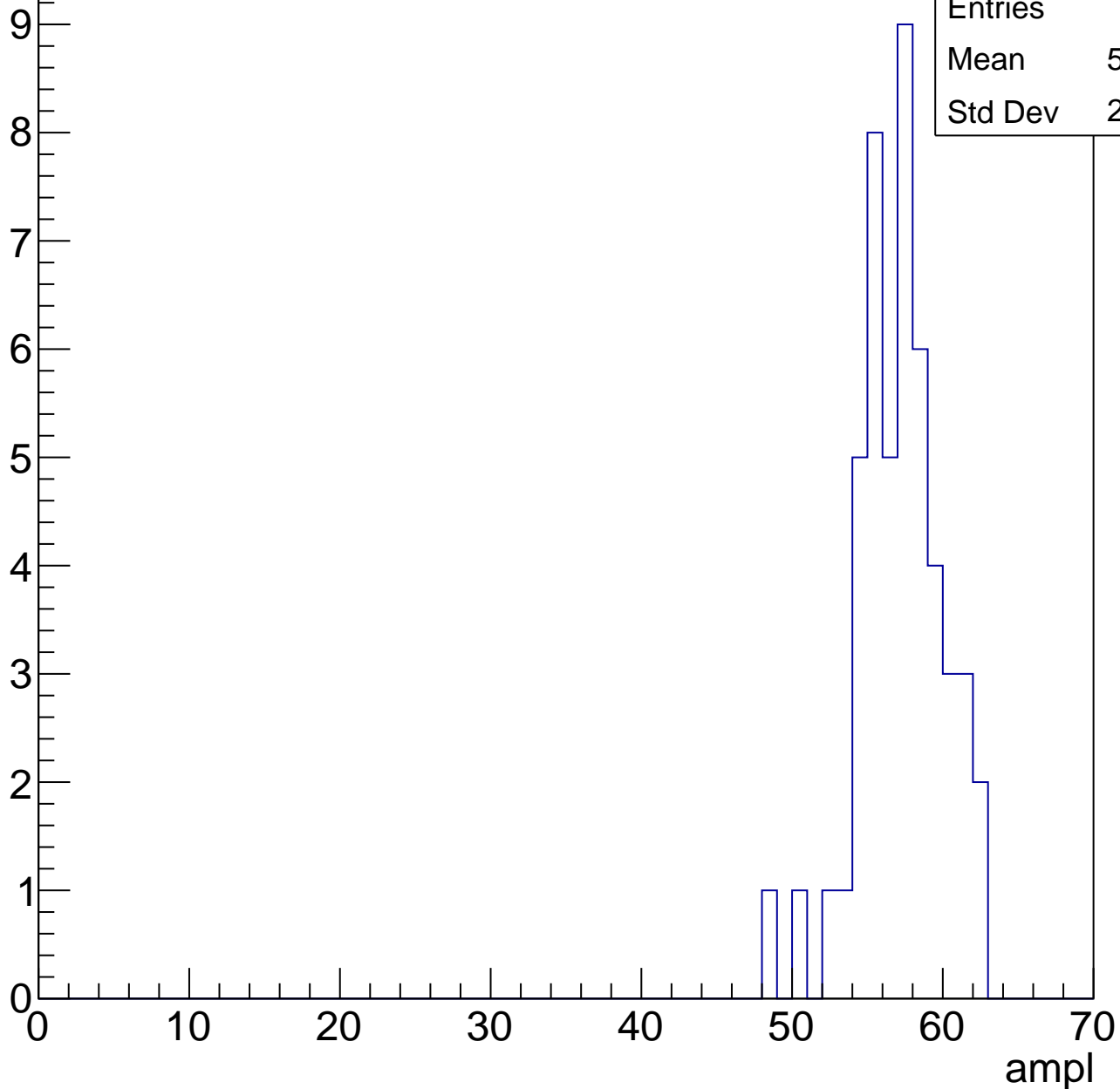
Entries	74
Mean	50.8
Std Dev	3.476



# B0L000S, U10-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

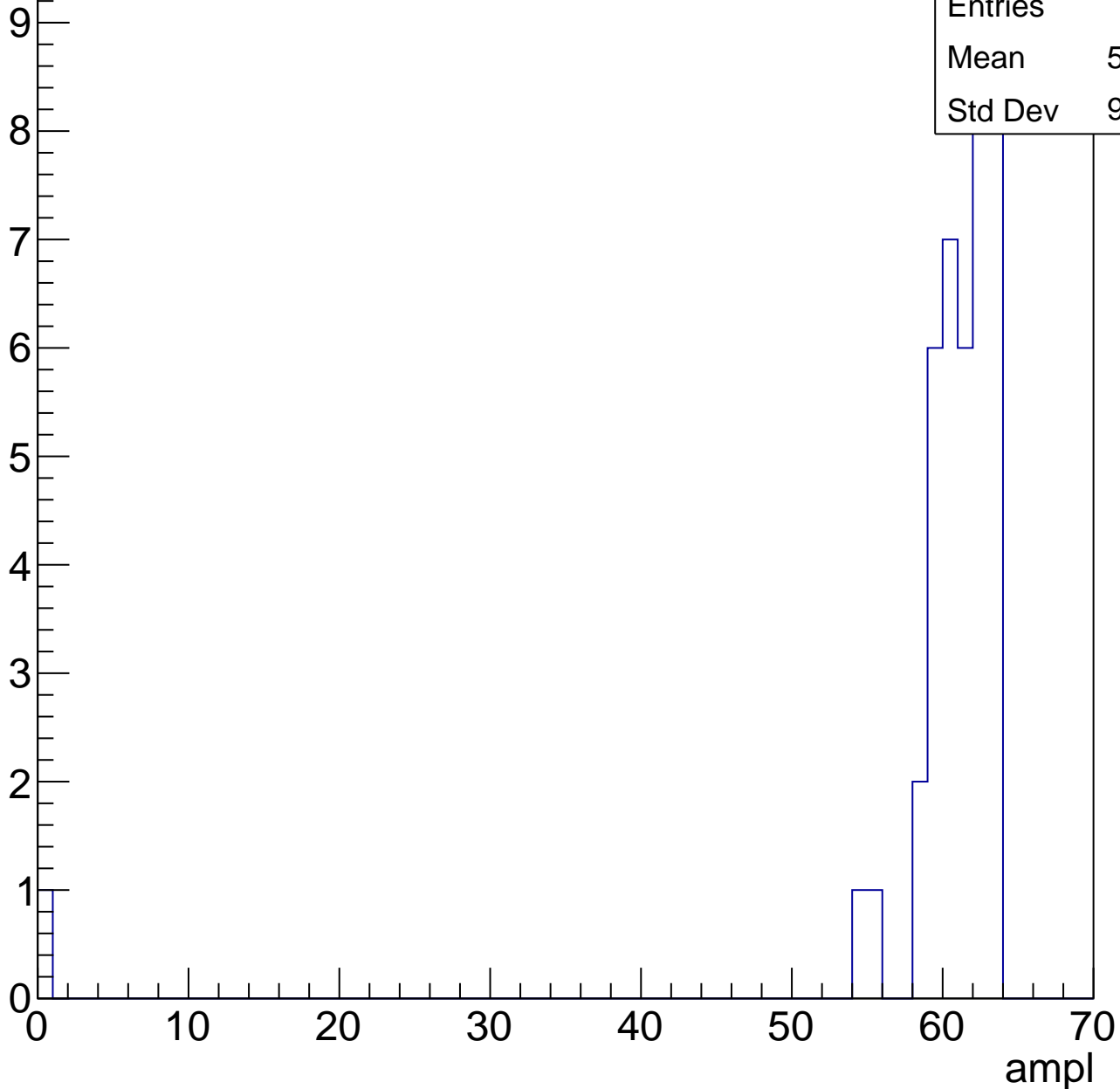


# B0L000S, U10-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	59.22
Std Dev	9.588



# B0L000S, U10-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U10-ch9, adc0

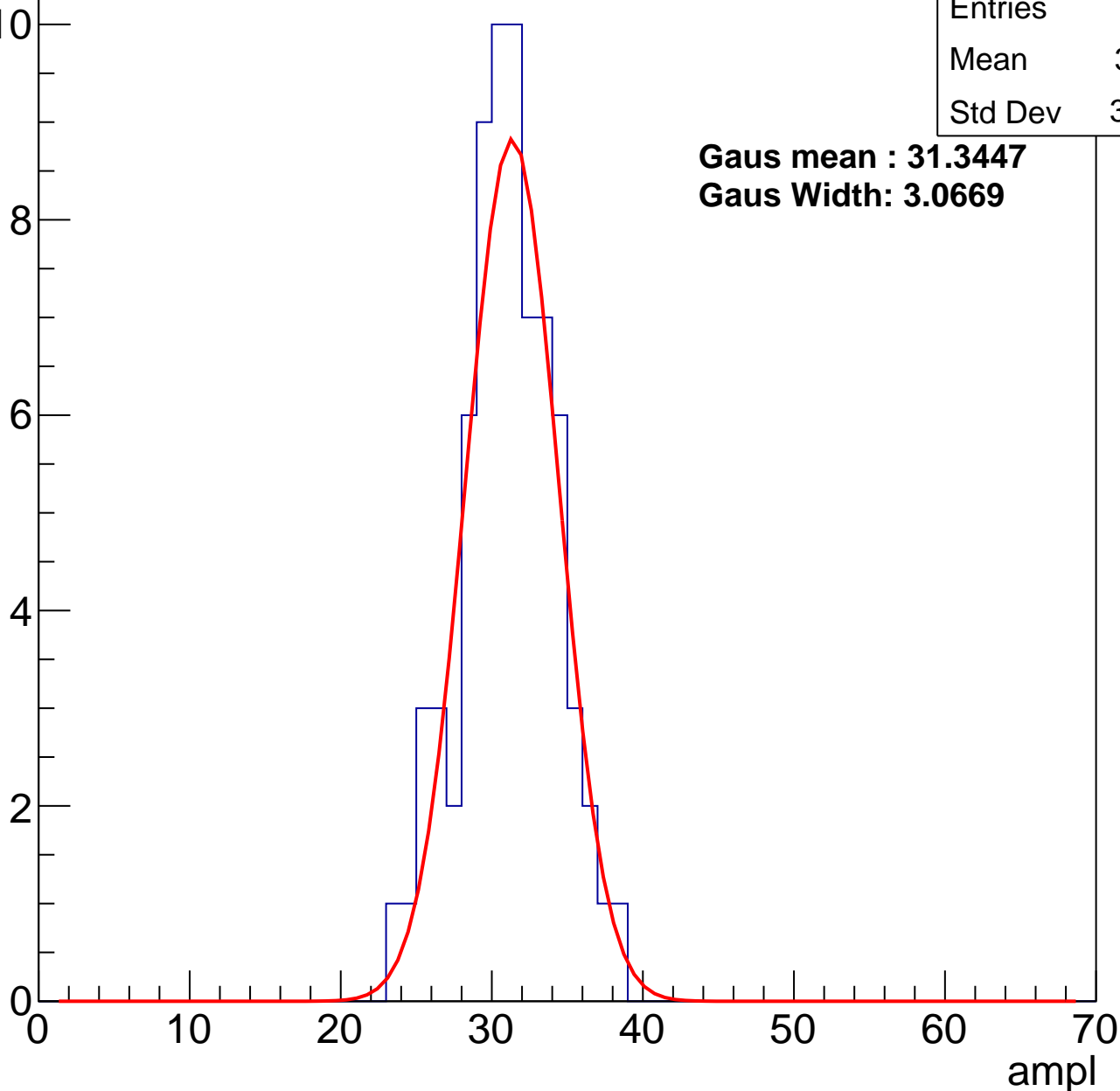
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.61
Std Dev	3.094

**Gaus mean : 31.3447**

**Gaus Width: 3.0669**



# B0L000S, U10-ch9, adc1

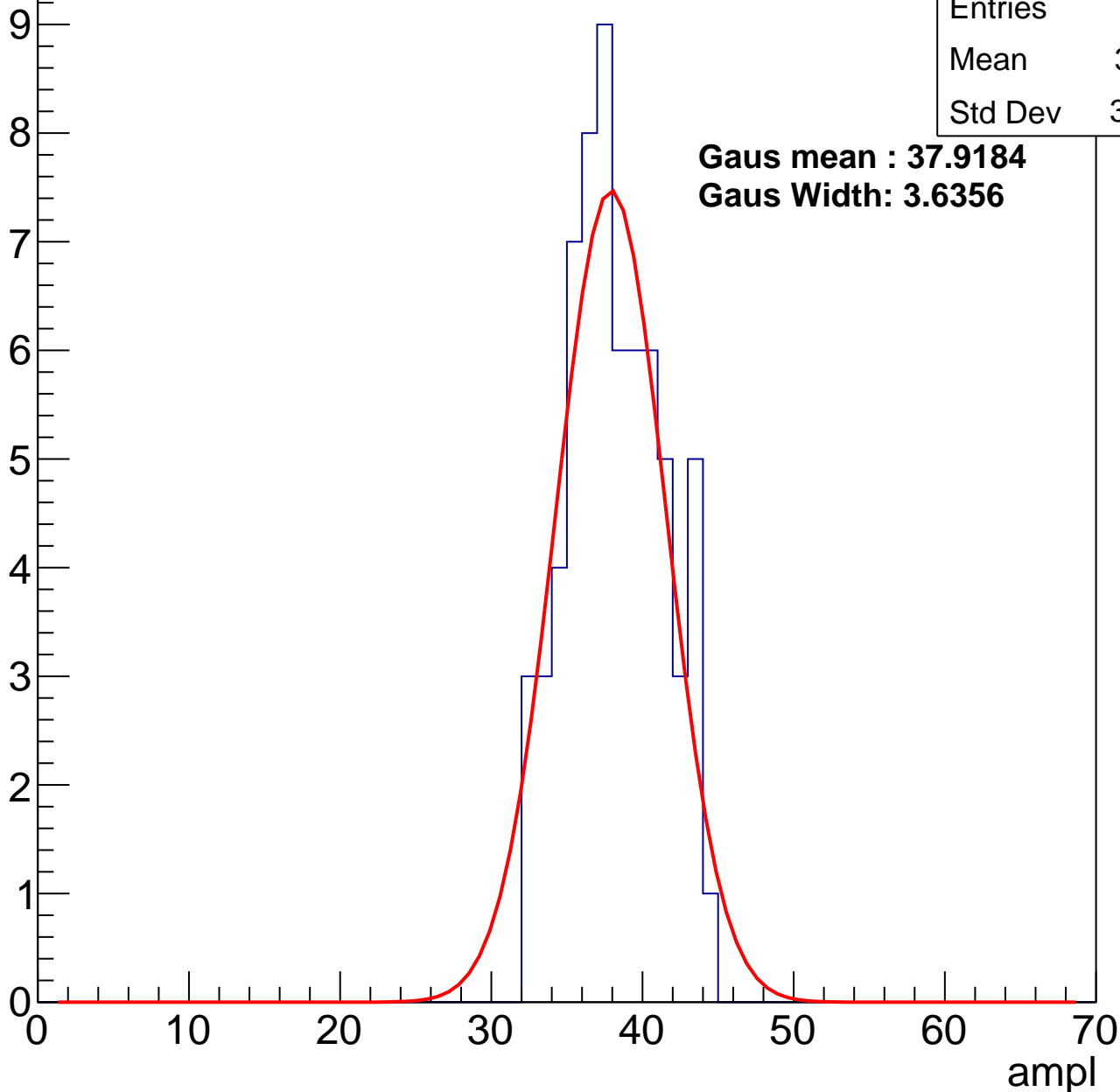
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.71
Std Dev	3.088

**Gaus mean : 37.9184**

**Gaus Width: 3.6356**



# B0L000S, U10-ch9, adc2

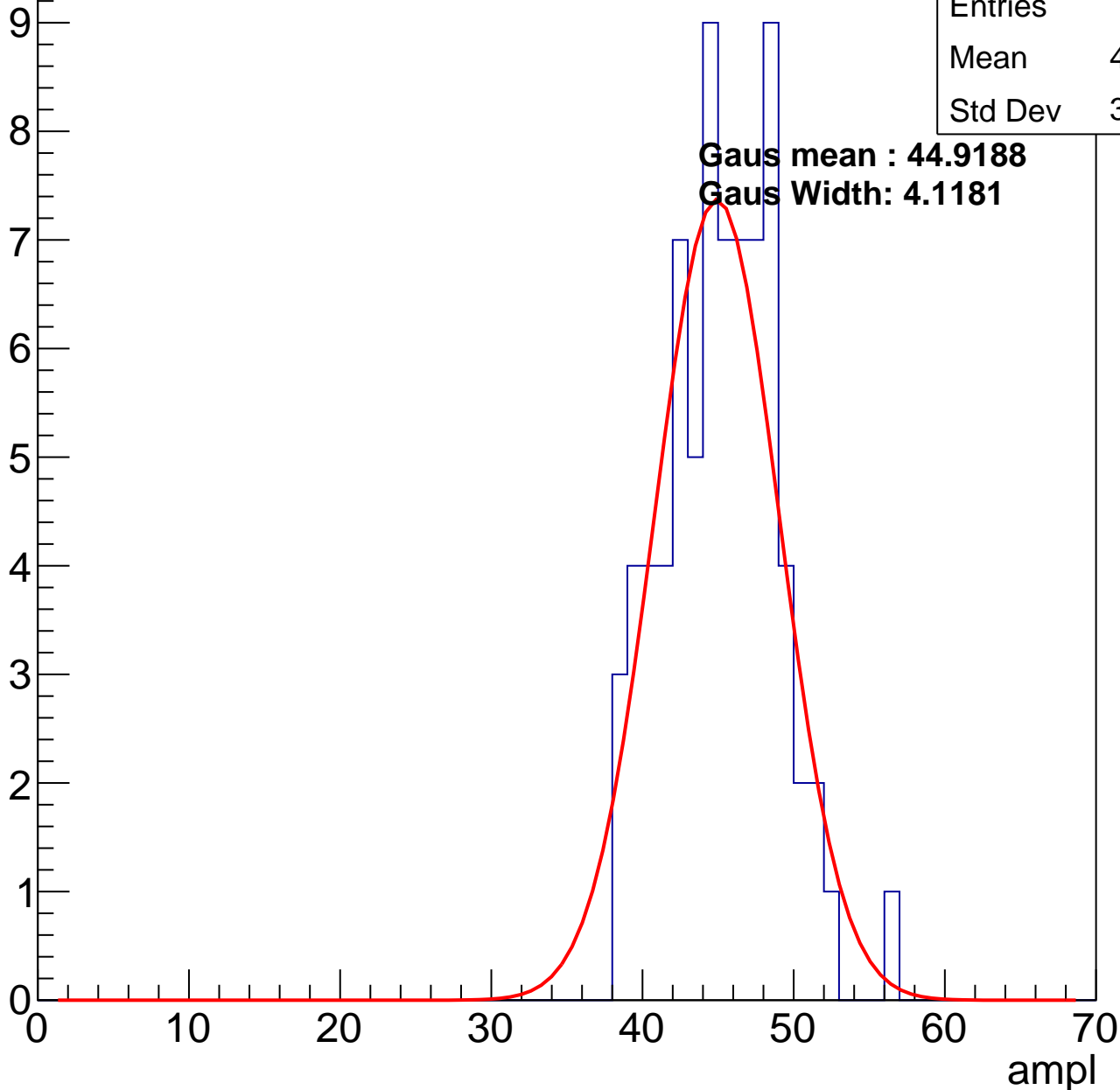
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	44.78
Std Dev	3.655

**Gaus mean : 44.9188**

**Gaus Width: 4.1181**

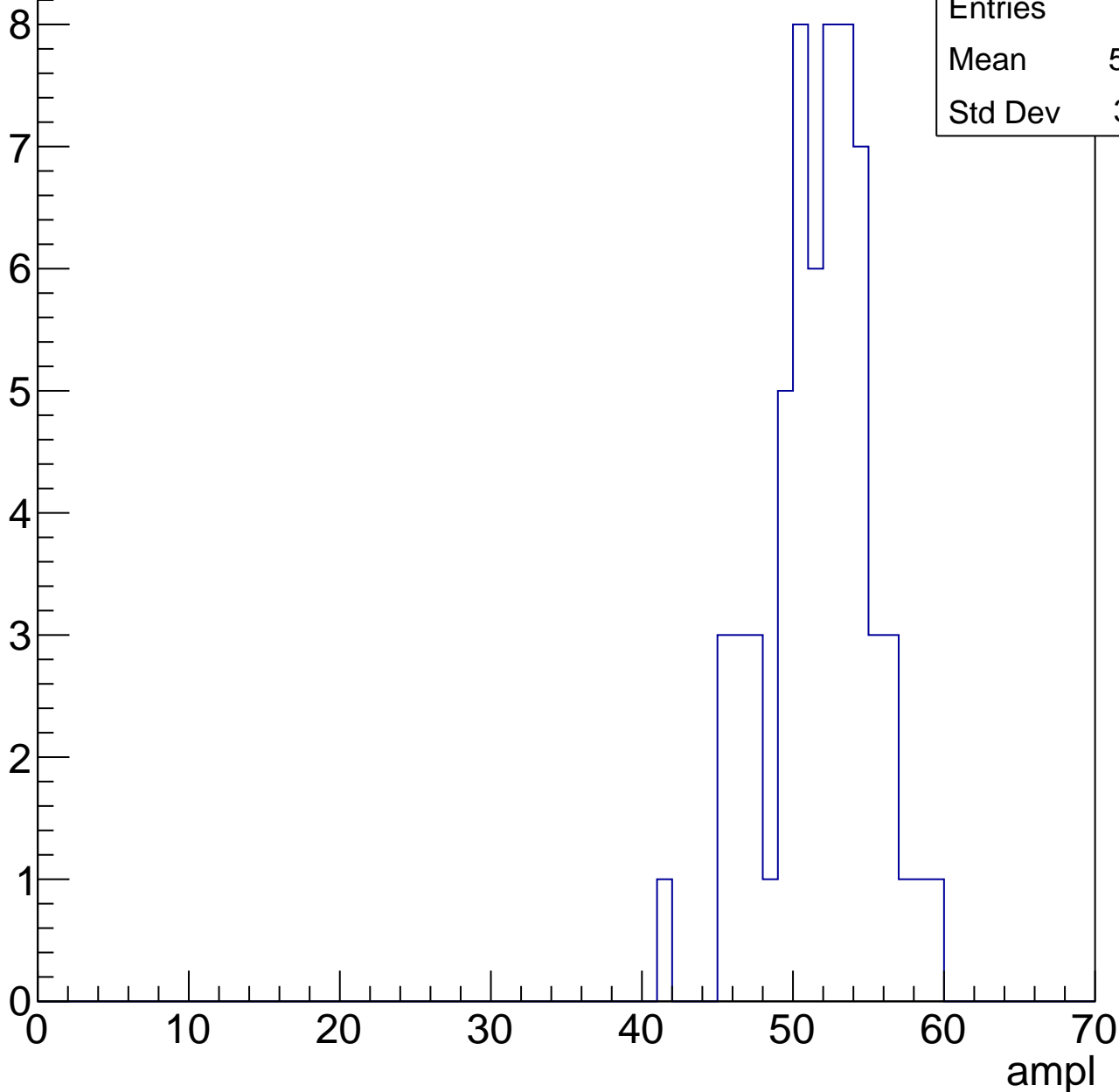


# B0L000S, U10-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	51.27
Std Dev	3.451

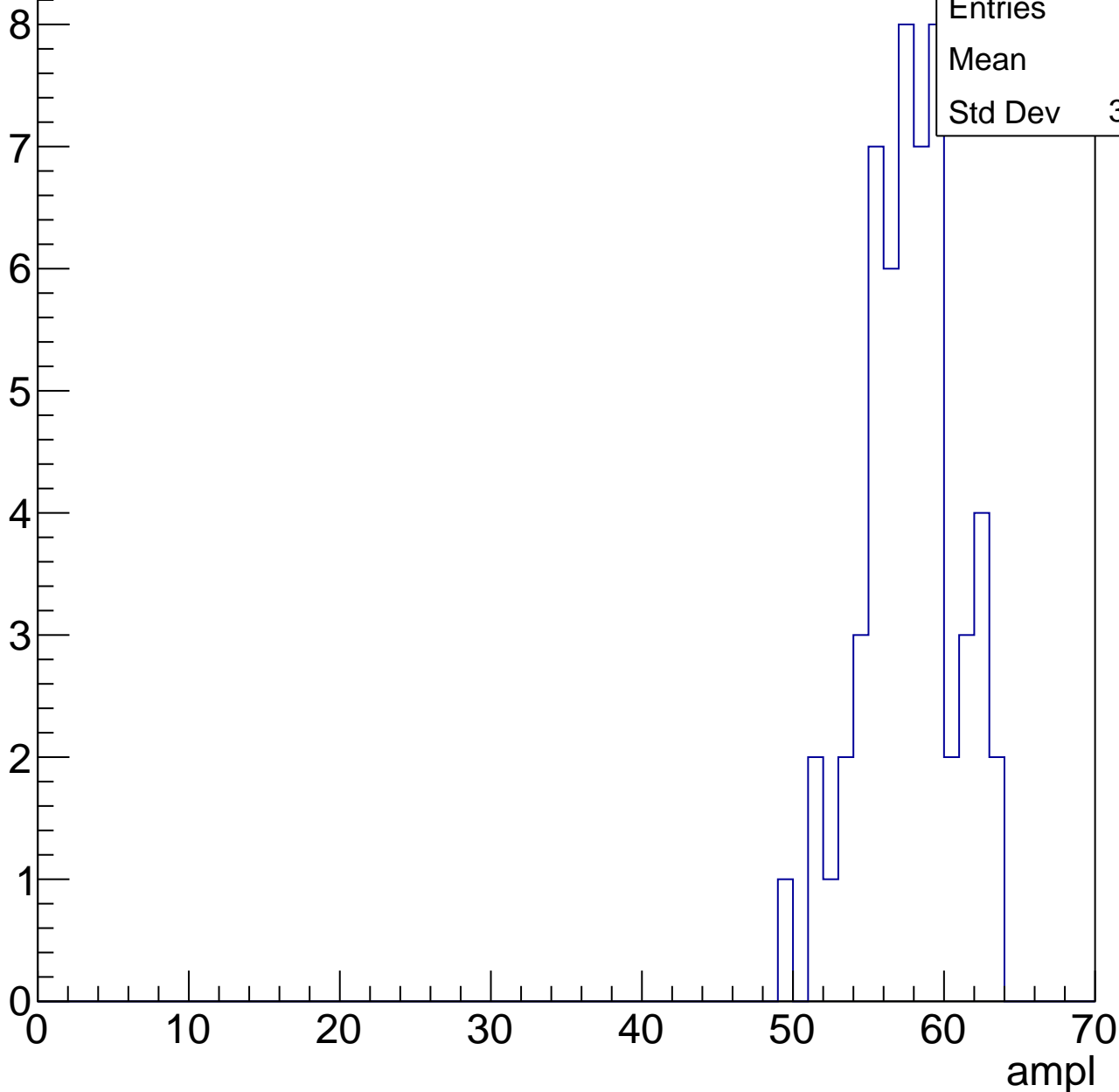


# B0L000S, U10-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

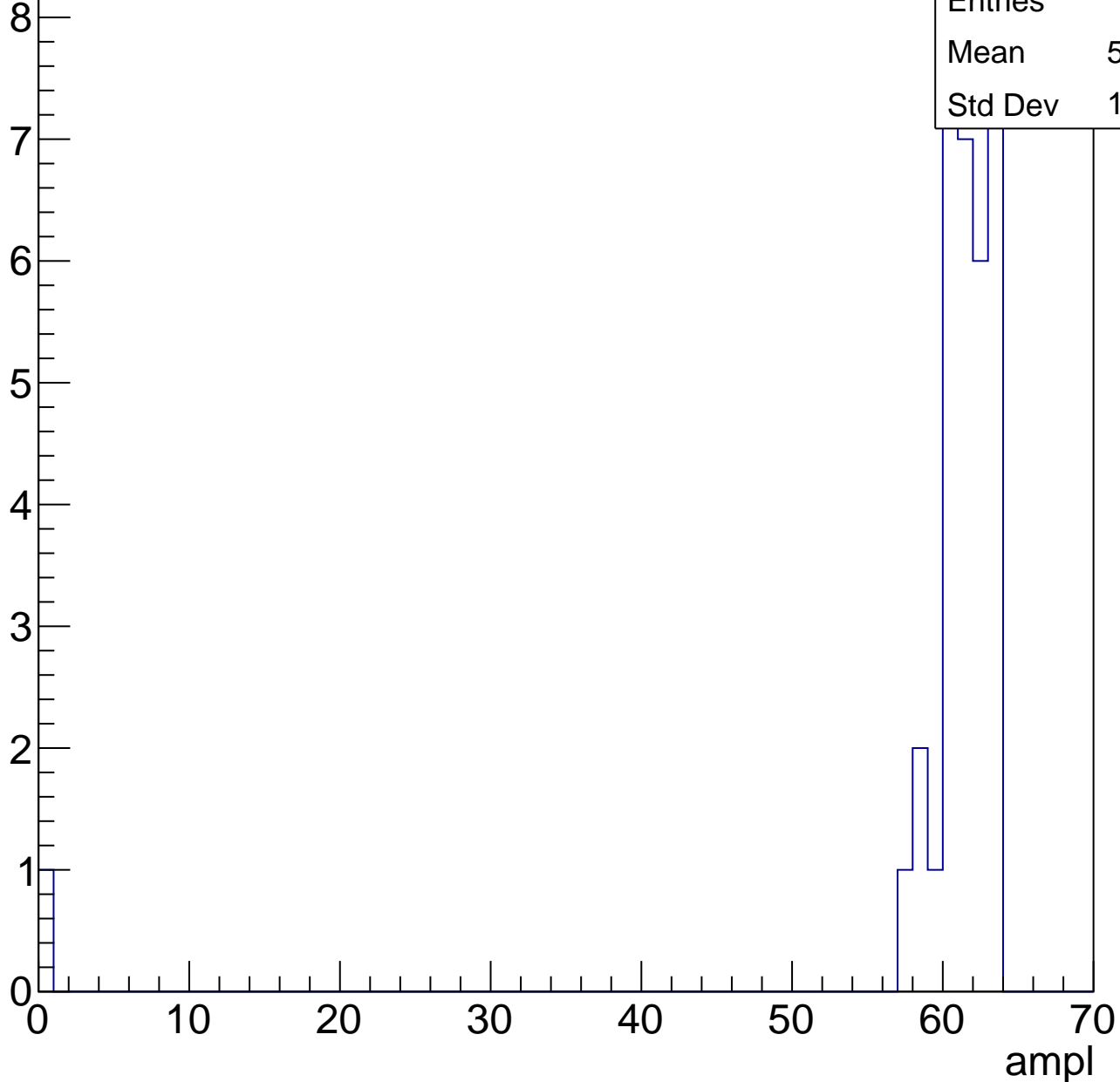
Entries	56
Mean	57.2
Std Dev	3.073



# B0L000S, U10-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



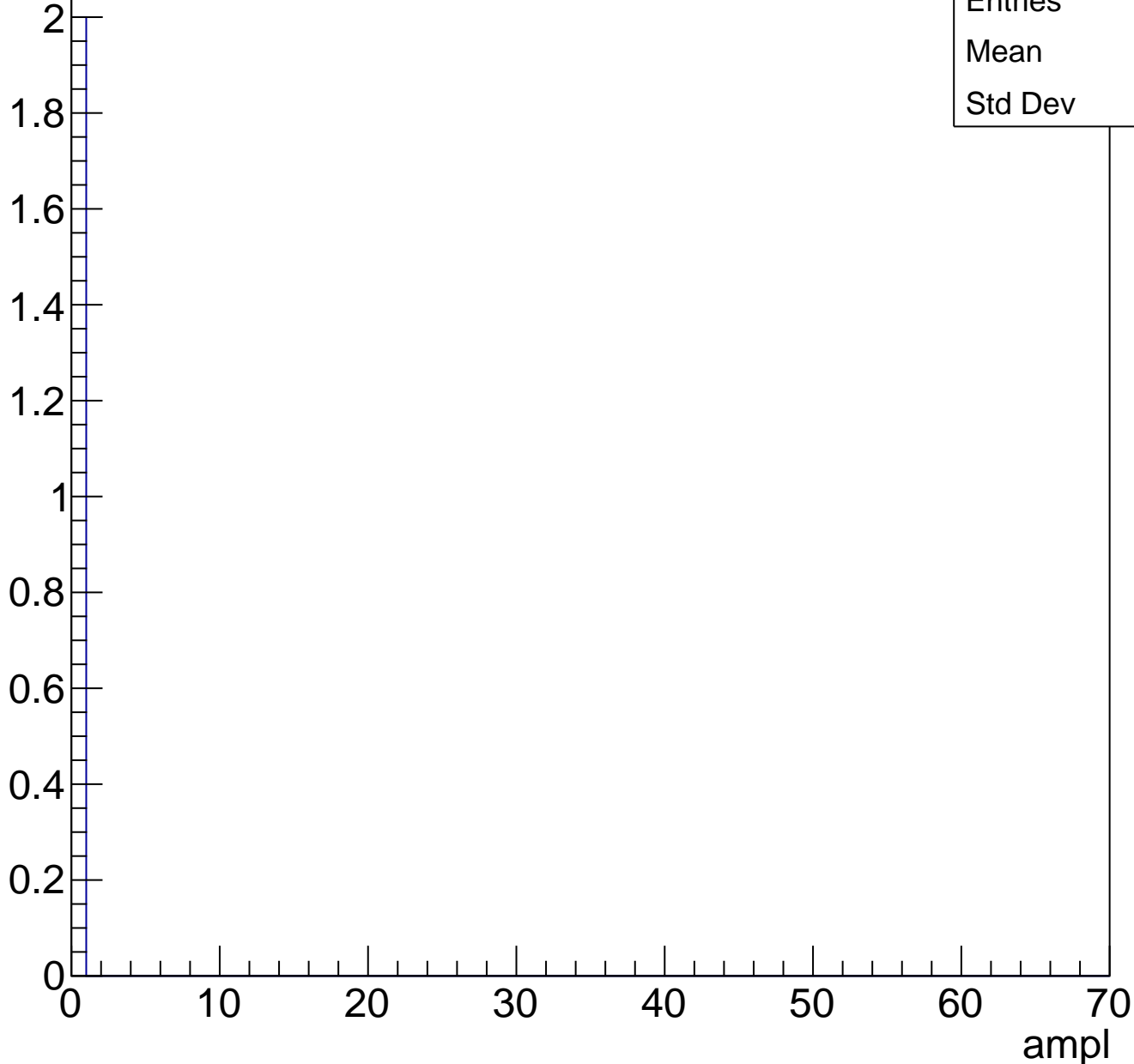
Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	31.37
Std Dev	3.419

**Gaus mean : 31.7088**

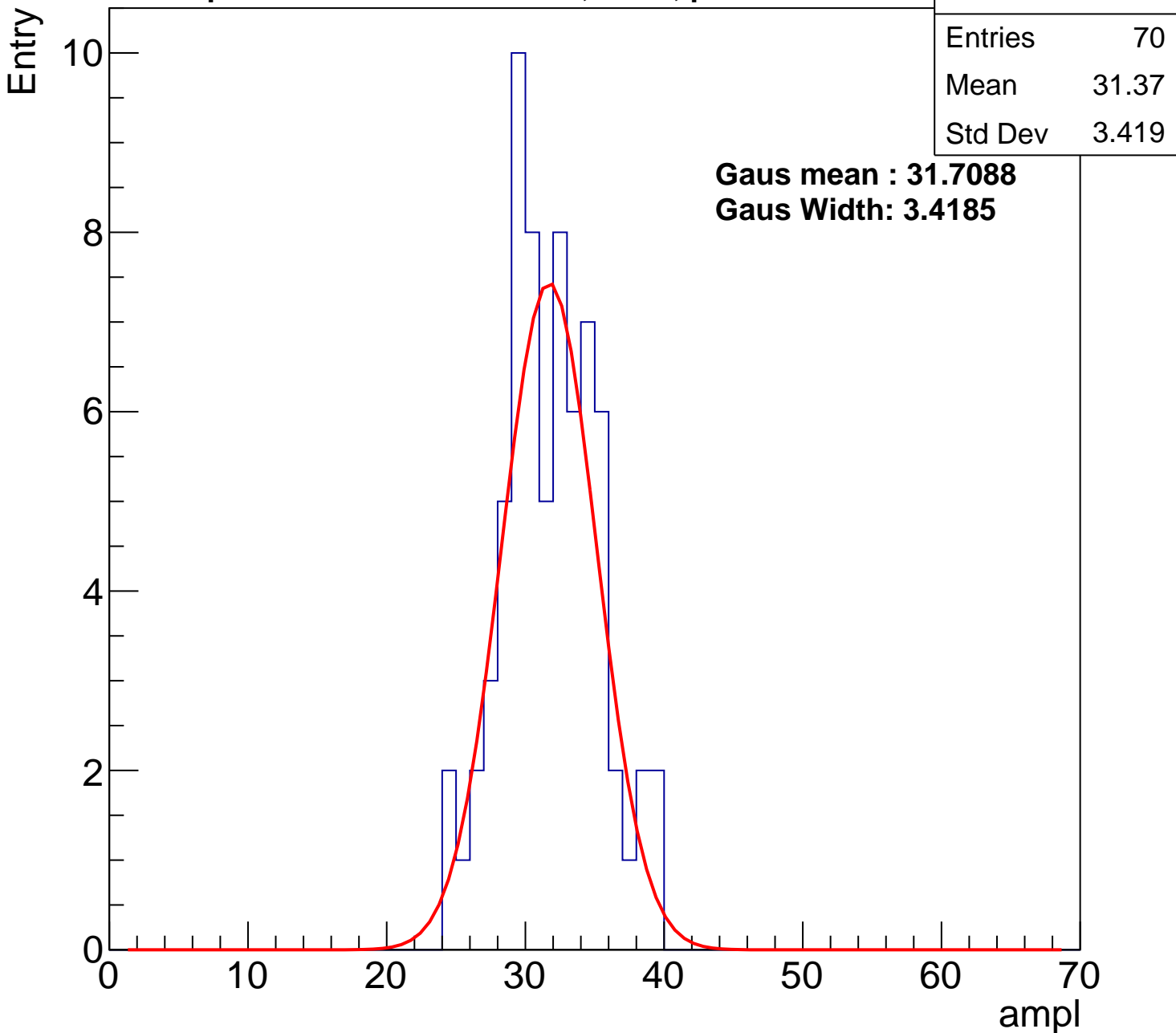
**Gaus Width: 3.4185**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch10, adc1

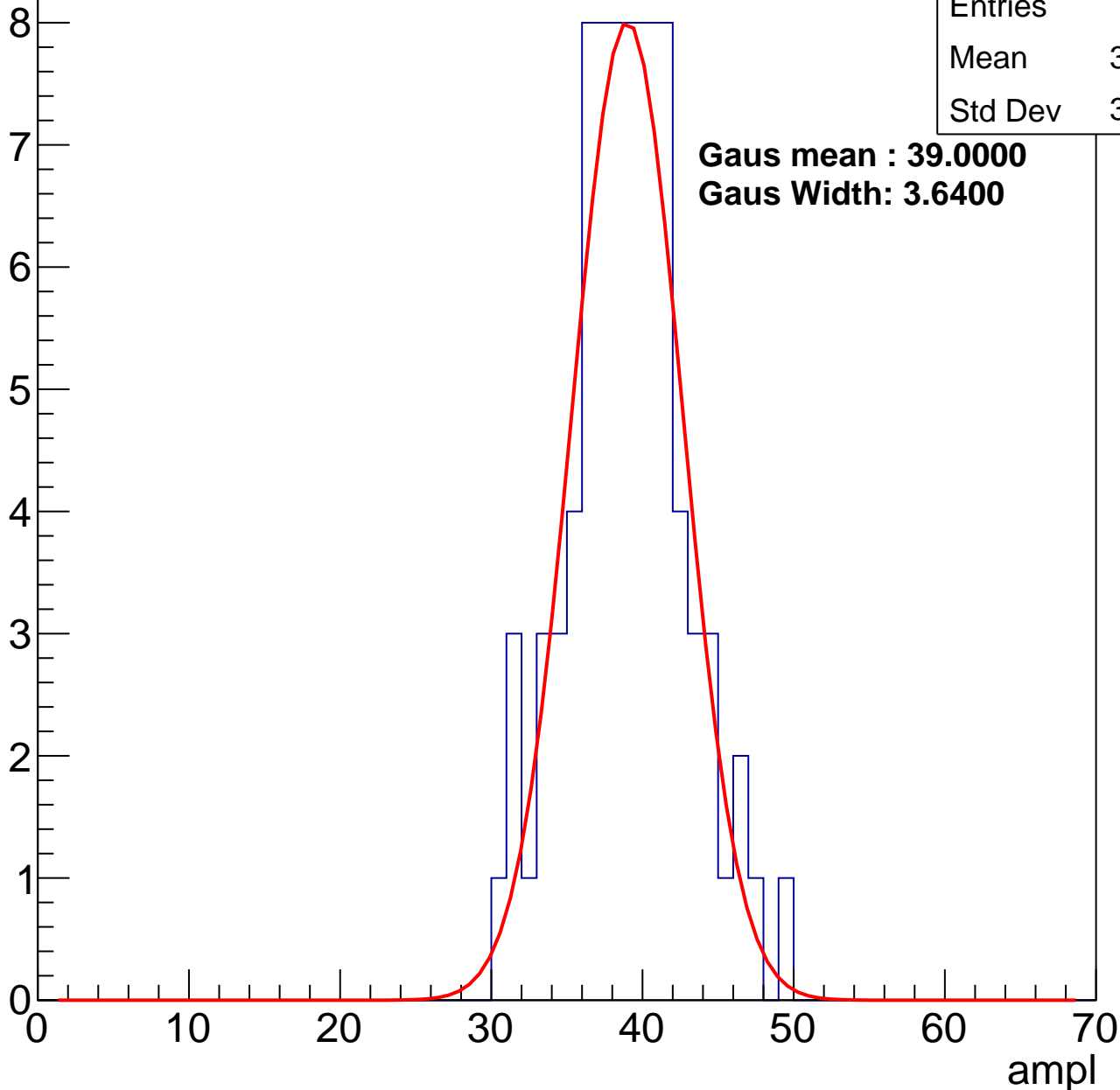
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	38.54
Std Dev	3.859

**Gaus mean : 39.0000**

**Gaus Width: 3.6400**



# B0L000S, U10-ch10, adc2

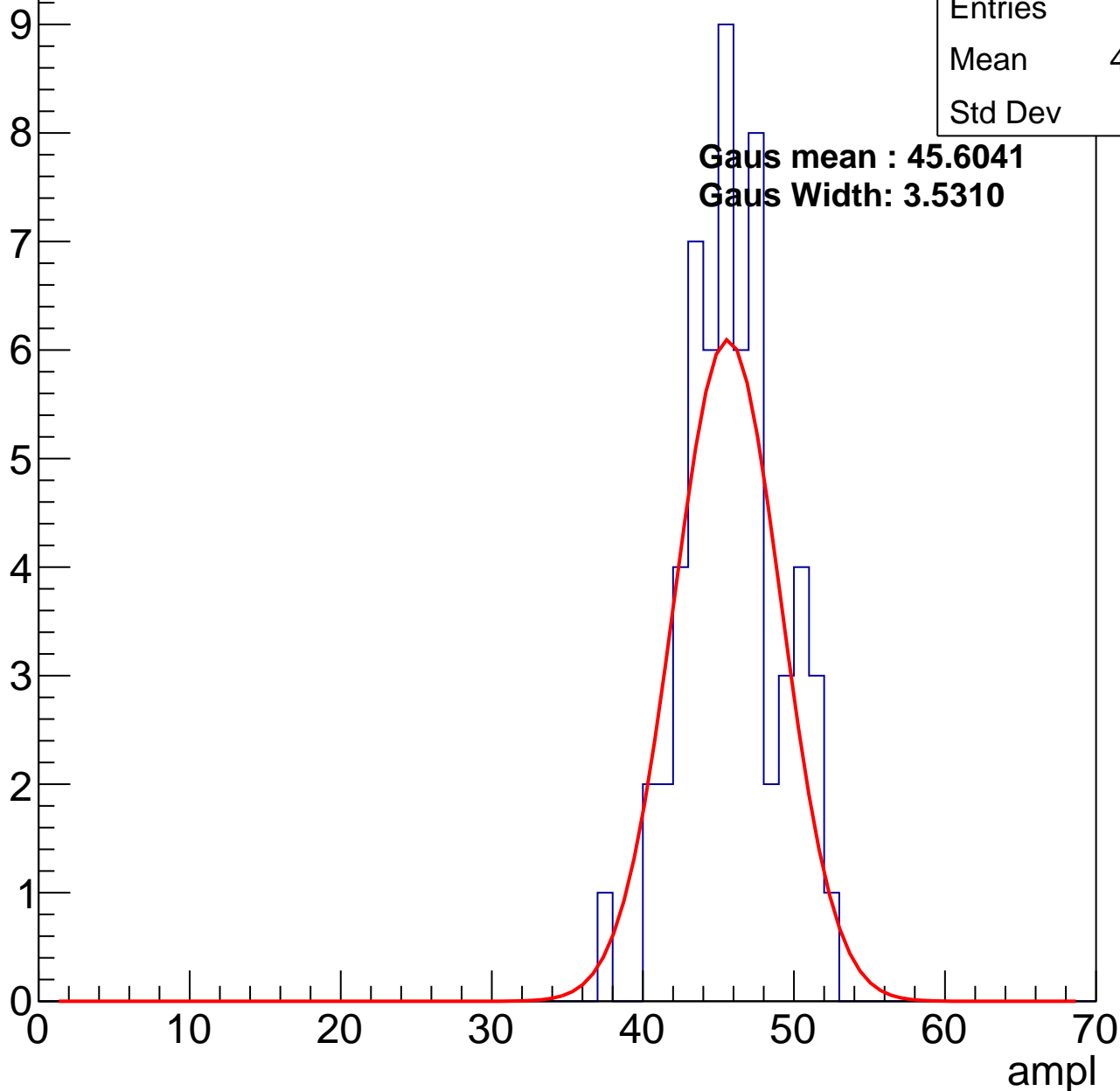
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.47
Std Dev	3.12

**Gaus mean : 45.6041**

**Gaus Width: 3.5310**

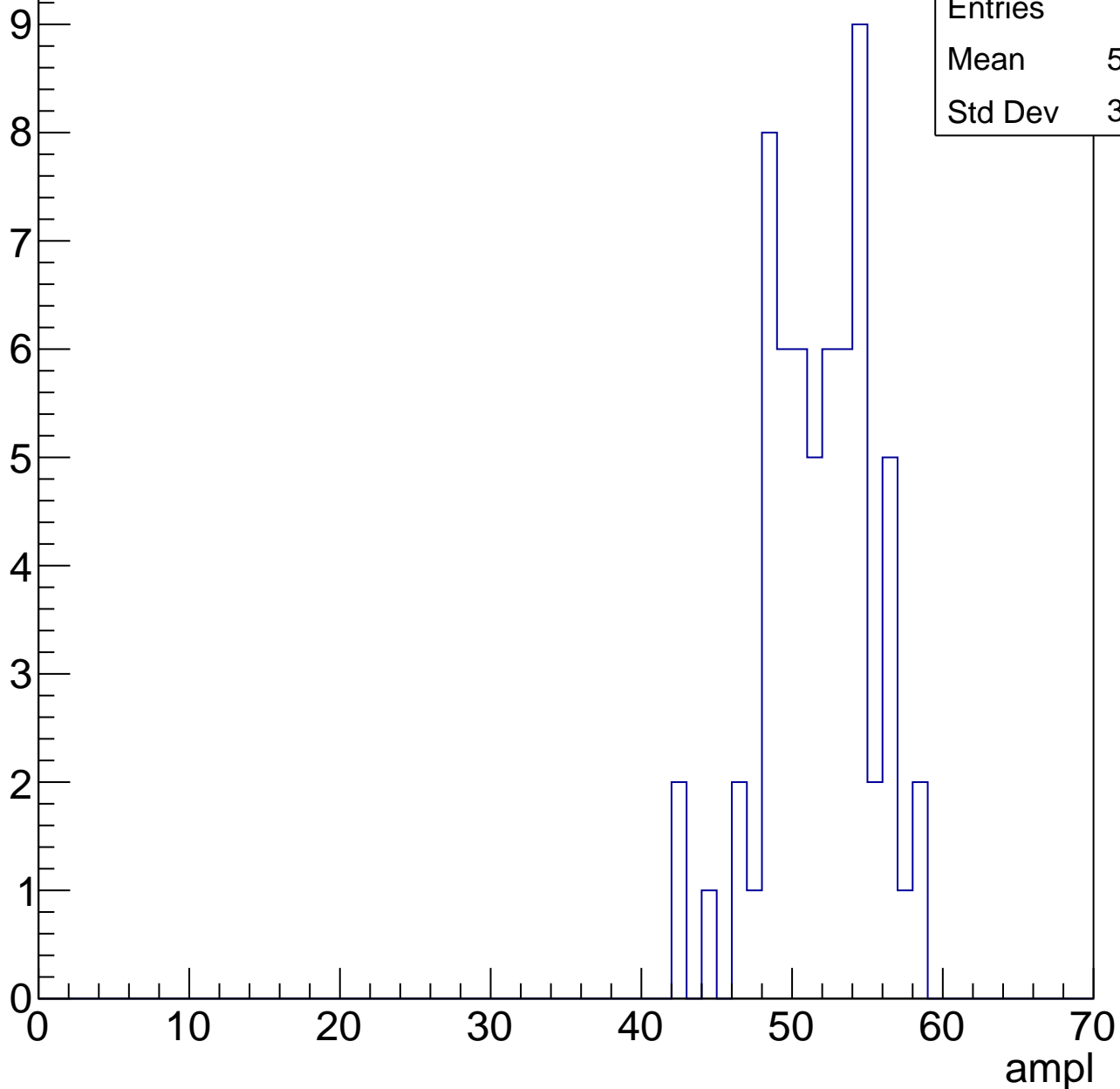


# B0L000S, U10-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	51.27
Std Dev	3.539

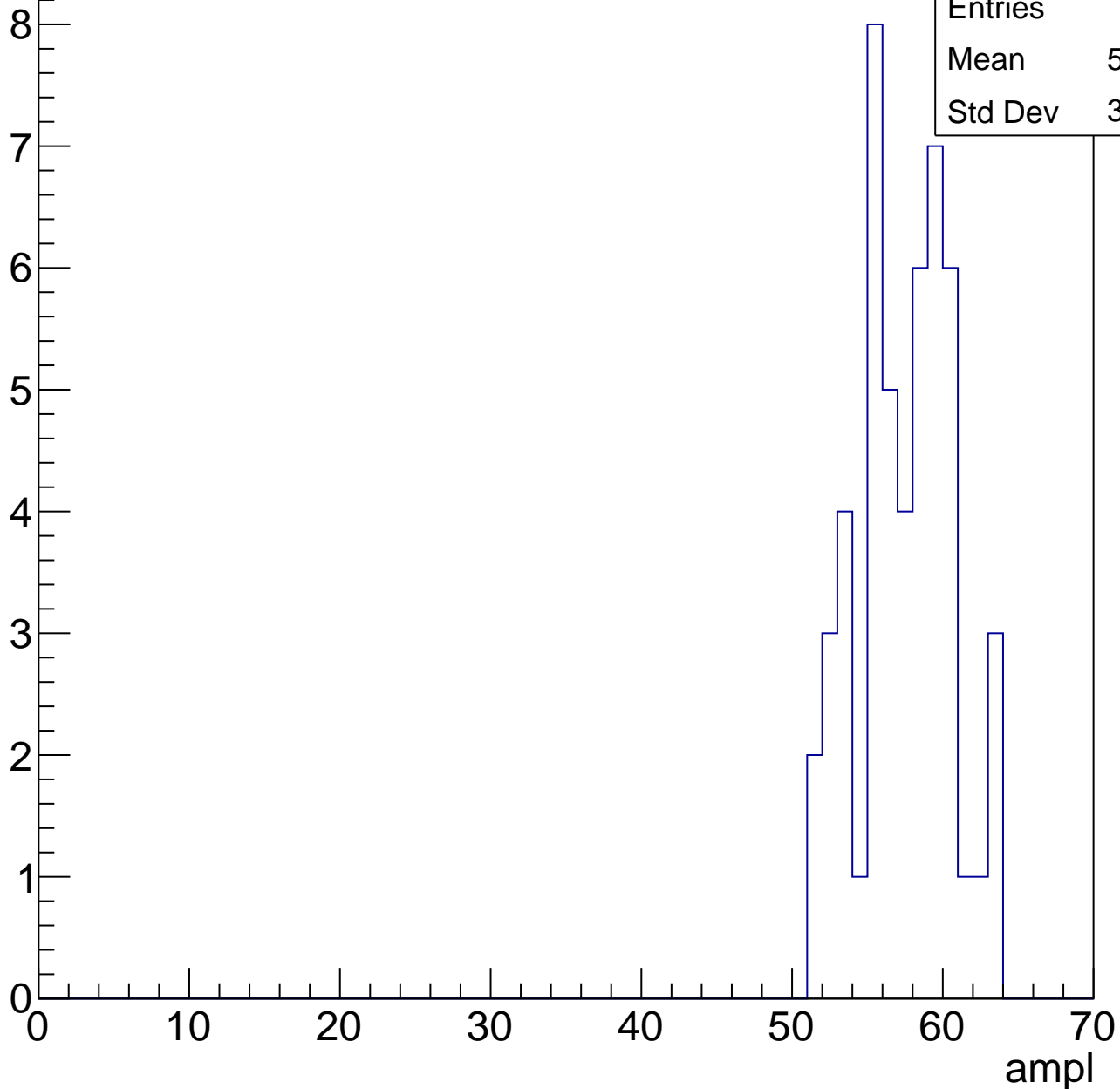


# B0L000S, U10-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

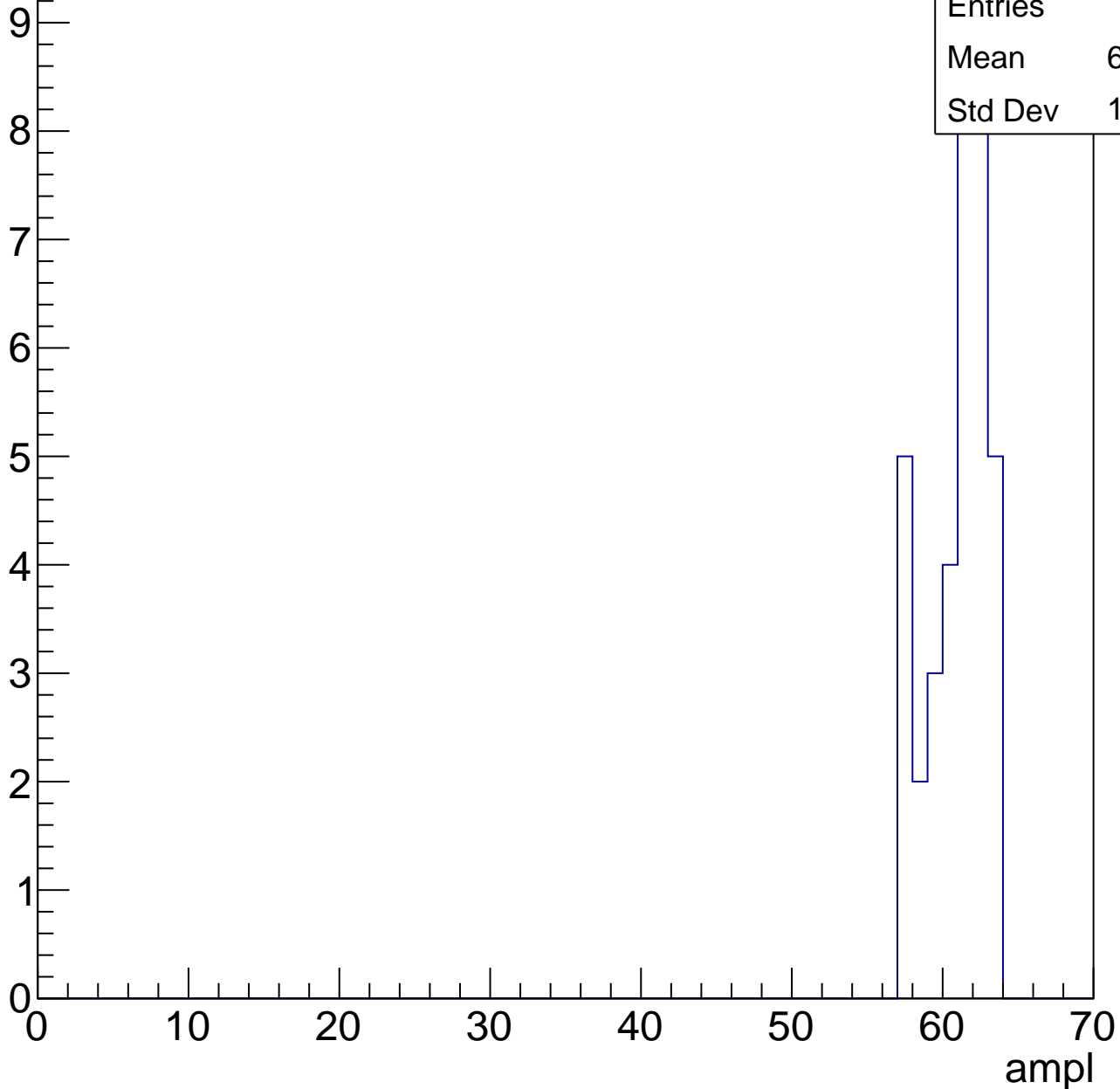
Entries	51
Mean	56.96
Std Dev	3.112



# B0L000S, U10-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	36
Mean	60.53
Std Dev	1.936

# B0L000S, U10-ch10, adc6

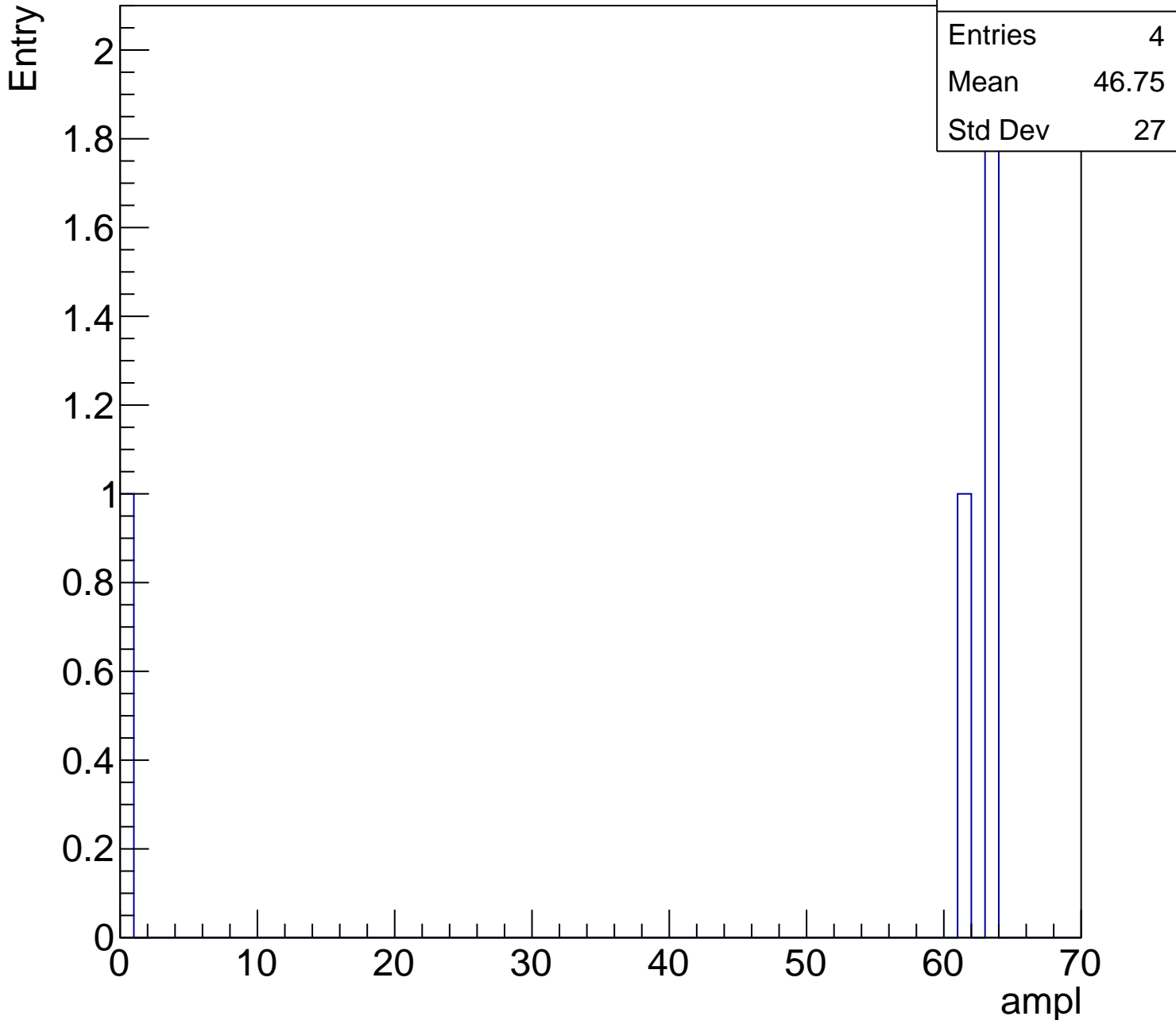
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	46.75
Std Dev	27

ampl





# B0L000S, U10-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch11, adc0

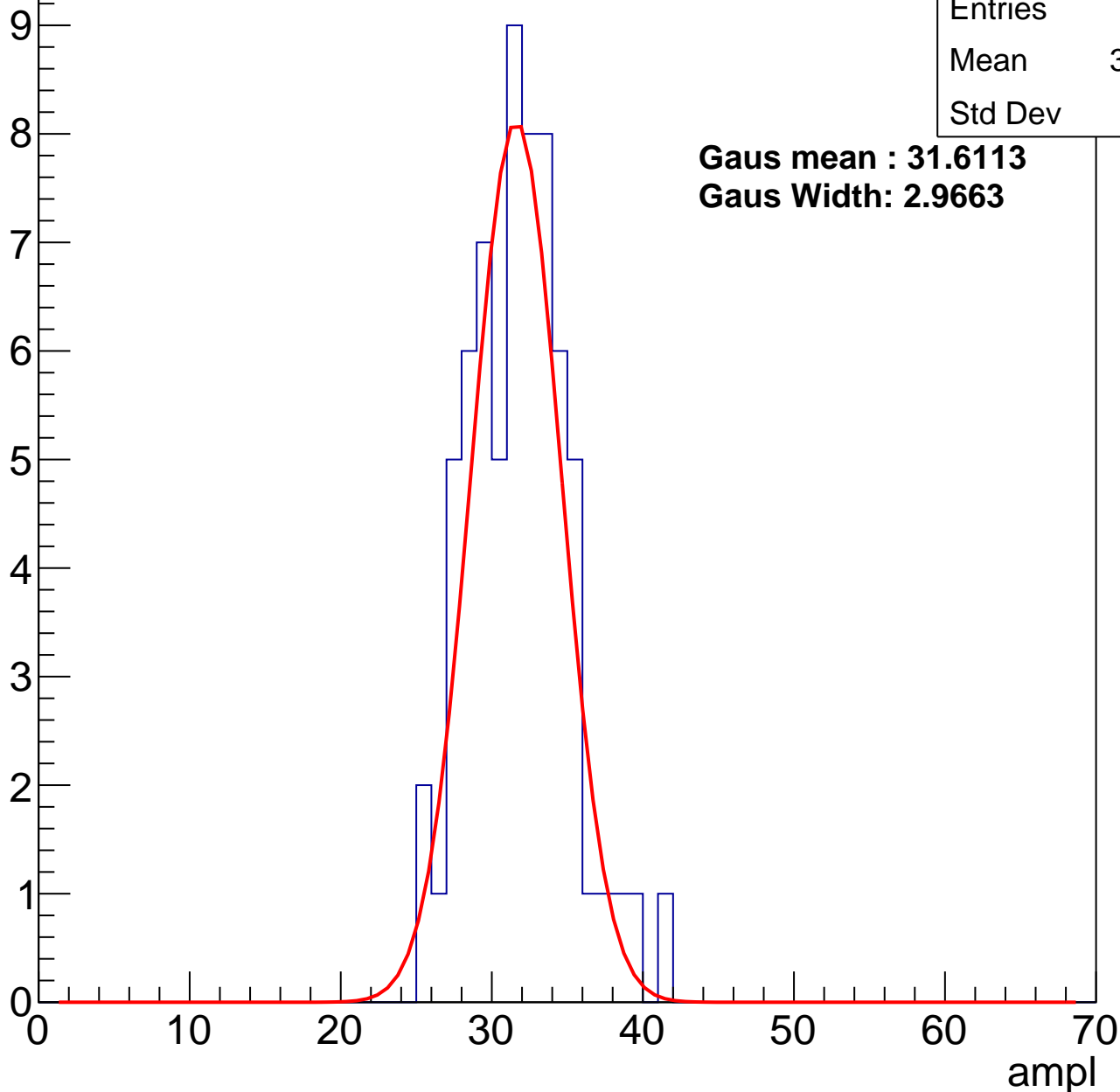
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	31.36
Std Dev	3.24

**Gaus mean : 31.6113**

**Gaus Width: 2.9663**



# B0L000S, U10-ch11, adc1

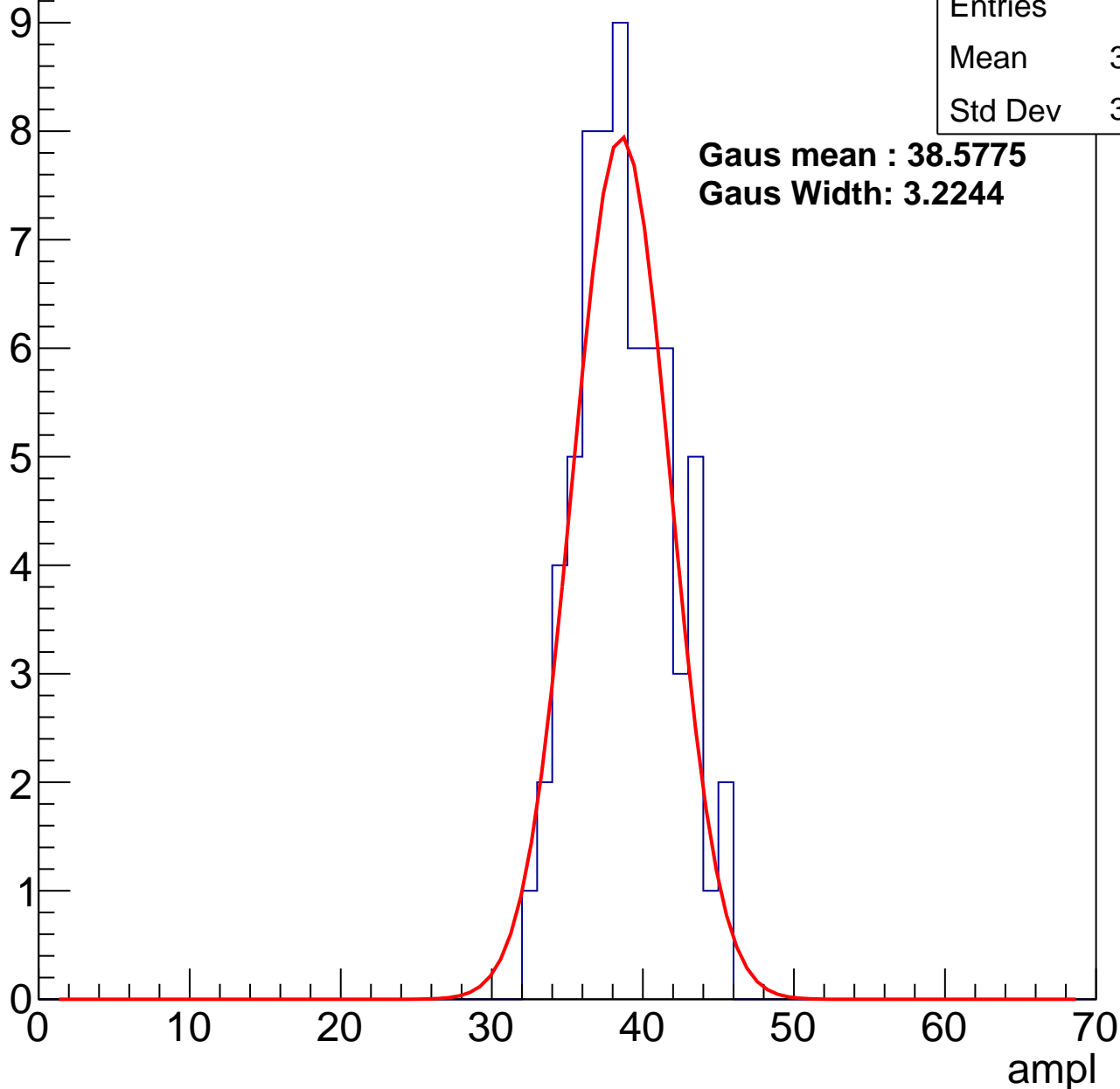
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	38.33
Std Dev	3.062

**Gaus mean : 38.5775**

**Gaus Width: 3.2244**



# B0L000S, U10-ch11, adc2

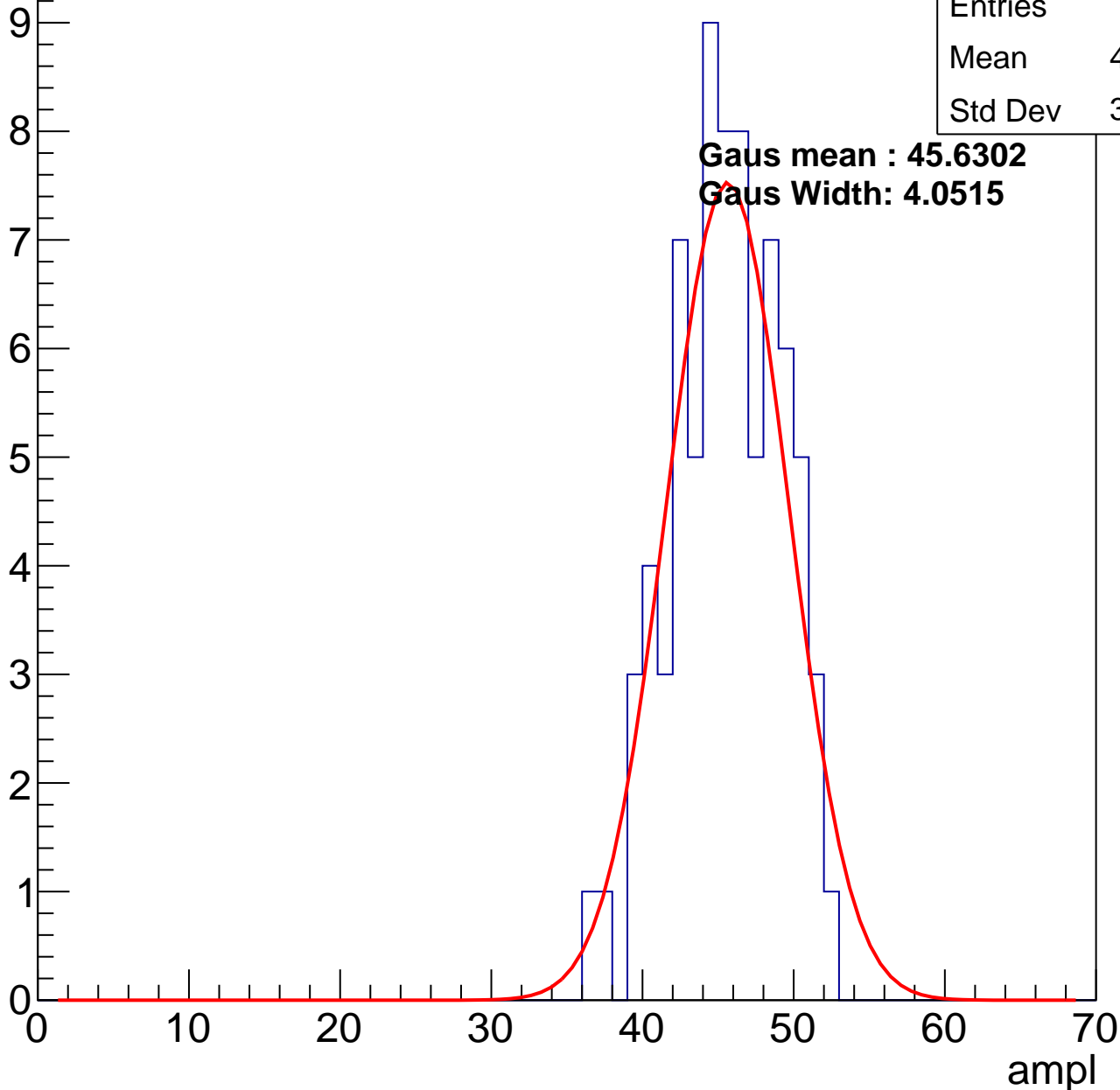
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	45.08
Std Dev	3.557

**Gaus mean : 45.6302**

**Gaus Width: 4.0515**

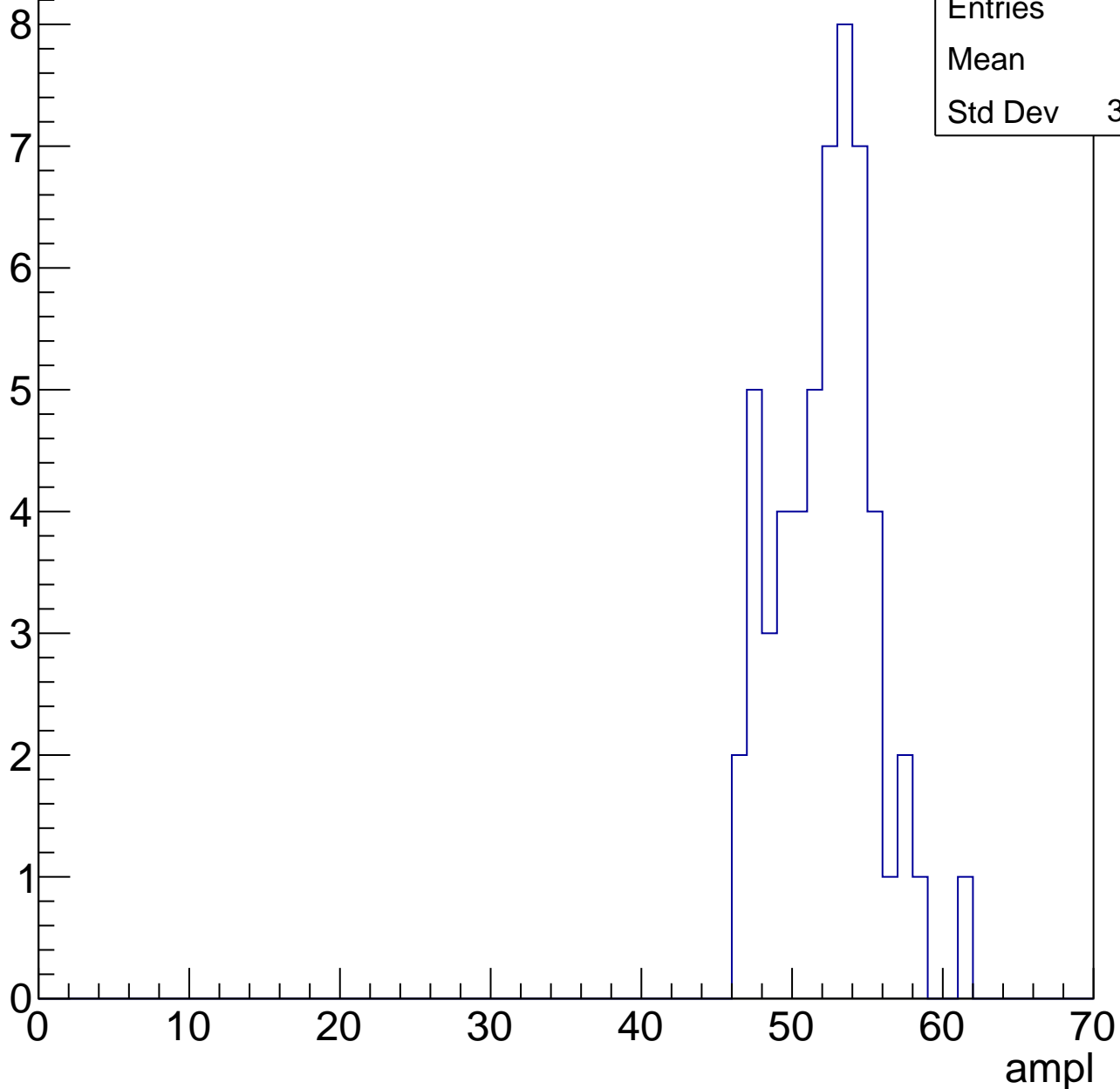


# B0L000S, U10-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	51.8
Std Dev	3.205

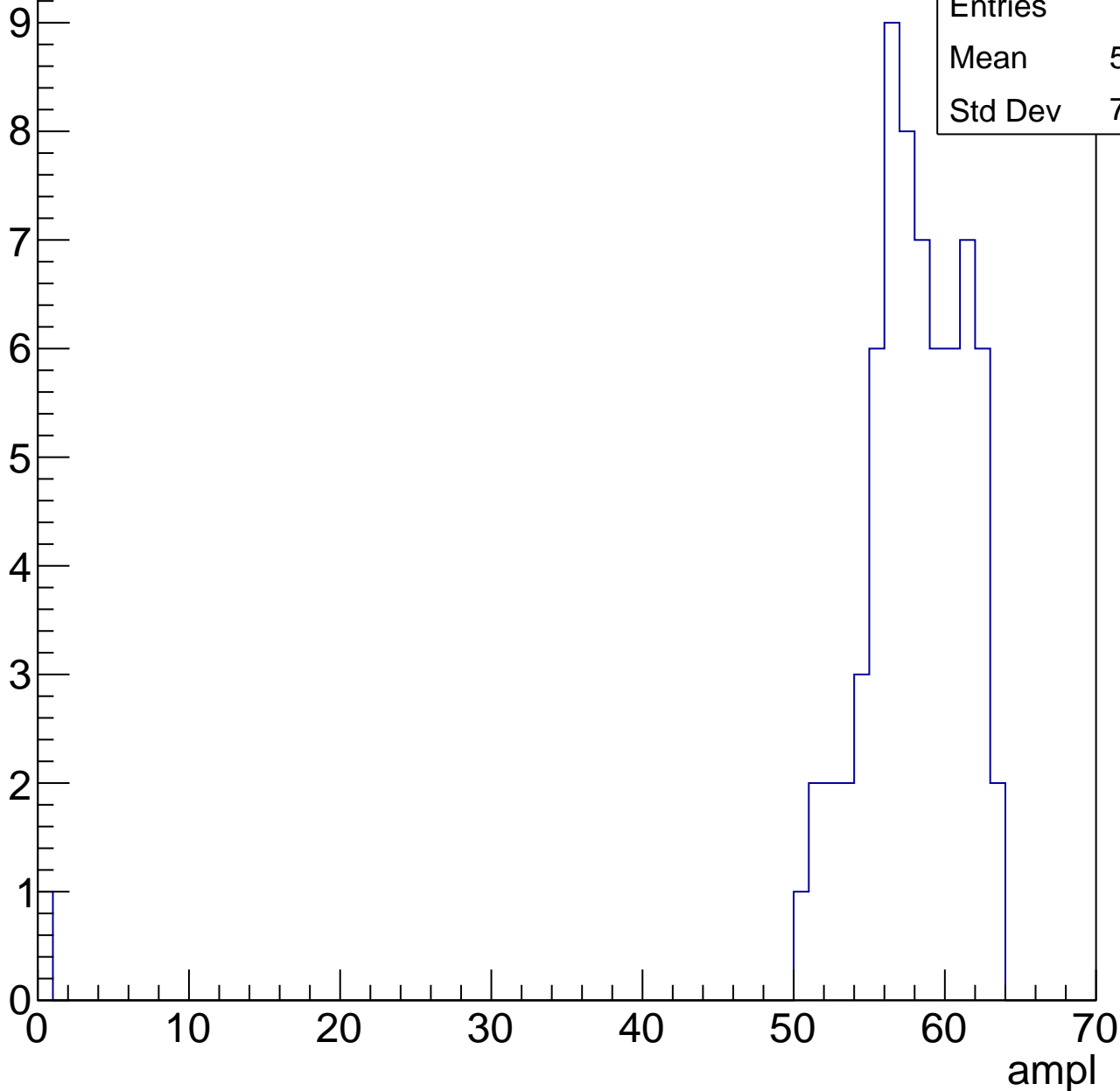


# B0L000S, U10-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	56.75
Std Dev	7.599

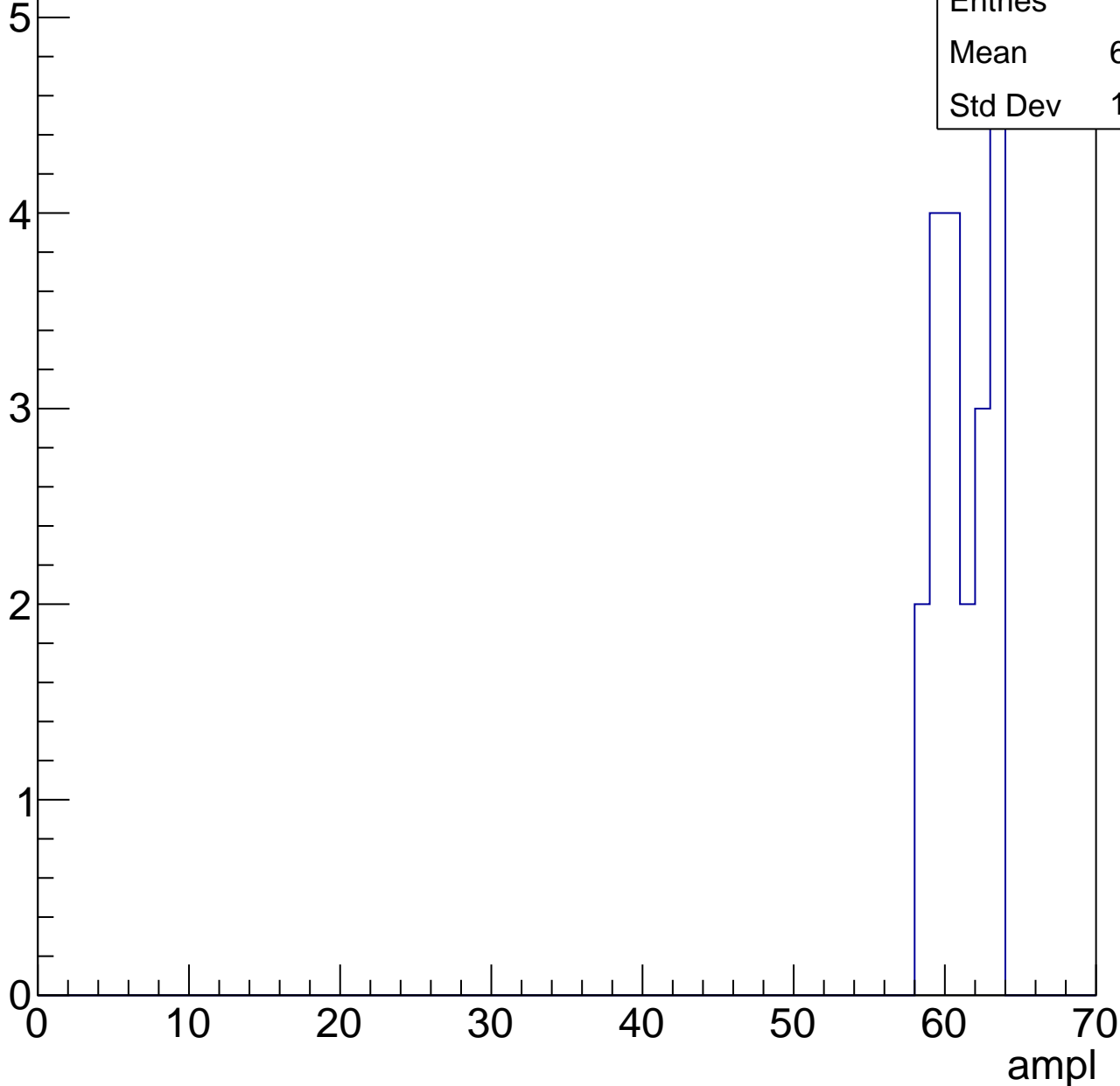


# B0L000S, U10-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	60.75
Std Dev	1.728



# B0L000S, U10-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L000S, U10-ch12, adc0

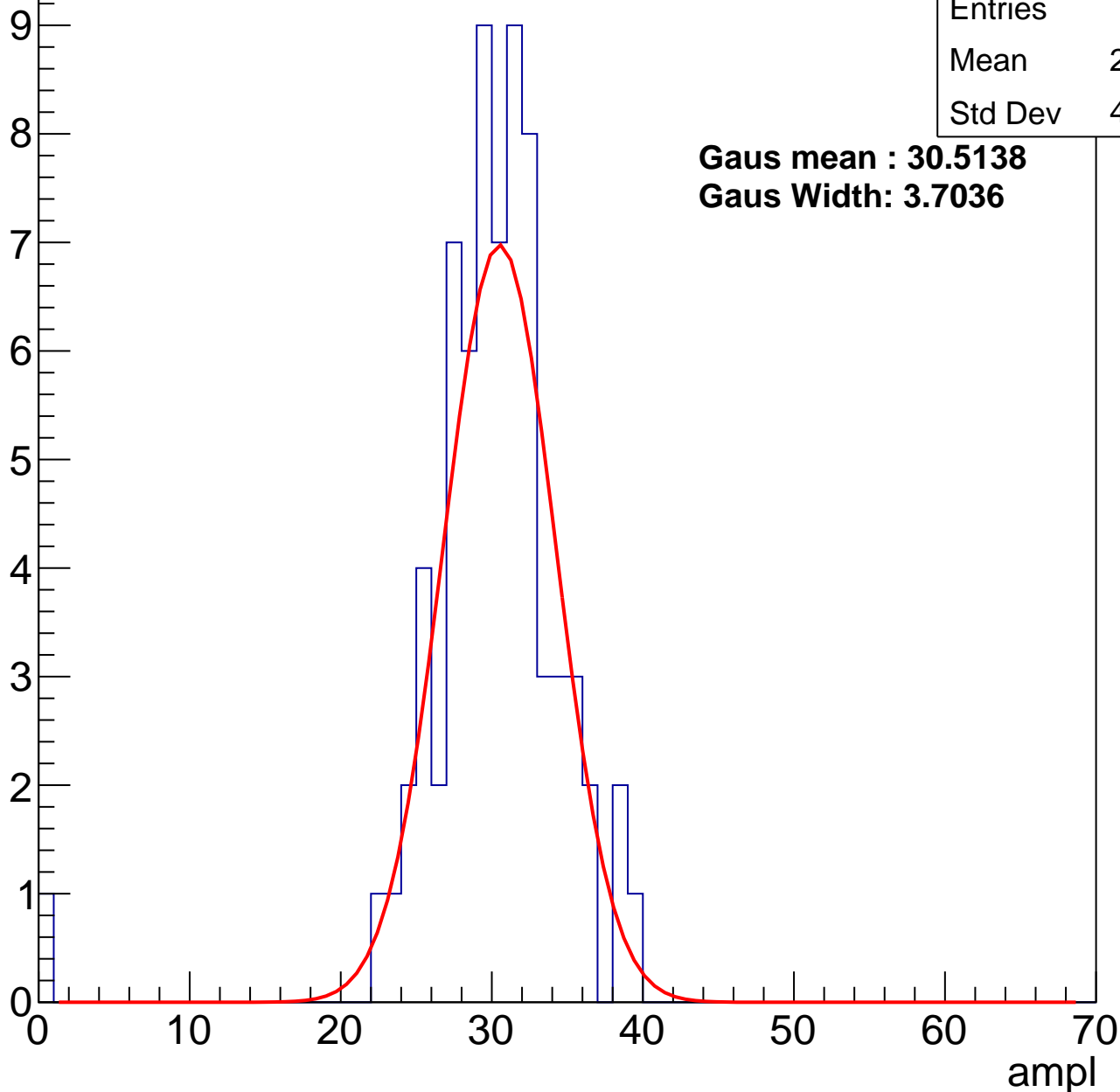
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.59
Std Dev	4.997

**Gaus mean : 30.5138**

**Gaus Width: 3.7036**



# B0L000S, U10-ch12, adc1

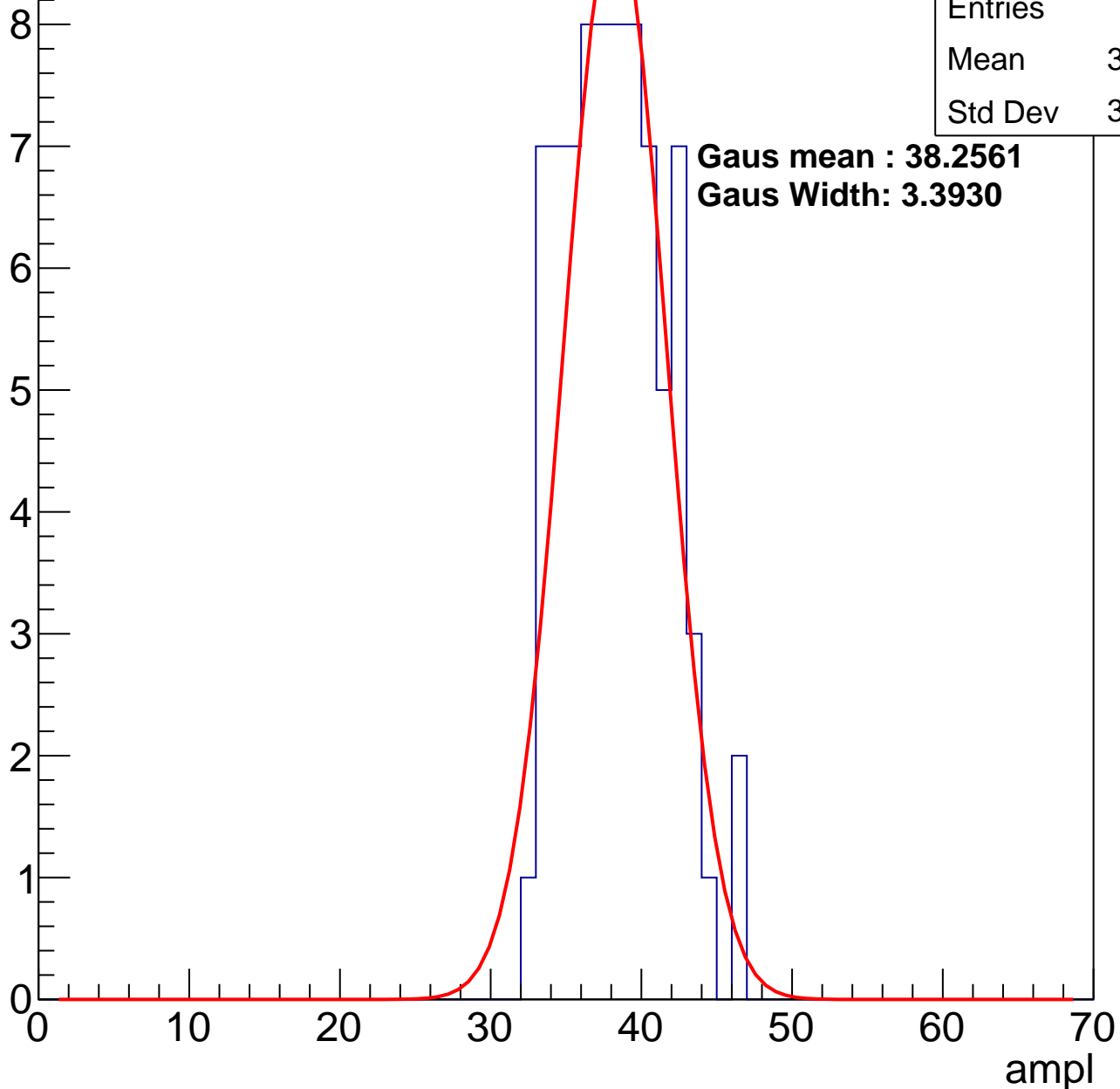
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	37.85
Std Dev	3.292

**Gaus mean : 38.2561**

**Gaus Width: 3.3930**



# B0L000S, U10-ch12, adc2

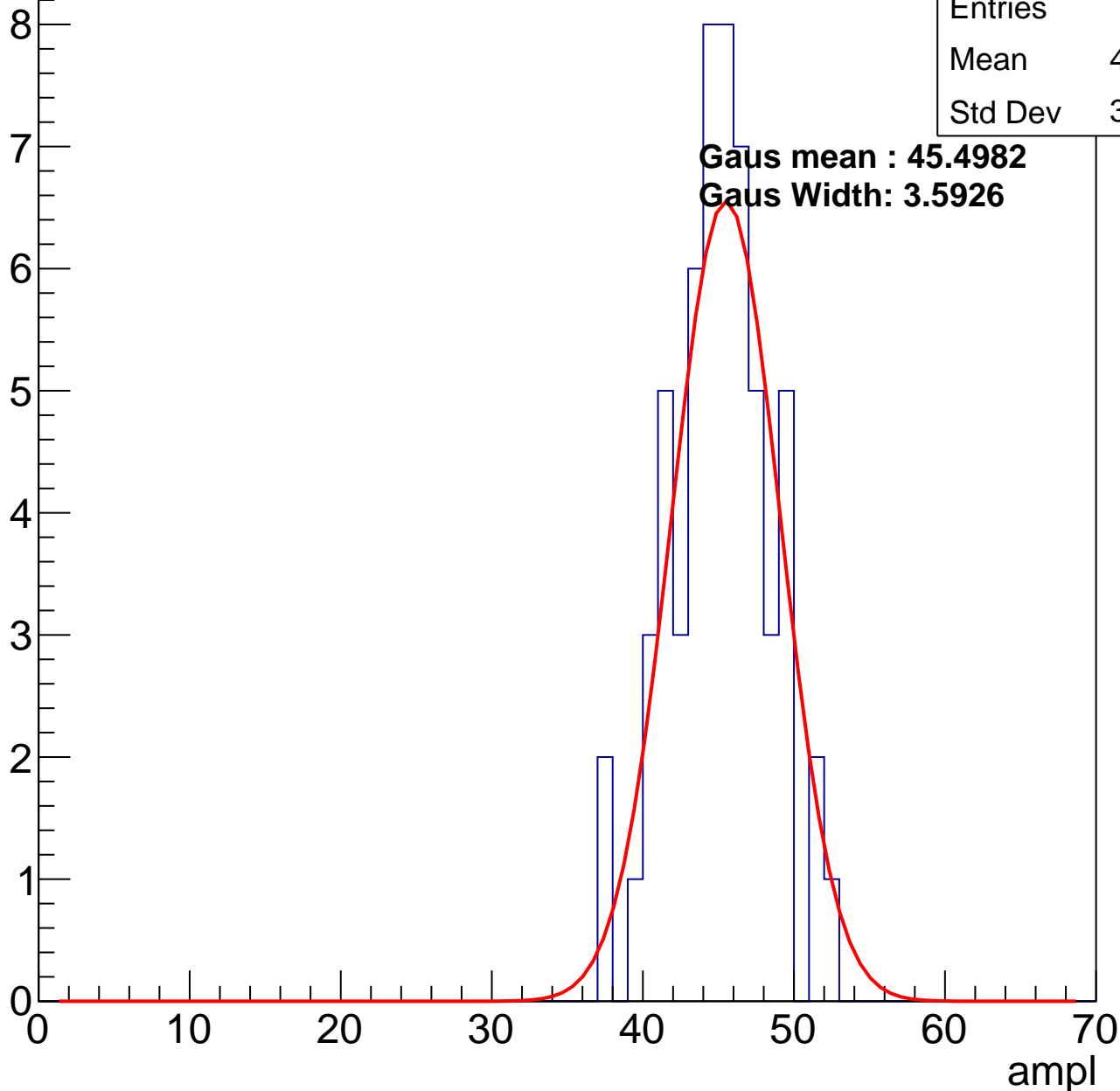
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	44.64
Std Dev	3.256

**Gaus mean : 45.4982**

**Gaus Width: 3.5926**

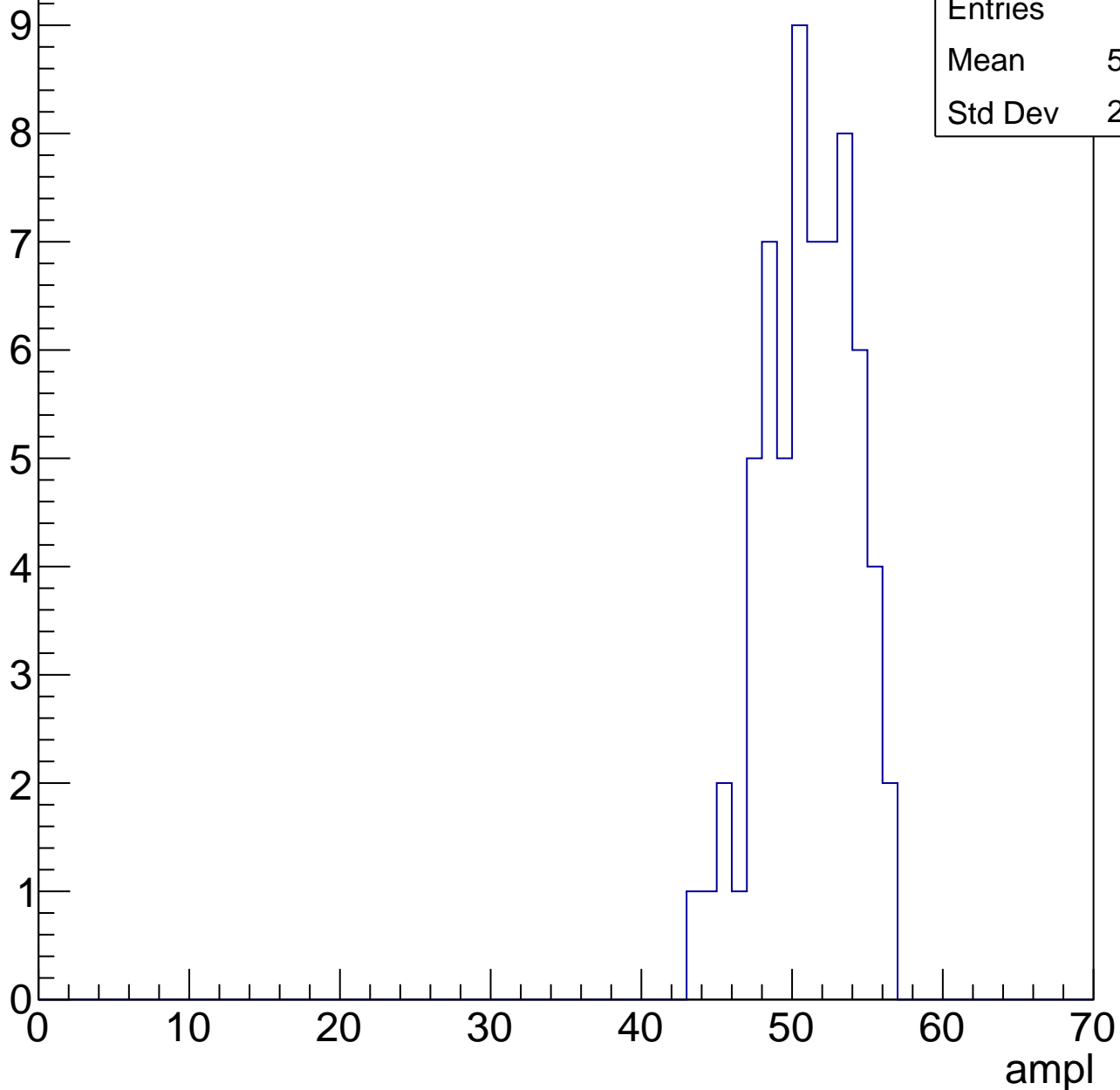


# B0L000S, U10-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	50.62
Std Dev	2.986

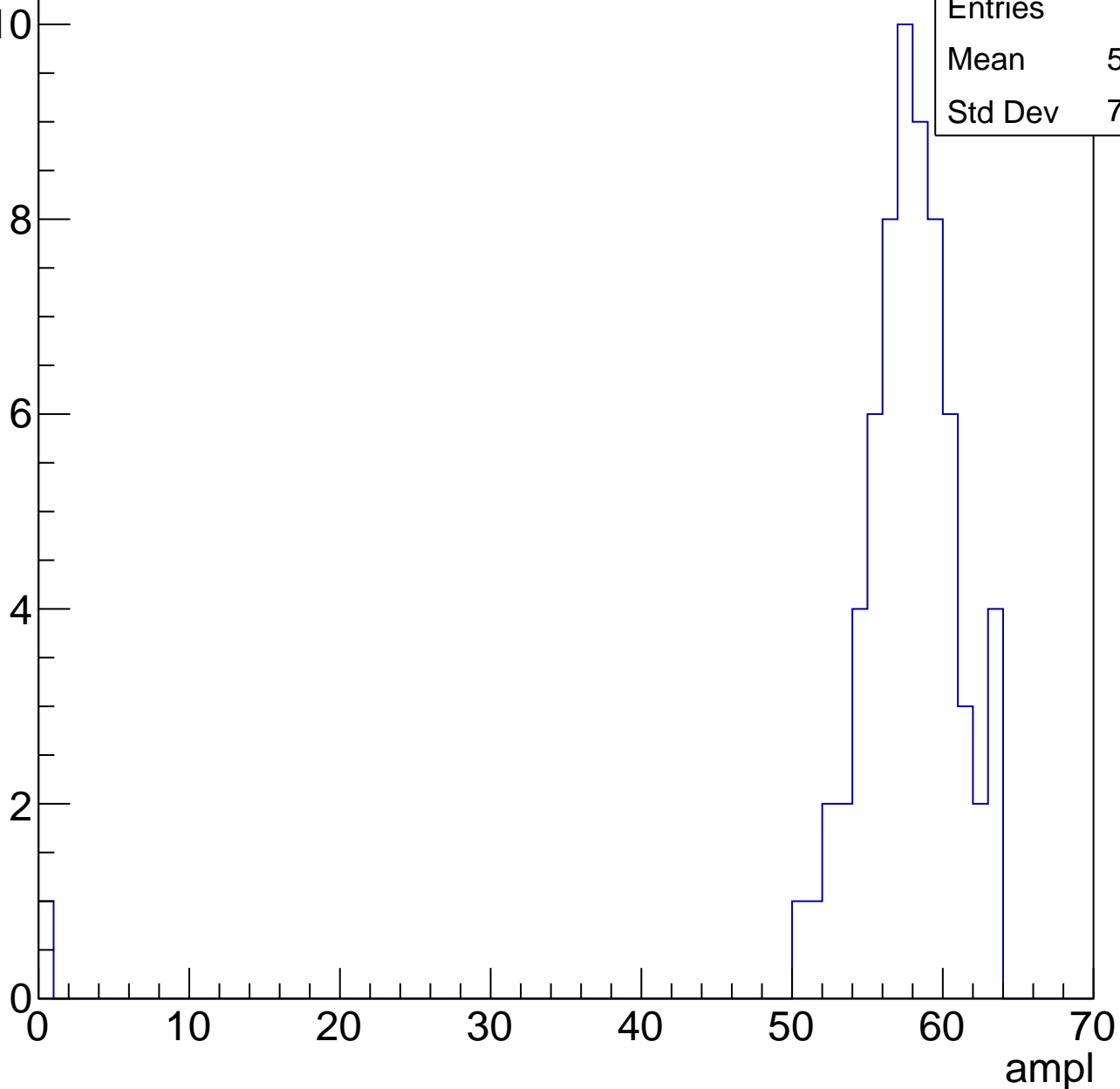


# B0L000S, U10-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

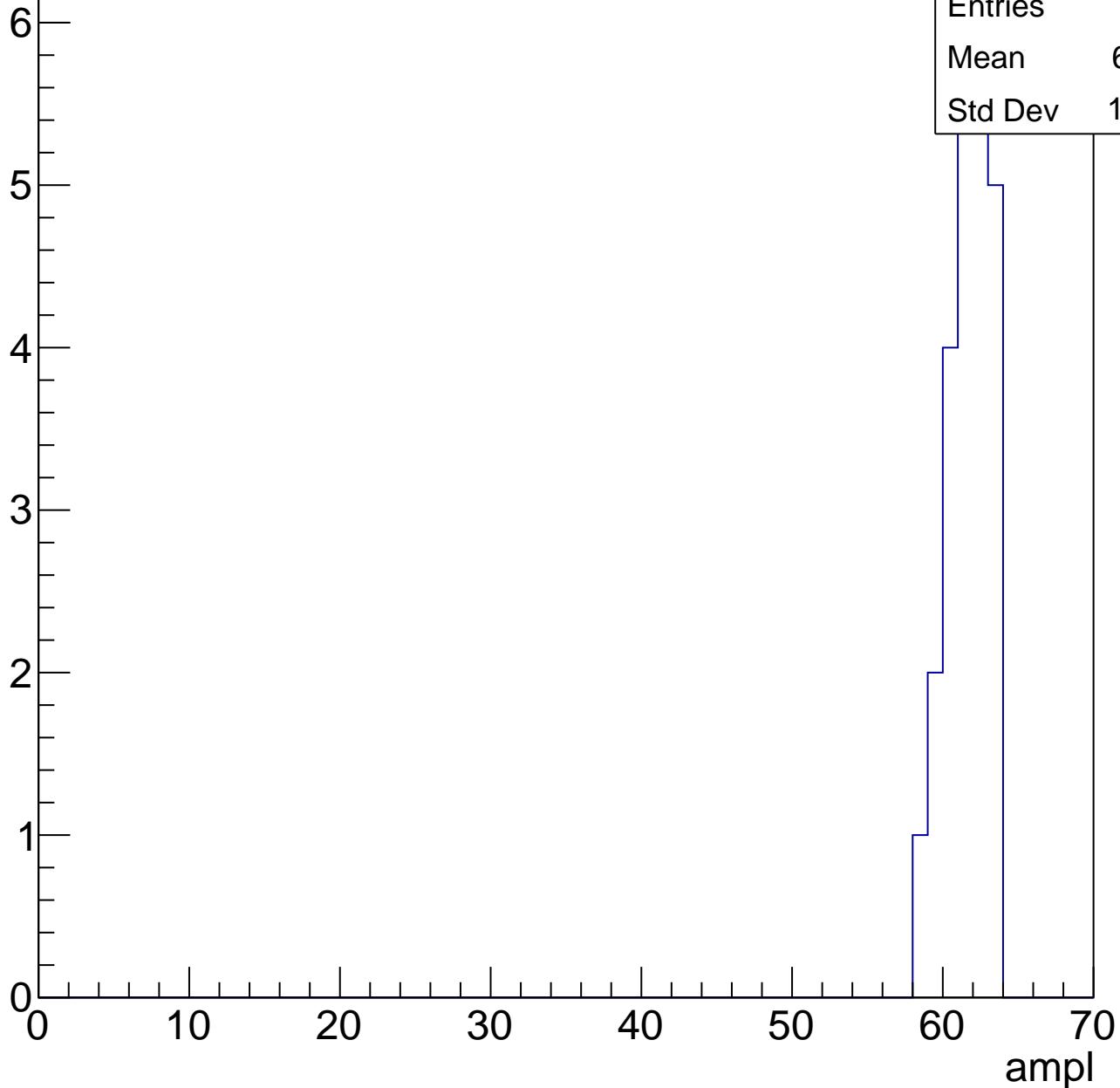
Entries	67
Mean	56.54
Std Dev	7.538



# B0L000S, U10-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch13, adc0

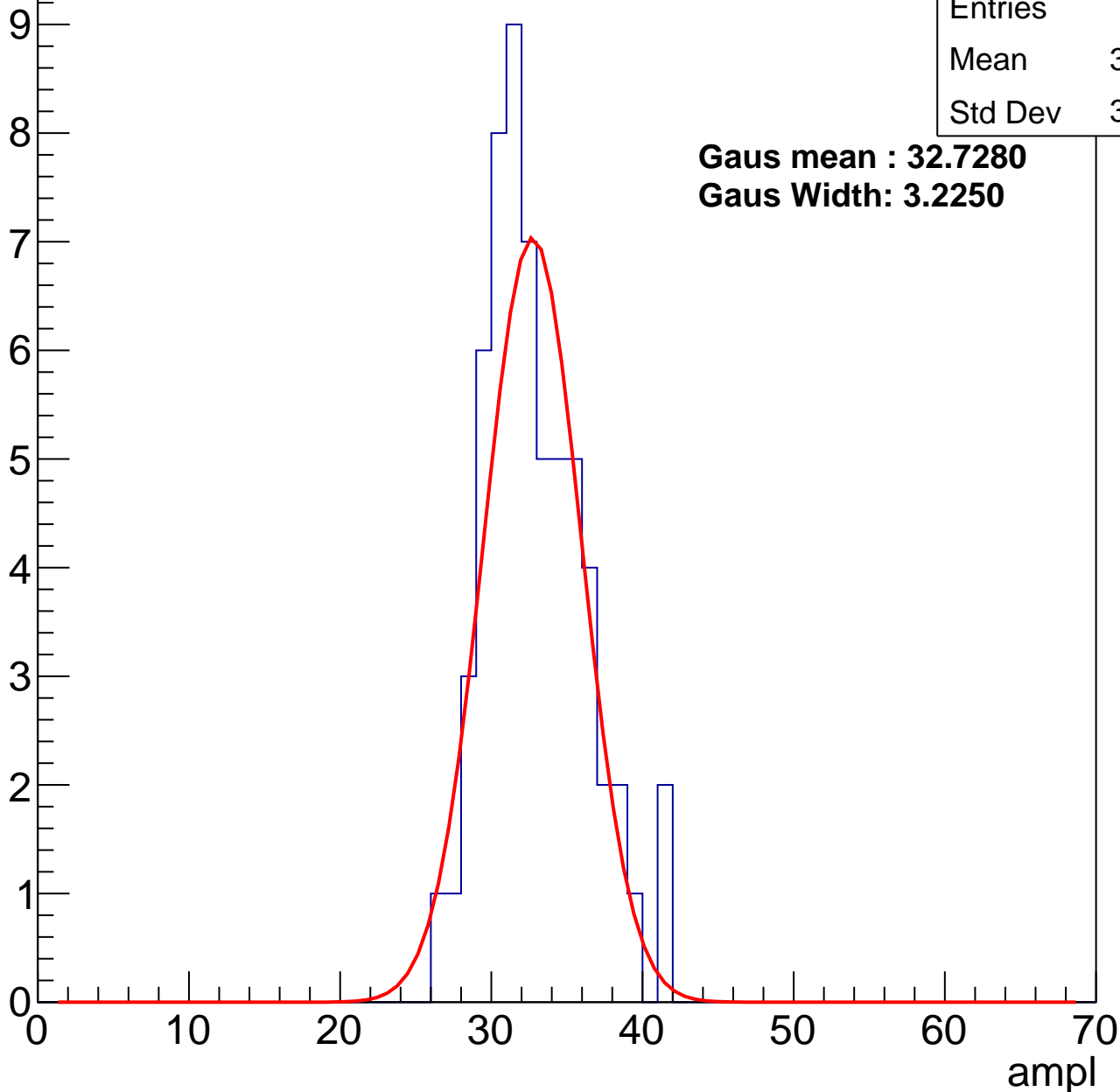
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	32.44
Std Dev	3.287

**Gaus mean : 32.7280**

**Gaus Width: 3.2250**



# B0L000S, U10-ch13, adc1

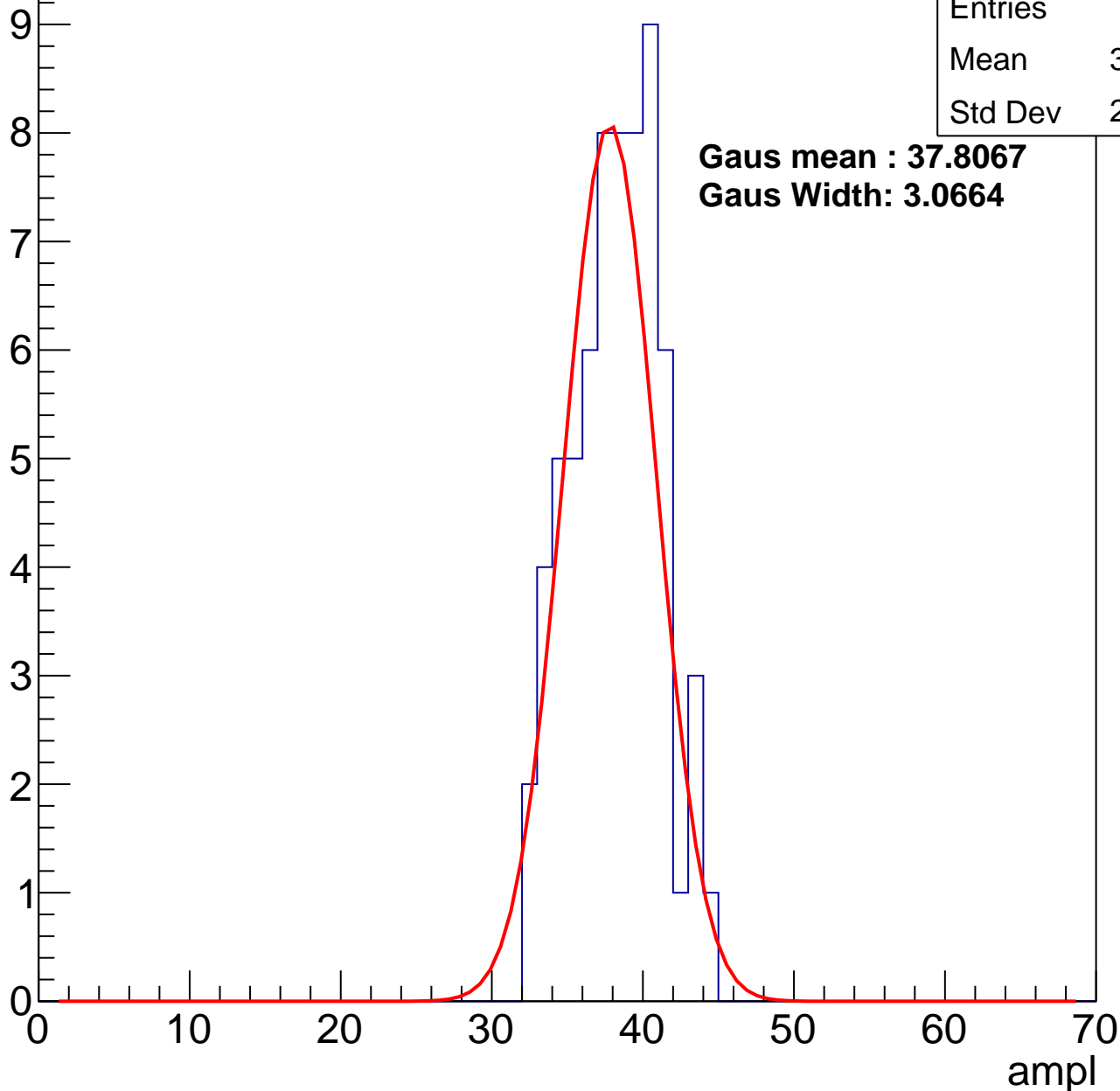
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.73
Std Dev	2.884

**Gaus mean : 37.8067**

**Gaus Width: 3.0664**



# B0L000S, U10-ch13, adc2

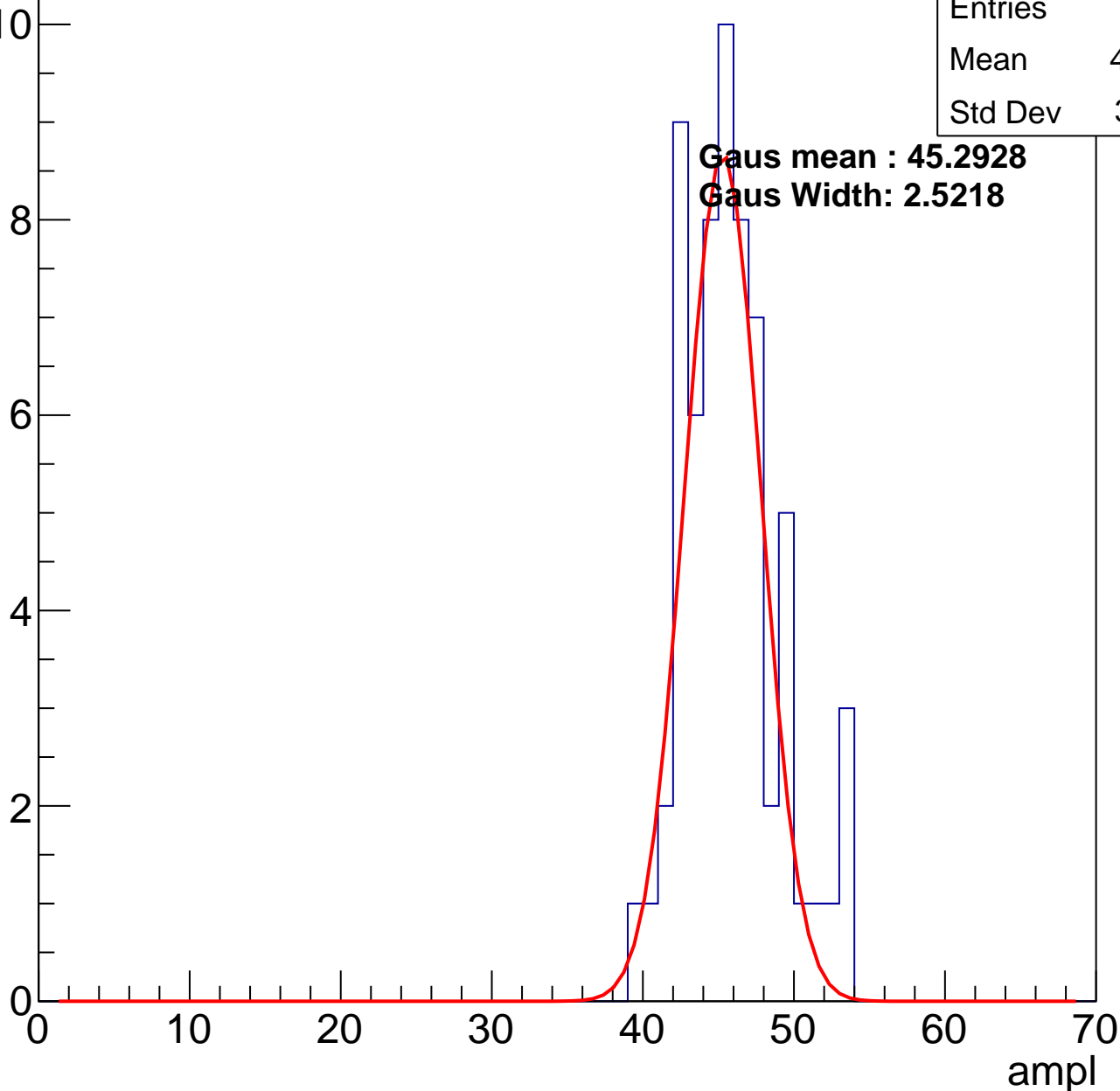
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	45.37
Std Dev	3.121

**Gaus mean : 45.2928**

**Gaus Width: 2.5218**

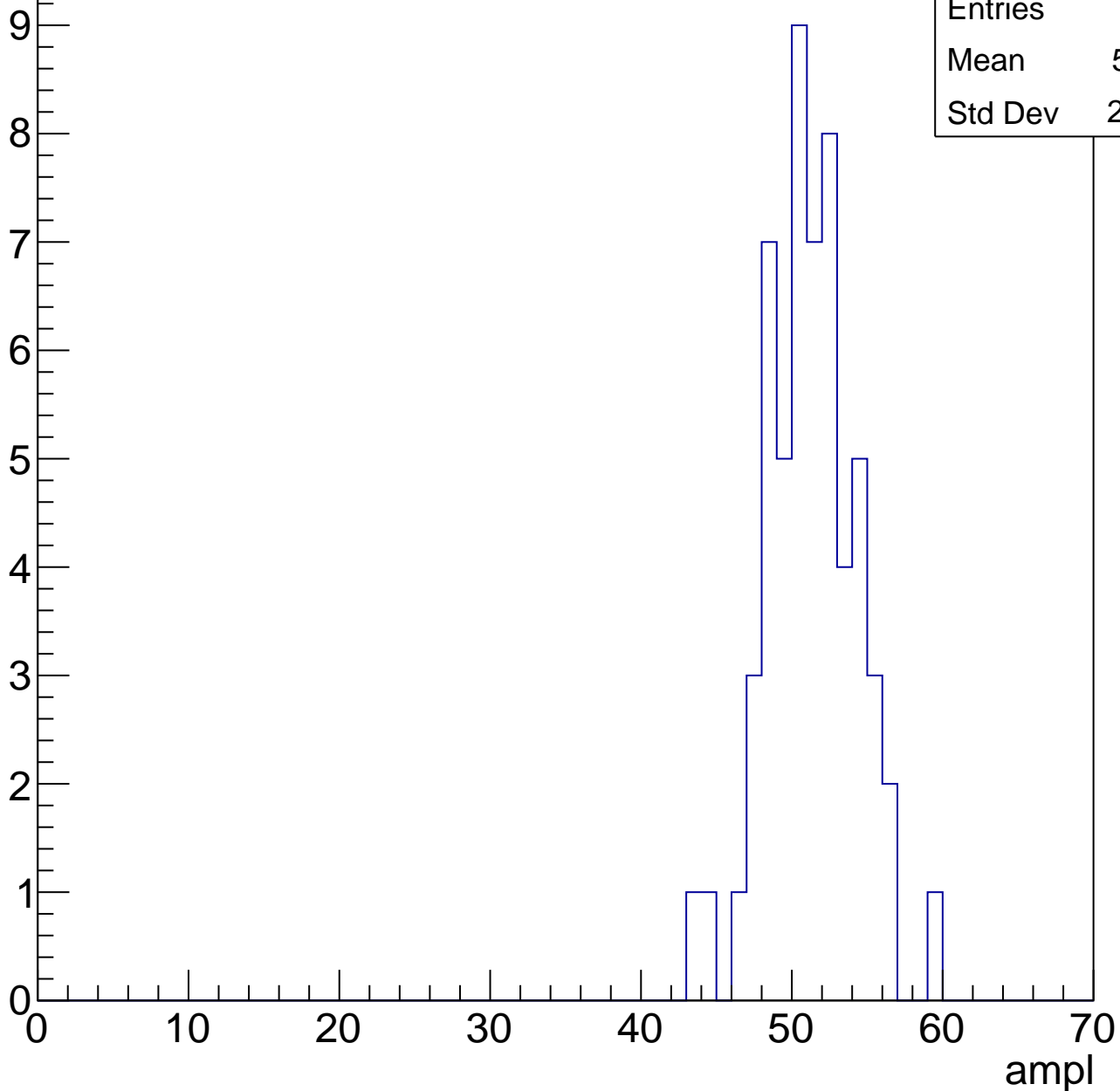


# B0L000S, U10-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

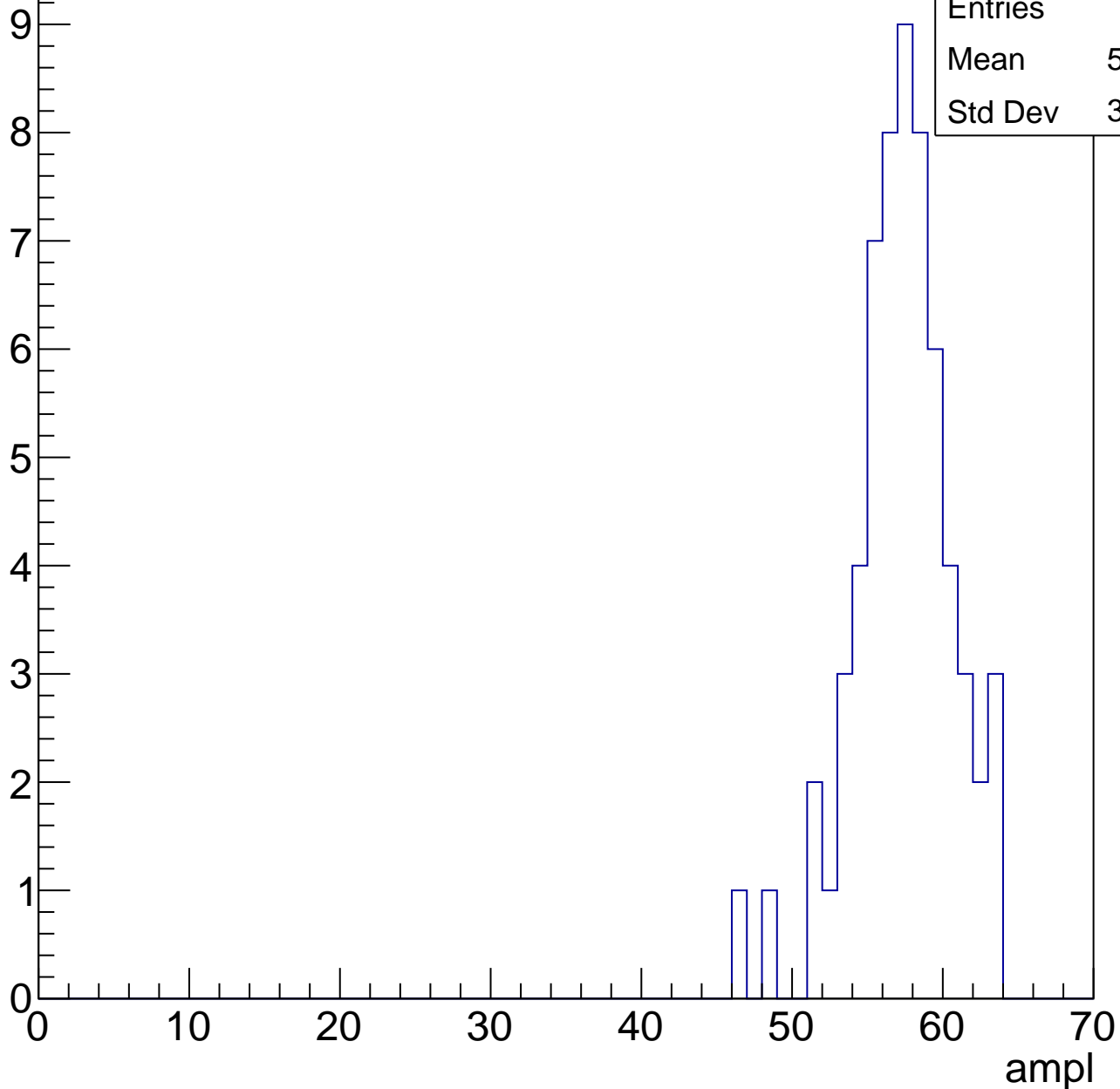
Entries	57
Mean	50.81
Std Dev	2.982



# B0L000S, U10-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

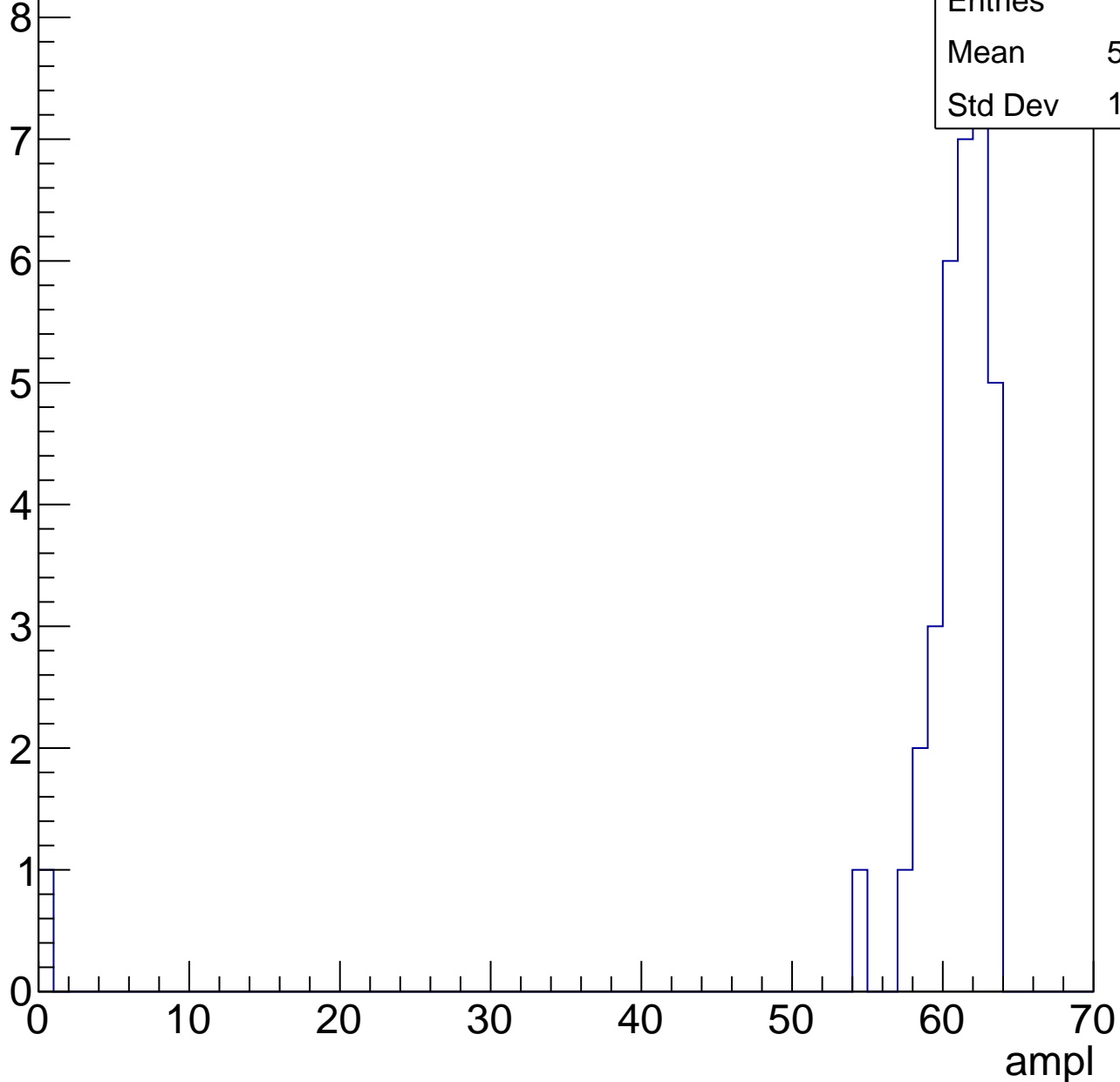


# B0L000S, U10-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.88
Std Dev	10.43



# B0L000S, U10-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U10-ch14, adc0

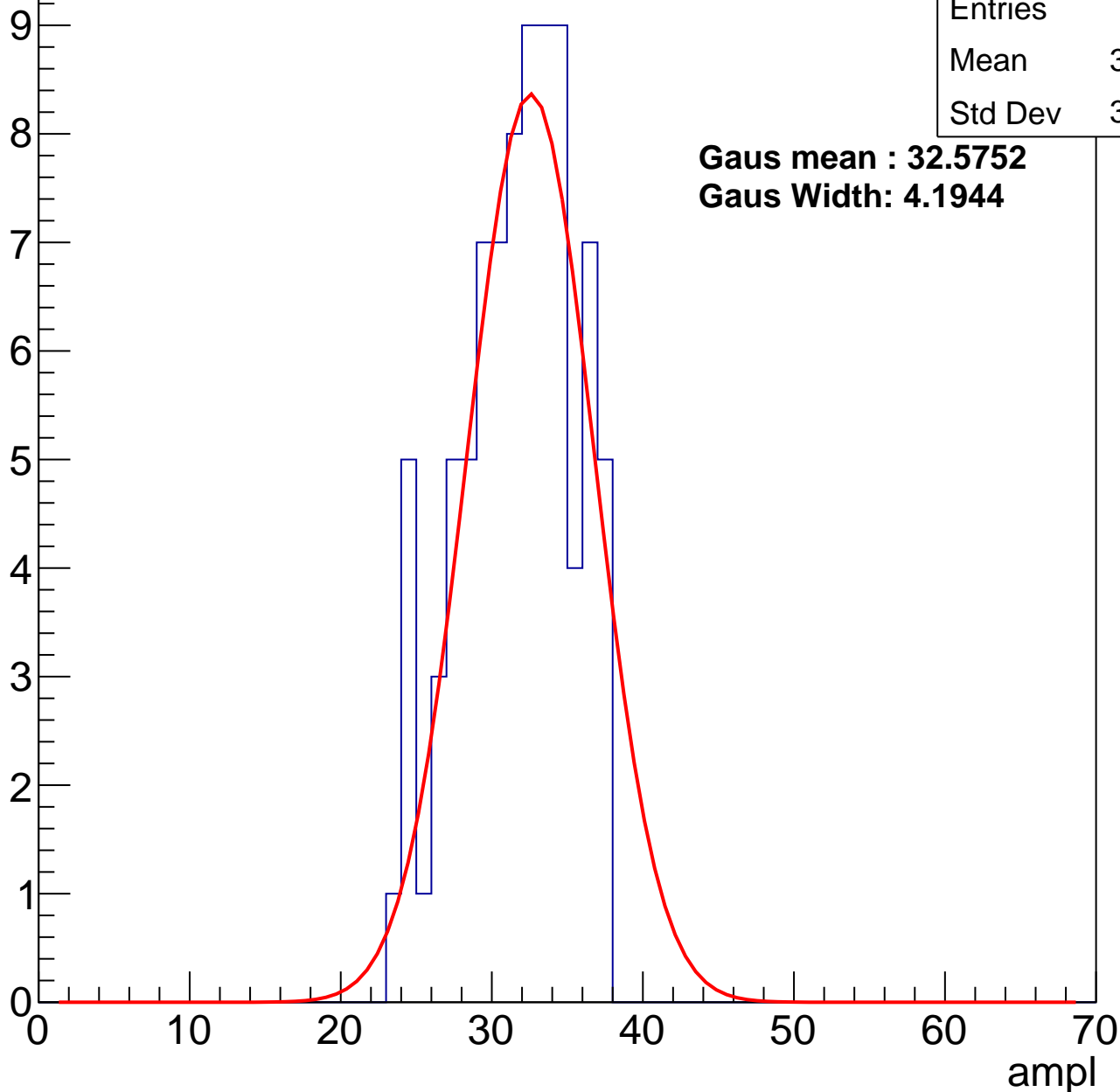
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	31.18
Std Dev	3.634

**Gaus mean : 32.5752**

**Gaus Width: 4.1944**



# B0L000S, U10-ch14, adc1

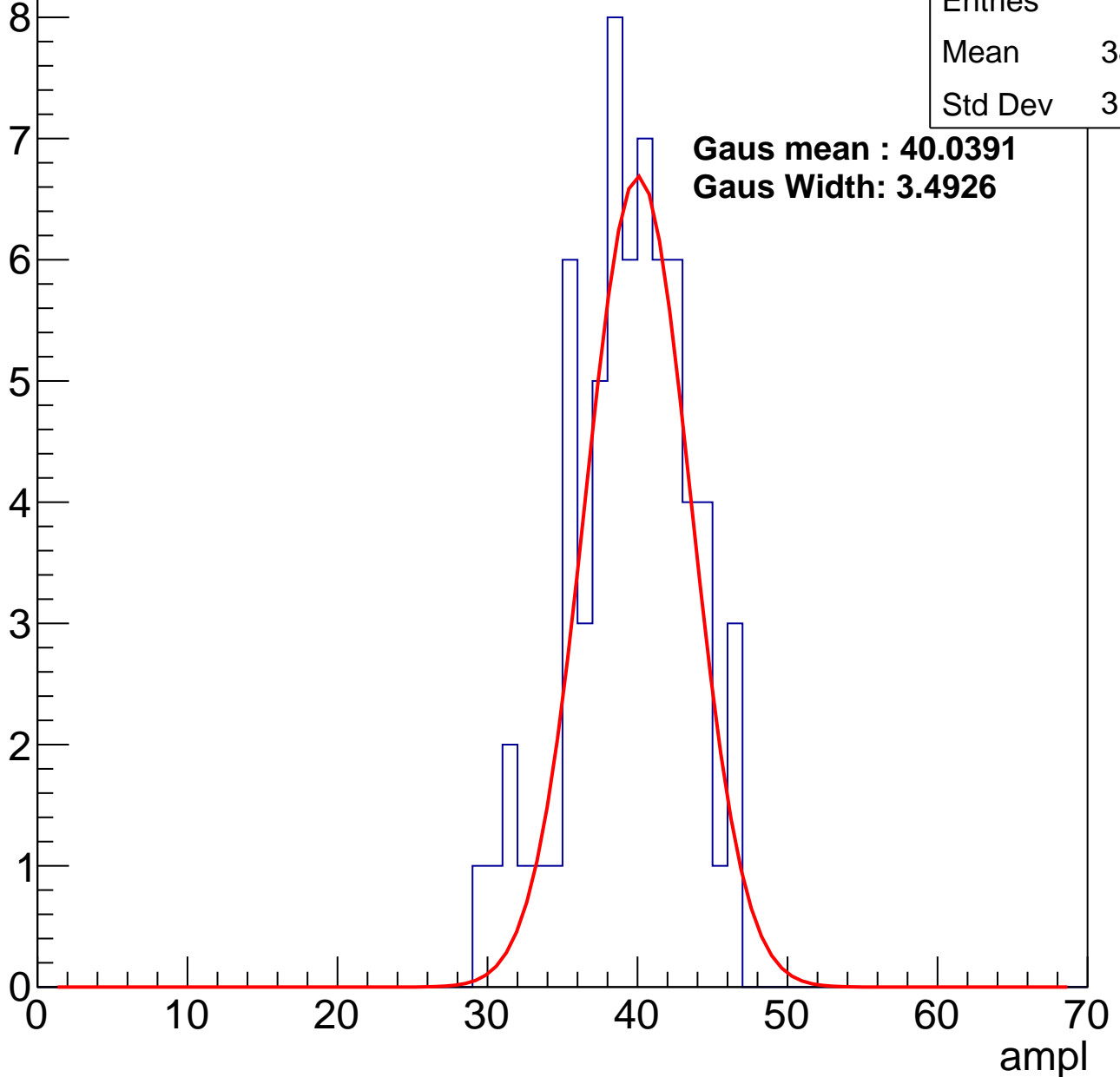
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	38.94
Std Dev	3.896

**Gaus mean : 40.0391**

**Gaus Width: 3.4926**



# B0L000S, U10-ch14, adc2

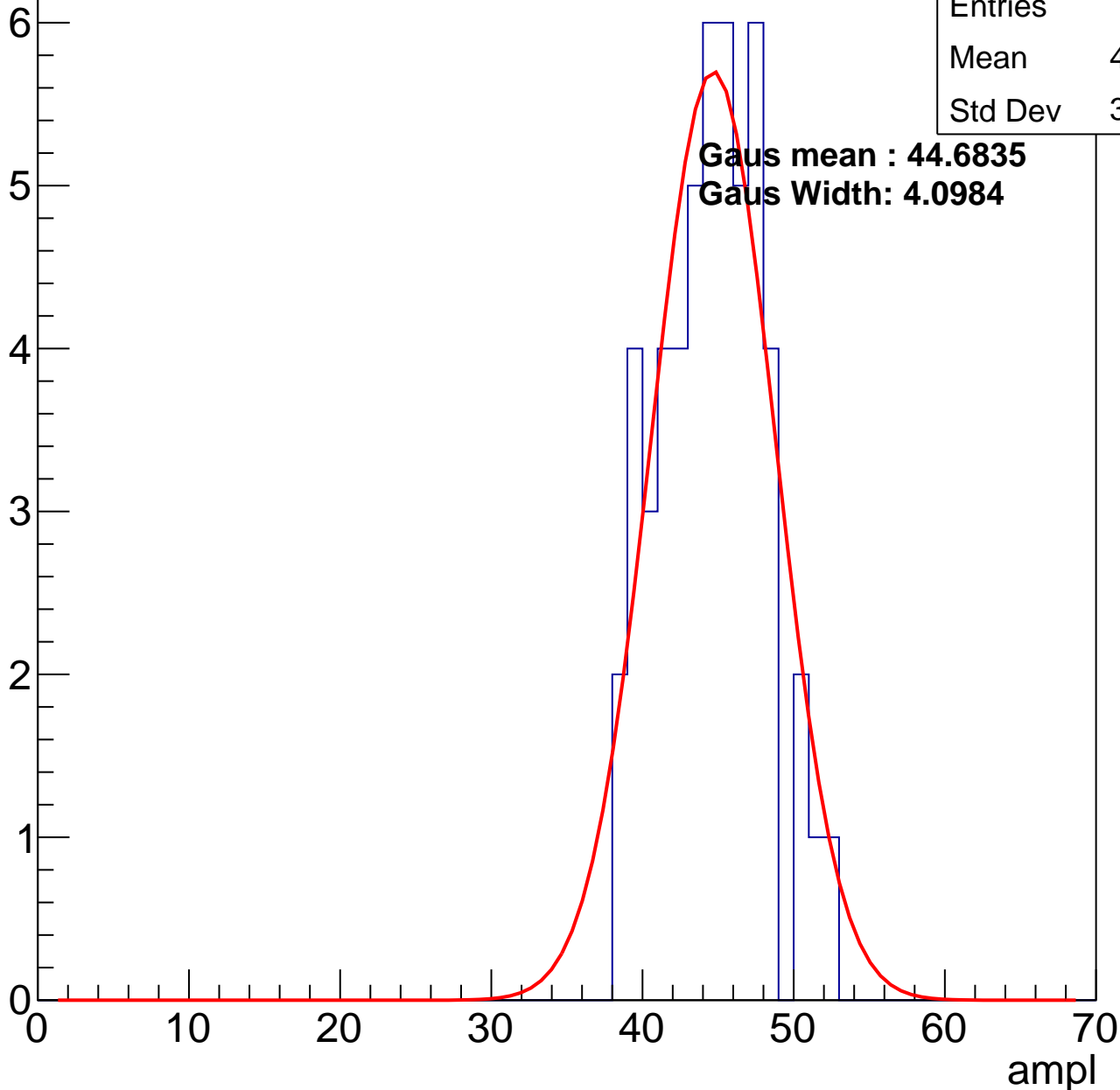
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	44.15
Std Dev	3.378

**Gaus mean : 44.6835**

**Gaus Width: 4.0984**



# B0L000S, U10-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

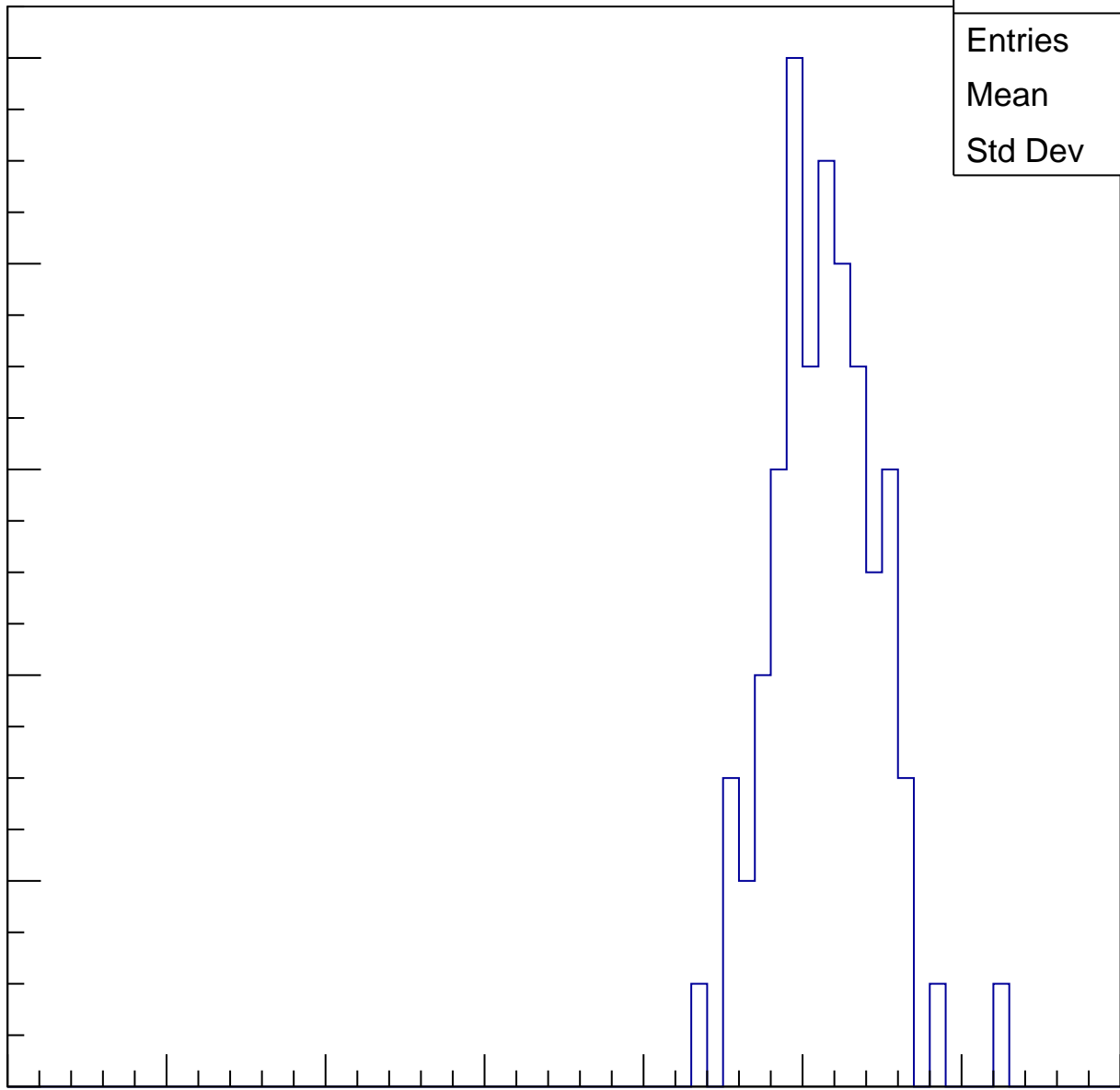
Entries	73
Mean	50.96
Std Dev	3.329

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	56.4
Std Dev	8.024

Entry

10

8

6

4

2

0

0

10

20

30

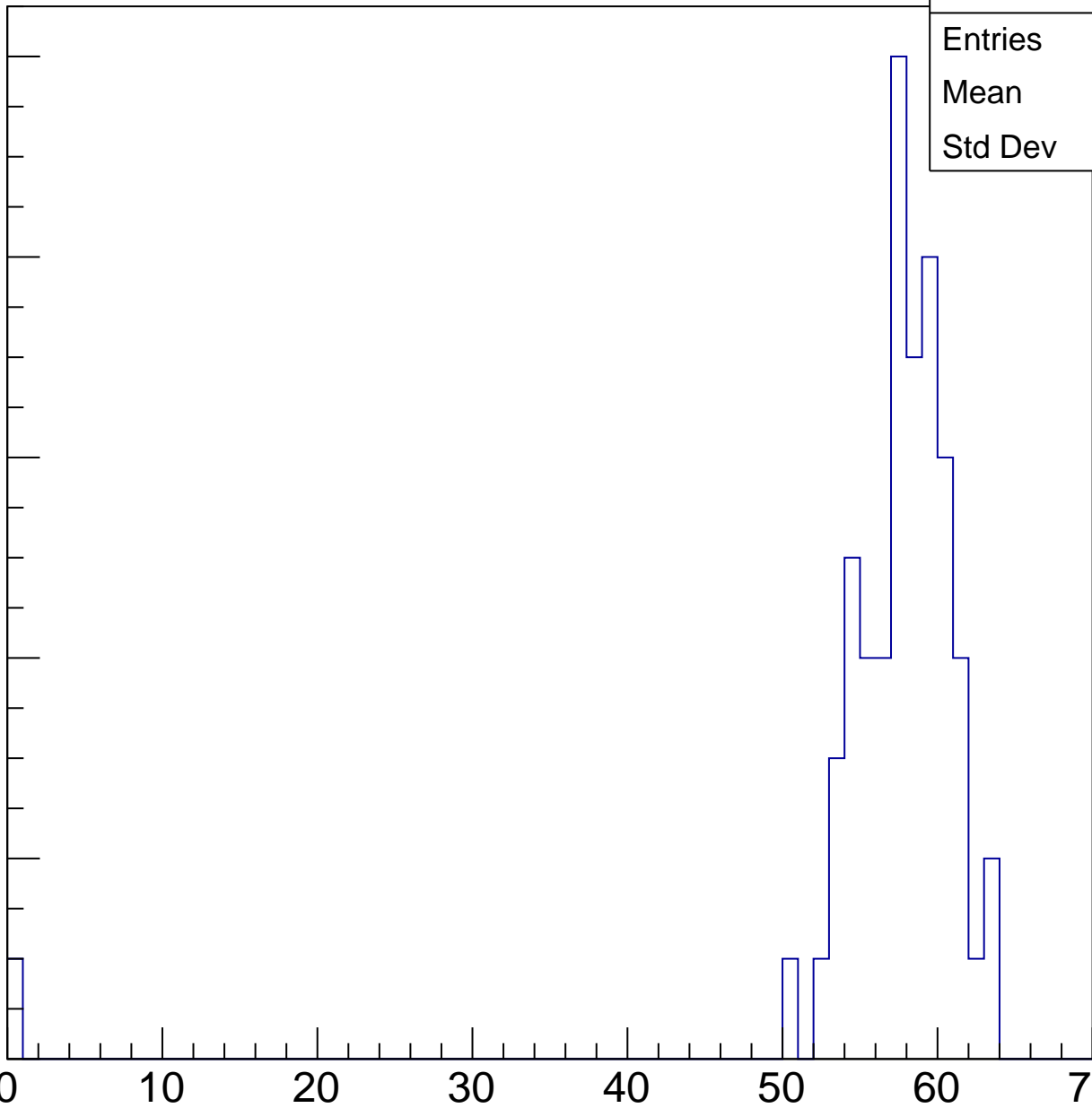
40

50

60

70

ampl

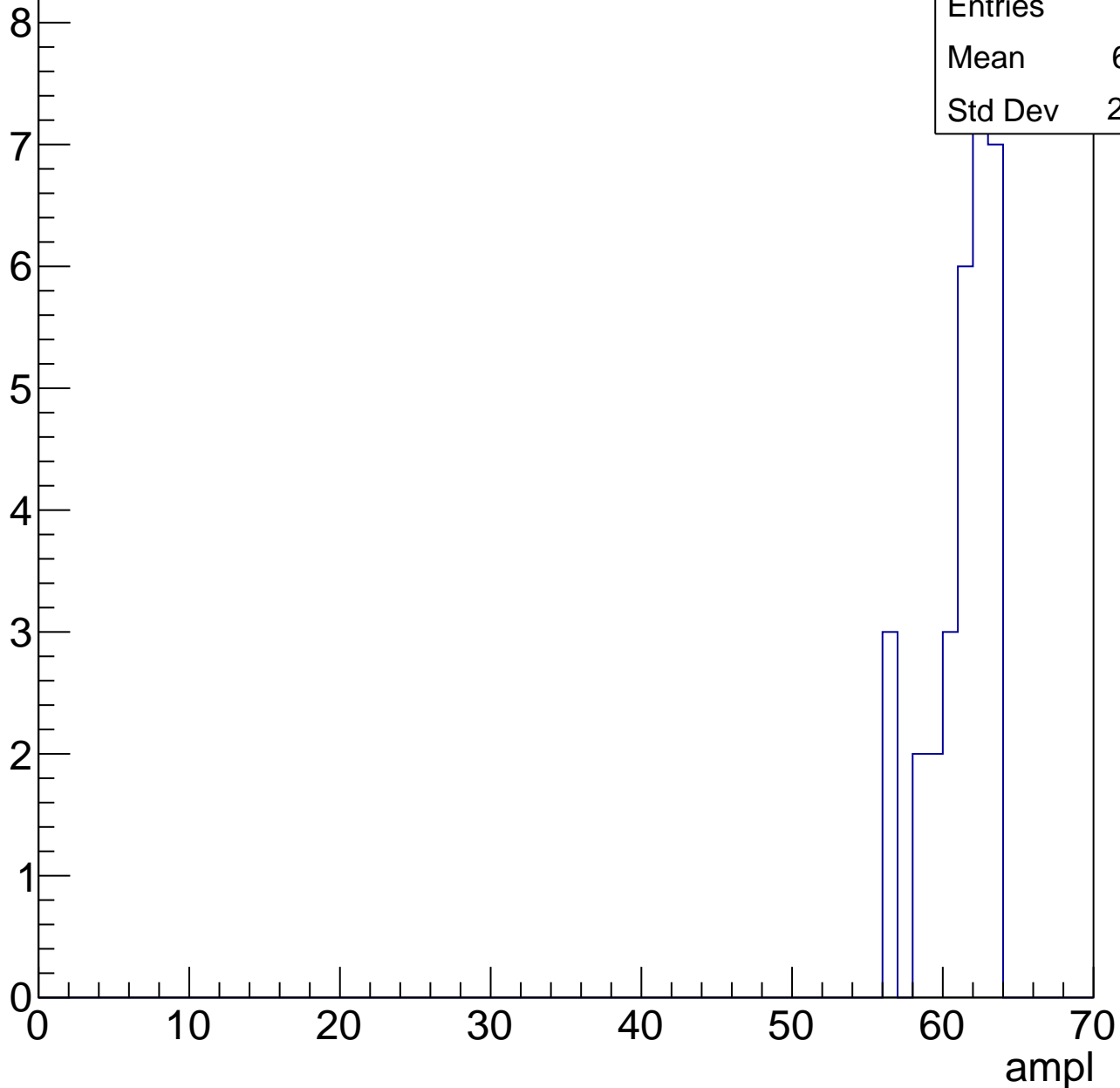


# B0L000S, U10-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	60.81
Std Dev	2.116



# B0L000S, U10-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch15, adc0

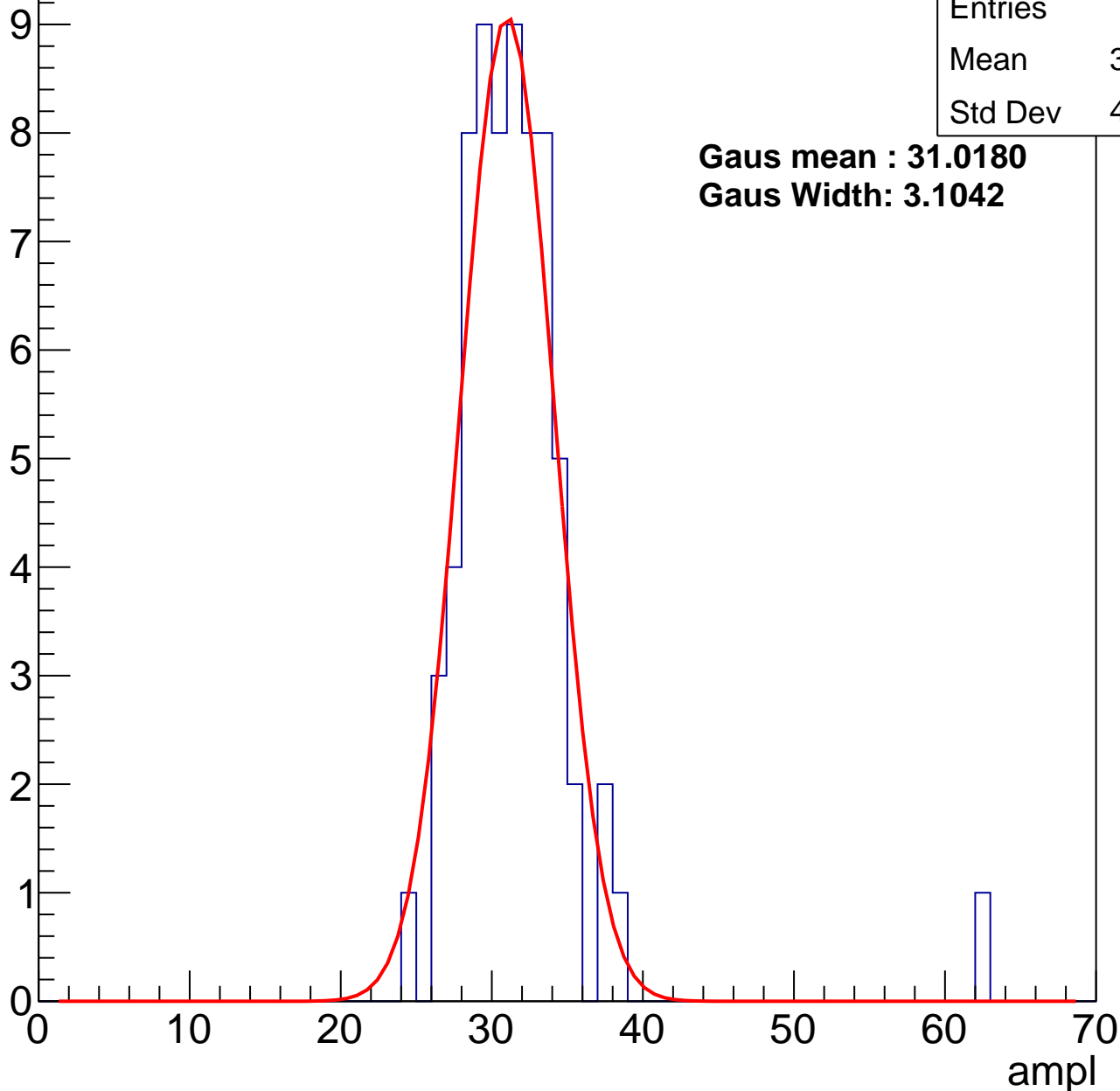
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	31.13
Std Dev	4.665

**Gaus mean : 31.0180**

**Gaus Width: 3.1042**



# B0L000S, U10-ch15, adc1

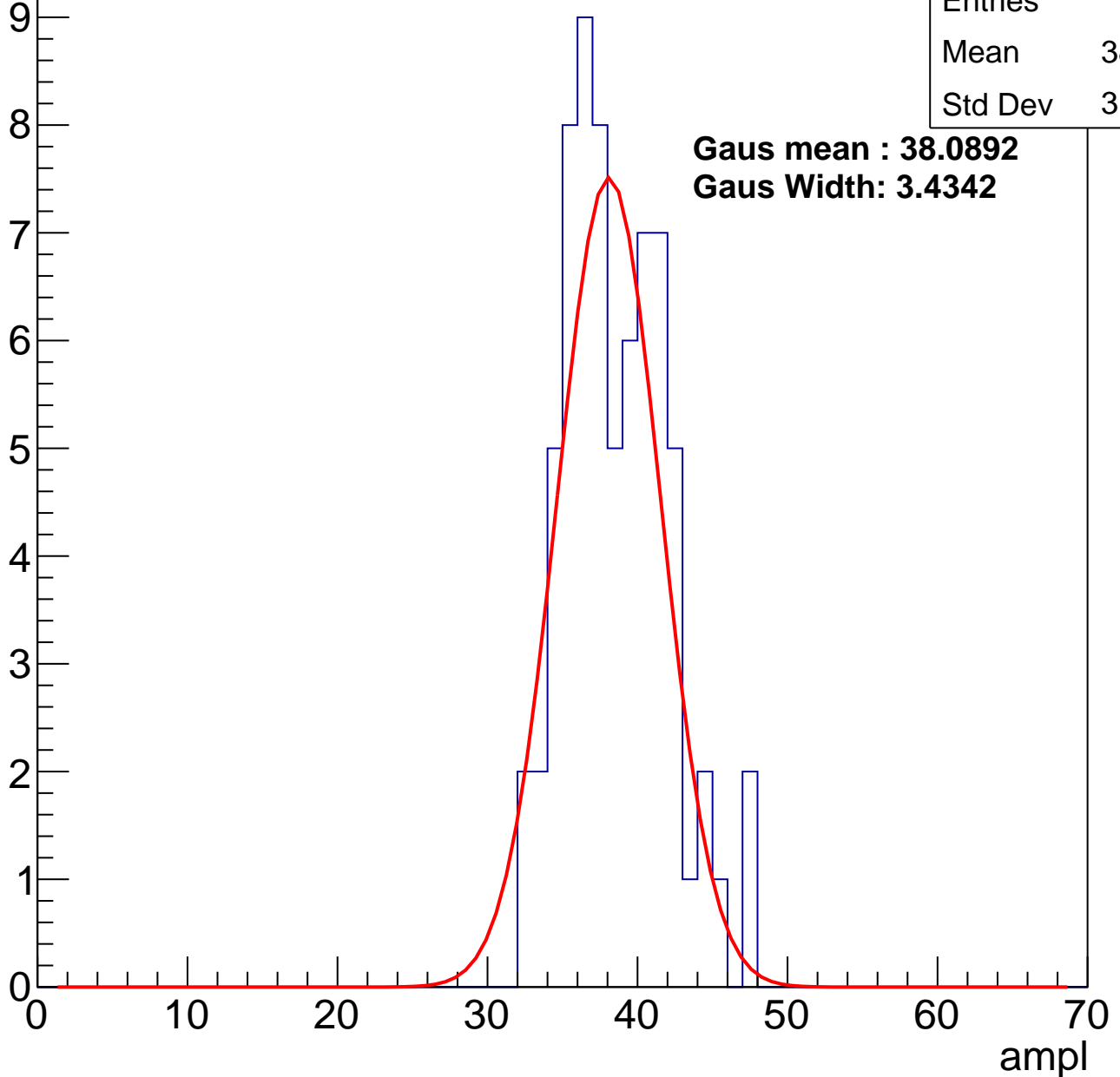
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	38.16
Std Dev	3.383

**Gaus mean : 38.0892**

**Gaus Width: 3.4342**



# B0L000S, U10-ch15, adc2

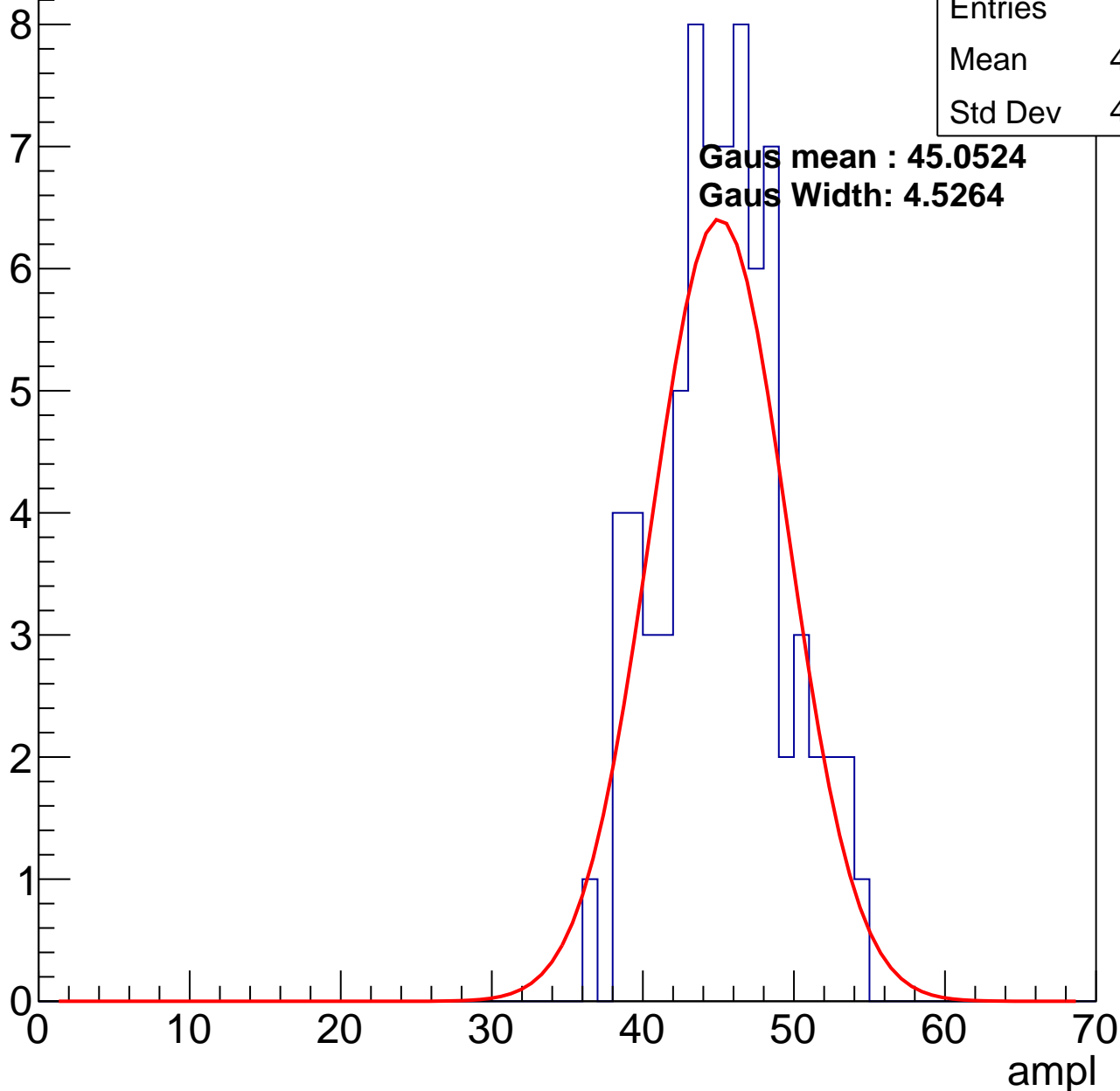
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.85
Std Dev	4.012

**Gaus mean : 45.0524**

**Gaus Width: 4.5264**

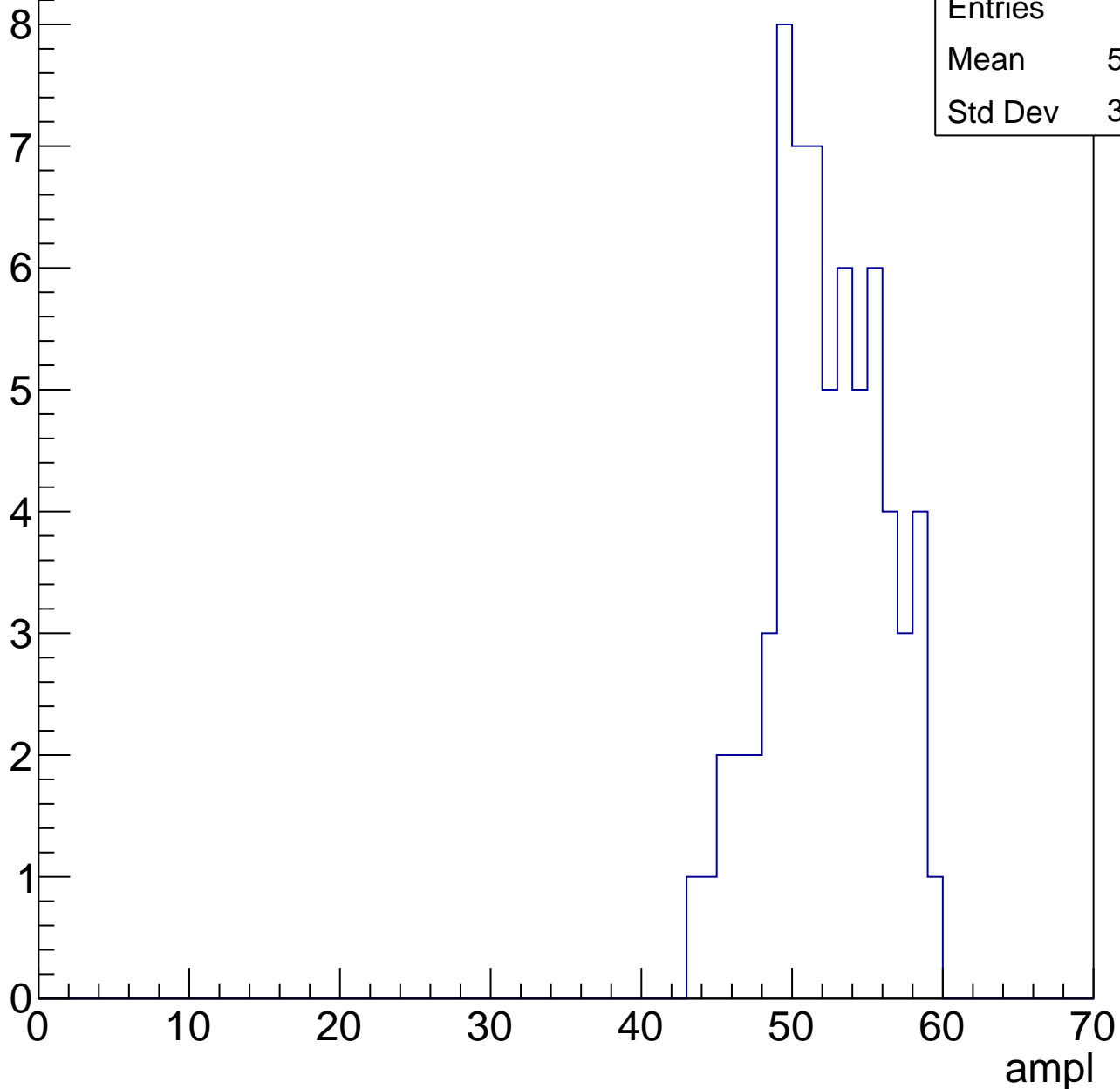


# B0L000S, U10-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	51.79
Std Dev	3.724



# B0L000S, U10-ch15, adc4

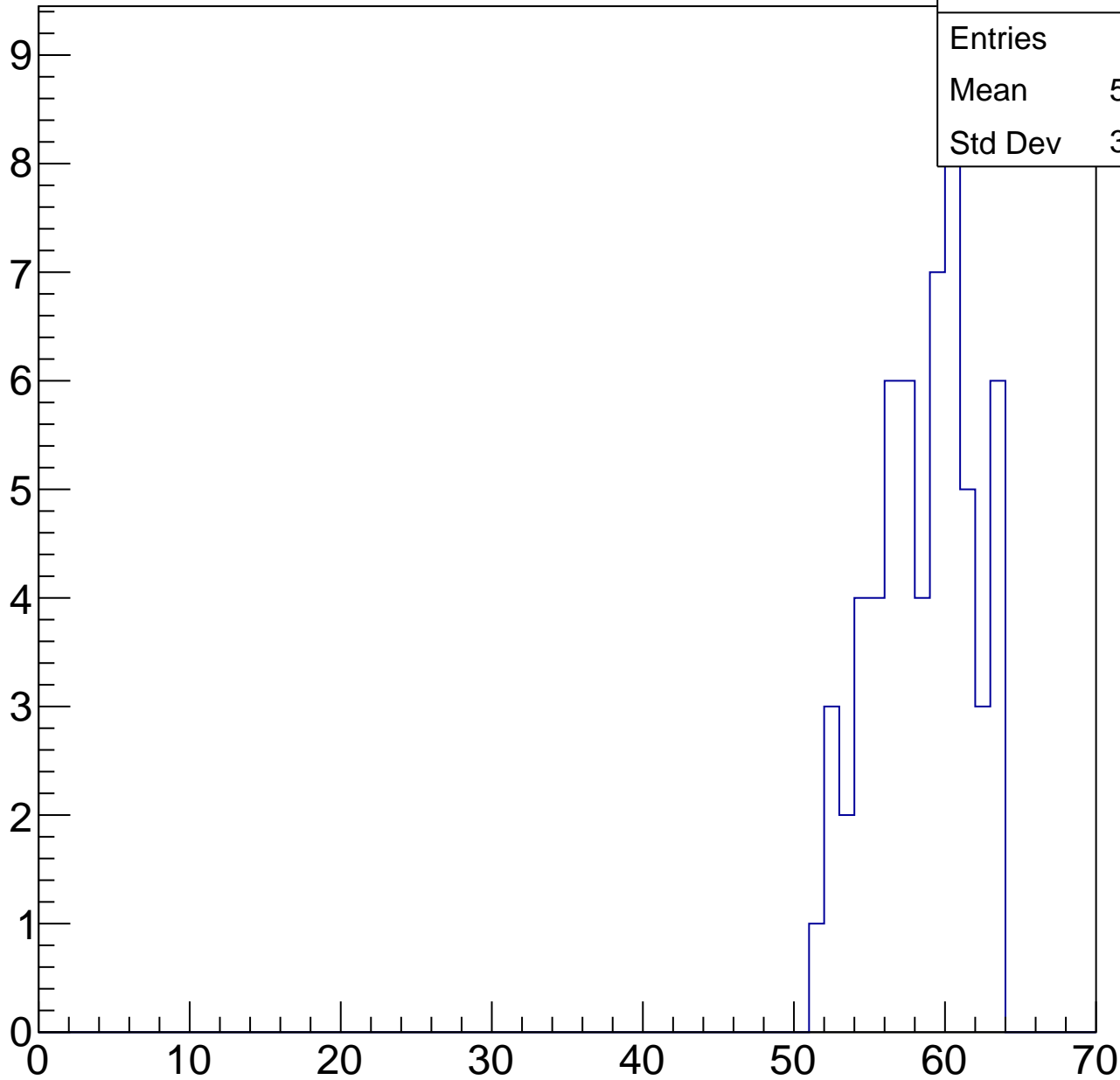
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	58.02
Std Dev	3.222

ampl

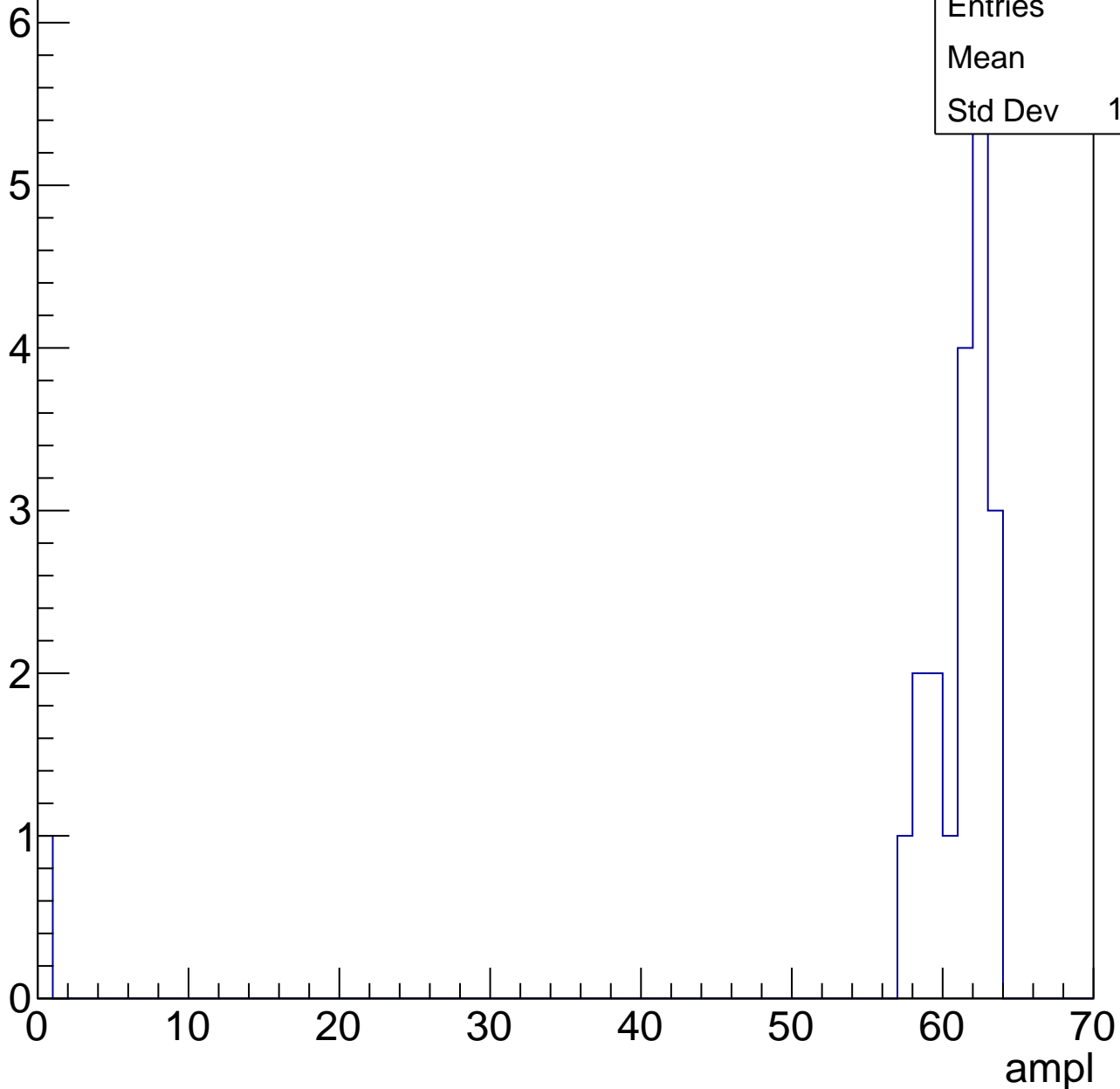


# B0L000S, U10-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	57.8
Std Dev	13.37



# B0L000S, U10-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

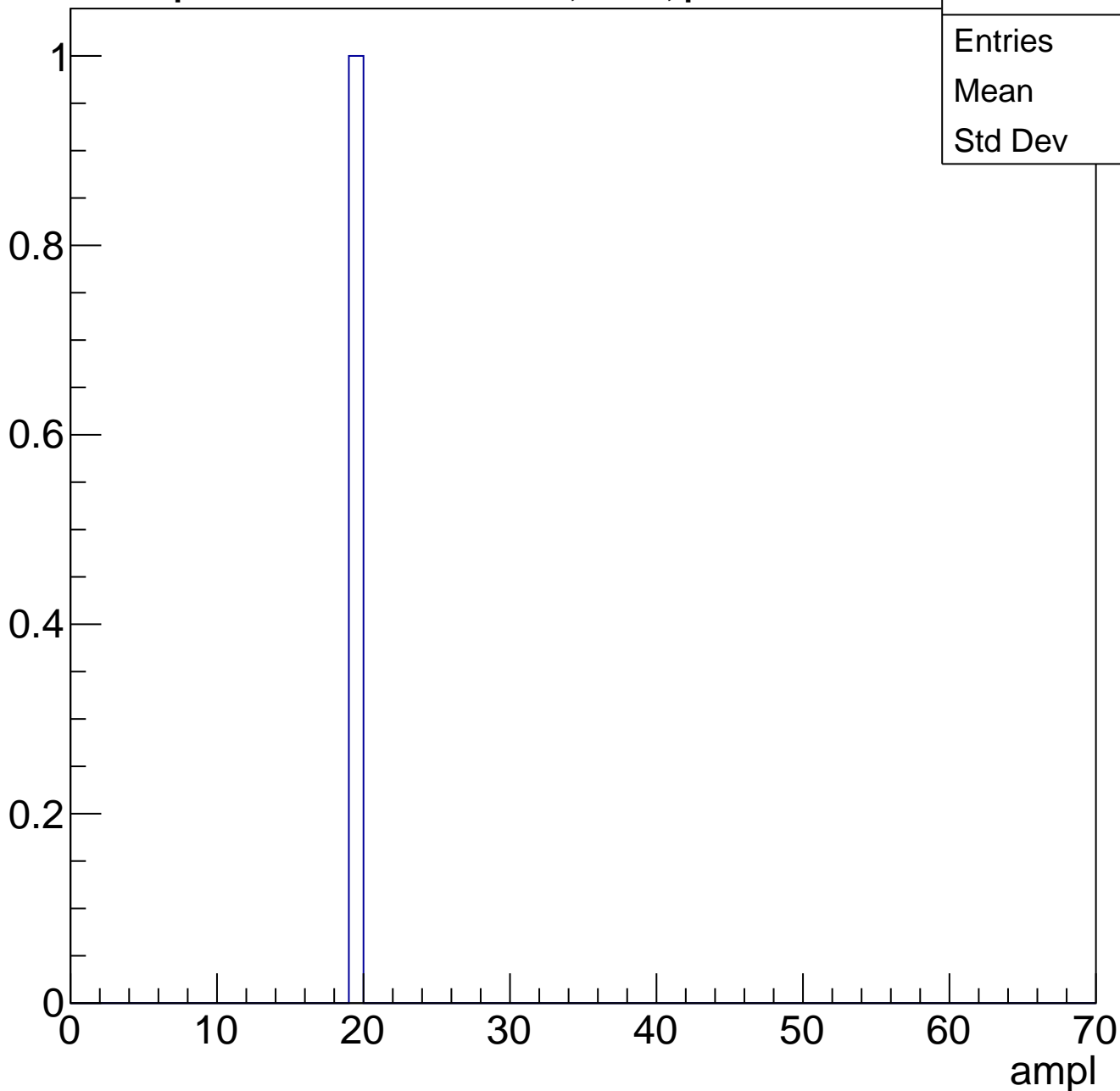




# B0L000S, U10-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch16, adc0

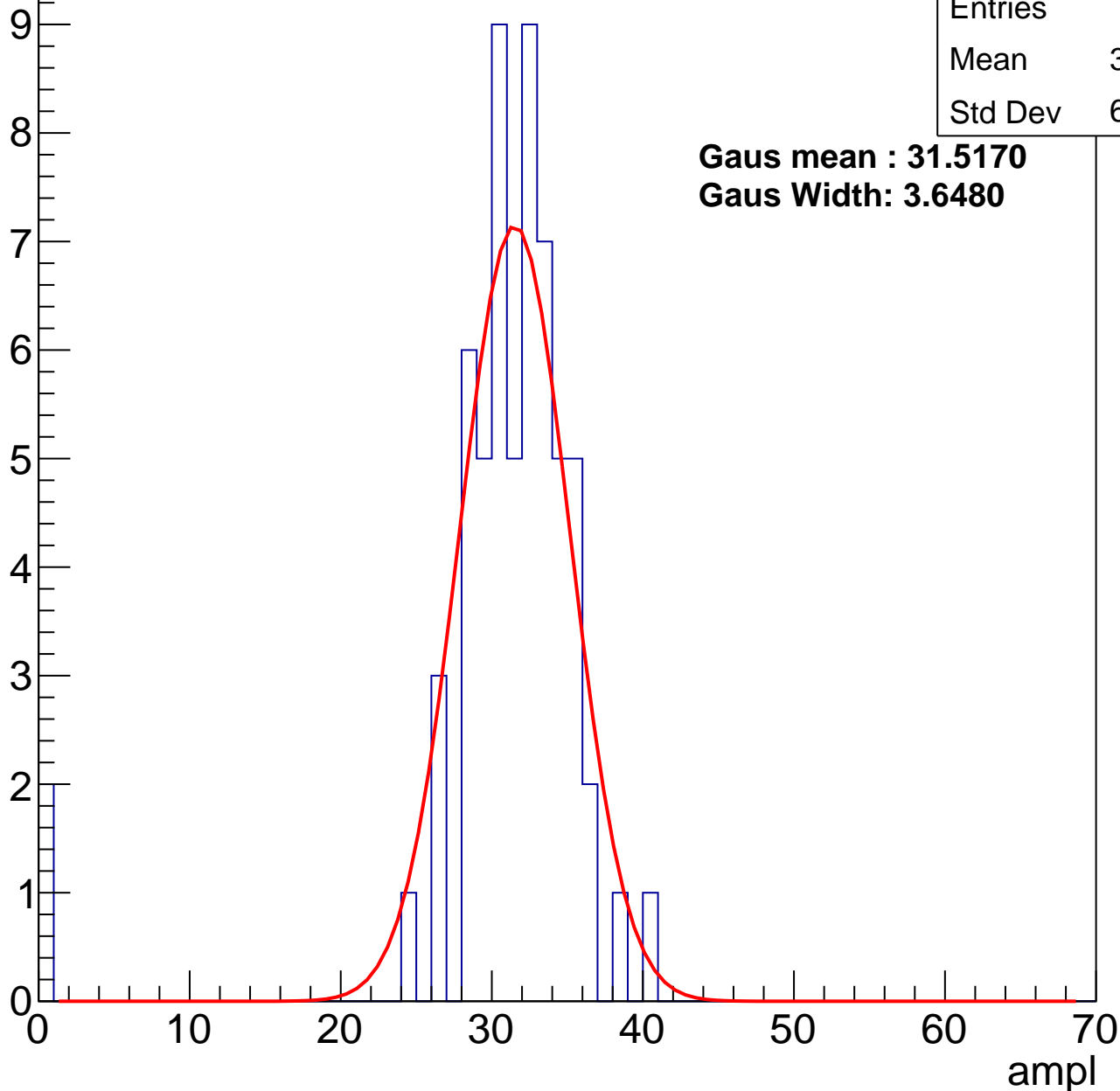
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.39
Std Dev	6.336

**Gaus mean : 31.5170**

**Gaus Width: 3.6480**



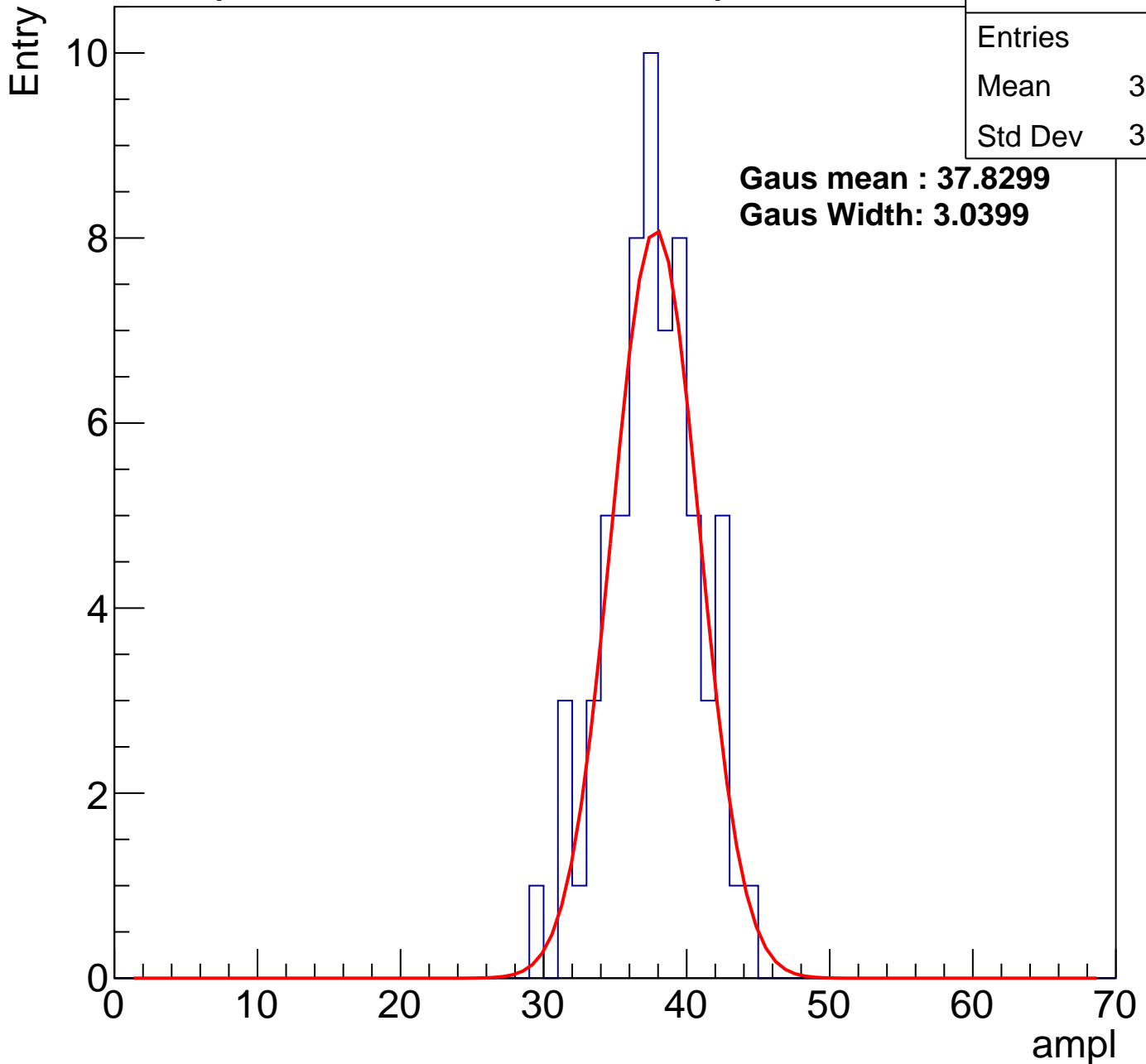
# B0L000S, U10-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	37.18
Std Dev	3.157

**Gaus mean : 37.8299**

**Gaus Width: 3.0399**



# B0L000S, U10-ch16, adc2

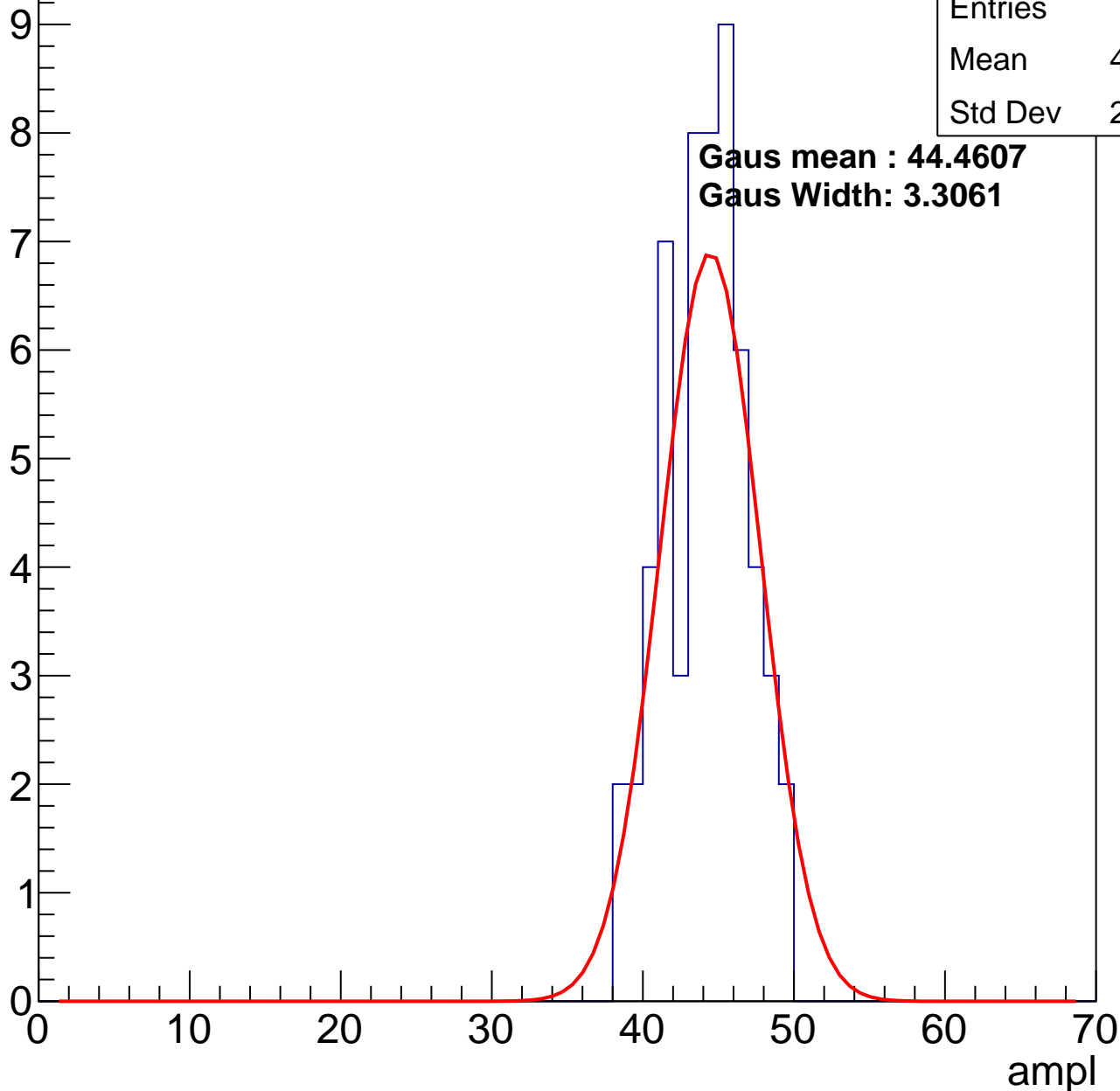
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.69
Std Dev	2.724

**Gaus mean : 44.4607**

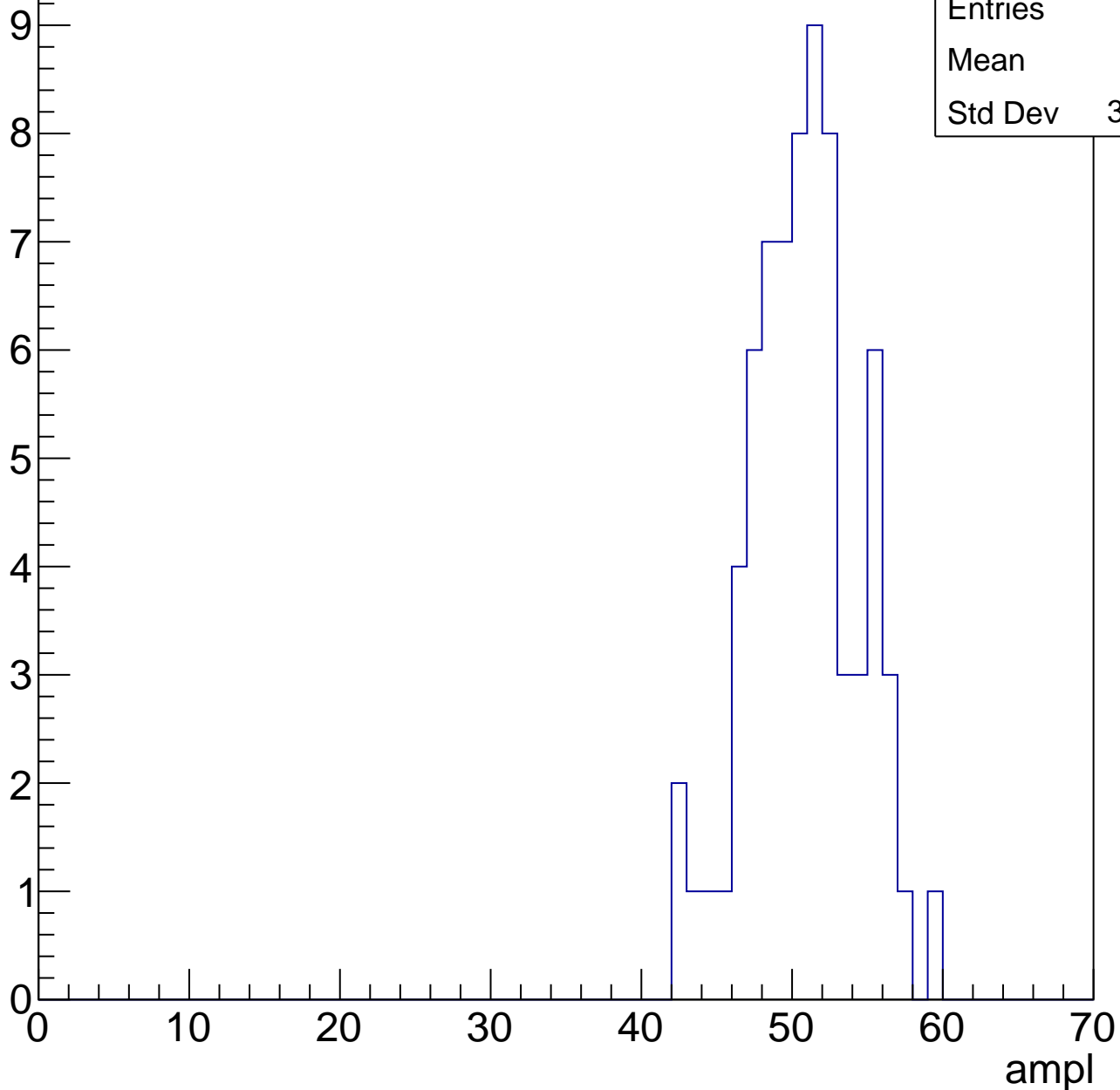
**Gaus Width: 3.3061**



# B0L000S, U10-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



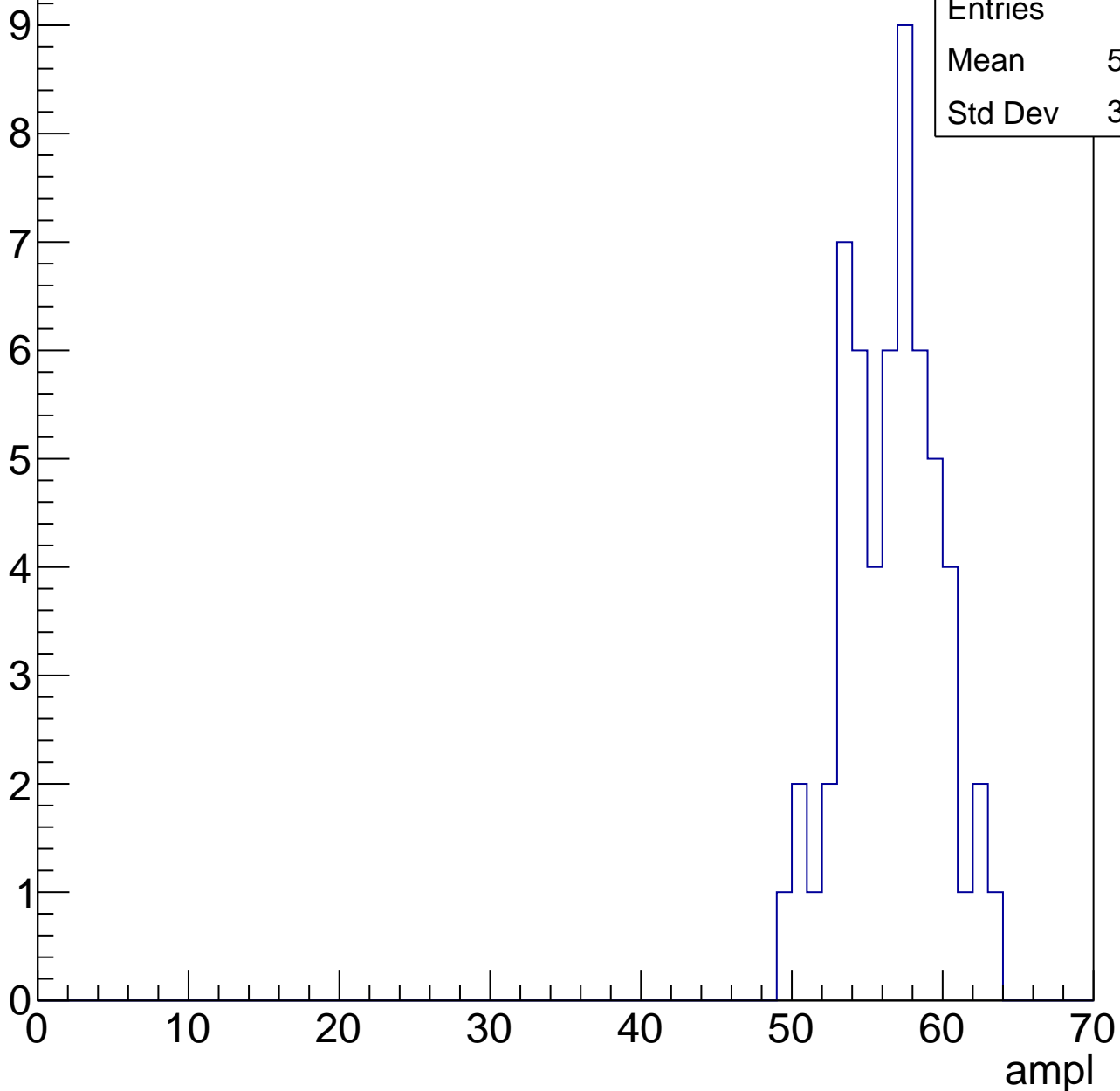
Entries	71
Mean	50.3
Std Dev	3.538

# B0L000S, U10-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.12
Std Dev	3.124

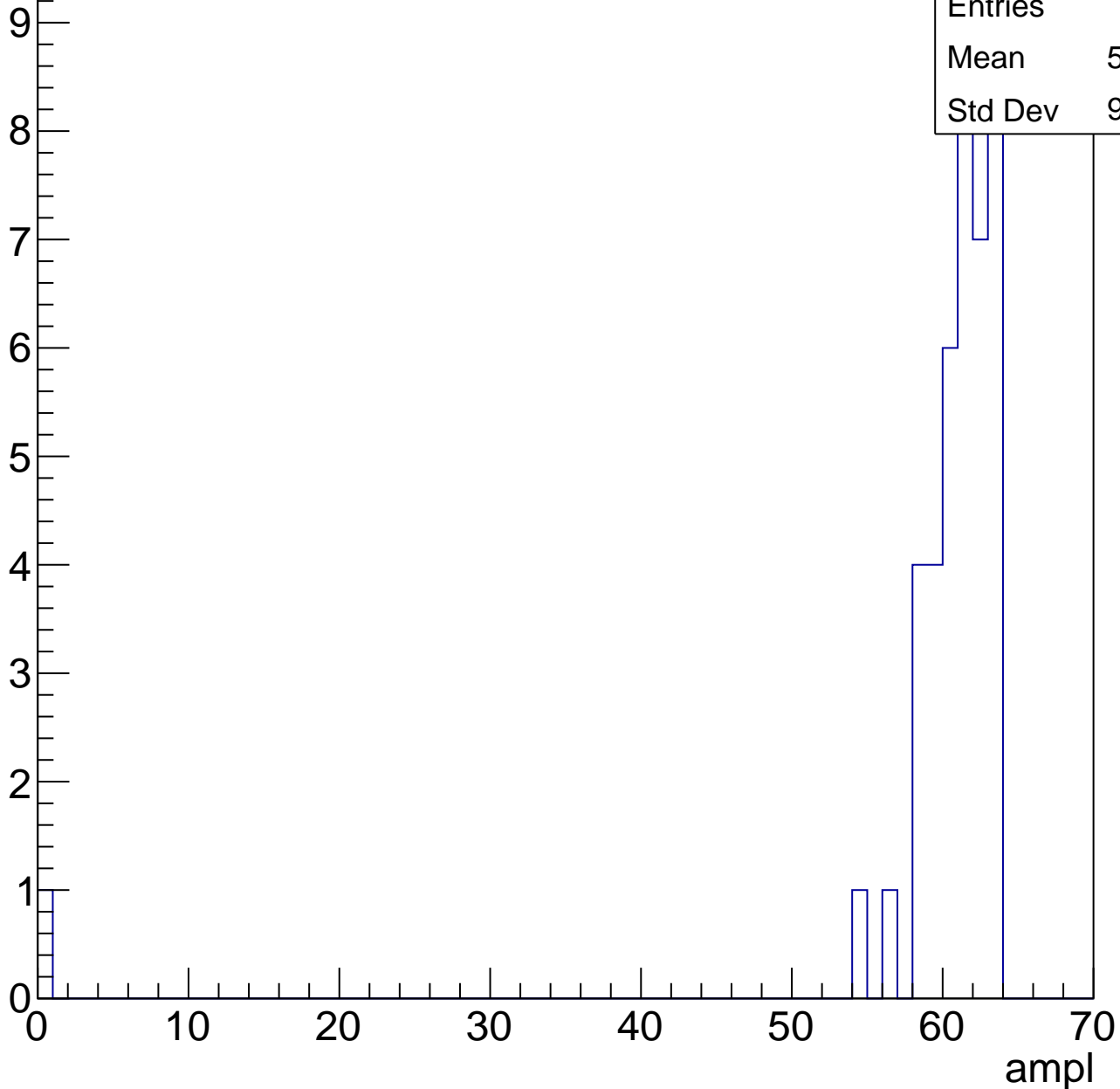


# B0L000S, U10-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	59.24
Std Dev	9.469



# B0L000S, U10-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch17, adc0

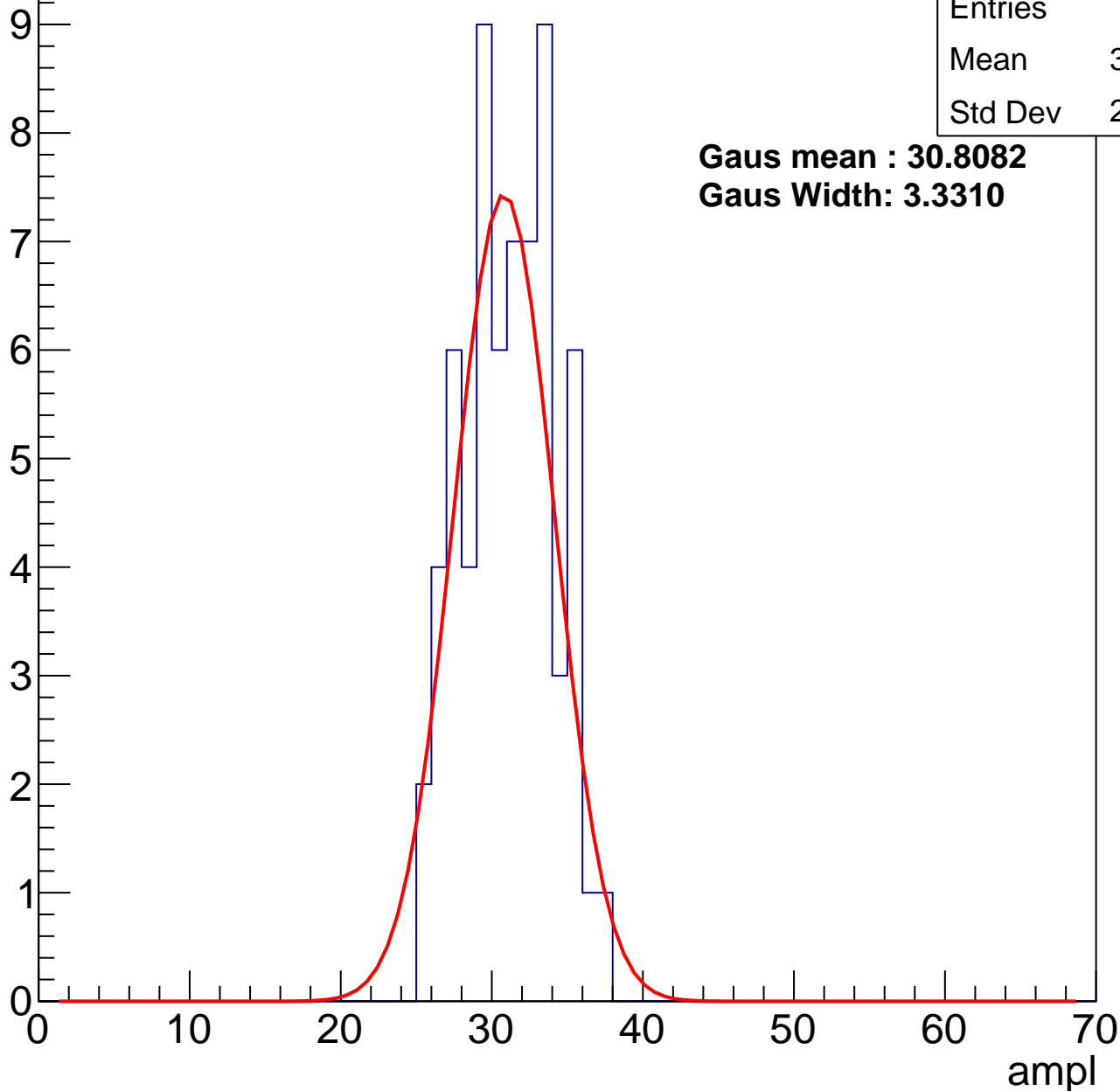
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	30.65
Std Dev	2.948

**Gaus mean : 30.8082**

**Gaus Width: 3.3310**

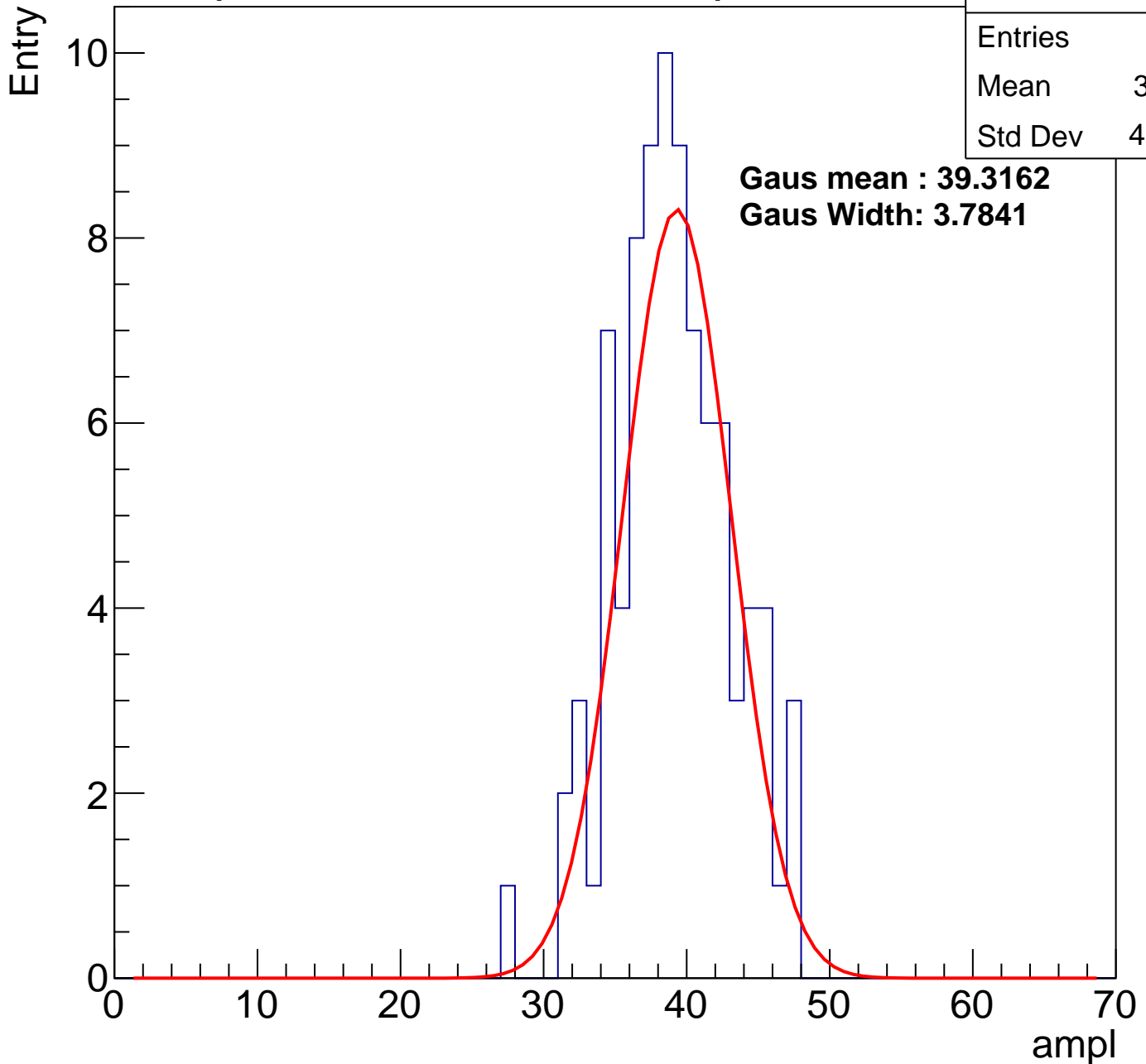


# B0L000S, U10-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	38.61
Std Dev	4.013

**Gaus mean : 39.3162**  
**Gaus Width: 3.7841**



# B0L000S, U10-ch17, adc2

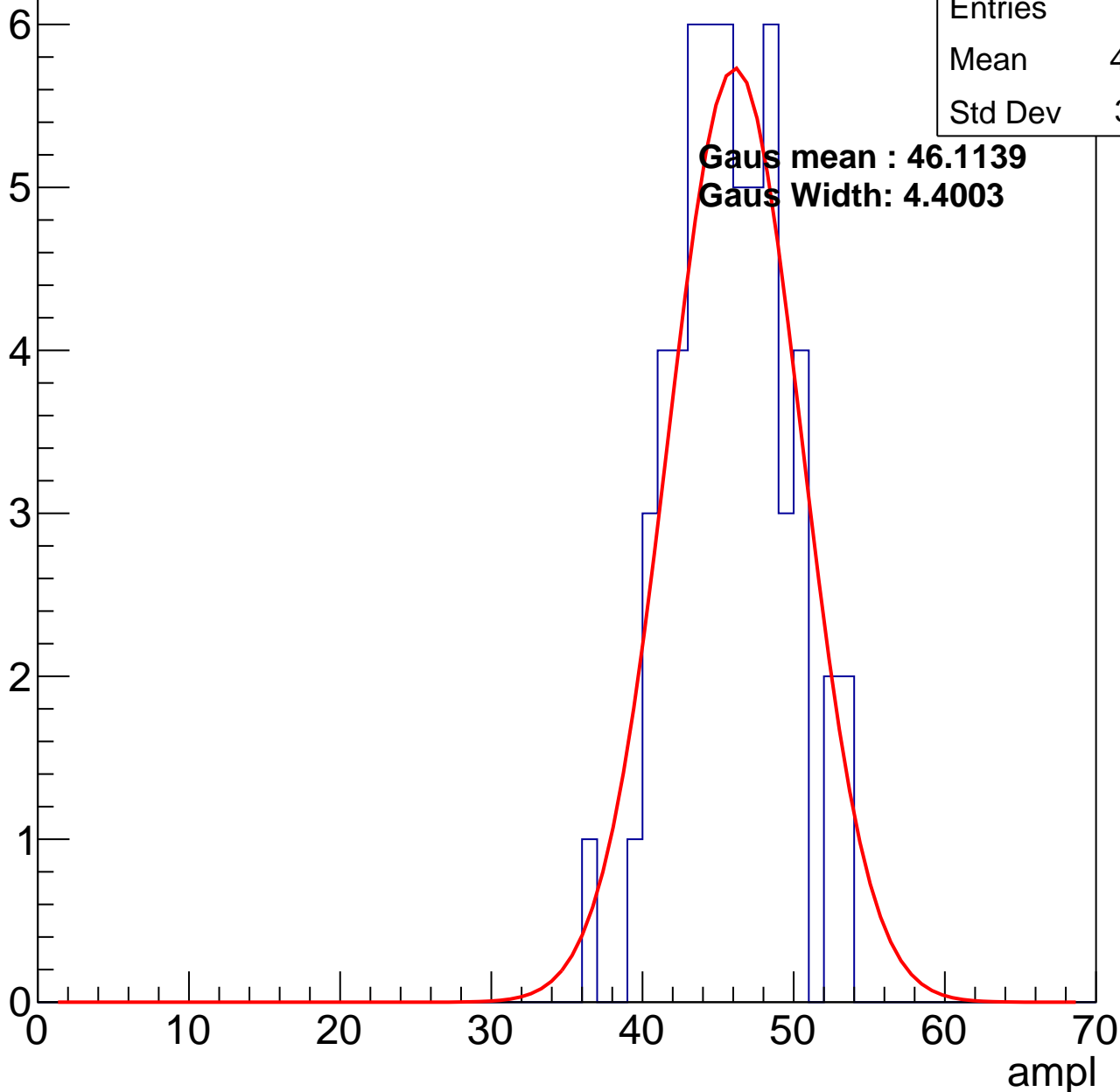
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.33
Std Dev	3.641

**Gaus mean : 46.1139**

**Gaus Width: 4.4003**

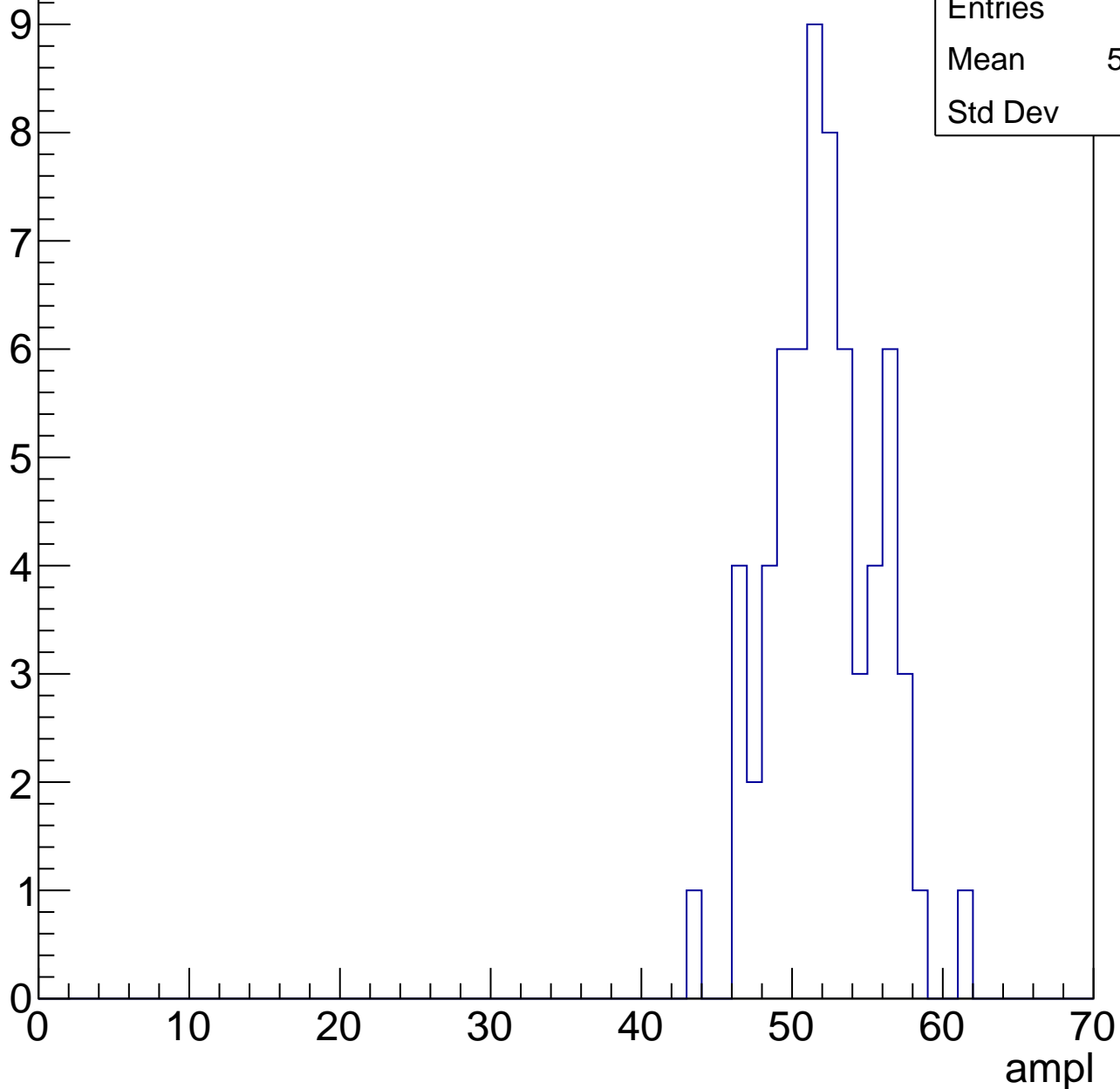


# B0L000S, U10-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.69
Std Dev	3.45

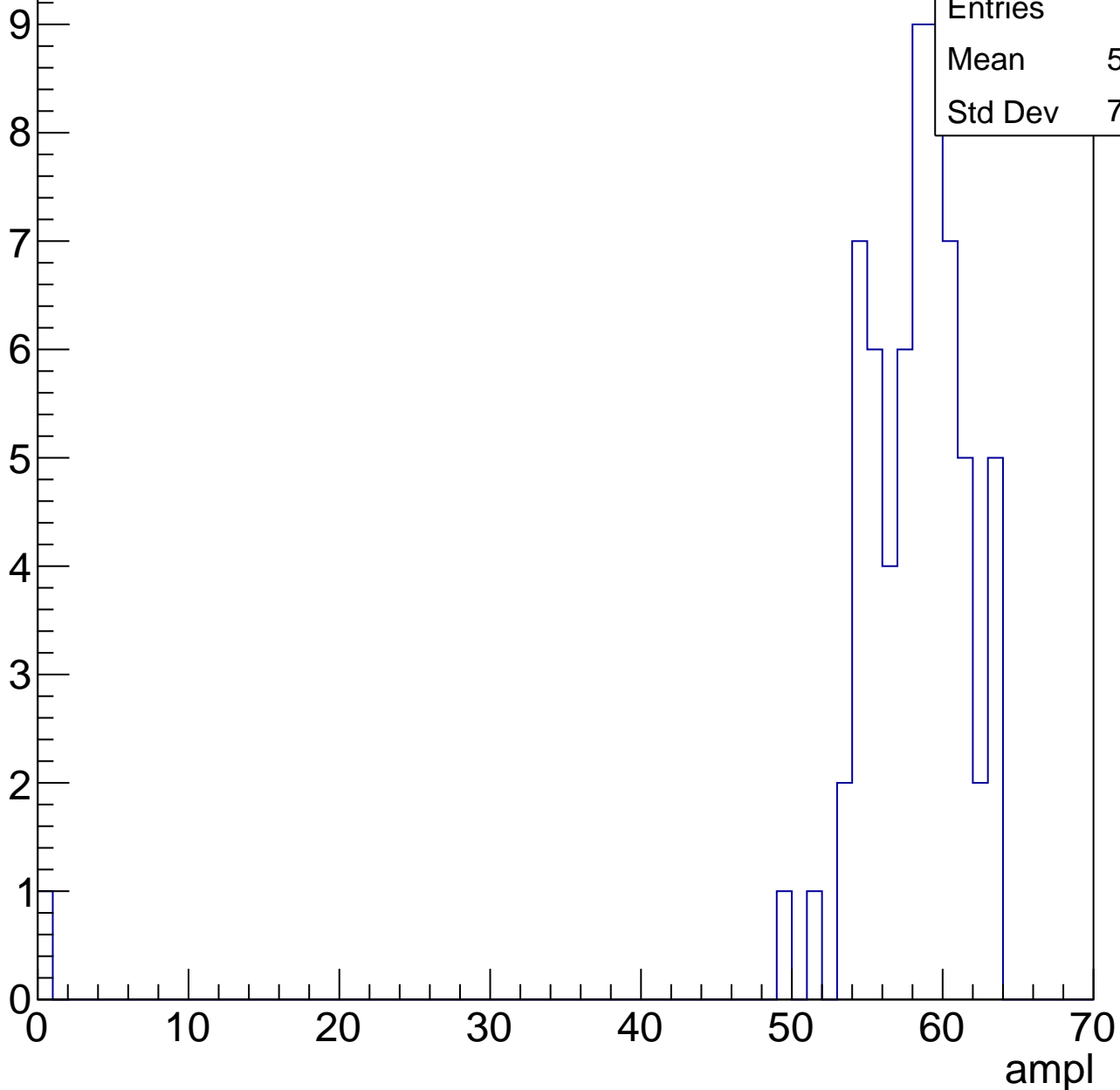


# B0L000S, U10-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.88
Std Dev	7.735

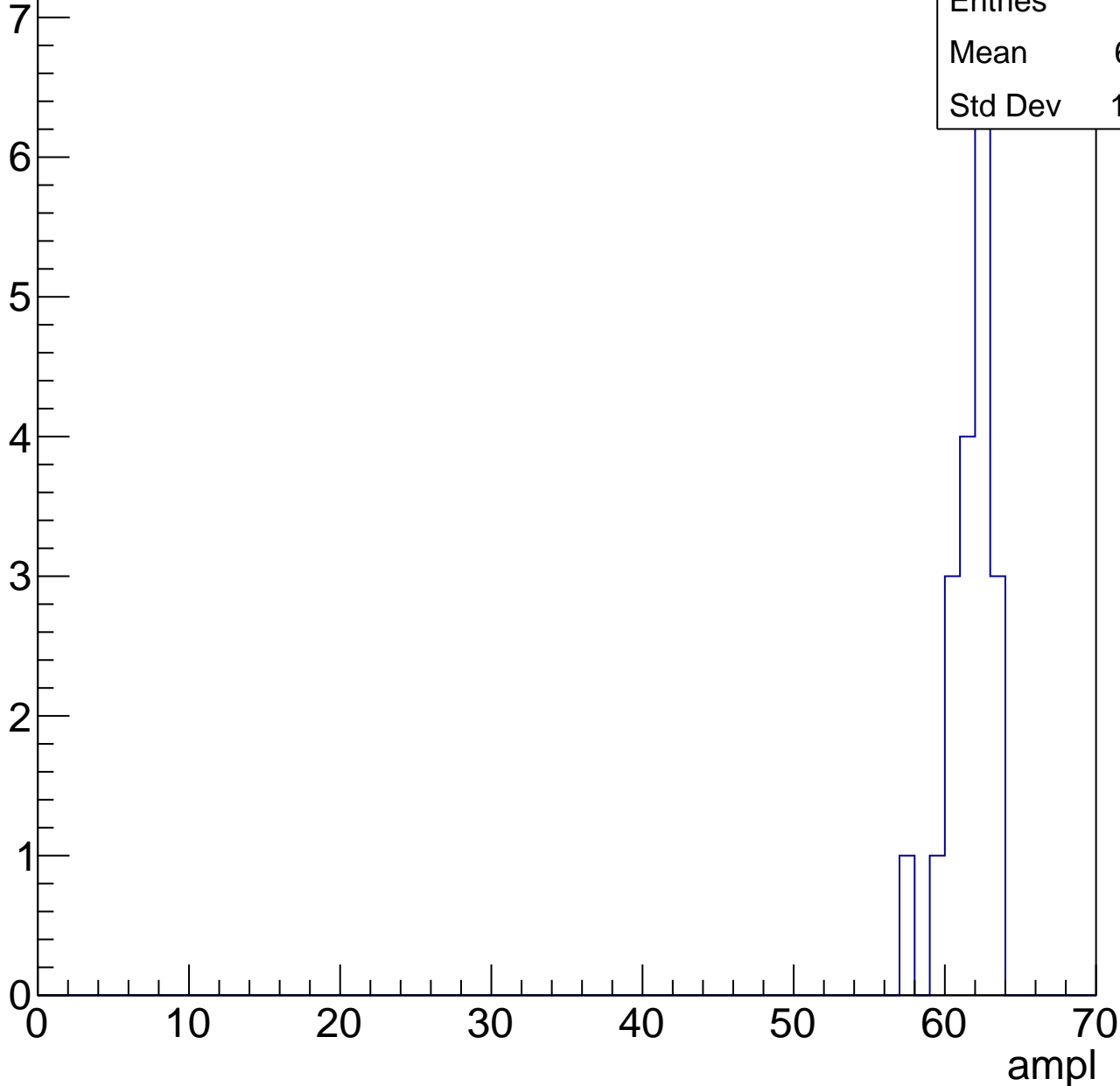


# B0L000S, U10-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	61.21
Std Dev	1.472



# B0L000S, U10-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl

# B0L000S, U10-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	30.06
Std Dev	3.824

**Gaus mean : 31.6245**

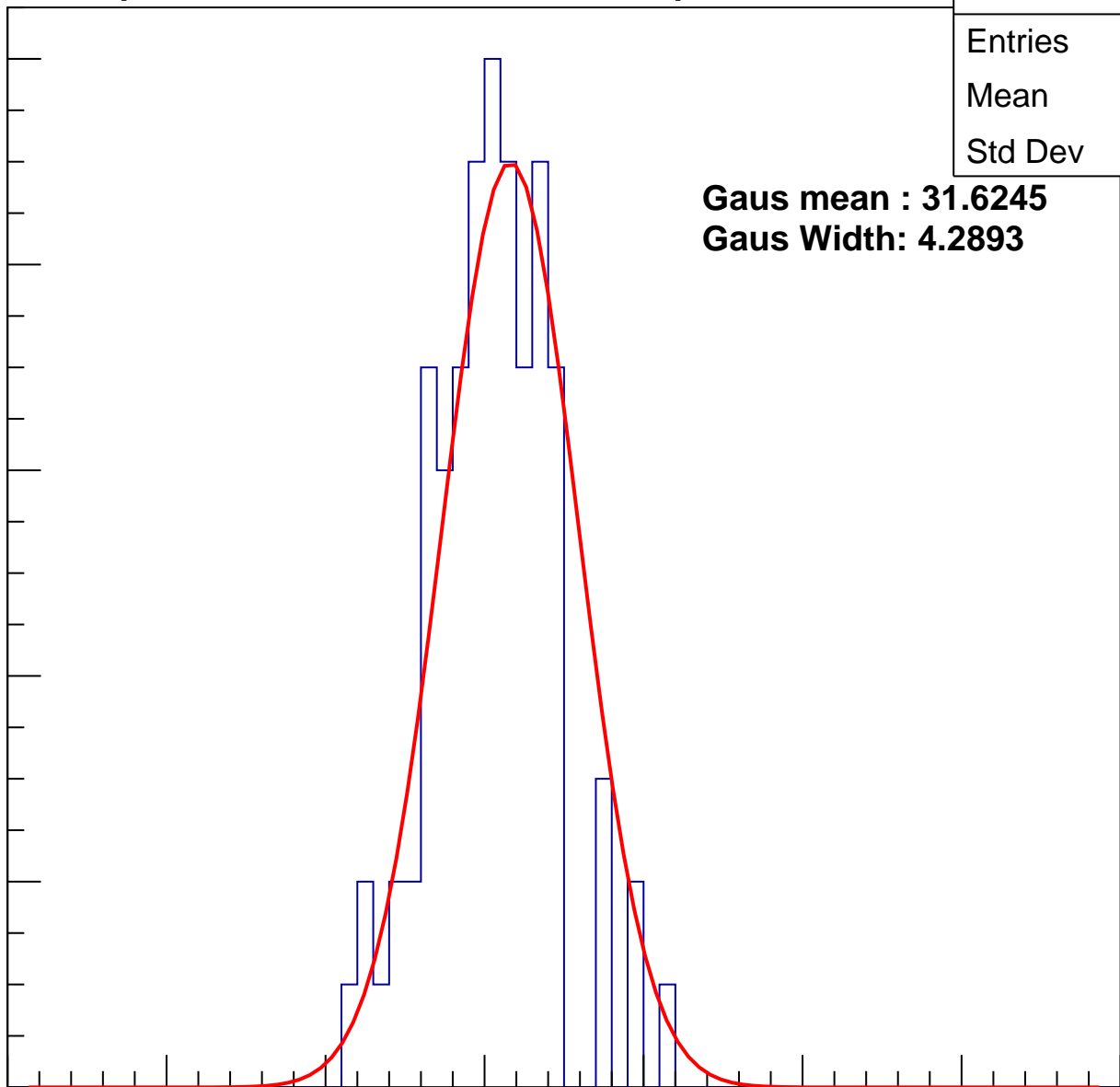
**Gaus Width: 4.2893**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch18, adc1

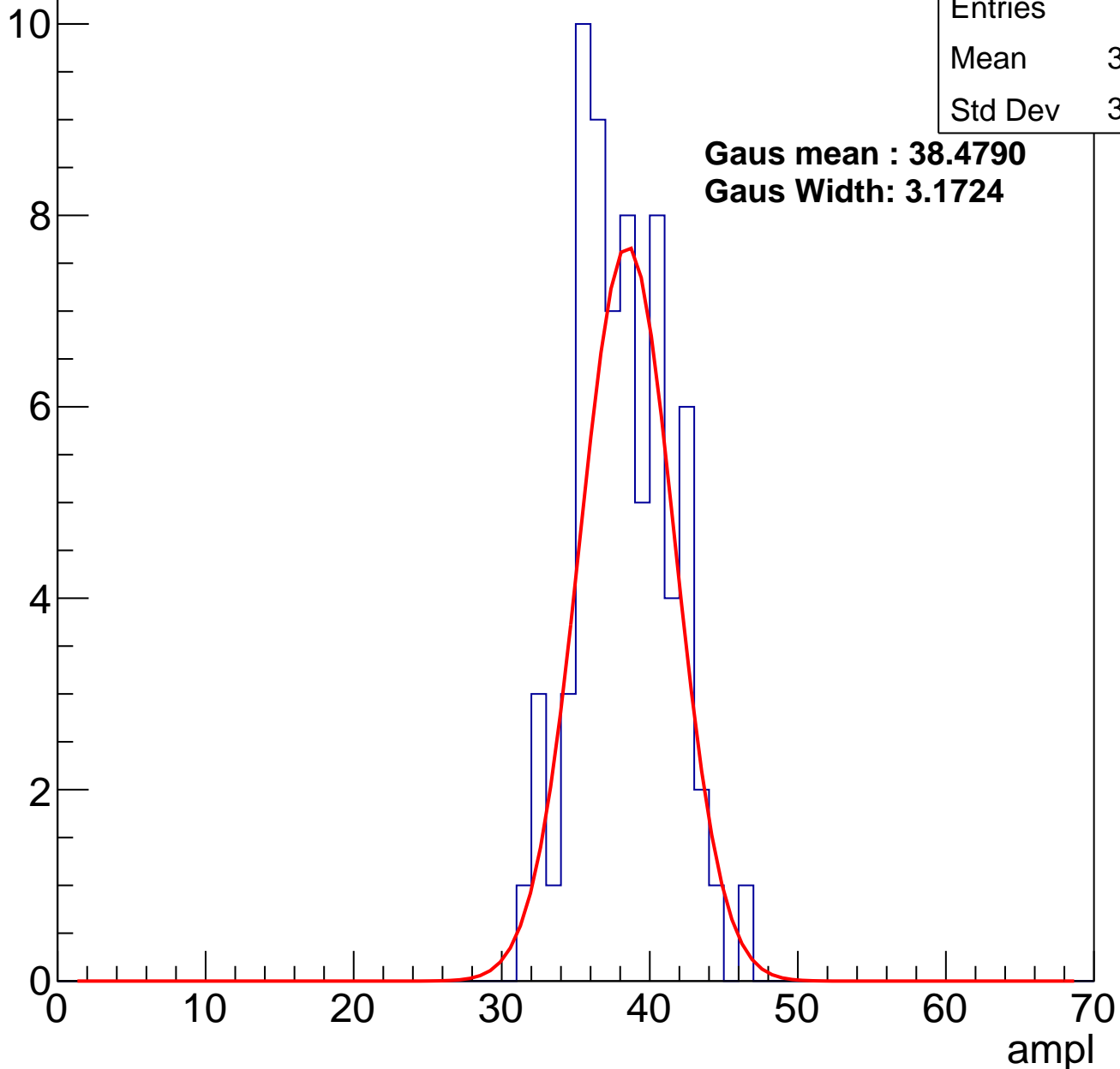
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	37.77
Std Dev	3.135

**Gaus mean : 38.4790**

**Gaus Width: 3.1724**

Entry



# B0L000S, U10-ch18, adc2

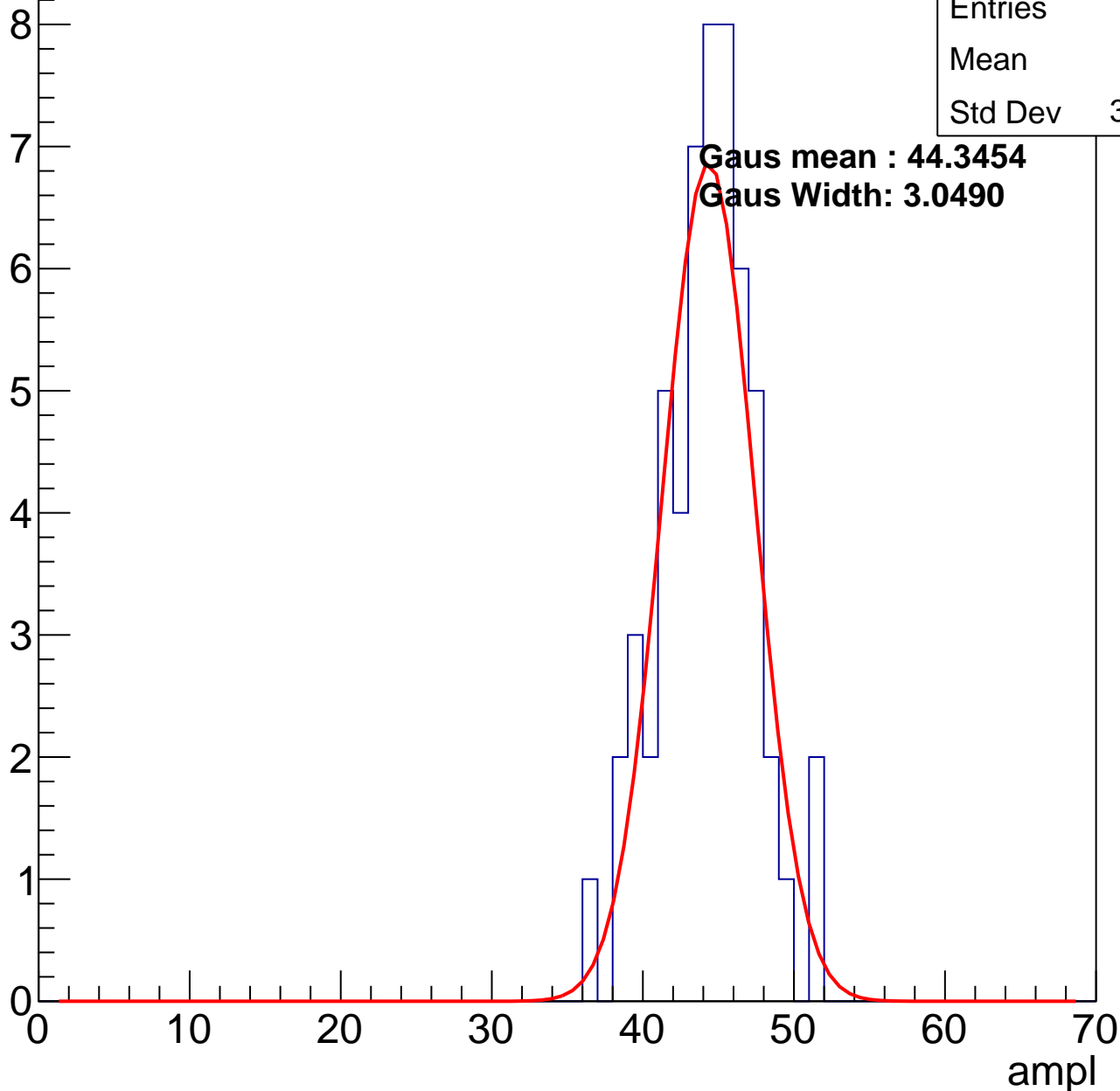
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.8
Std Dev	3.108

**Gaus mean : 44.3454**

**Gaus Width: 3.0490**

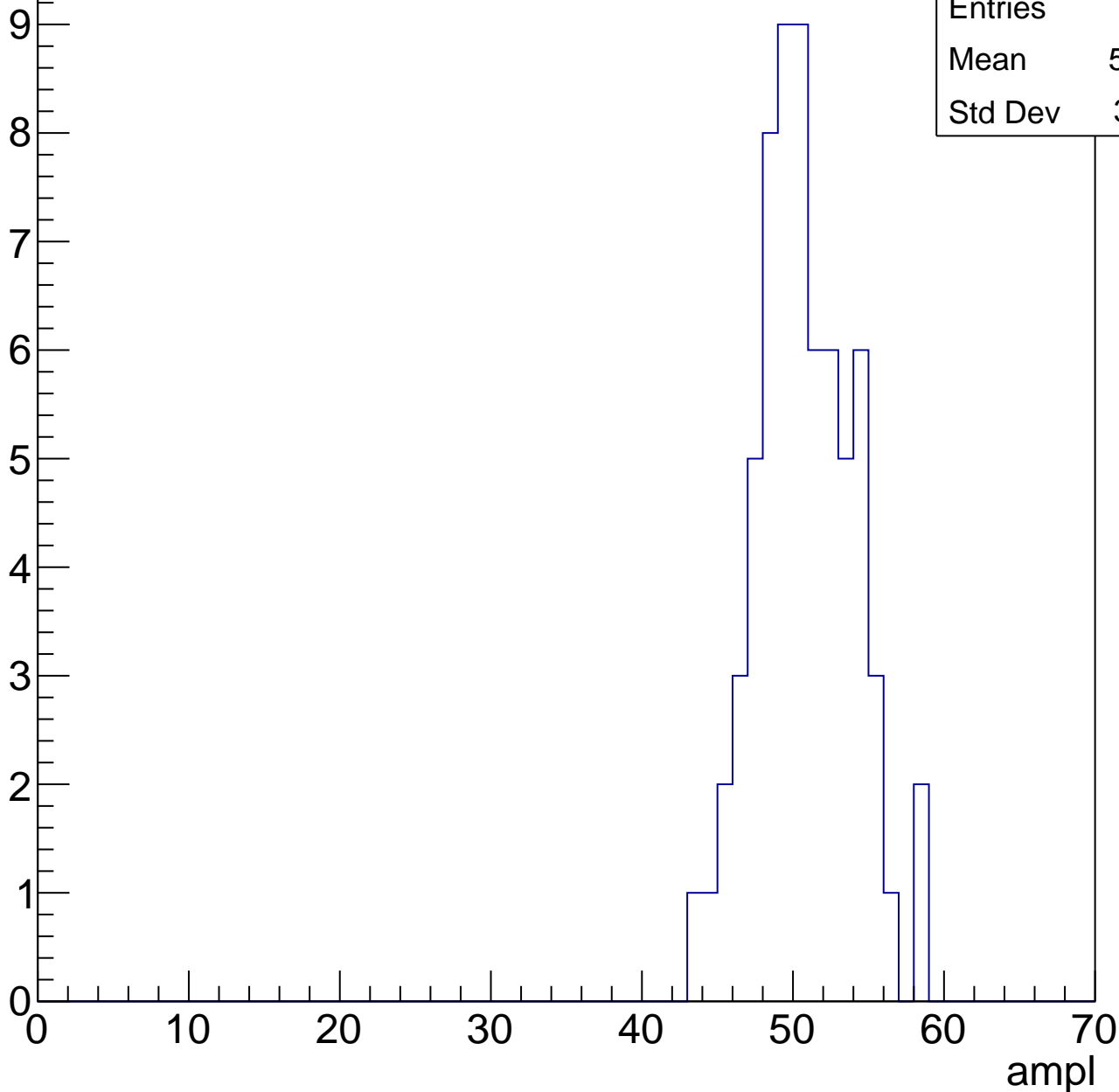


# B0L000S, U10-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	50.28
Std Dev	3.171



# B0L000S, U10-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	57.04
Std Dev	3.143

Entry

10

8

6

4

2

0

0

10

20

30

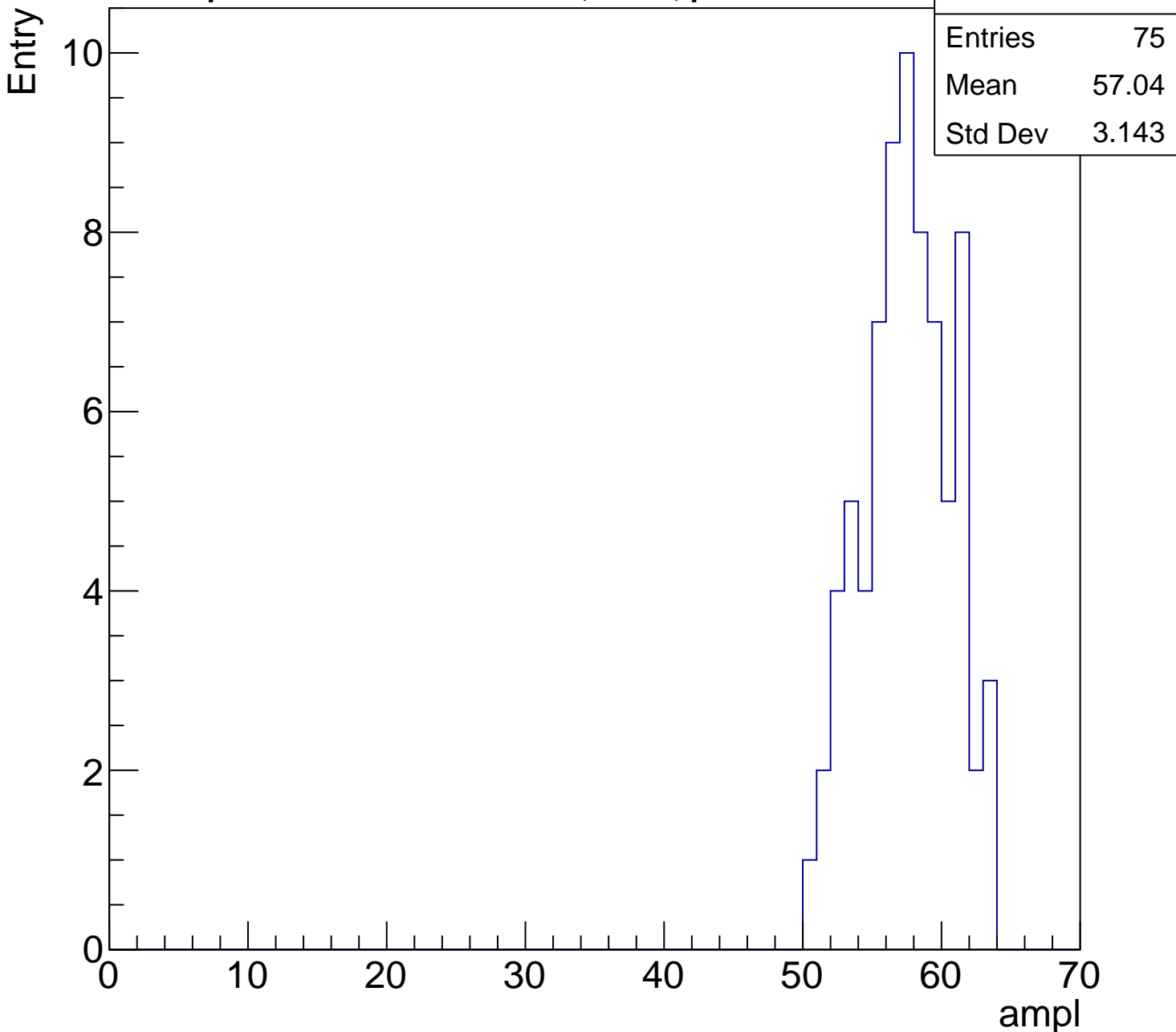
40

50

60

ampl

70



# B0L000S, U10-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

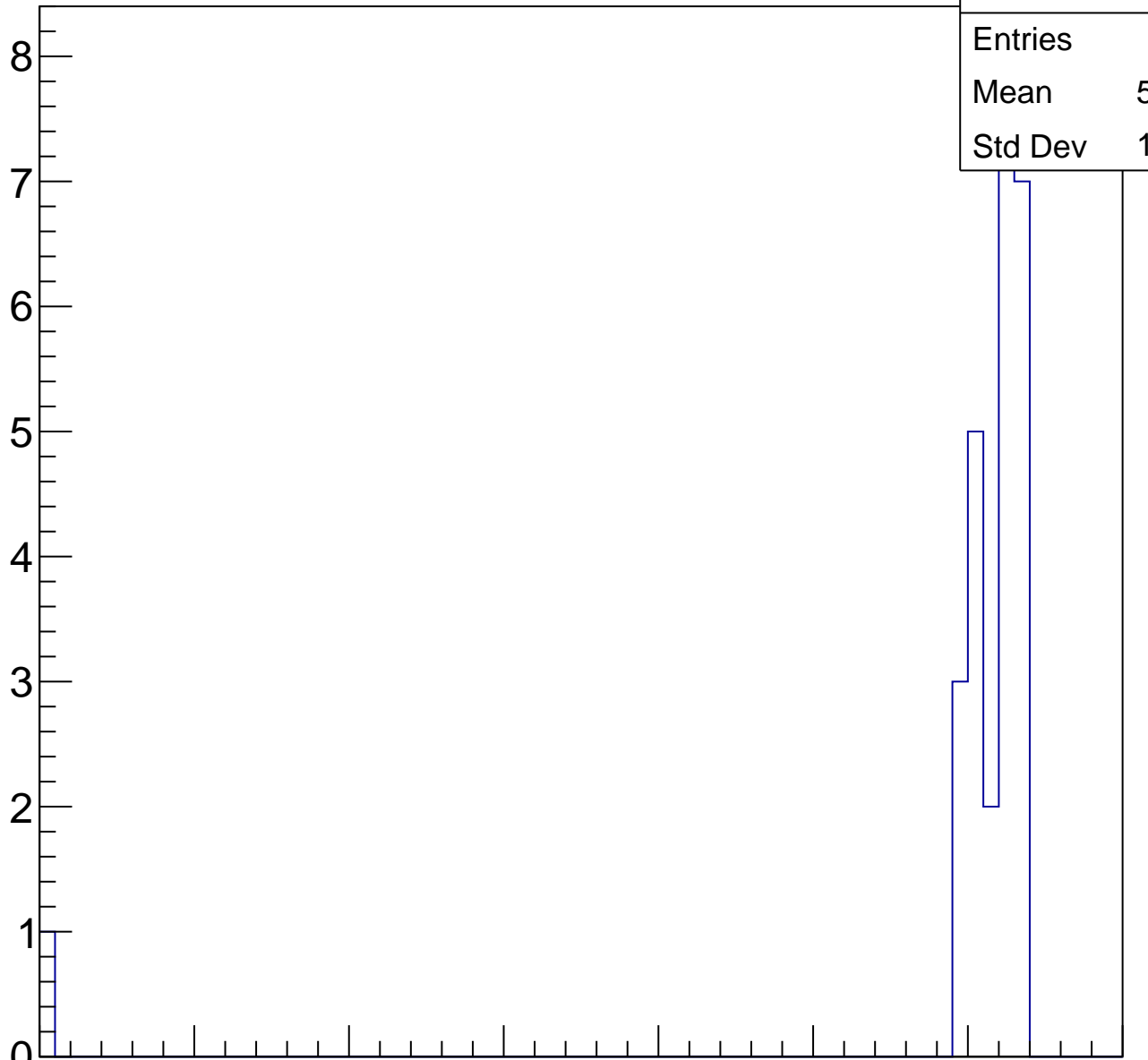
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	26
Mean	59.08
Std Dev	11.89

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.63
Std Dev	2.99

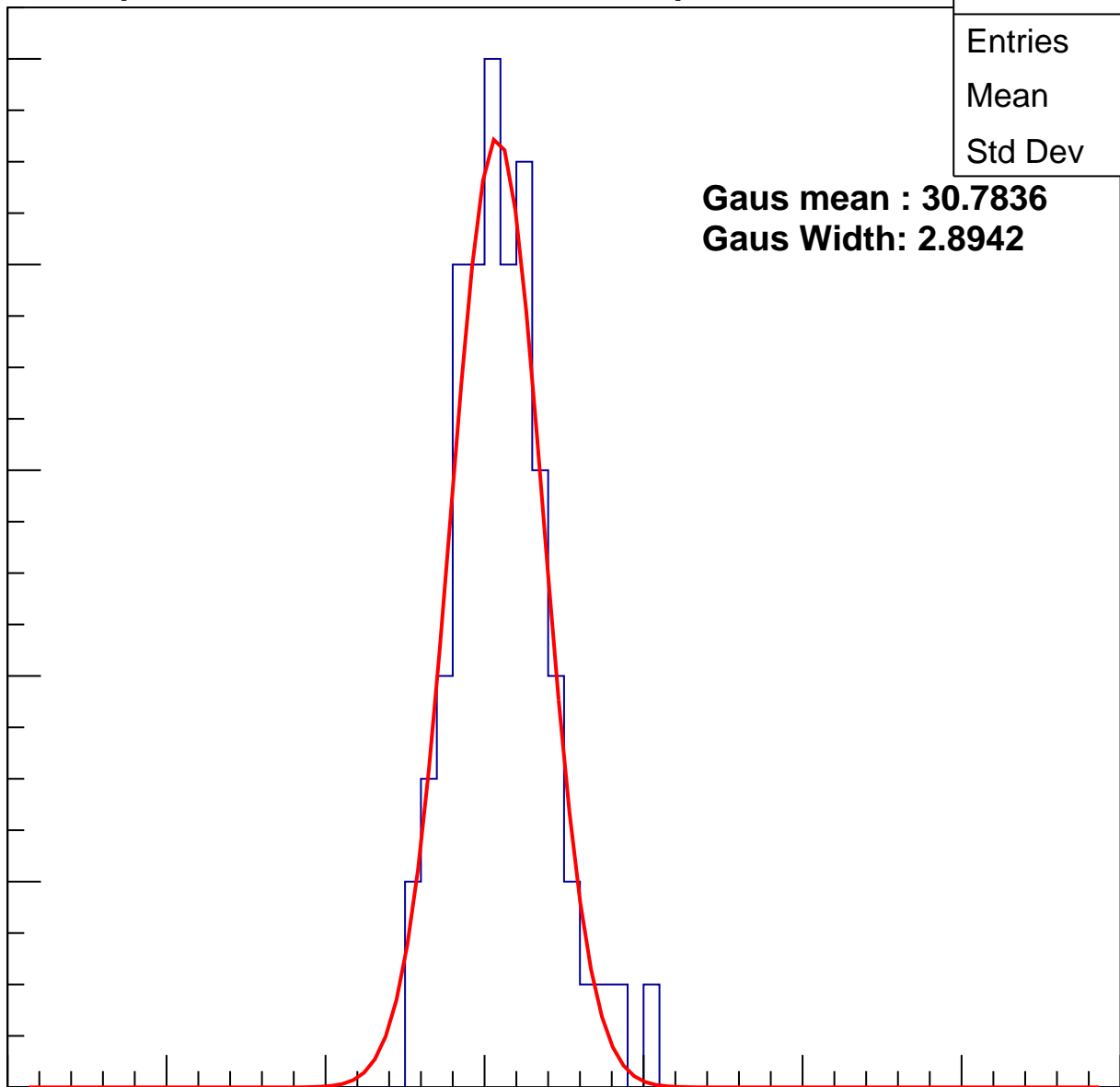
**Gaus mean : 30.7836**

**Gaus Width: 2.8942**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



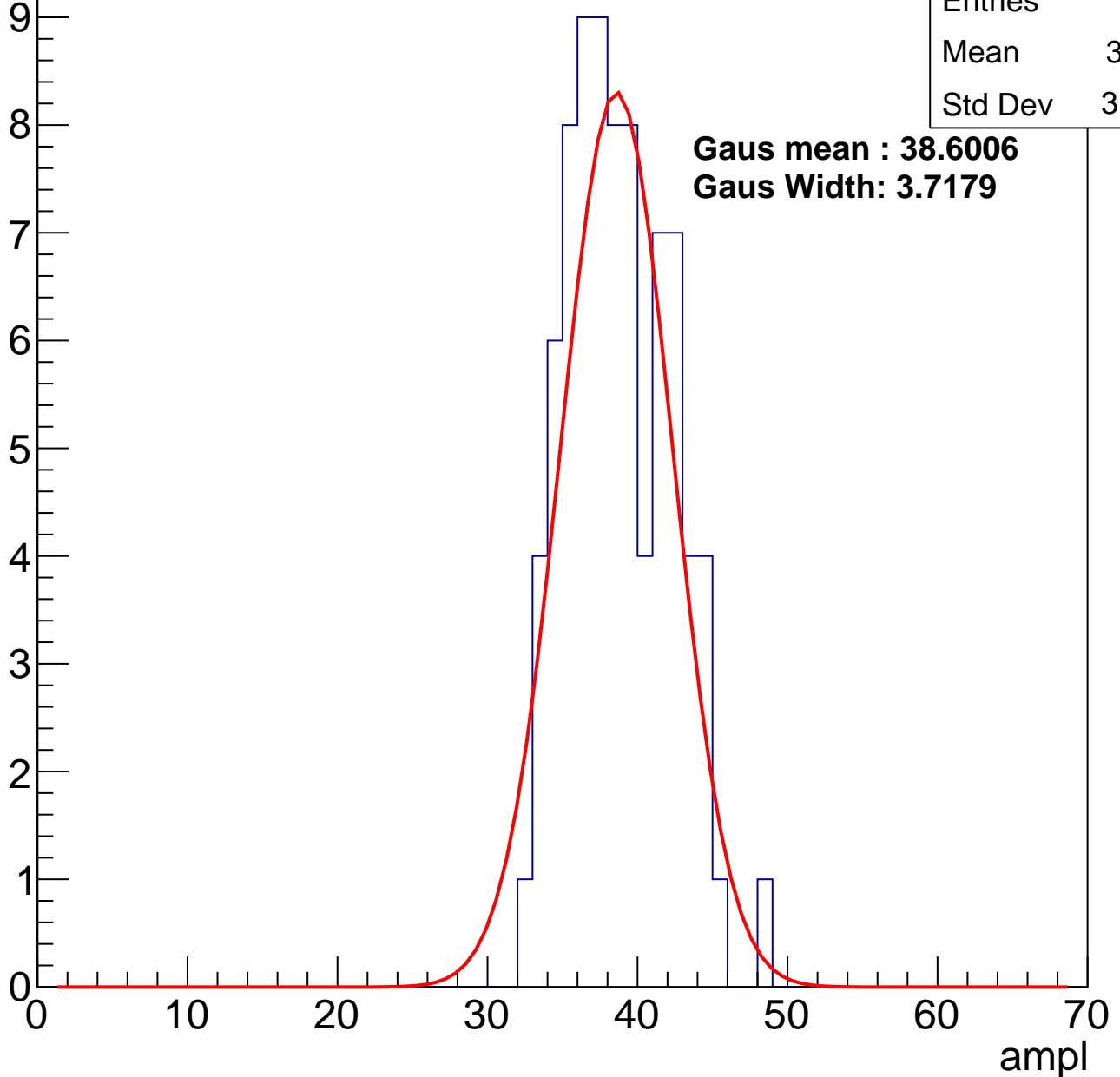
# B0L000S, U10-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	38.31
Std Dev	3.394

**Gaus mean : 38.6006**  
**Gaus Width: 3.7179**



# B0L000S, U10-ch19, adc2

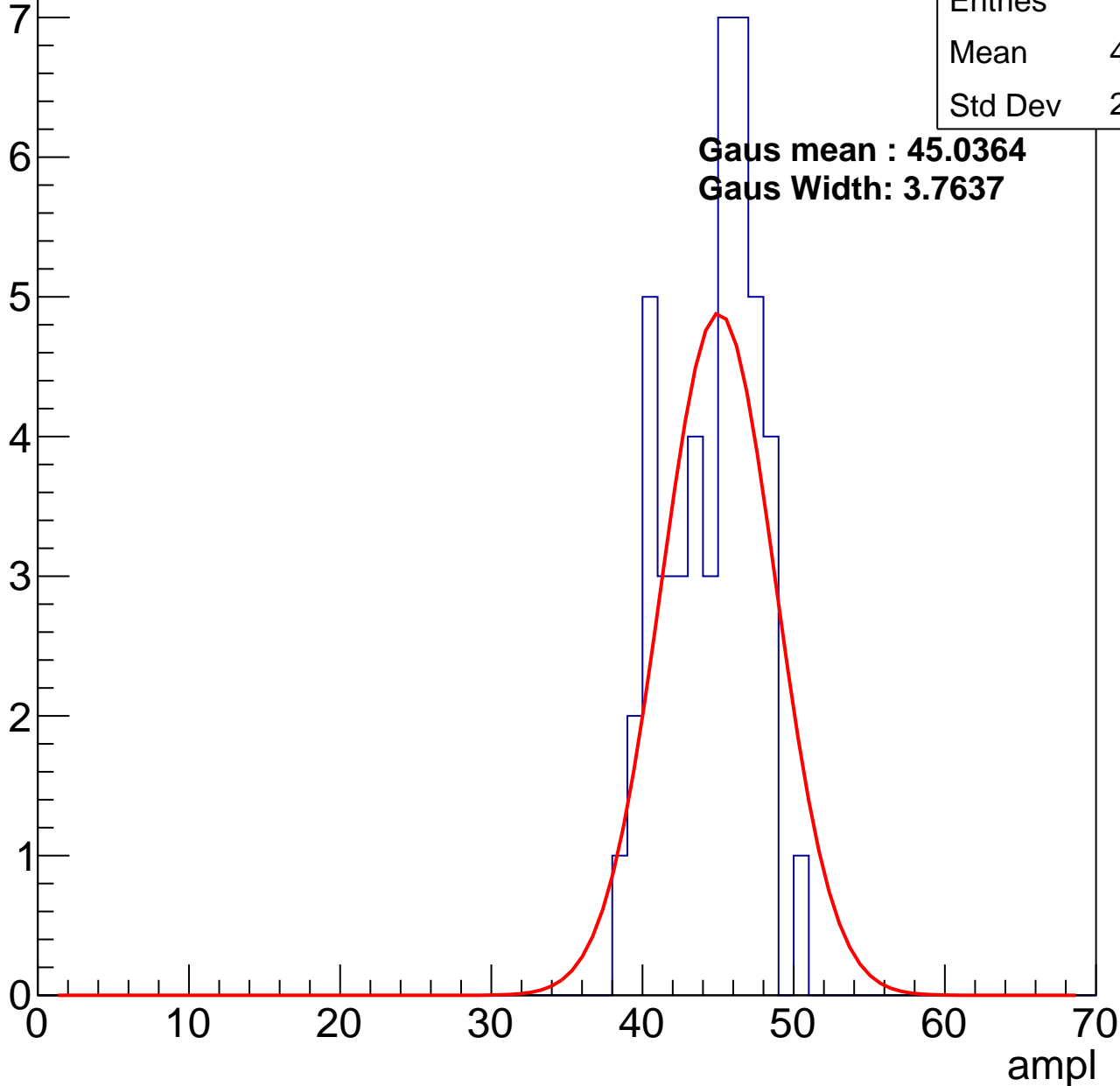
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	44.07
Std Dev	2.939

**Gaus mean : 45.0364**

**Gaus Width: 3.7637**

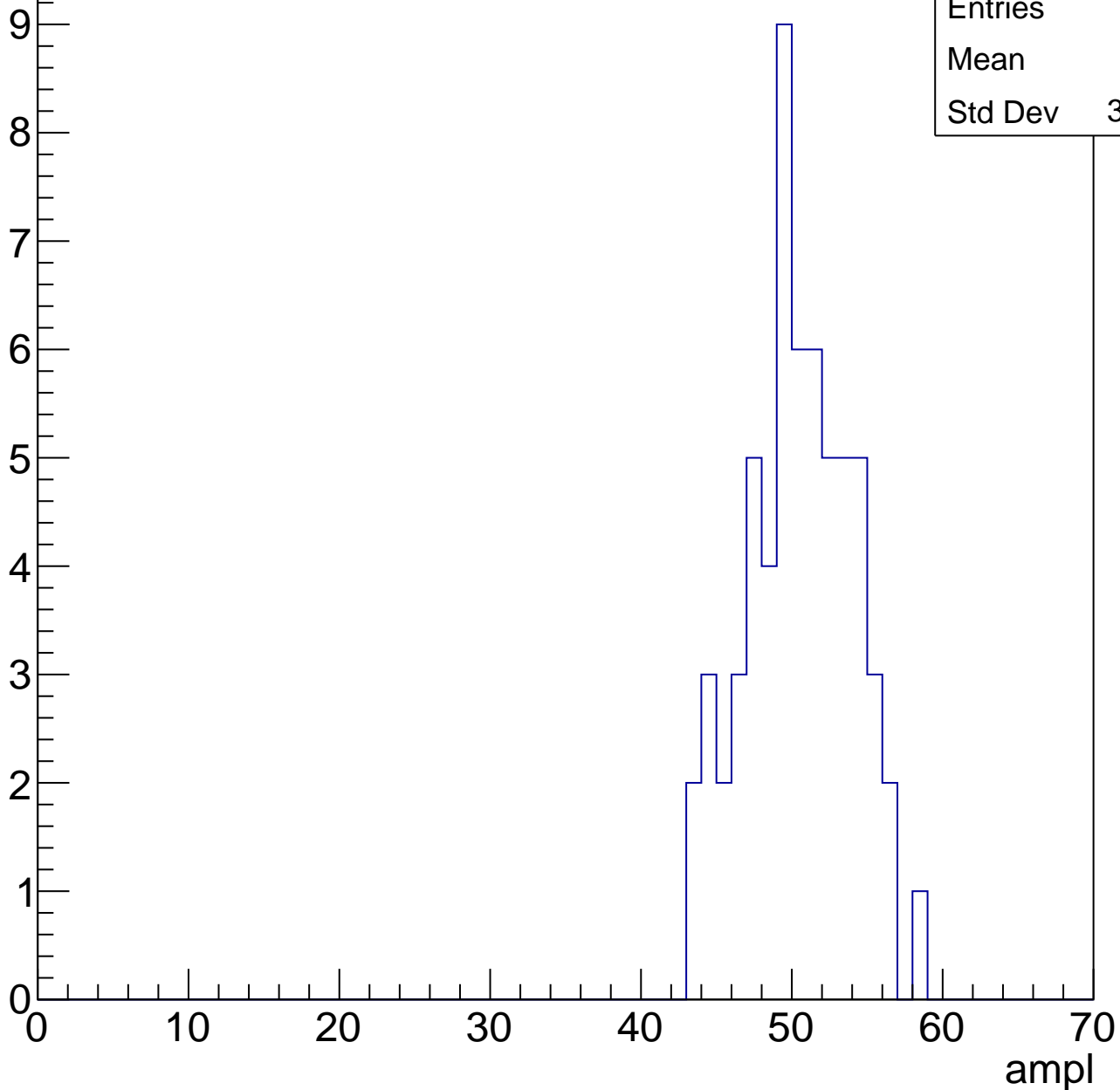


# B0L000S, U10-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

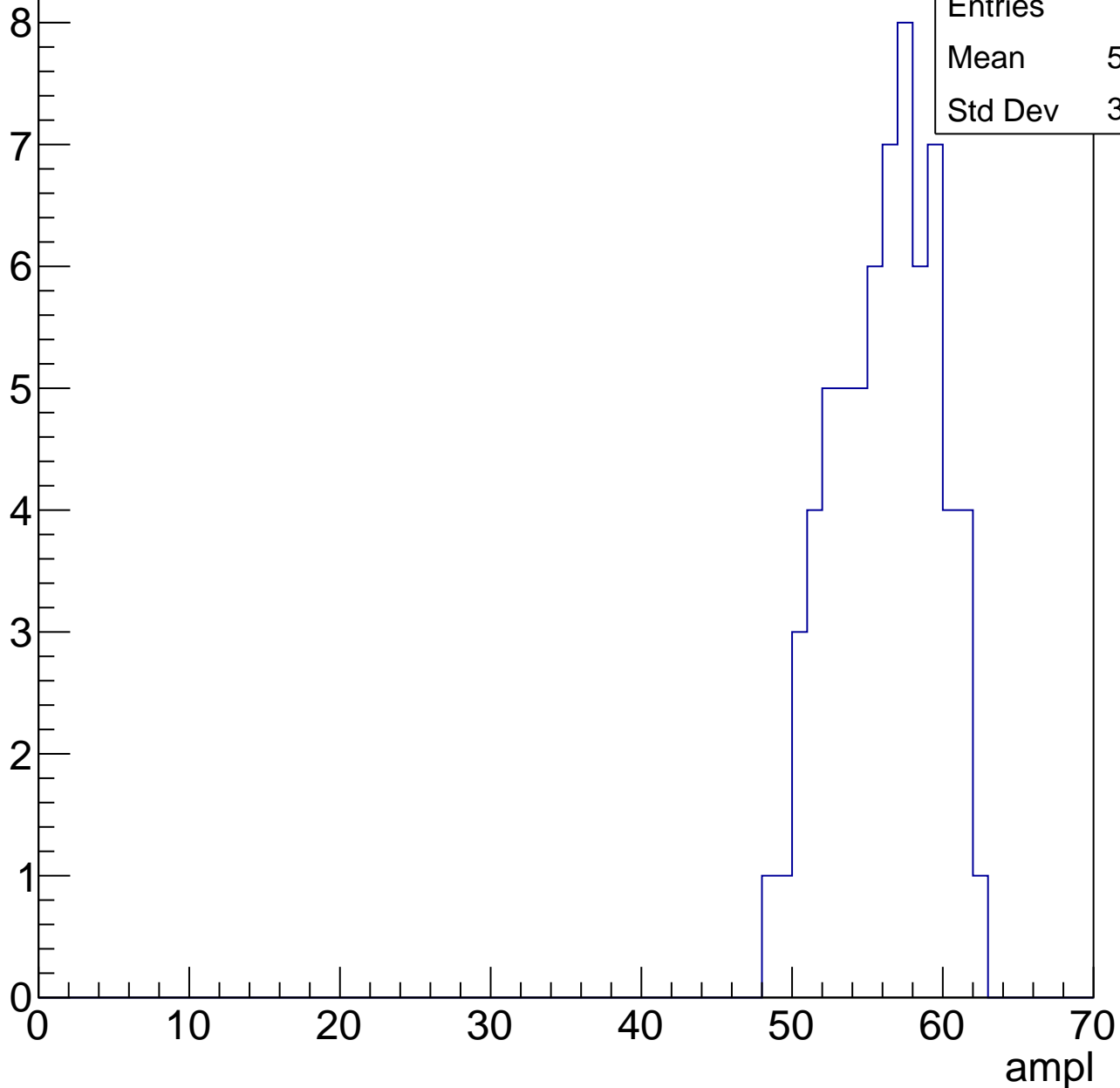
Entries	61
Mean	50
Std Dev	3.474



# B0L000S, U10-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

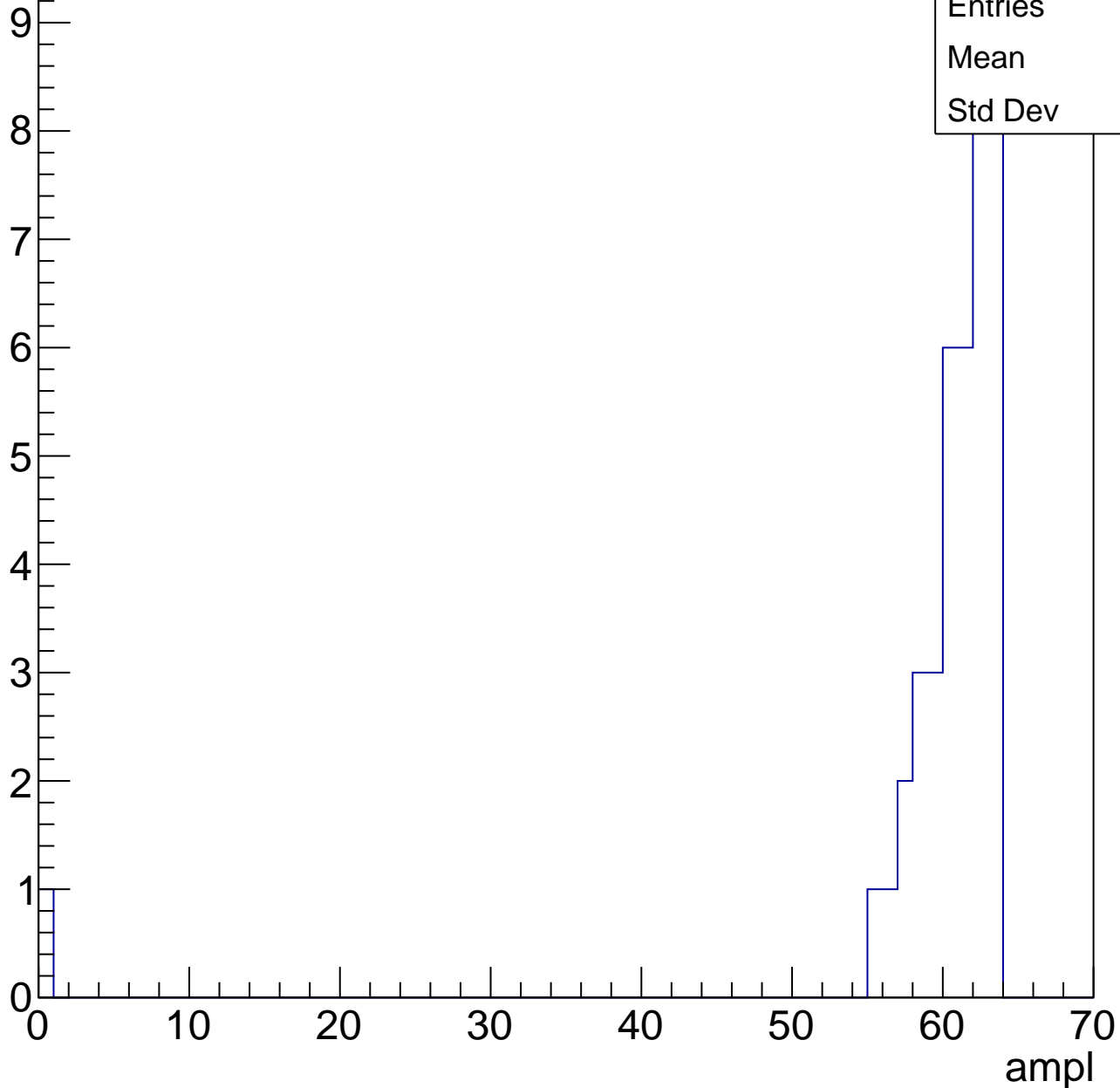
Entry



# B0L000S, U10-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	30.73
Std Dev	3.628

**Gaus mean : 31.0546**

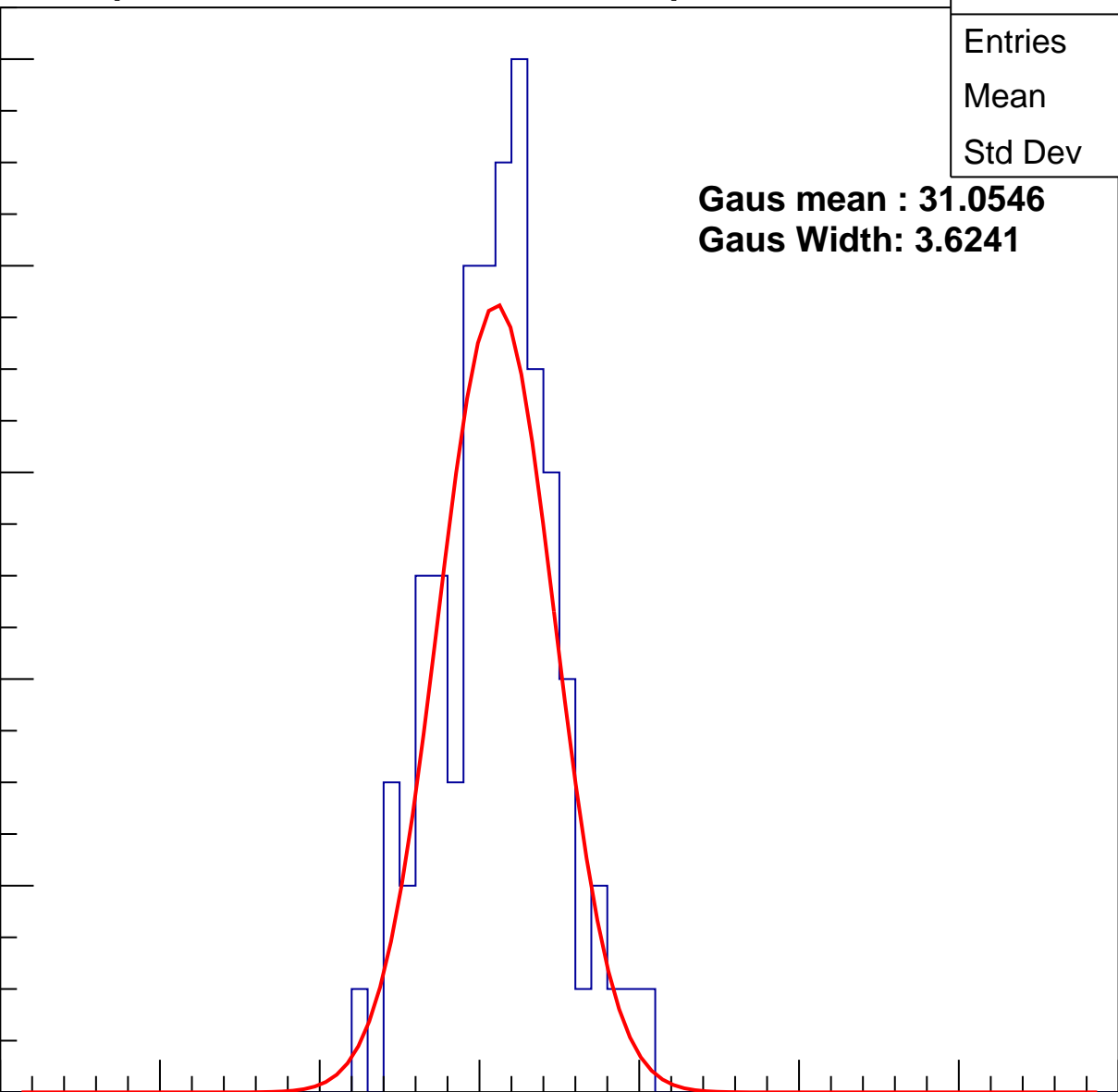
**Gaus Width: 3.6241**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch20, adc1

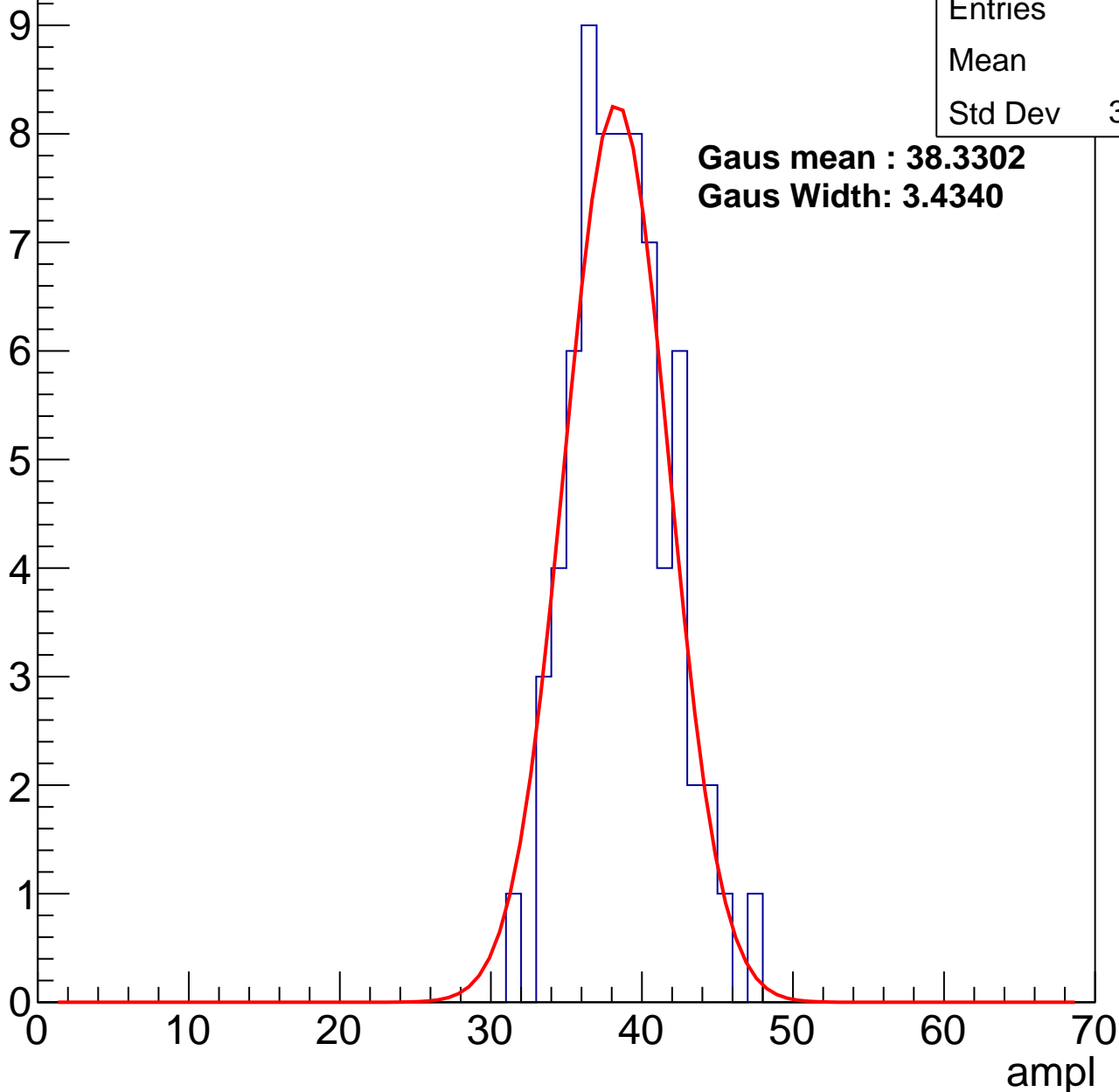
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	38.2
Std Dev	3.169

**Gaus mean : 38.3302**

**Gaus Width: 3.4340**



# B0L000S, U10-ch20, adc2

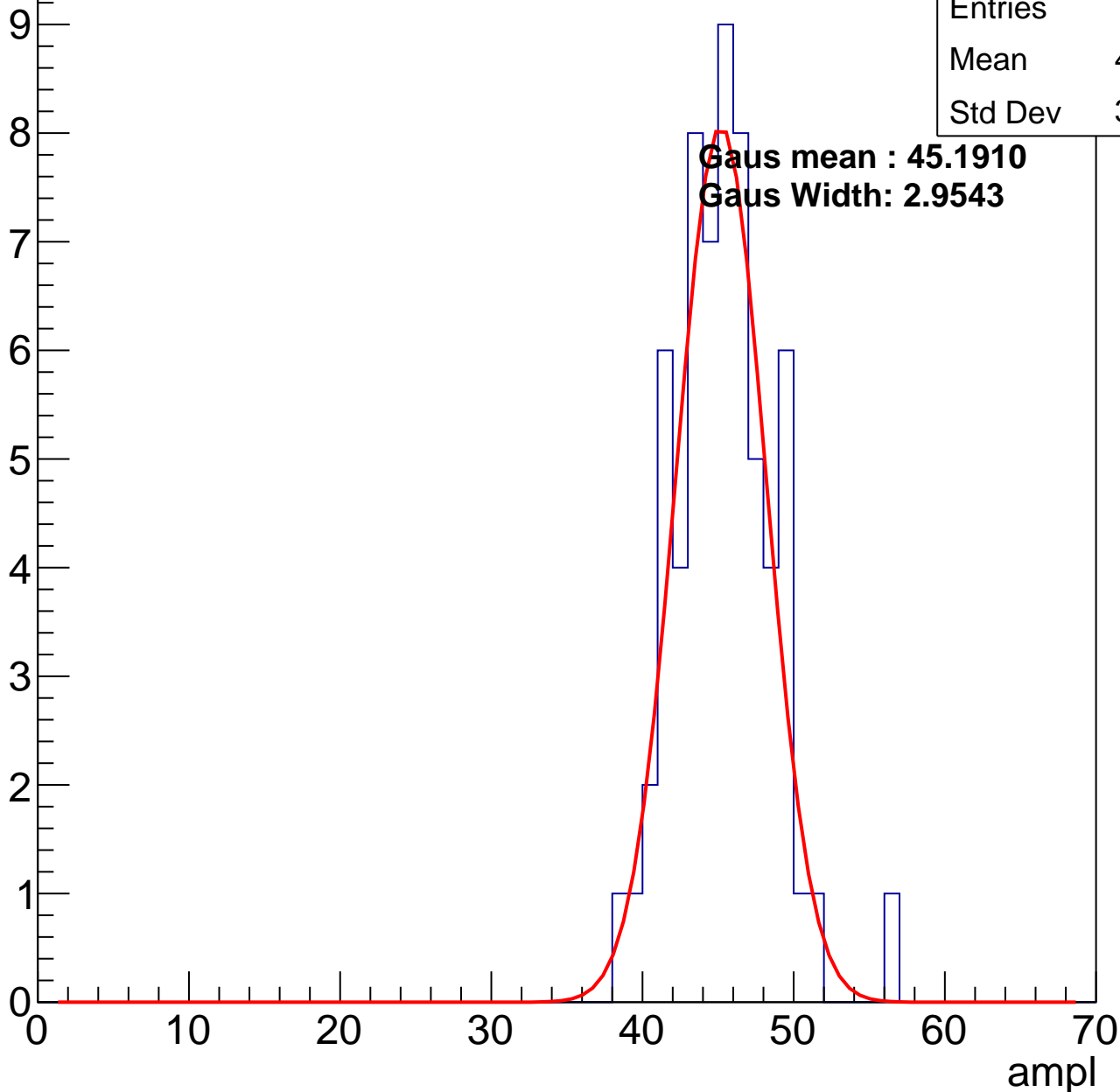
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	44.91
Std Dev	3.181

**Gaus mean : 45.1910**

**Gaus Width: 2.9543**

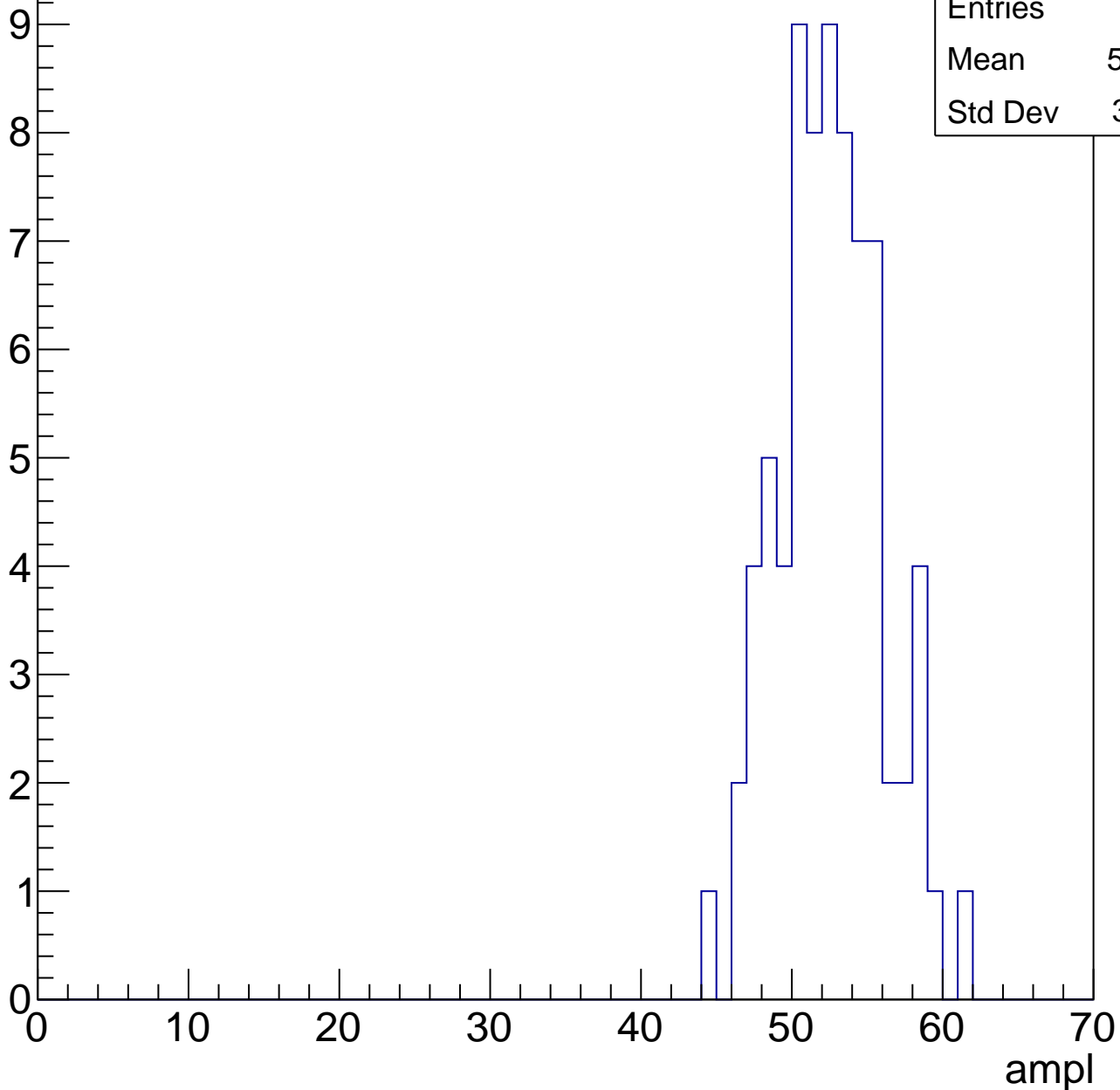


# B0L000S, U10-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	52.04
Std Dev	3.391

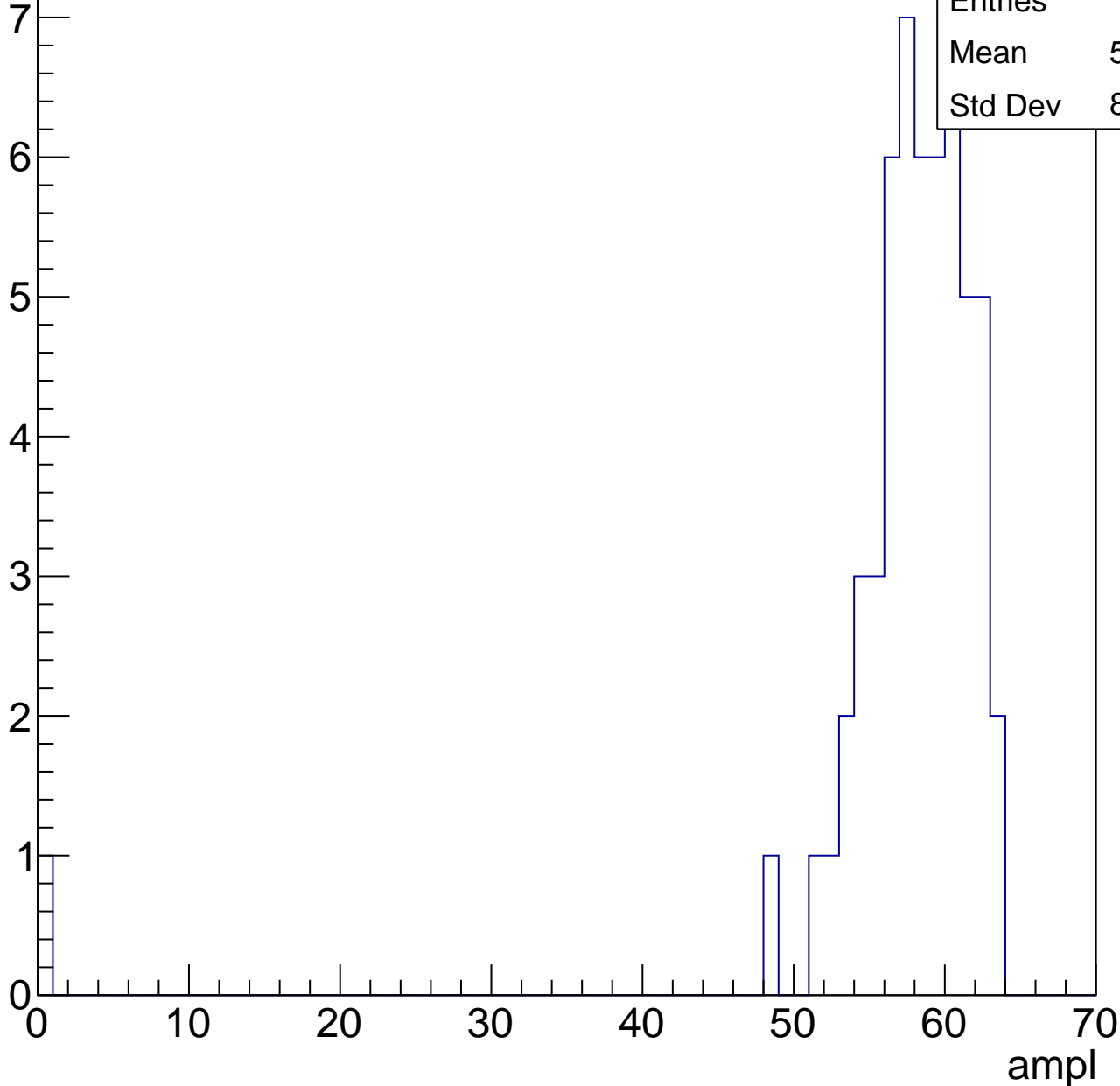


# B0L000S, U10-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	56.82
Std Dev	8.277

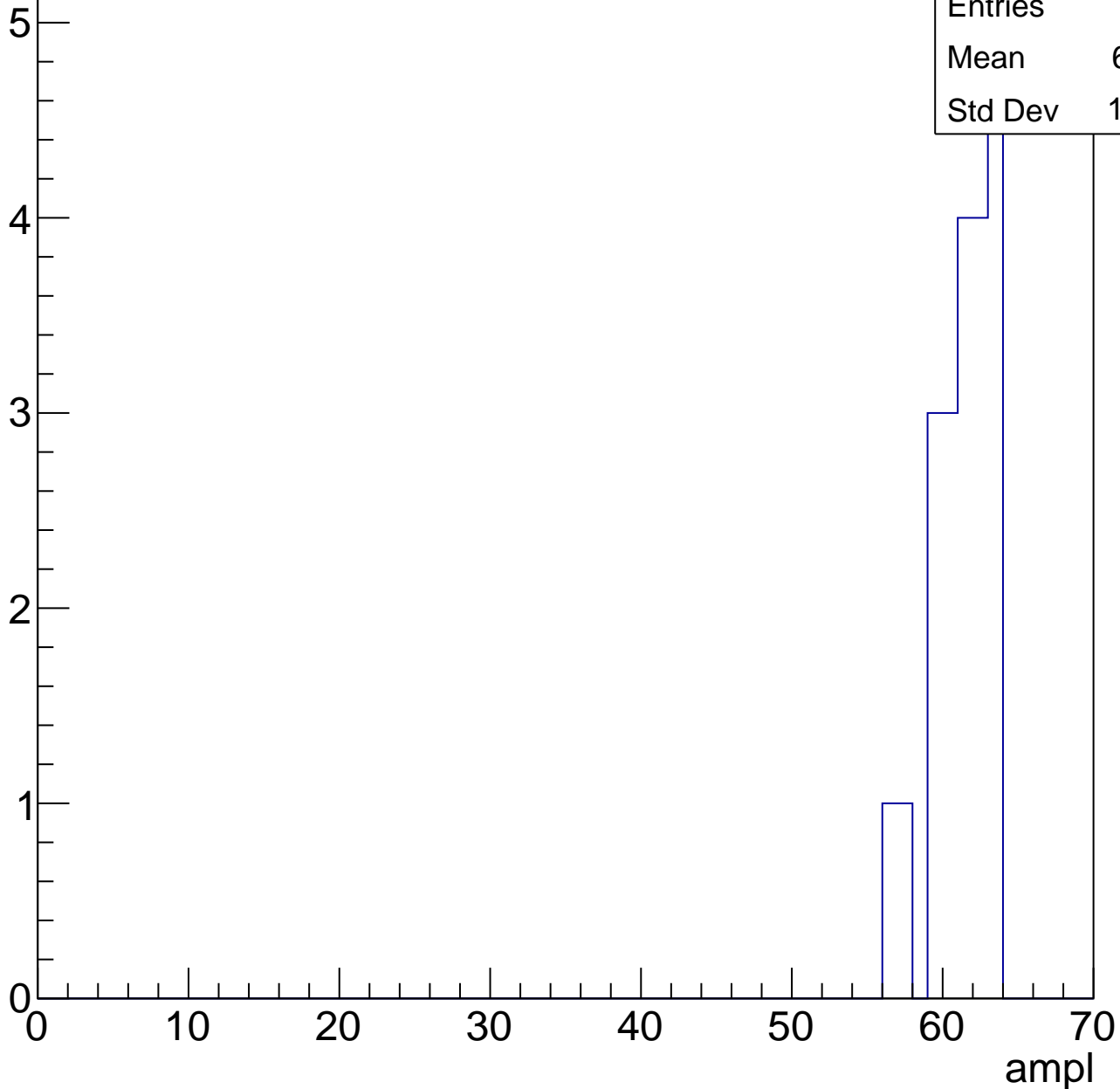


# B0L000S, U10-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	60.81
Std Dev	1.942



# B0L000S, U10-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch21, adc0

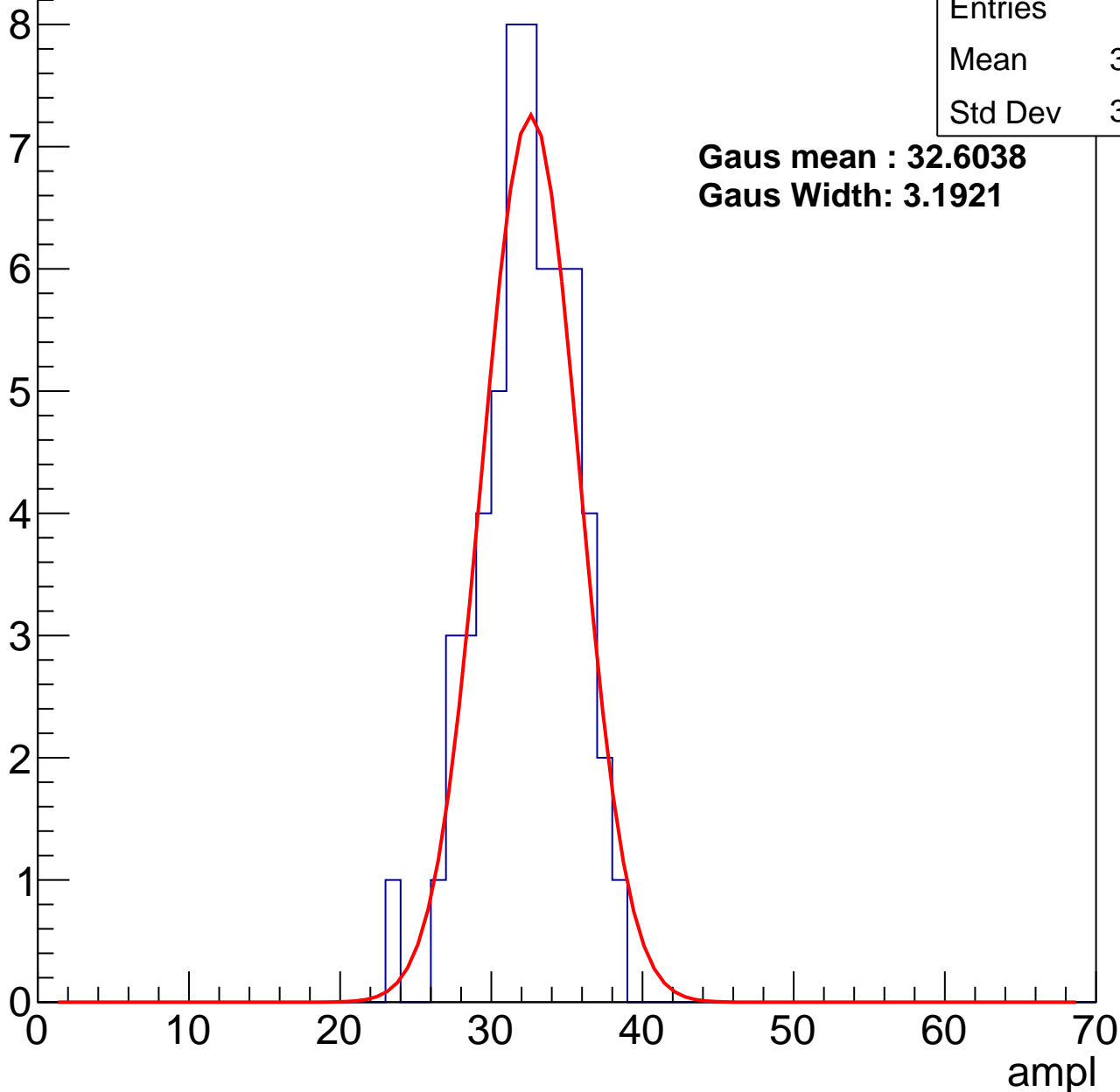
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	31.93
Std Dev	3.045

**Gaus mean : 32.6038**

**Gaus Width: 3.1921**



# B0L000S, U10-ch21, adc1

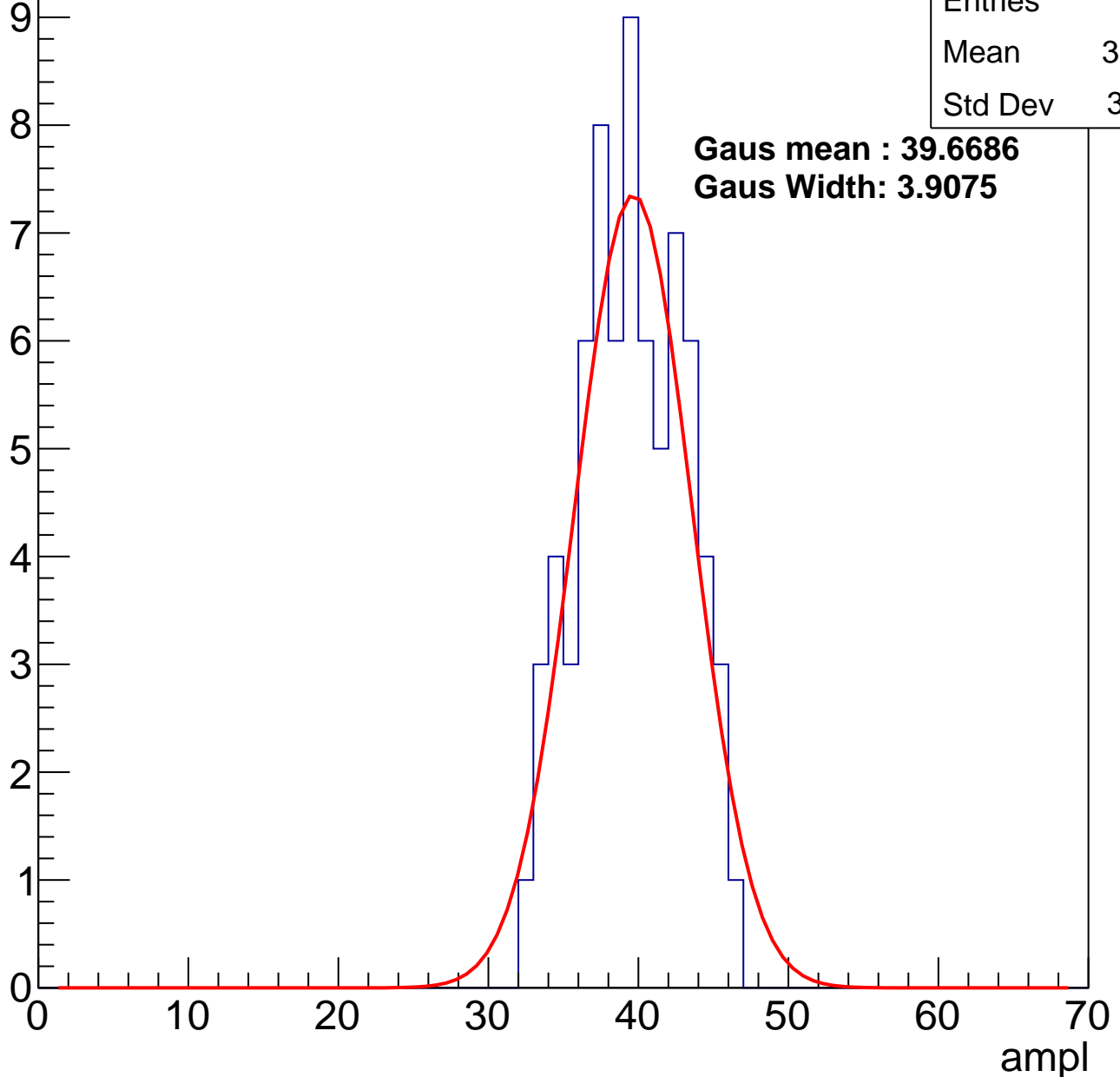
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	39.12
Std Dev	3.411

**Gaus mean : 39.6686**

**Gaus Width: 3.9075**



# B0L000S, U10-ch21, adc2

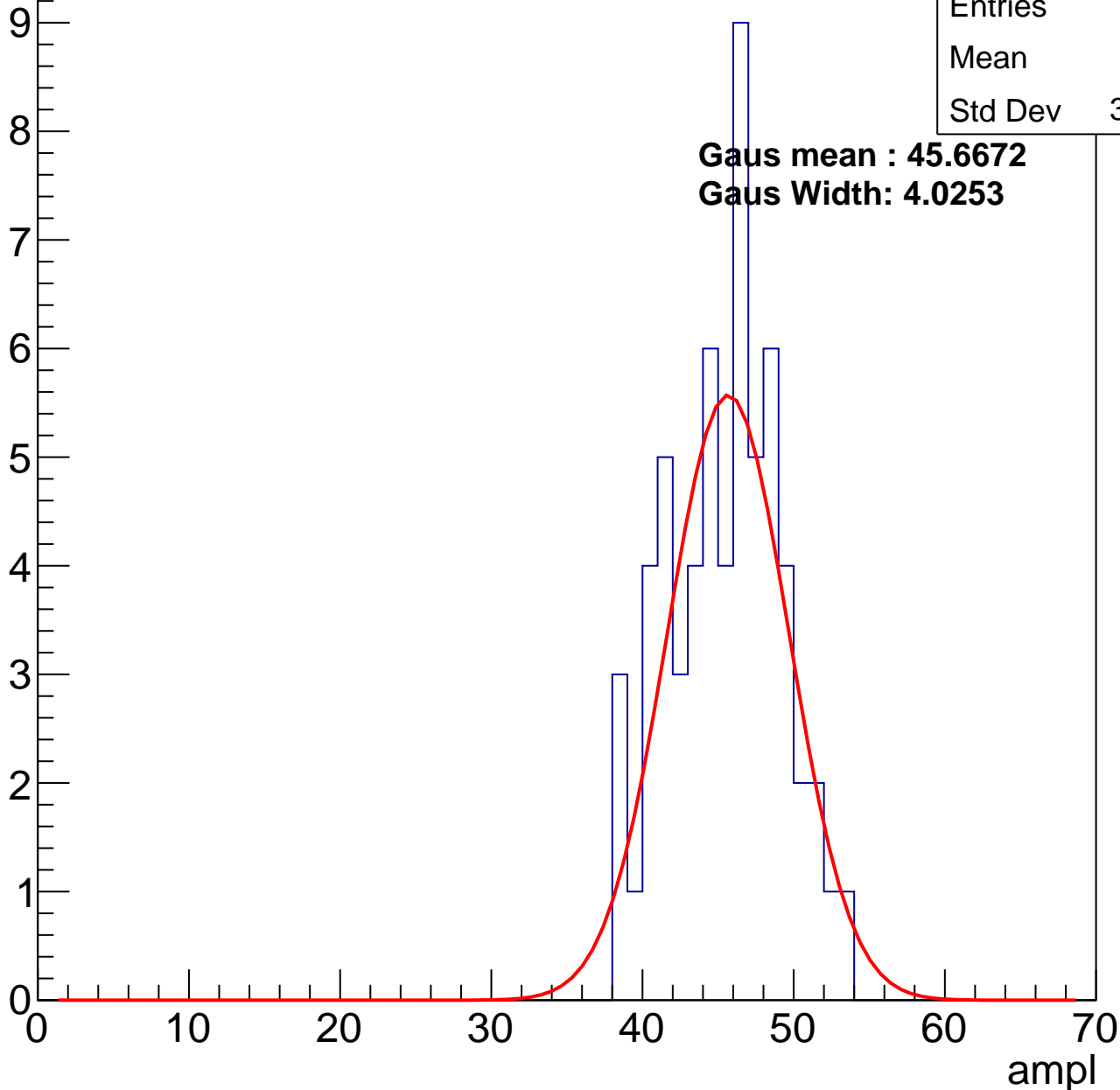
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	45
Std Dev	3.638

**Gaus mean : 45.6672**

**Gaus Width: 4.0253**

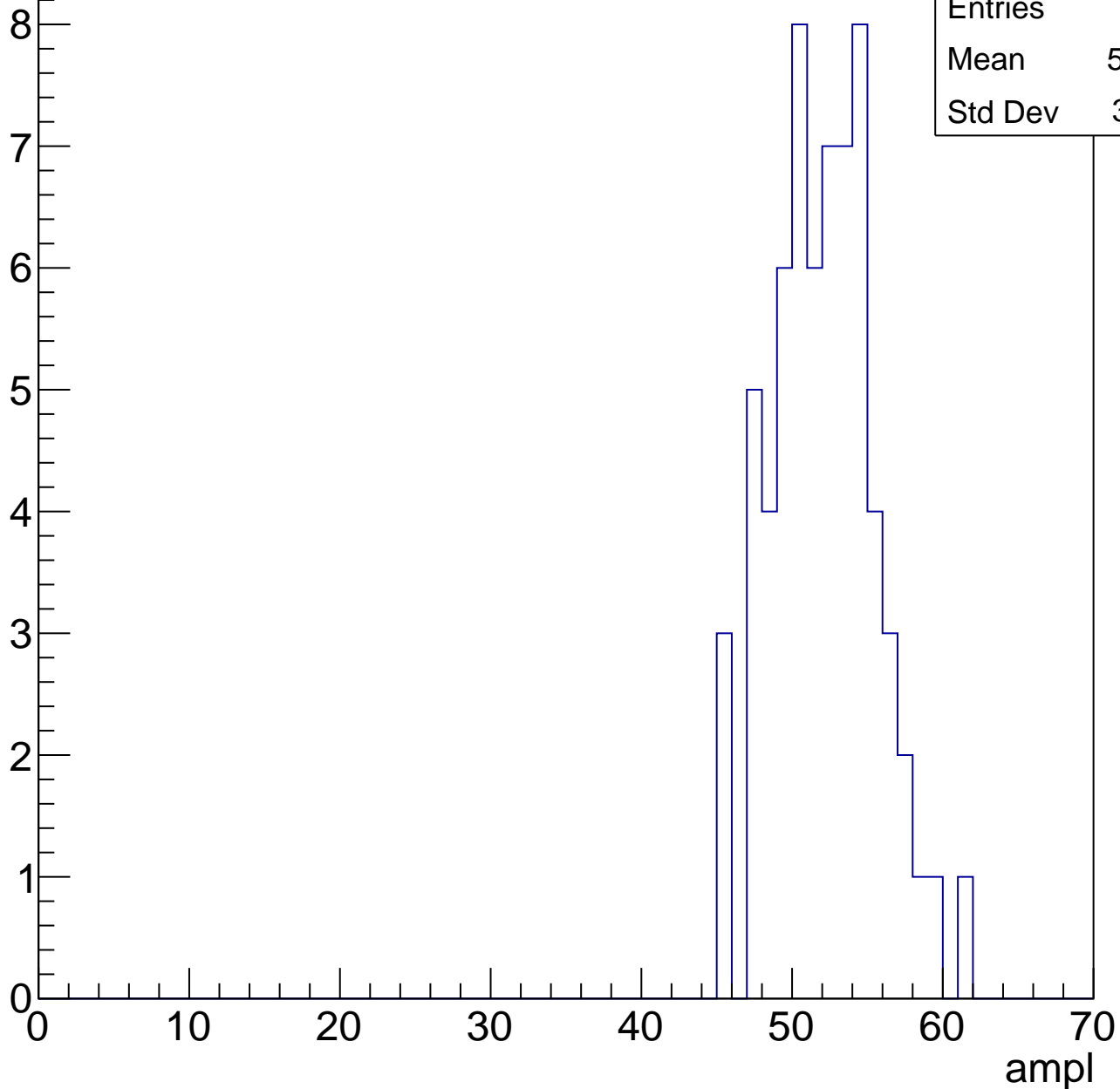


# B0L000S, U10-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

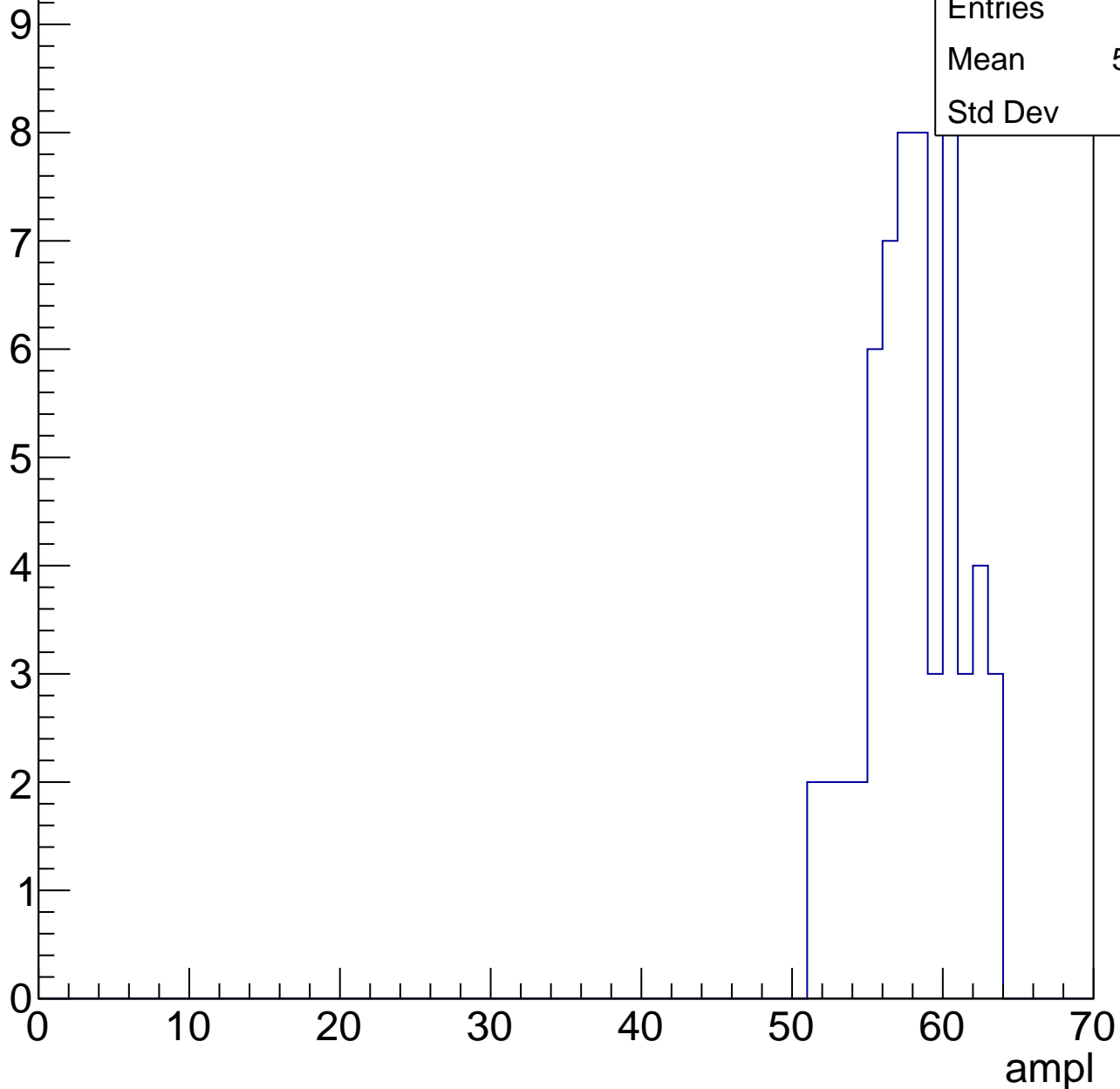
Entries	66
Mean	51.65
Std Dev	3.391



# B0L000S, U10-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



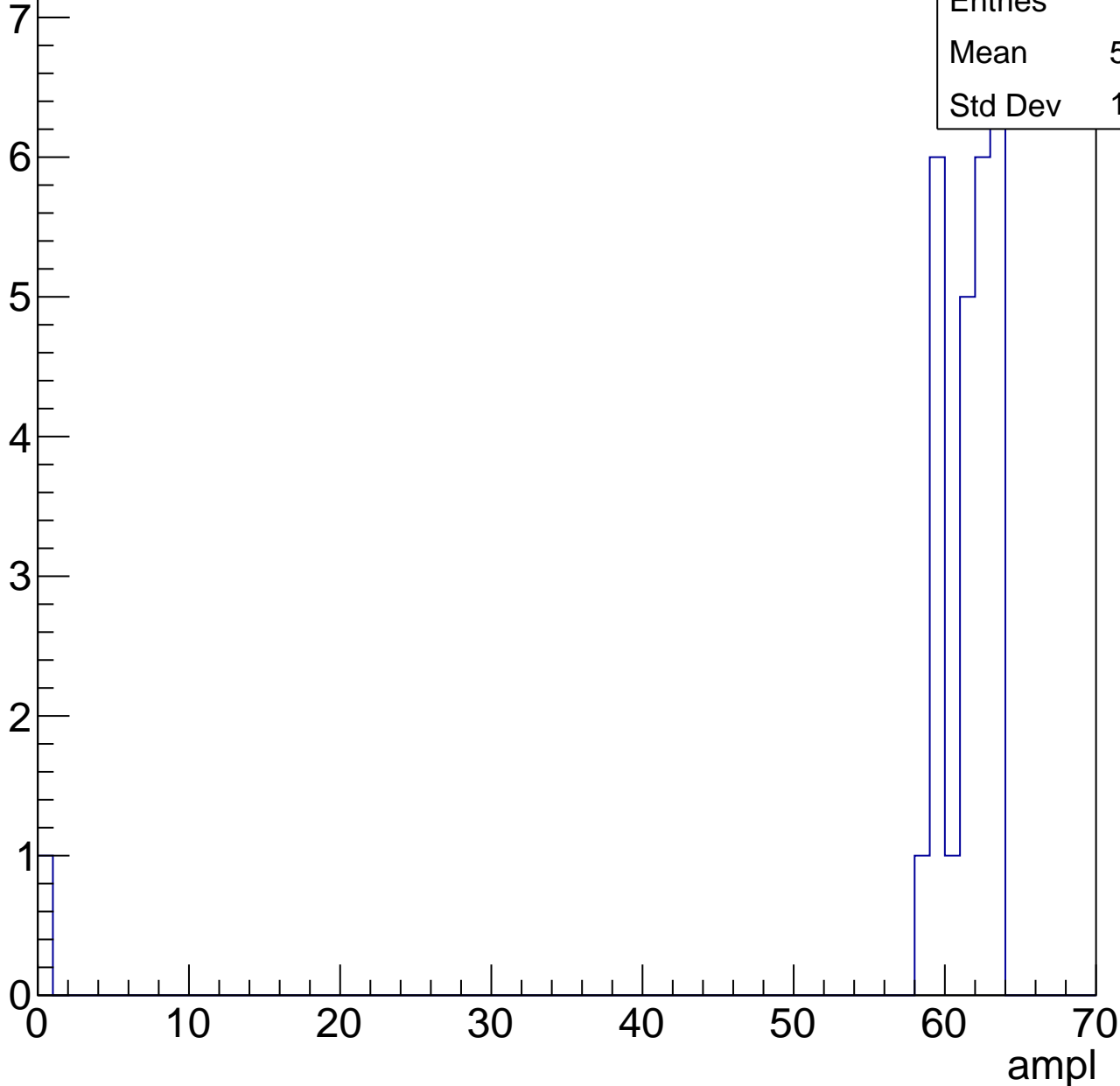
Entries	59
Mean	57.61
Std Dev	3.02

# B0L000S, U10-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	58.89
Std Dev	11.66



# B0L000S, U10-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch22, adc0

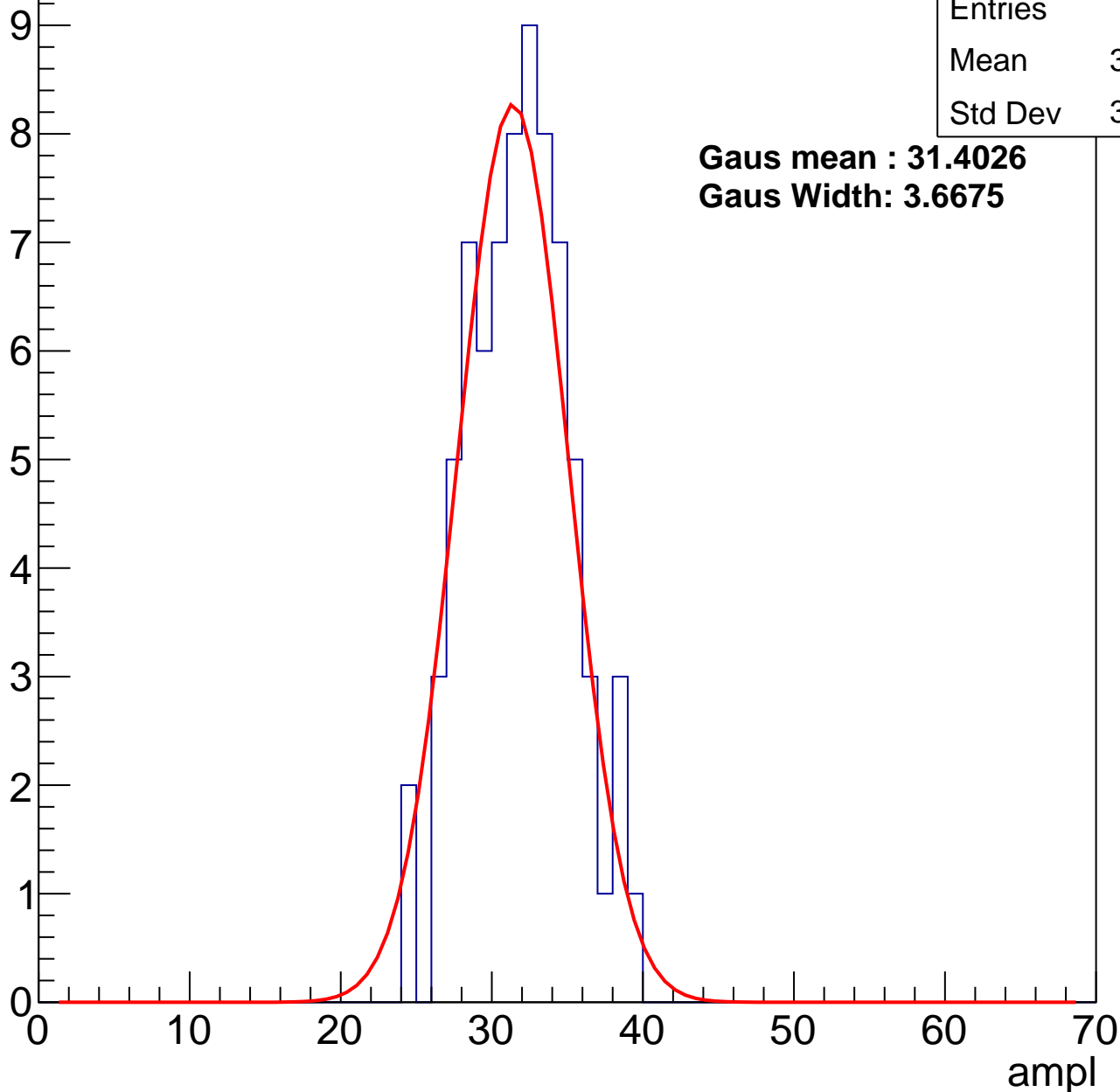
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	31.36
Std Dev	3.353

**Gaus mean : 31.4026**

**Gaus Width: 3.6675**



# B0L000S, U10-ch22, adc1

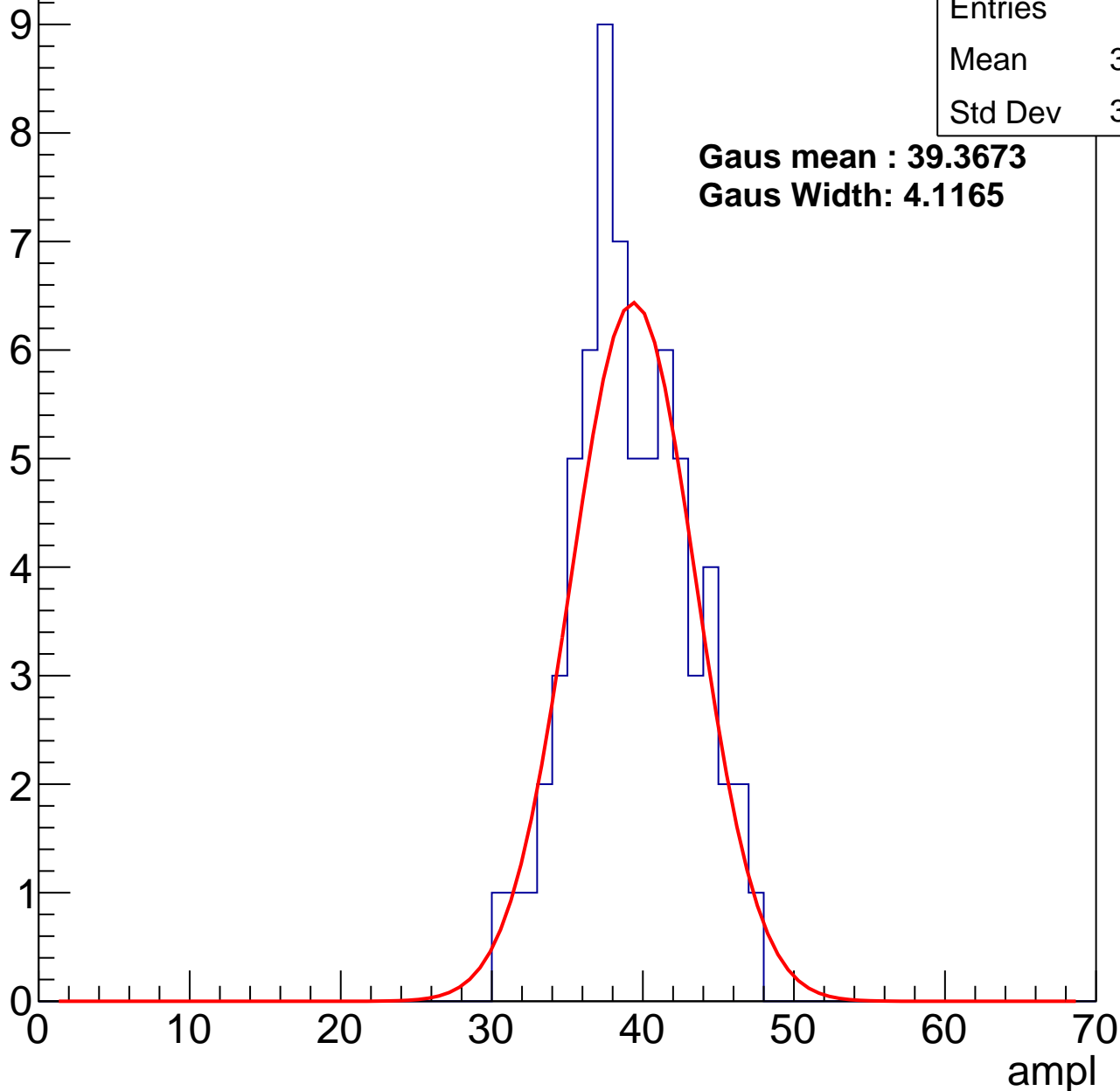
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	38.76
Std Dev	3.777

**Gaus mean : 39.3673**

**Gaus Width: 4.1165**



# B0L000S, U10-ch22, adc2

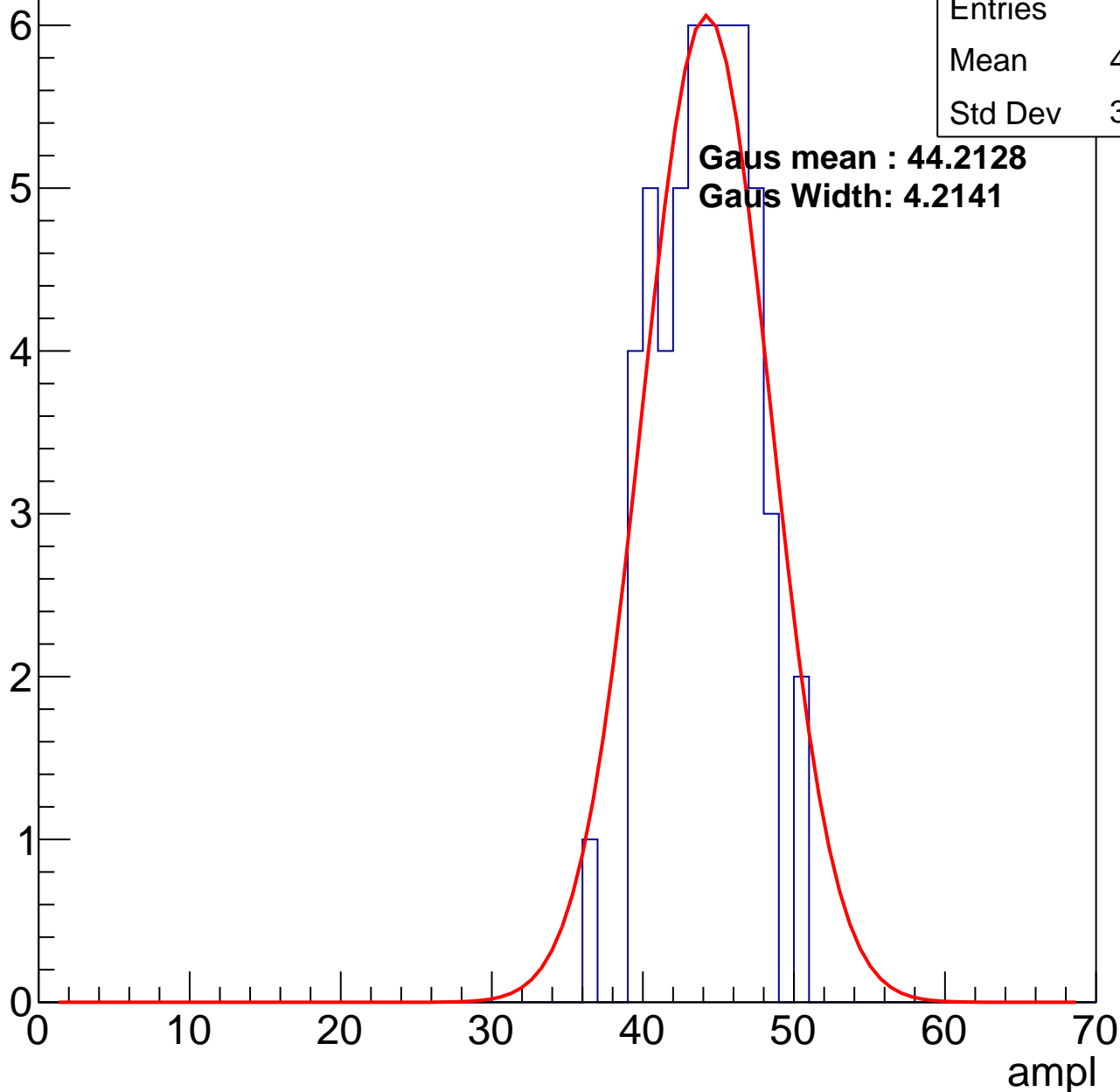
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.64
Std Dev	3.054

**Gaus mean : 44.2128**

**Gaus Width: 4.2141**

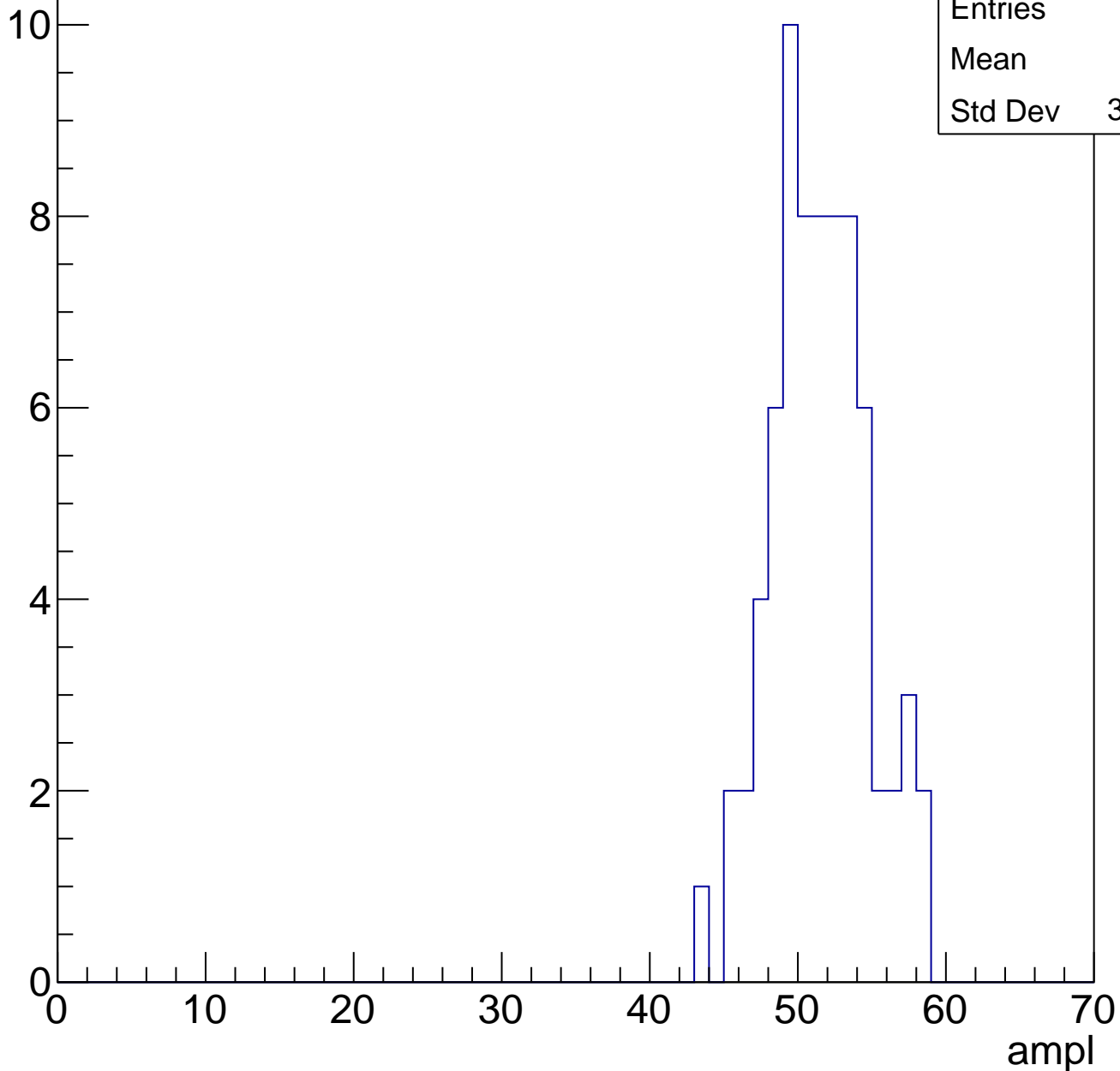


# B0L000S, U10-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	51
Std Dev	3.193

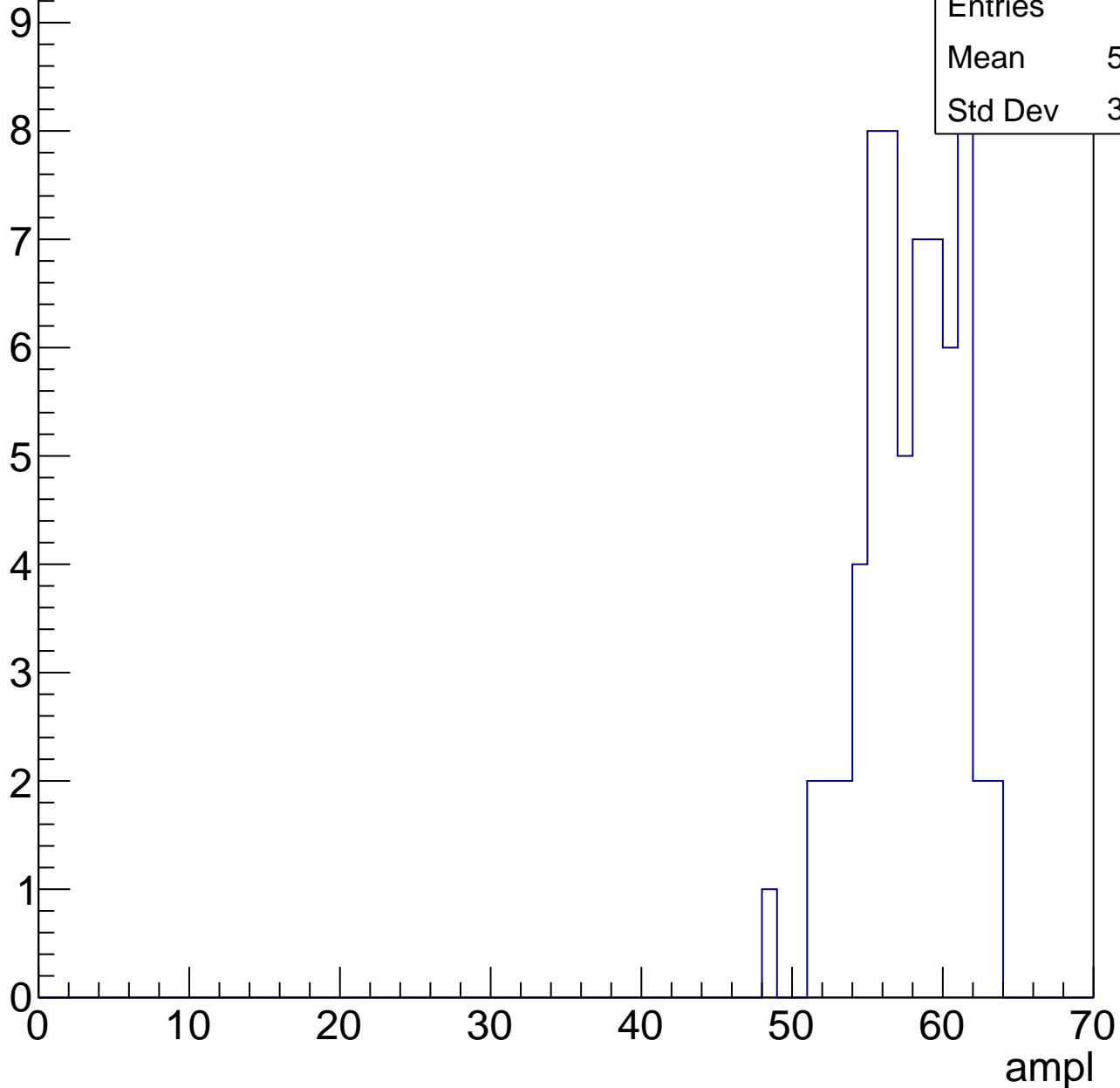
Entry



# B0L000S, U10-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



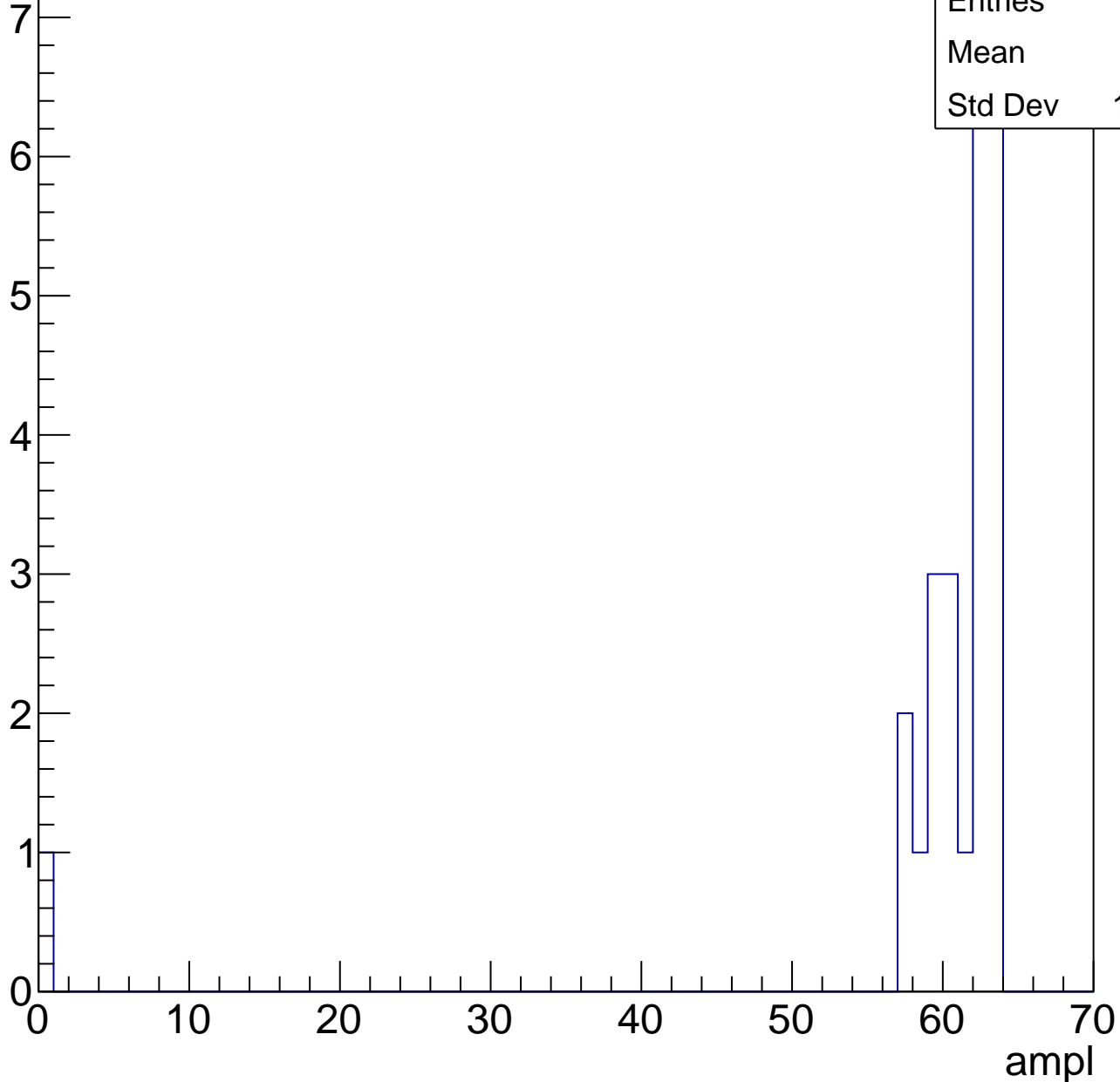
Entries	65
Mean	57.34
Std Dev	3.183

# B0L000S, U10-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

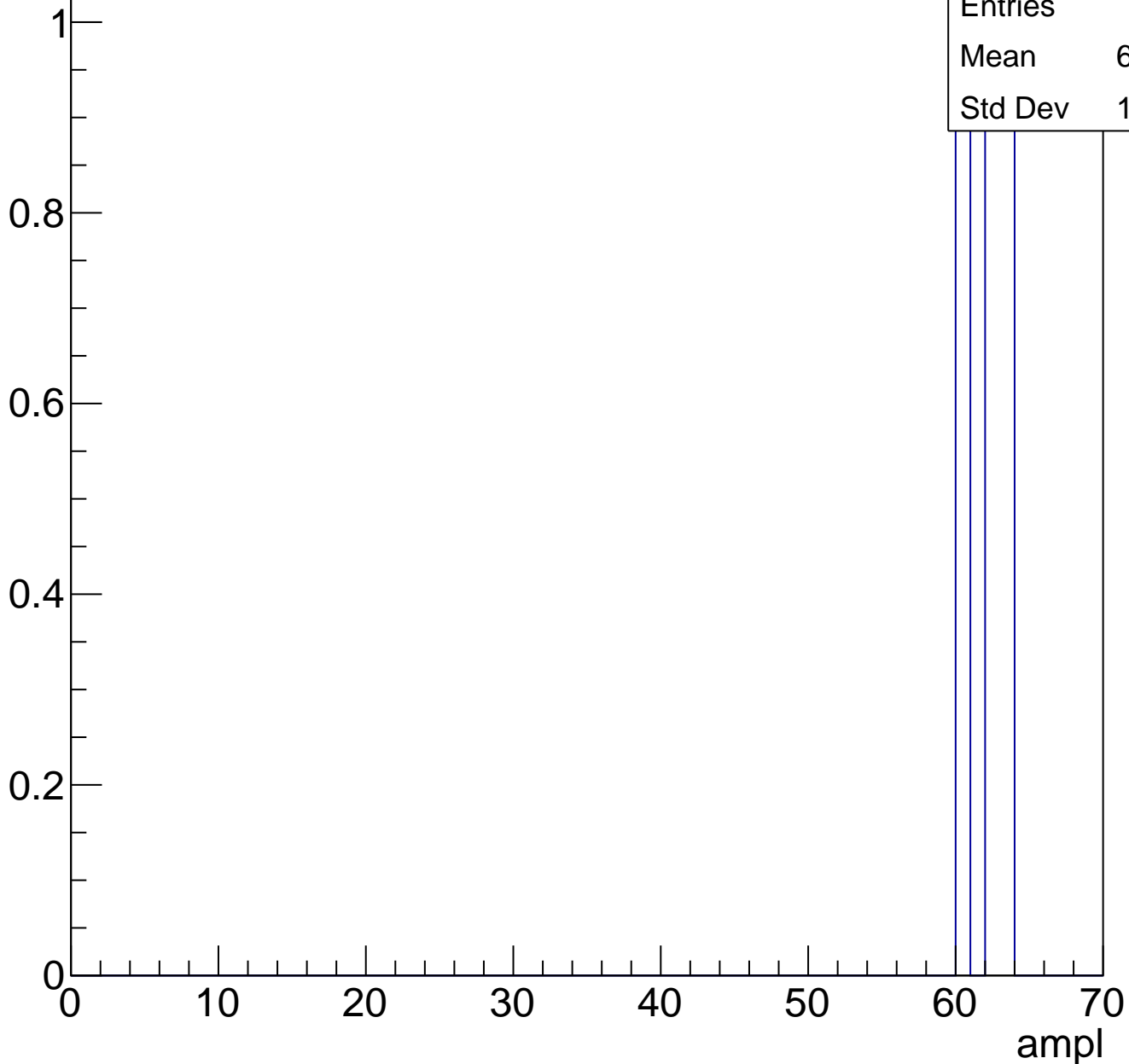
Entries	25
Mean	58.6
Std Dev	12.11



# B0L000S, U10-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	28.63
Std Dev	2.833

**Gaus mean : 28.9673**

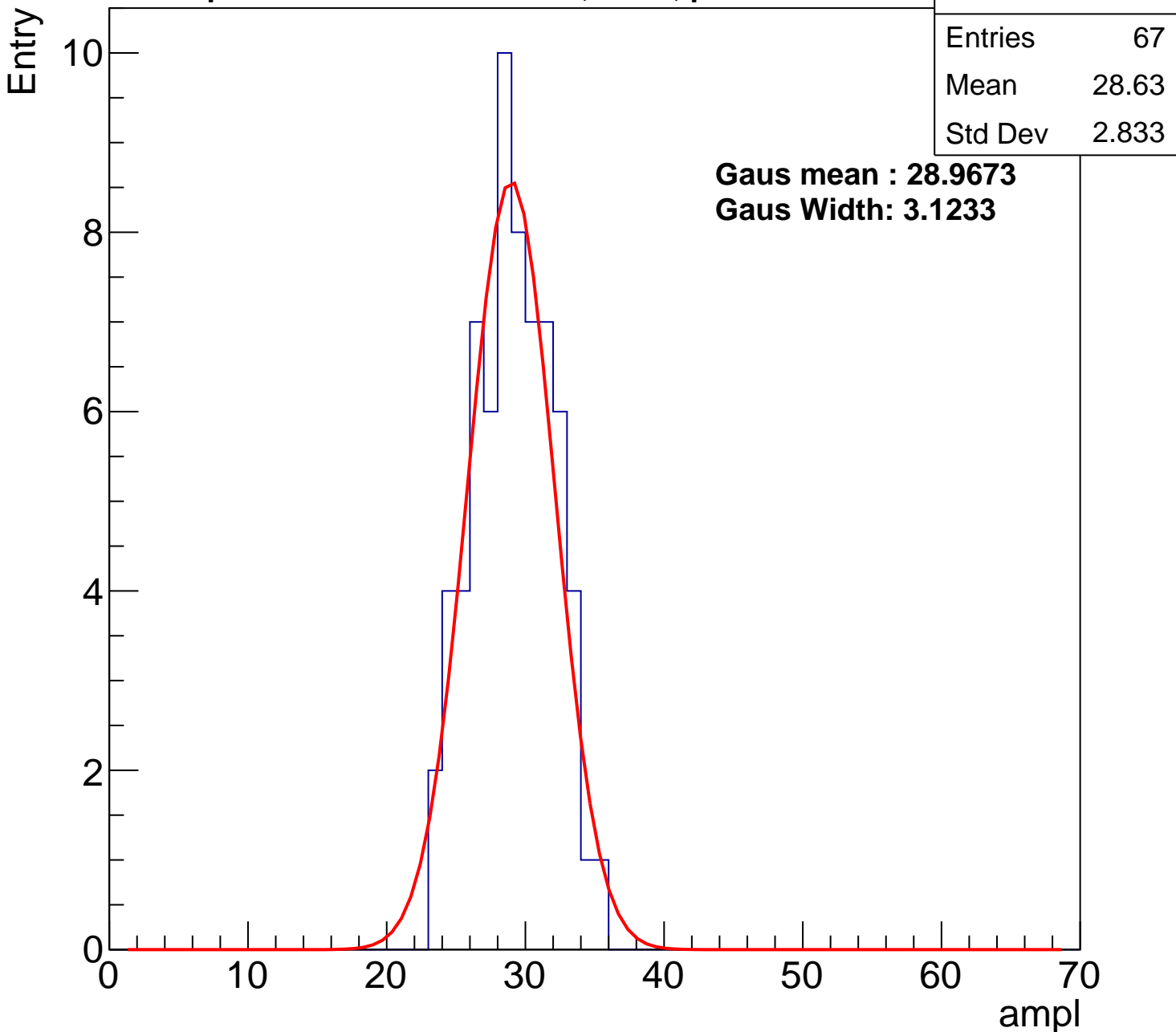
**Gaus Width: 3.1233**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

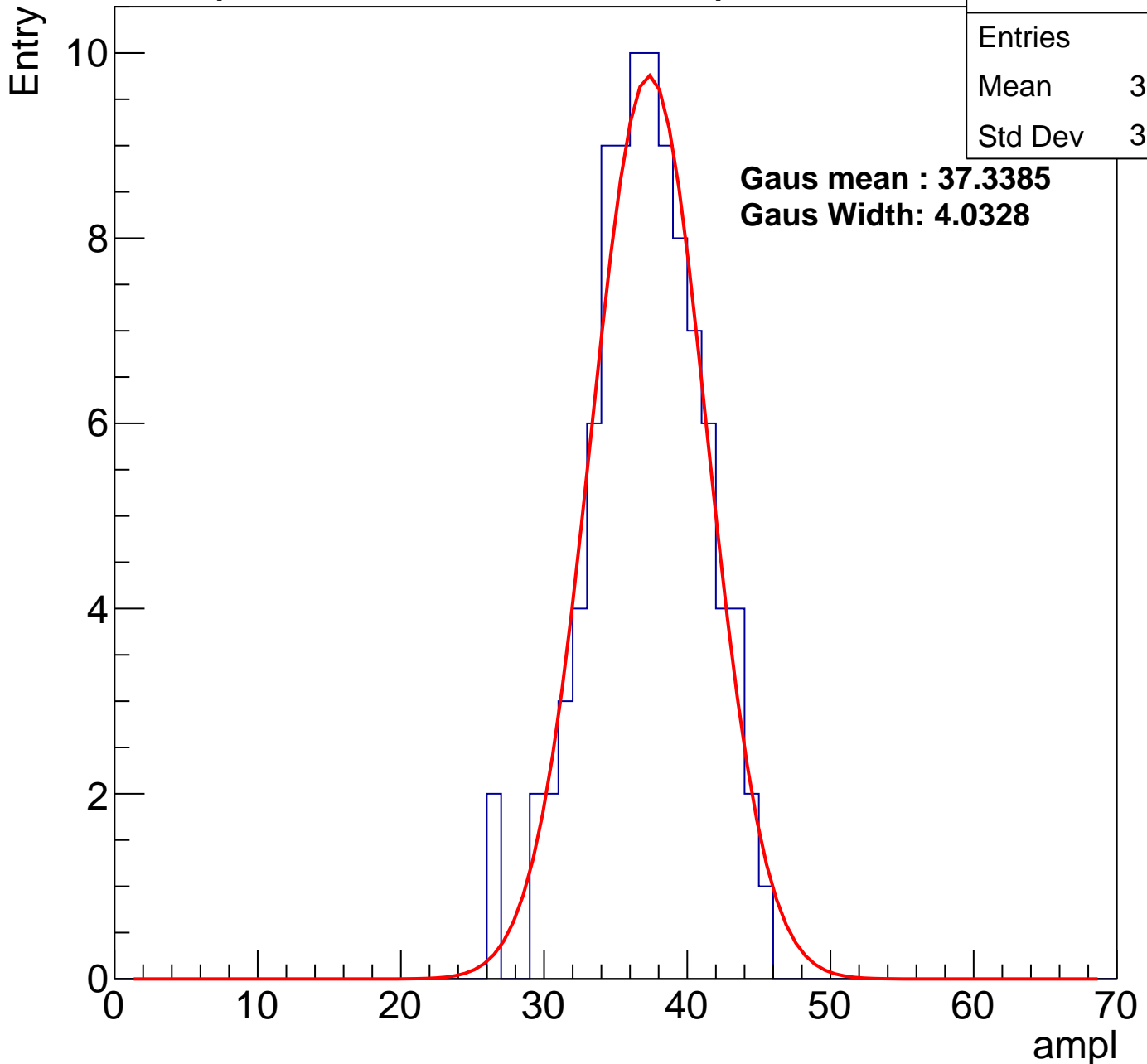


# B0L000S, U10-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	98
Mean	36.66
Std Dev	3.904

**Gaus mean : 37.3385**  
**Gaus Width: 4.0328**



# B0L000S, U10-ch23, adc2

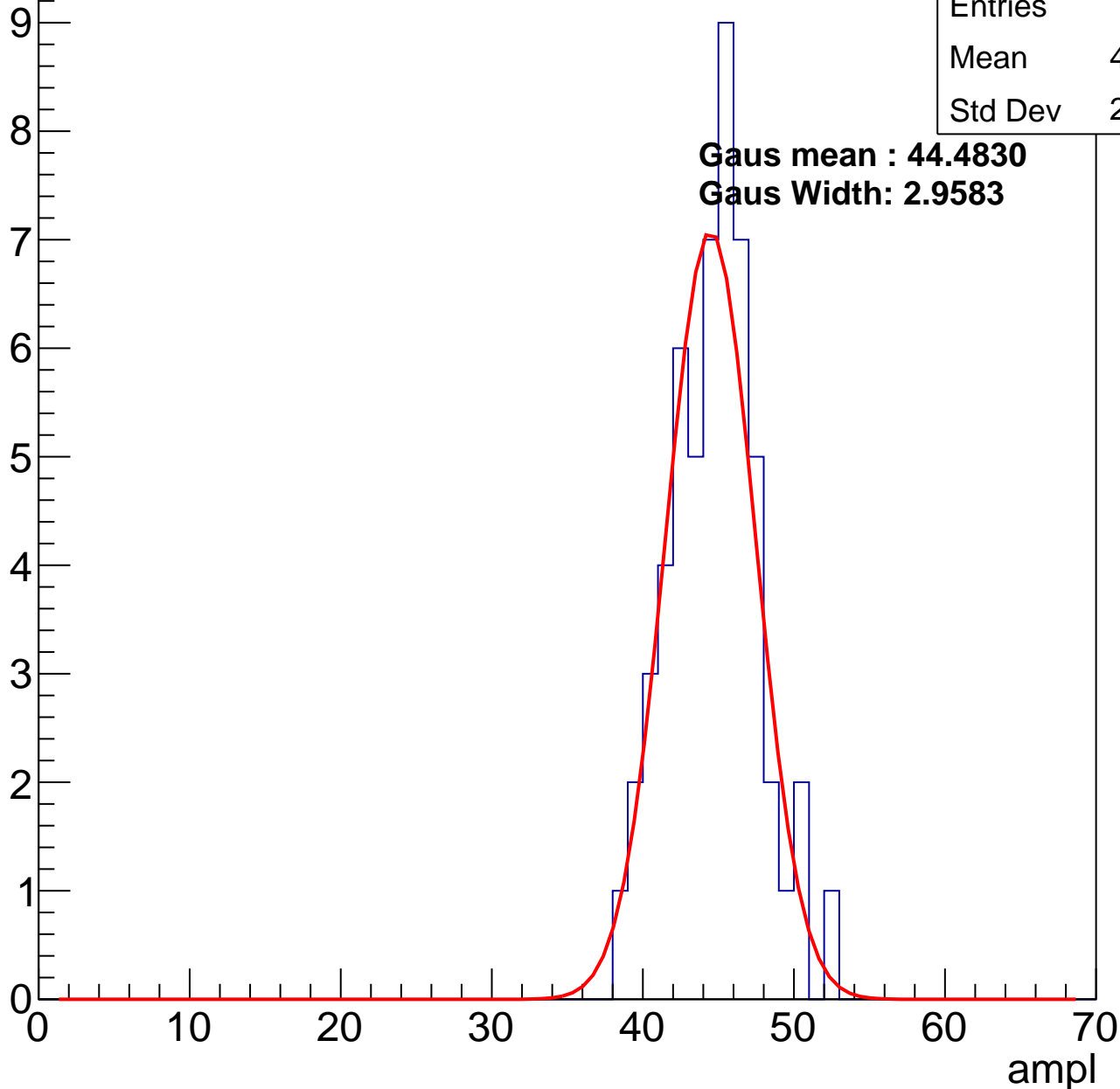
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	44.25
Std Dev	2.925

**Gaus mean : 44.4830**

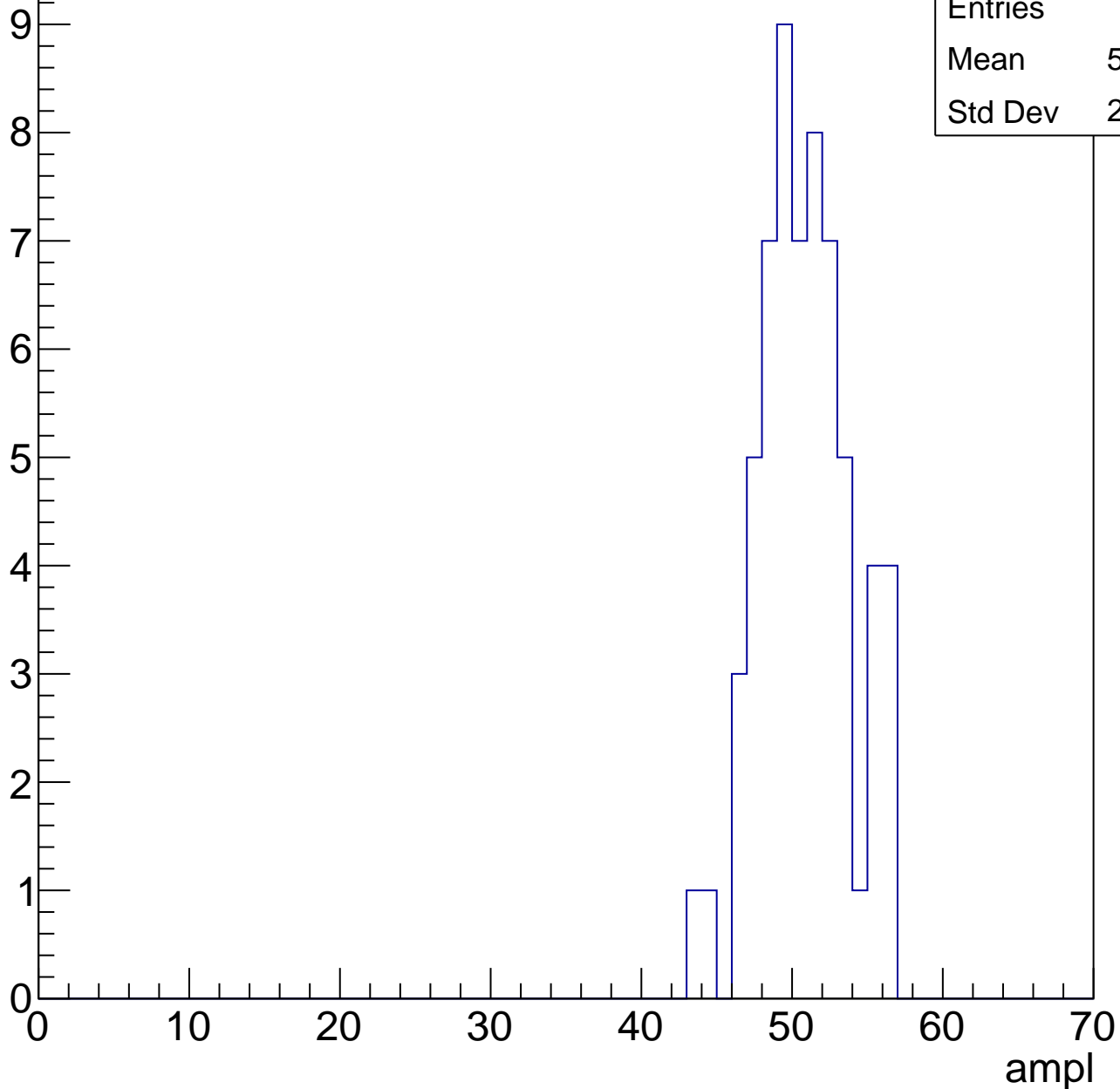
**Gaus Width: 2.9583**



# B0L000S, U10-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

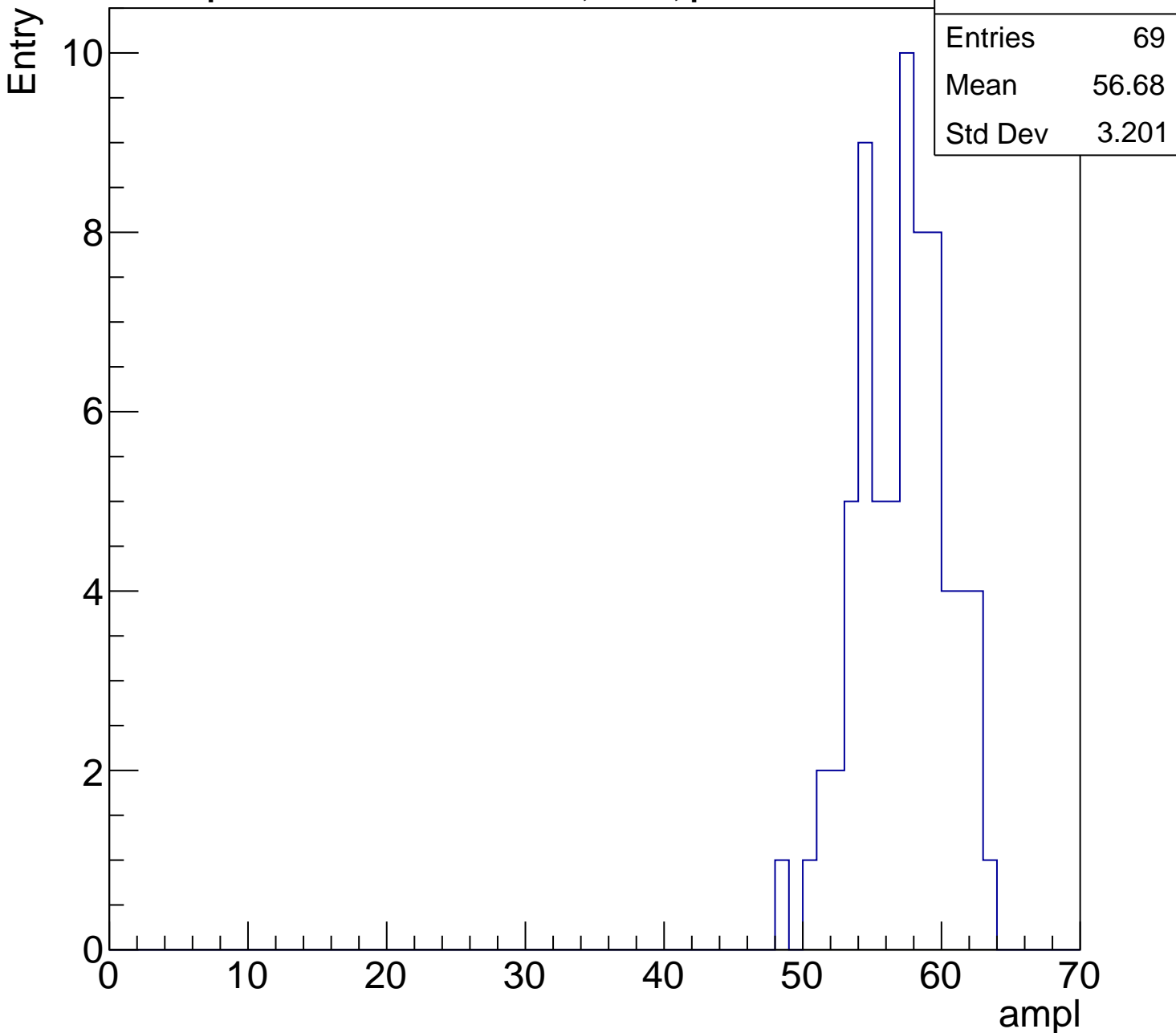
Entries	69
Mean	56.68
Std Dev	3.201

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

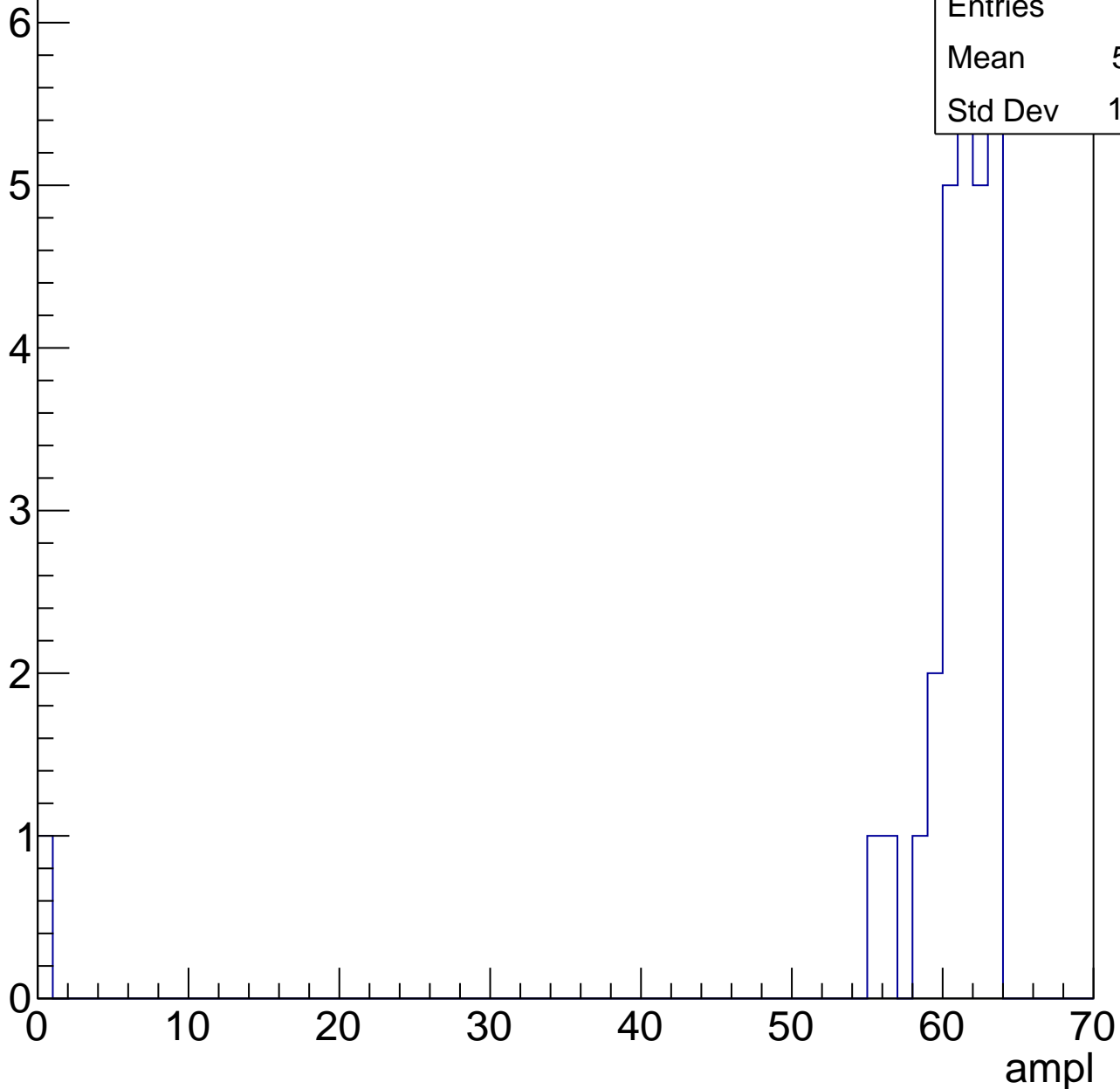


# B0L000S, U10-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

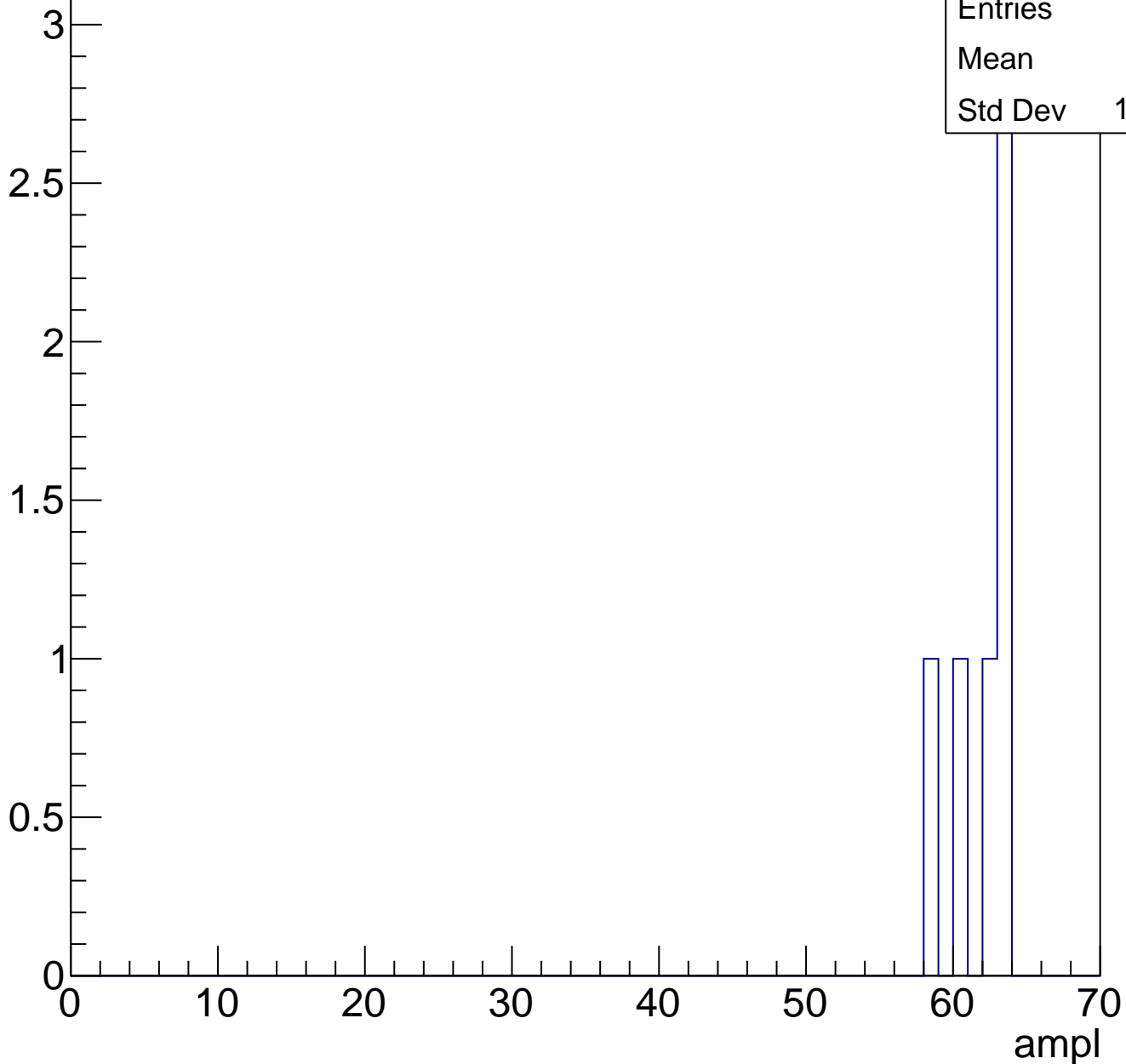
Entries	28
Mean	58.61
Std Dev	11.45



# B0L000S, U10-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	19
Std Dev	0

# B0L000S, U10-ch24, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	30.15
Std Dev	3.459

**Gaus mean : 30.5650**

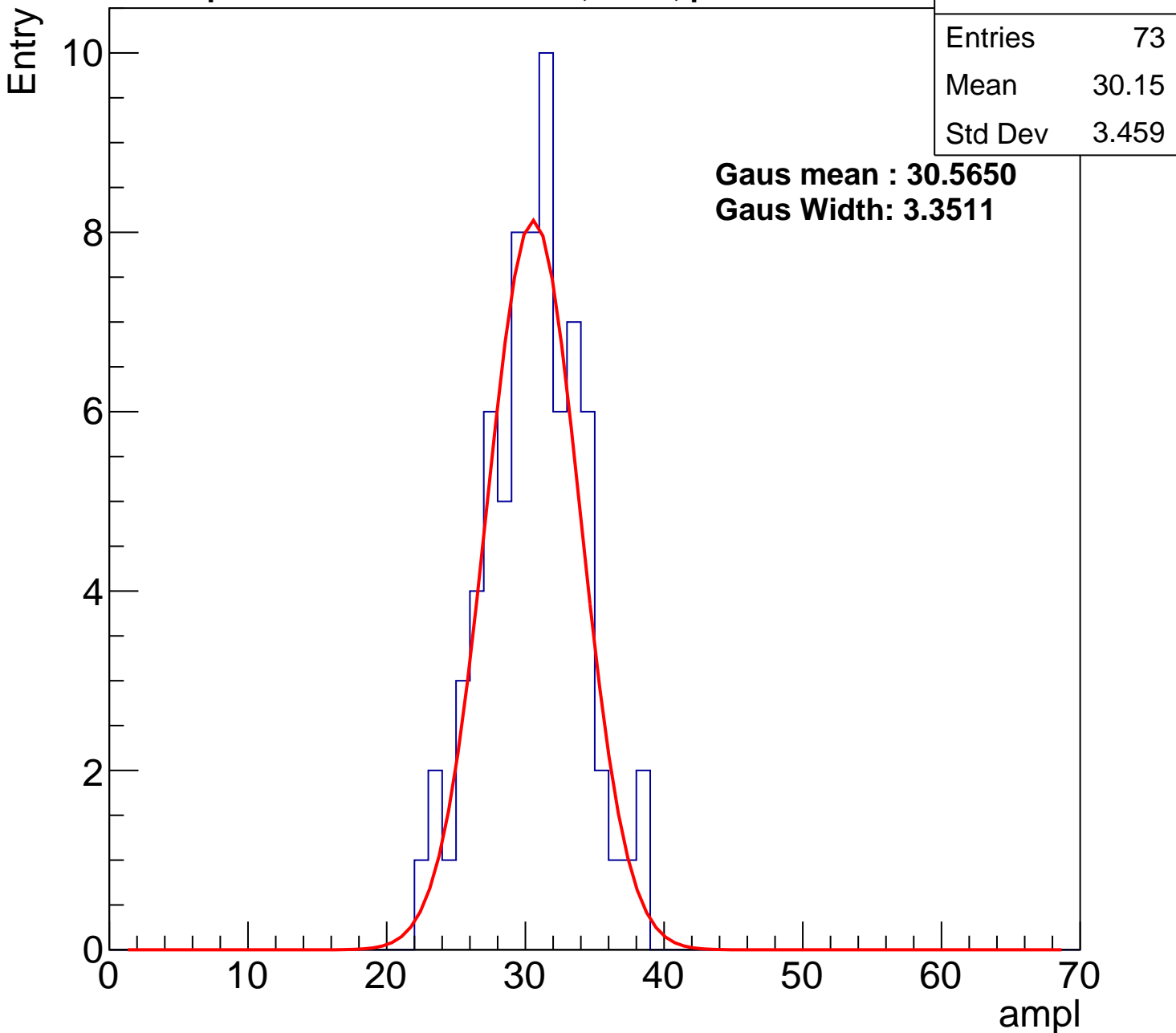
**Gaus Width: 3.3511**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch24, adc1

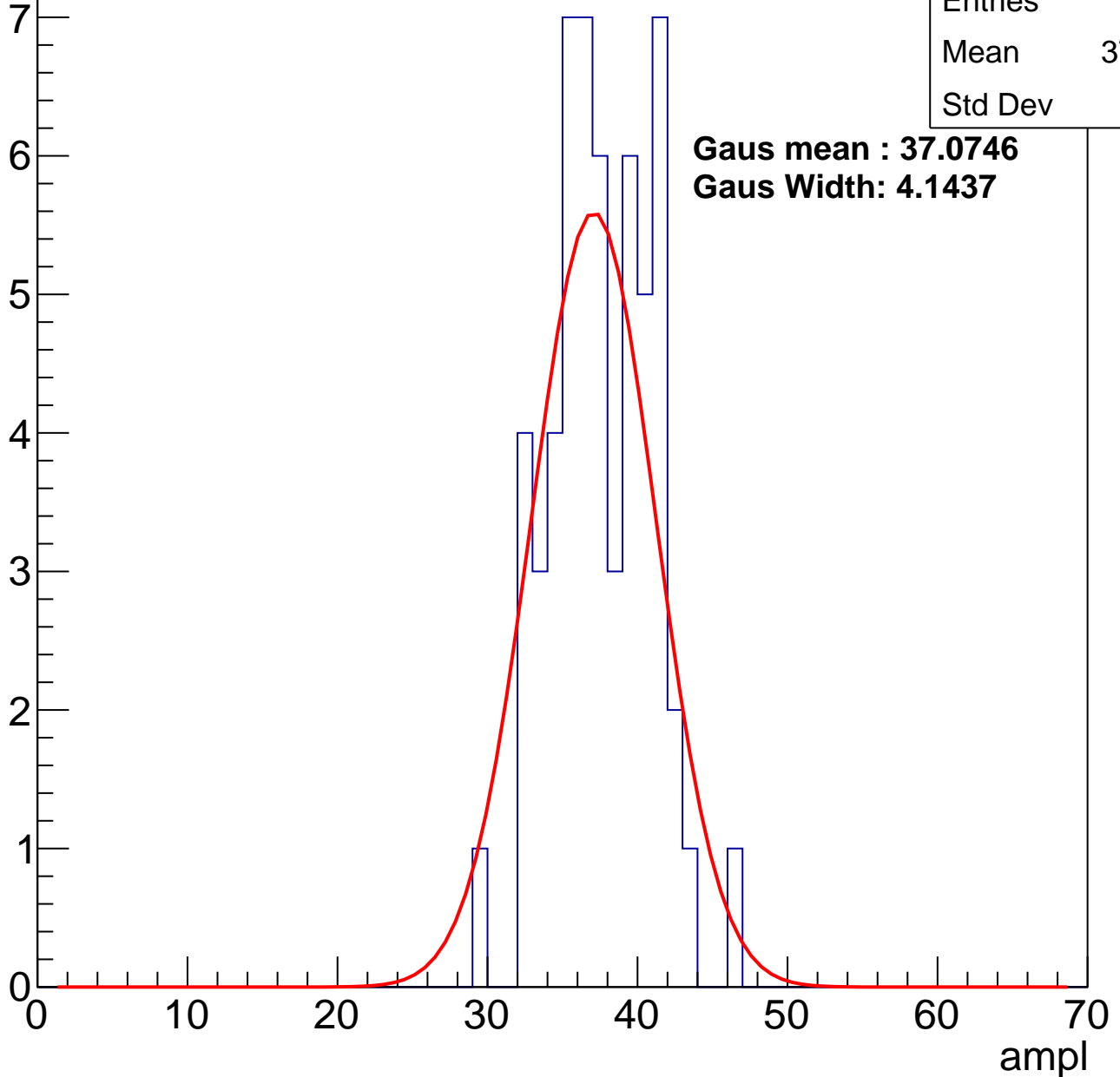
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	37.18
Std Dev	3.33

**Gaus mean : 37.0746**

**Gaus Width: 4.1437**



# B0L000S, U10-ch24, adc2

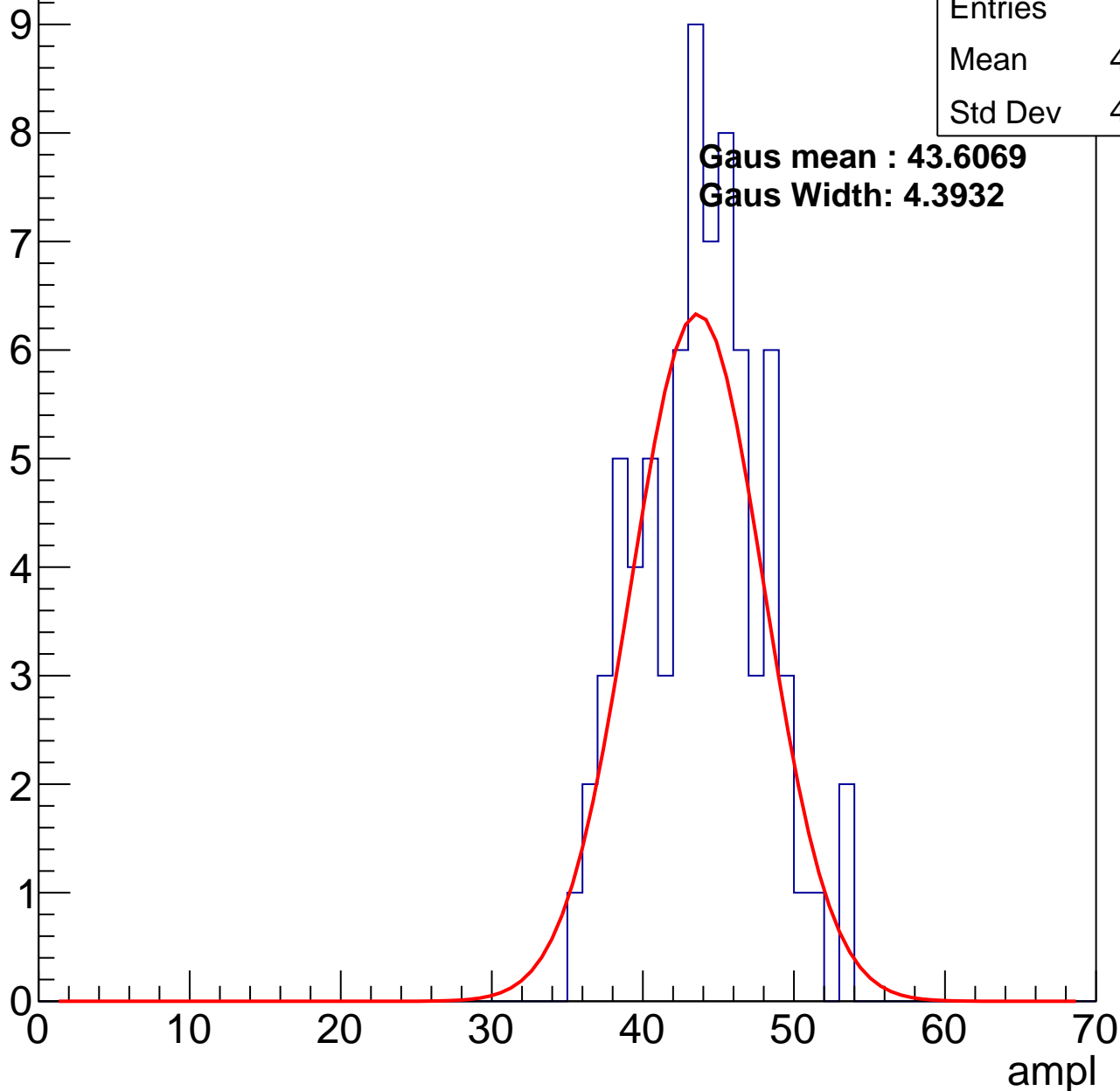
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	43.37
Std Dev	4.039

**Gaus mean : 43.6069**

**Gaus Width: 4.3932**

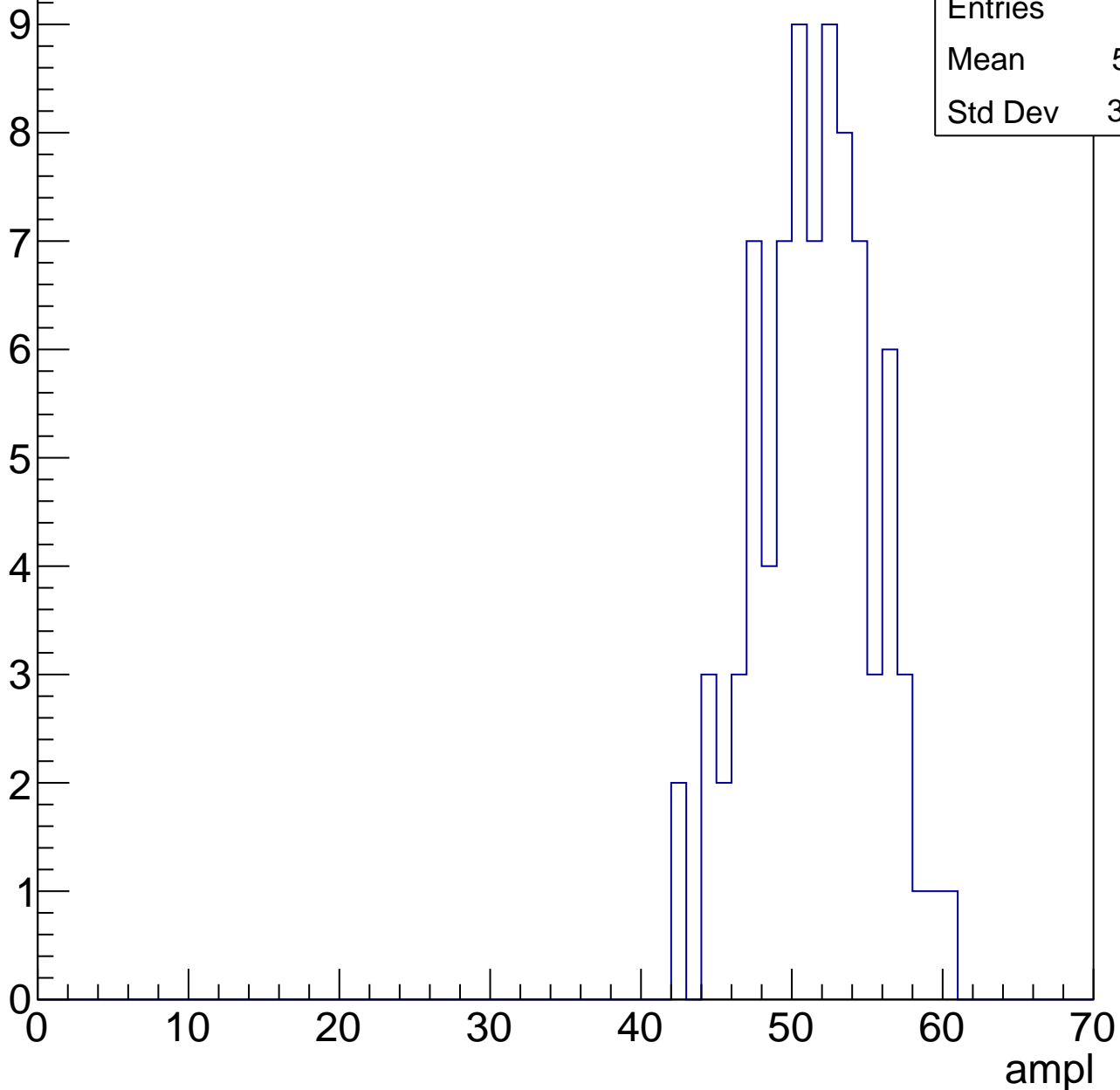


# B0L000S, U10-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	51.01
Std Dev	3.873

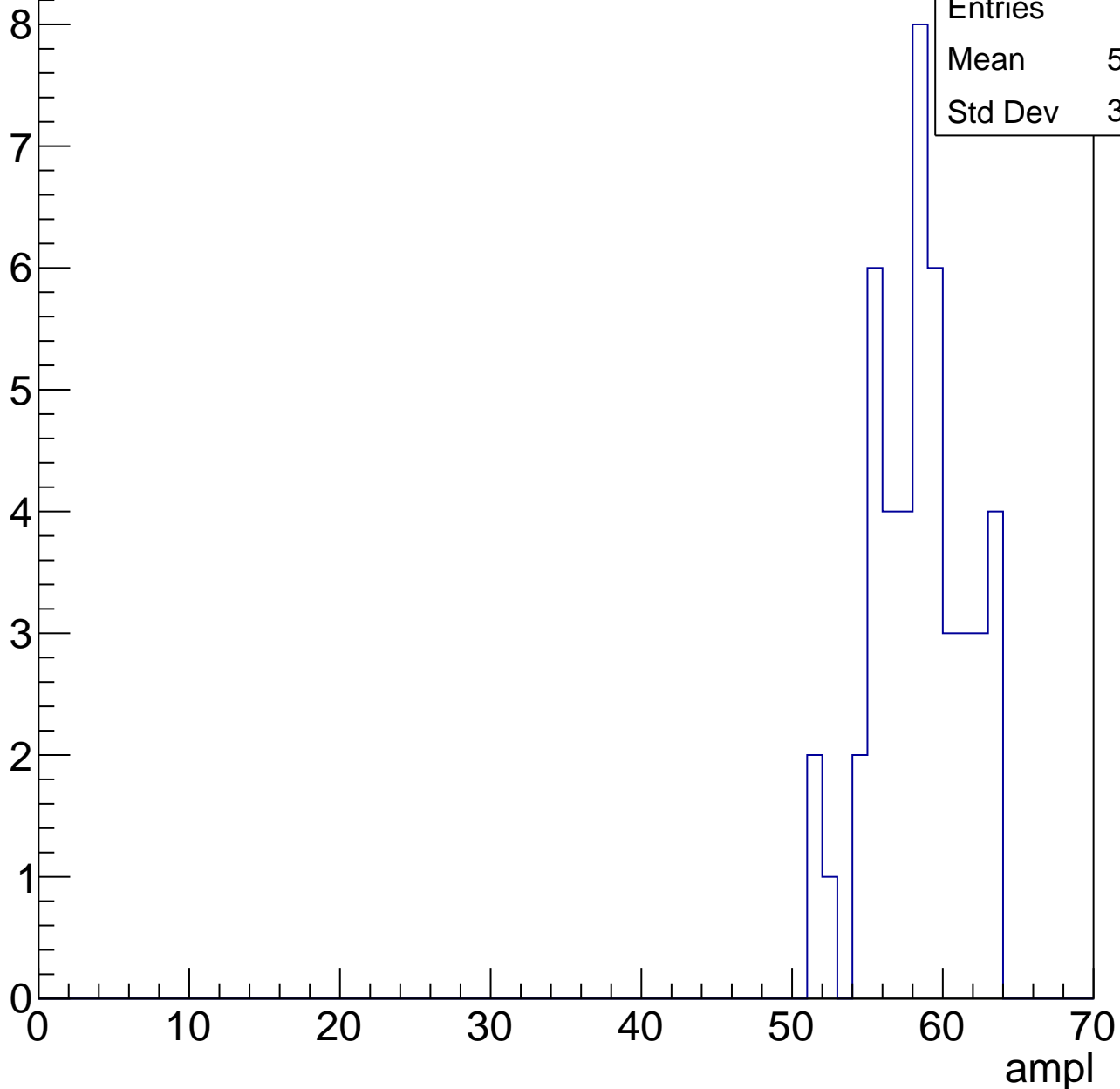


# B0L000S, U10-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	57.89
Std Dev	3.066

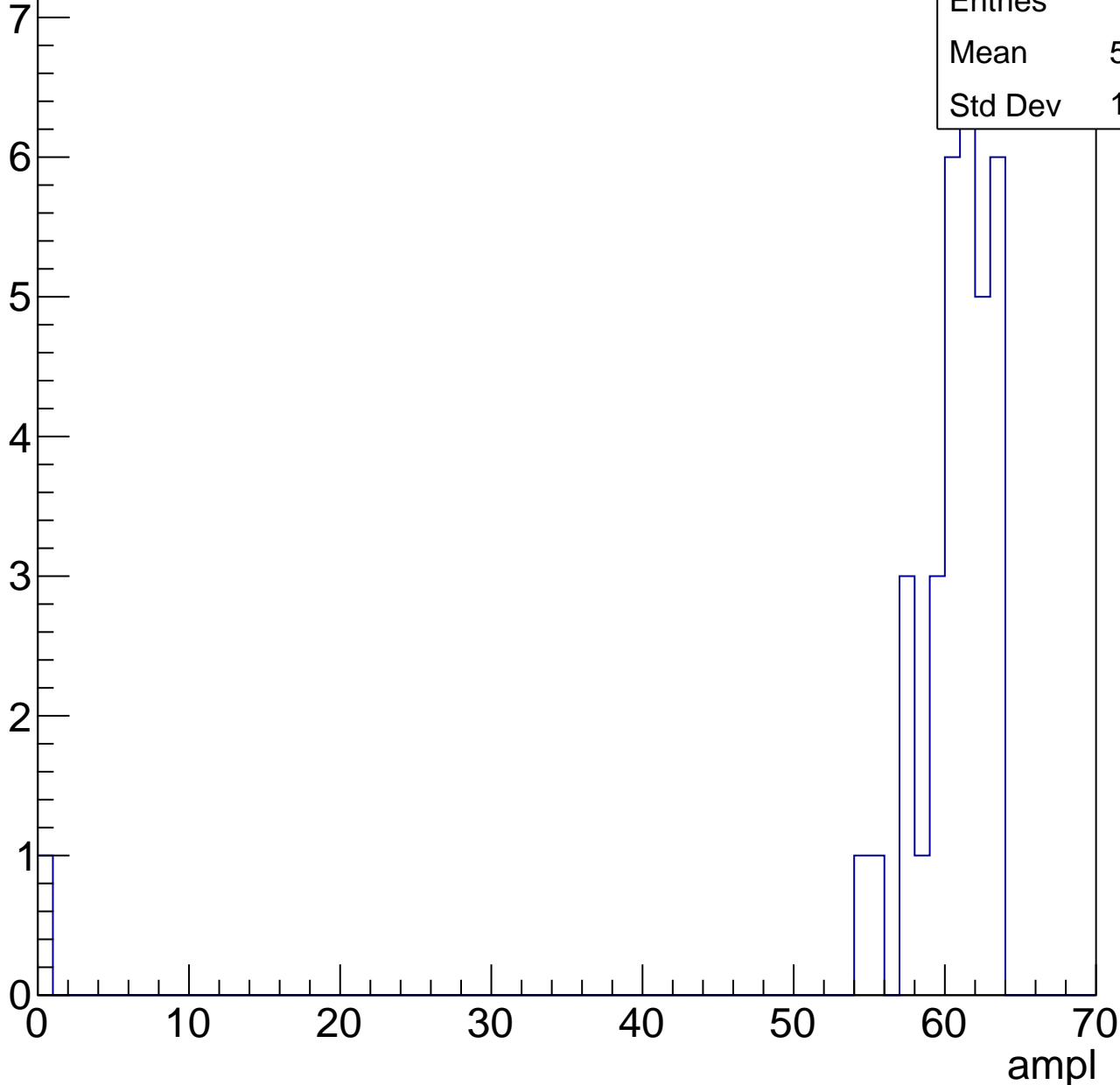


# B0L000S, U10-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.53
Std Dev	10.44



# B0L000S, U10-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	31.21
Std Dev	3.166

**Gaus mean : 31.6648**

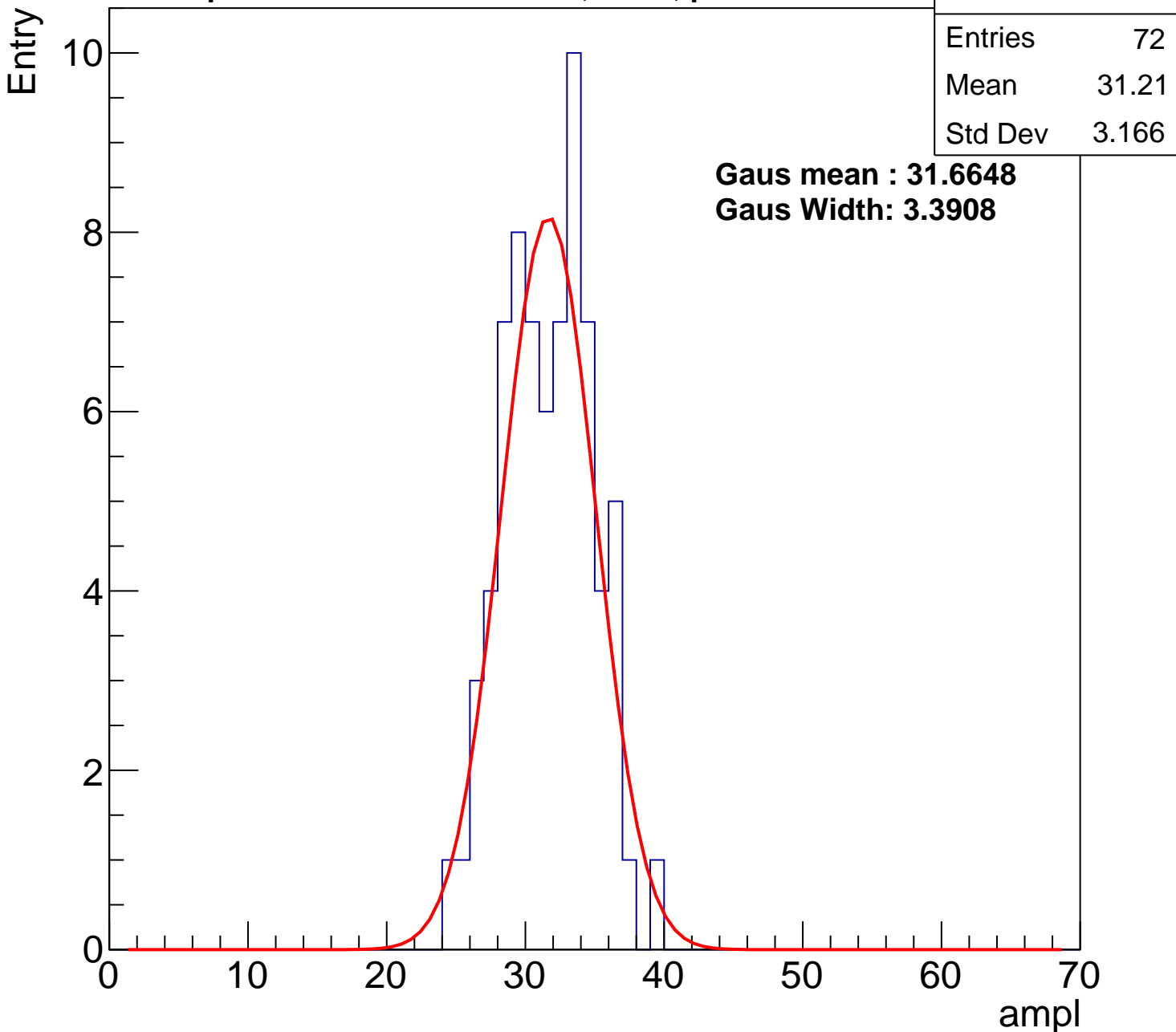
**Gaus Width: 3.3908**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch25, adc1

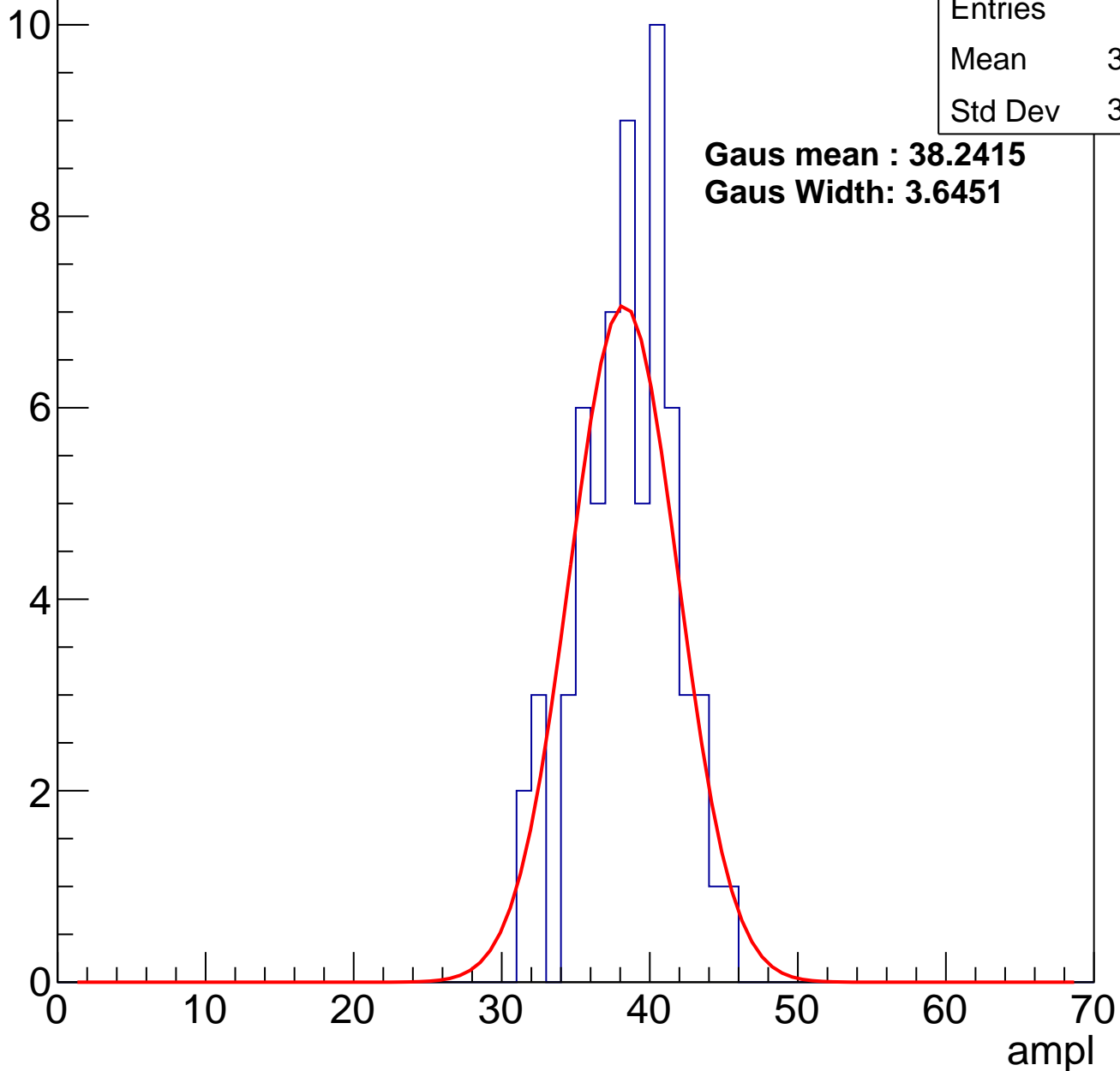
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	38.06
Std Dev	3.167

**Gaus mean : 38.2415**

**Gaus Width: 3.6451**

Entry



# B0L000S, U10-ch25, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	86
Mean	45.51
Std Dev	3.678

**Gaus mean : 46.0592**

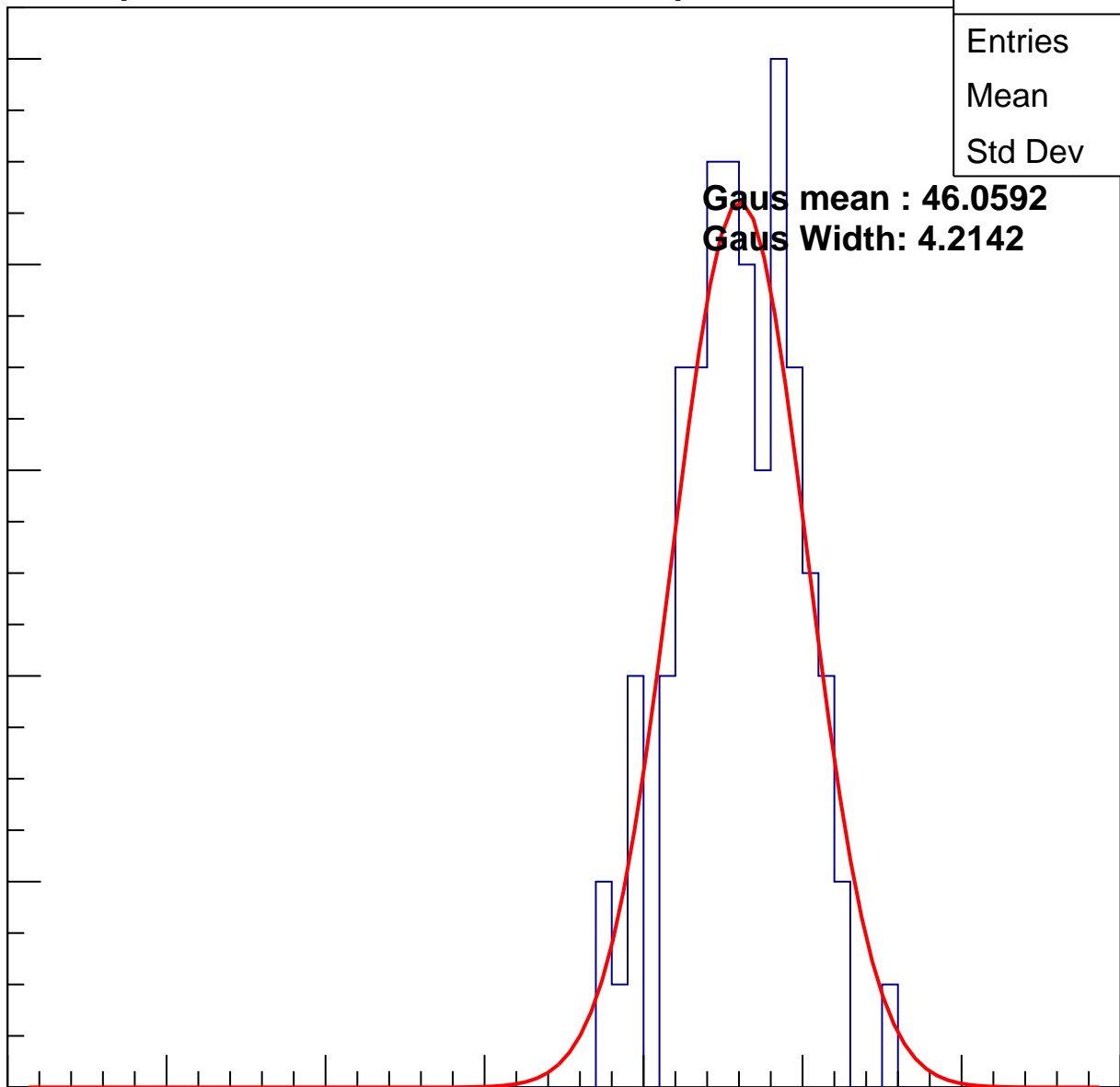
**Gaus Width: 4.2142**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

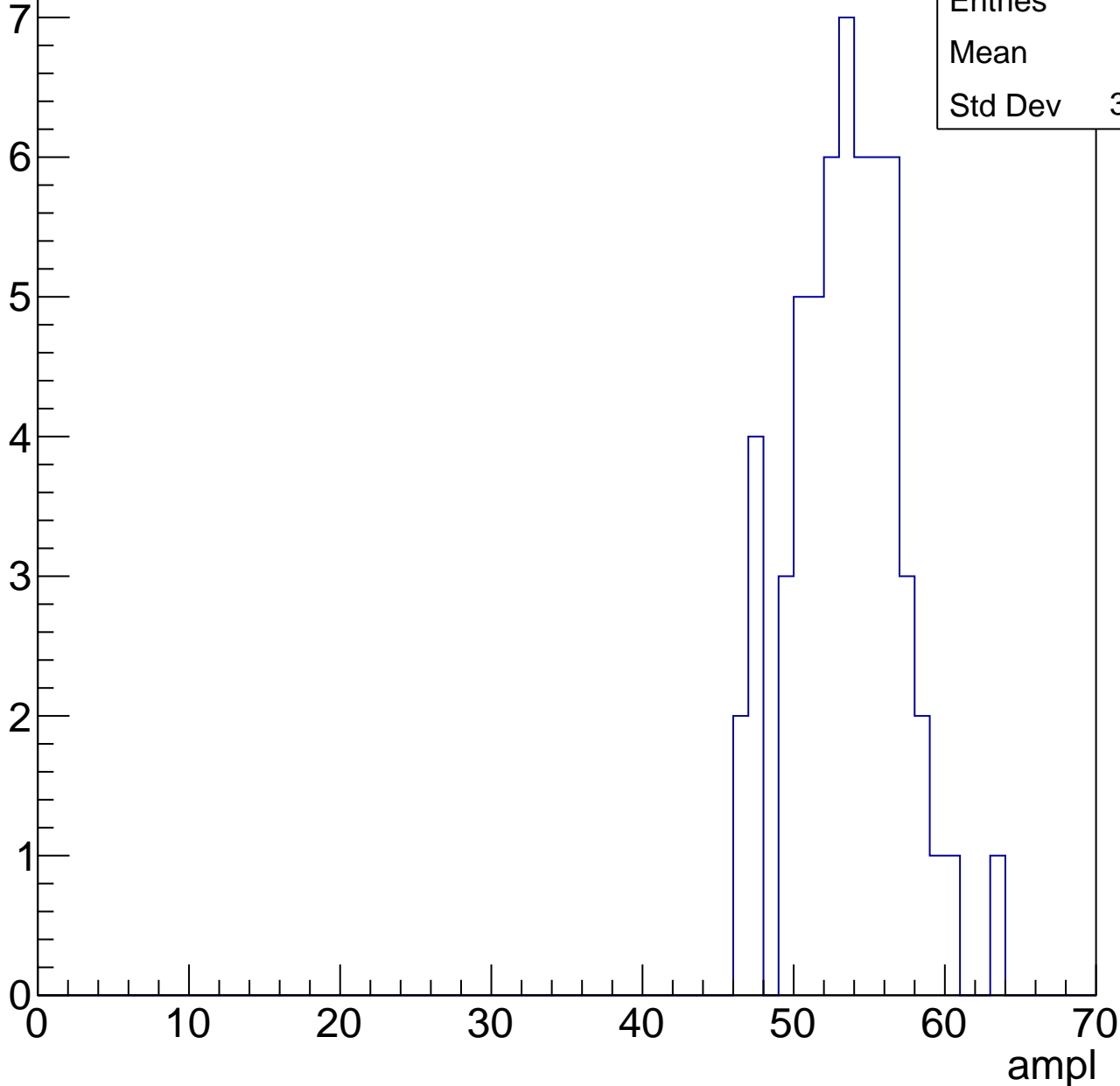


# B0L000S, U10-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	53
Std Dev	3.543

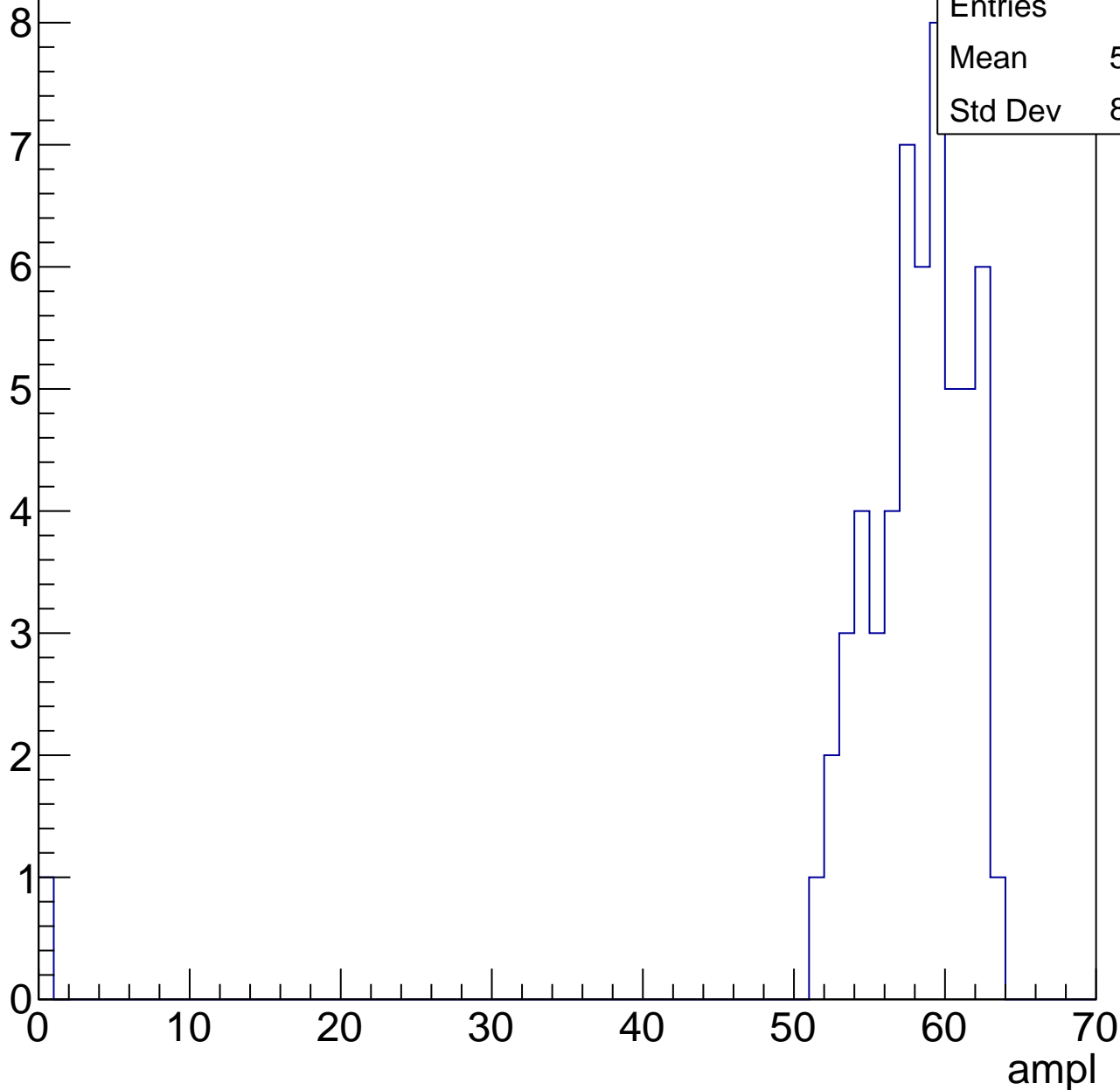


# B0L000S, U10-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

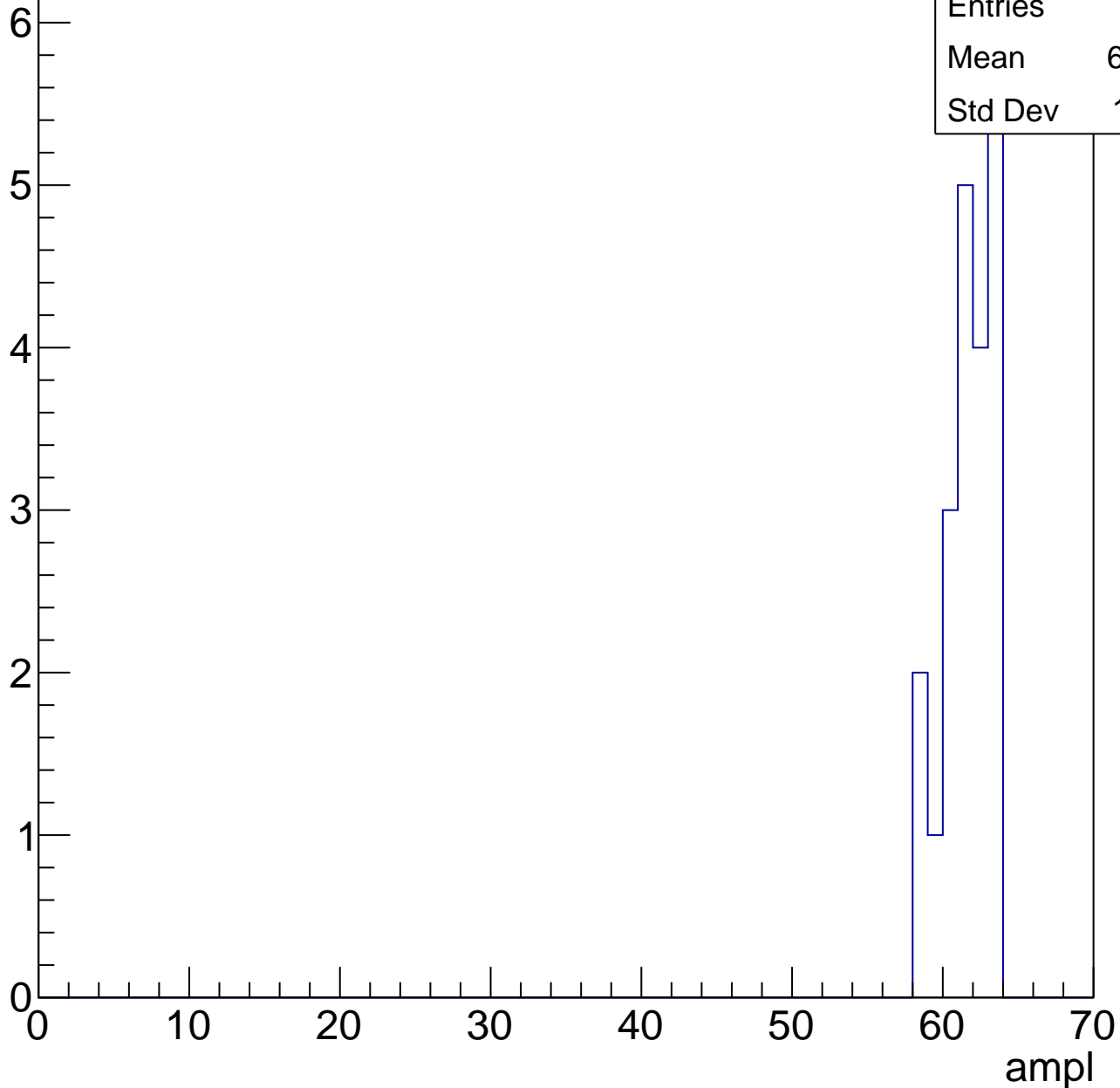
Entries	56
Mean	56.75
Std Dev	8.216



# B0L000S, U10-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch26, adc0

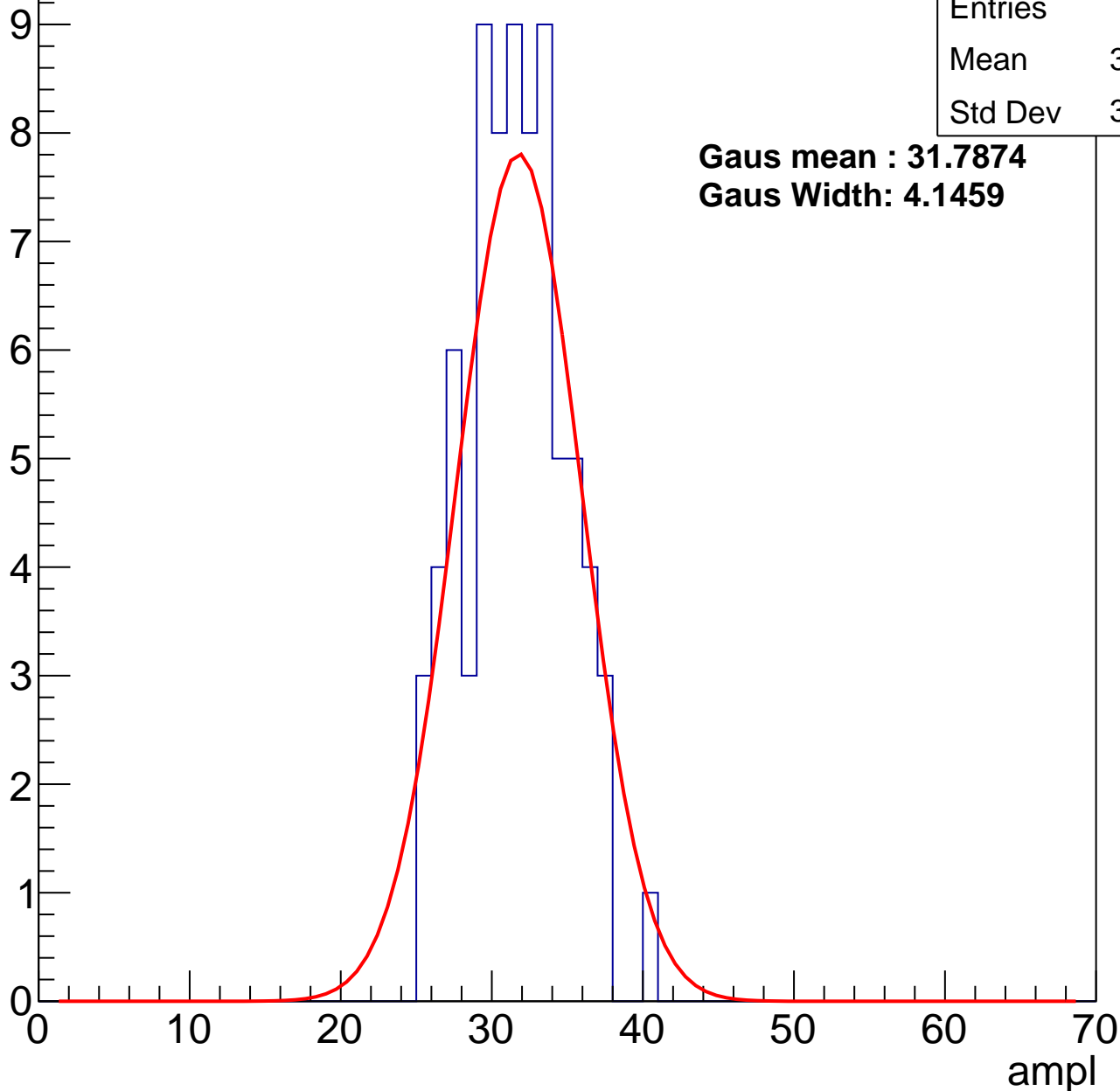
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	31.14
Std Dev	3.286

**Gaus mean : 31.7874**

**Gaus Width: 4.1459**

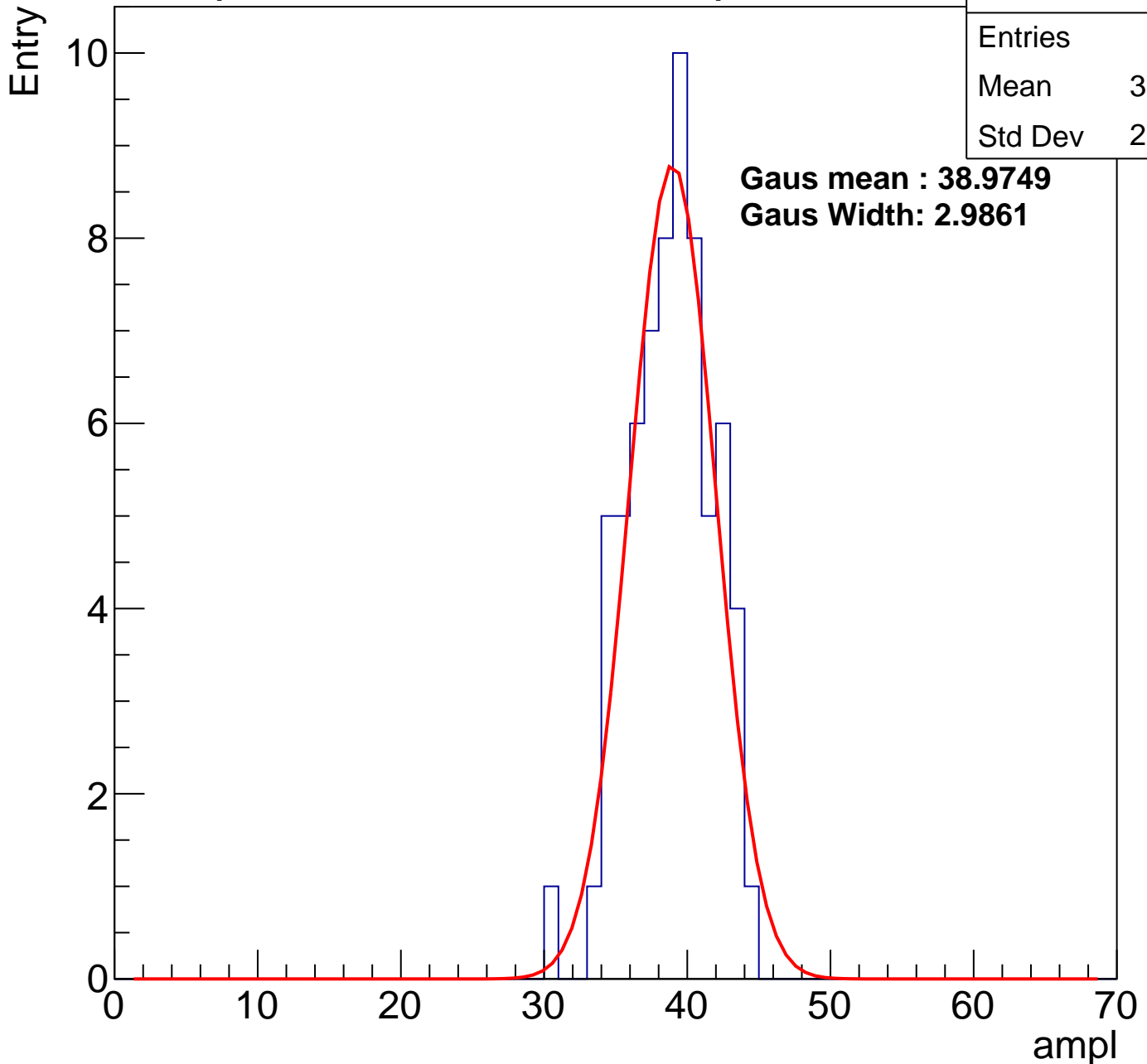


# B0L000S, U10-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	38.36
Std Dev	2.879

**Gaus mean : 38.9749**  
**Gaus Width: 2.9861**



# B0L000S, U10-ch26, adc2

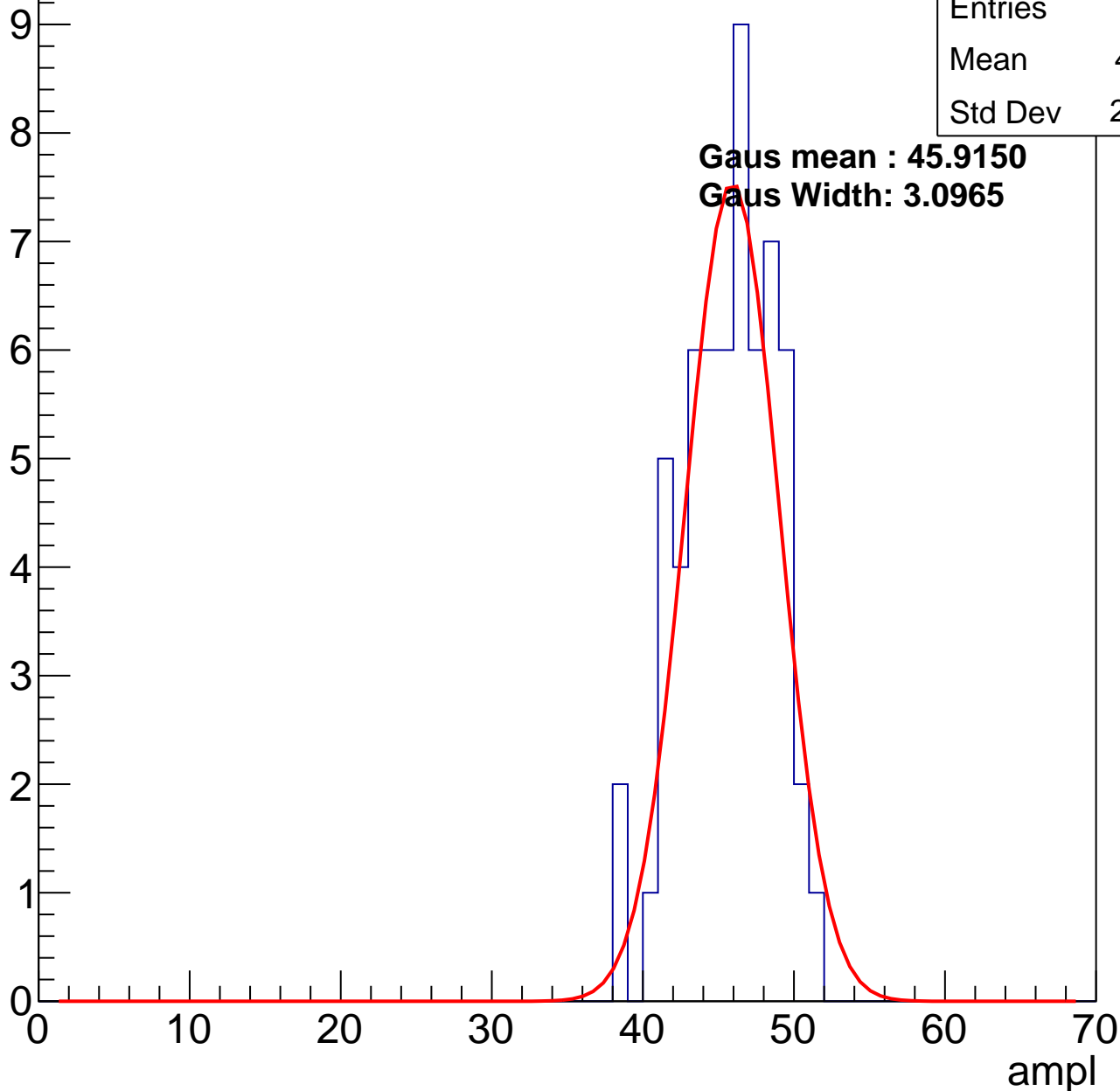
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	45.21
Std Dev	2.987

**Gaus mean : 45.9150**

**Gaus Width: 3.0965**

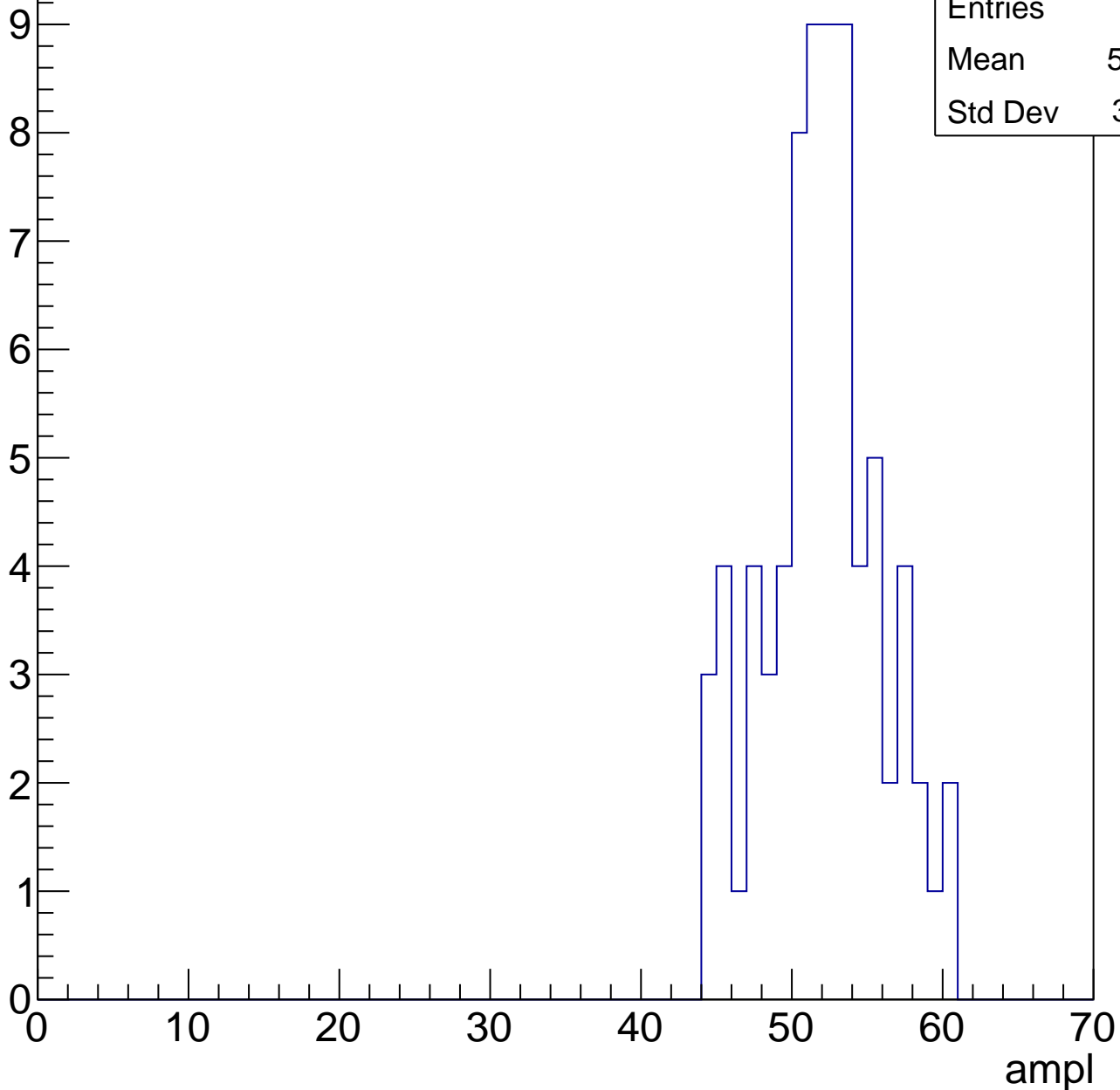


# B0L000S, U10-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

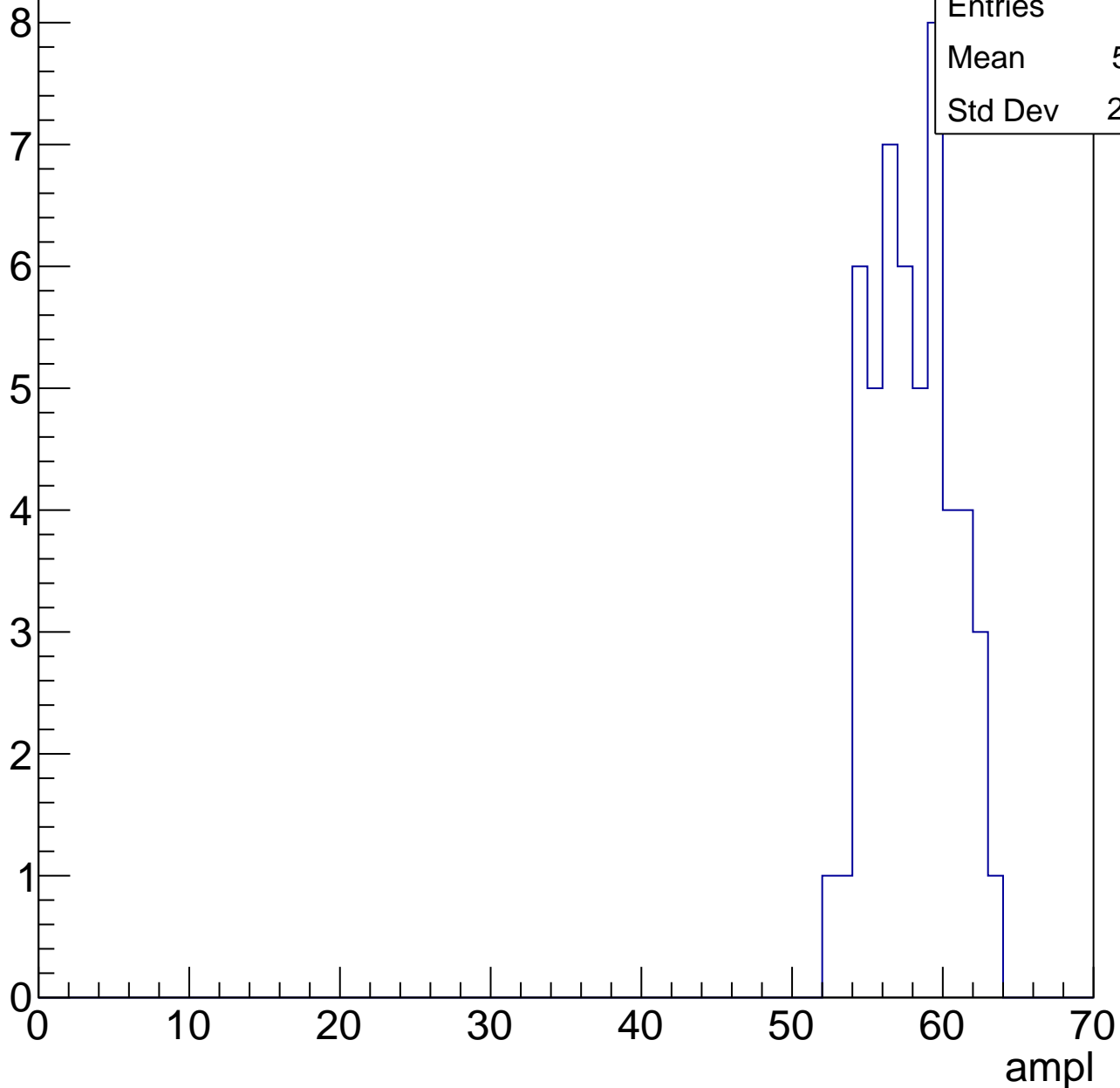
Entries	74
Mean	51.57
Std Dev	3.831



# B0L000S, U10-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



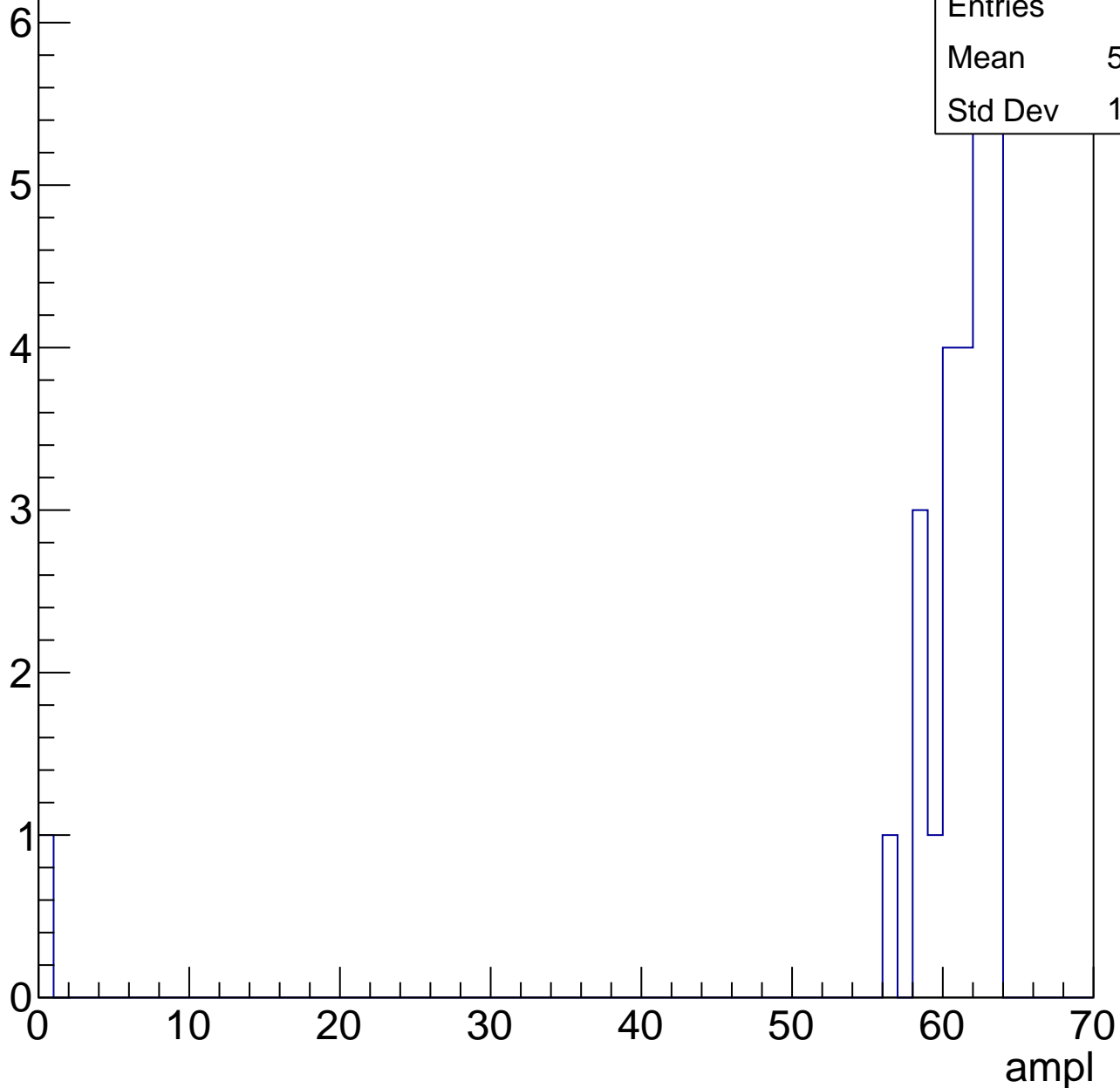
Entries	51
Mean	57.51
Std Dev	2.645

# B0L000S, U10-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.58
Std Dev	11.86



# B0L000S, U10-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U10-ch27, adc0

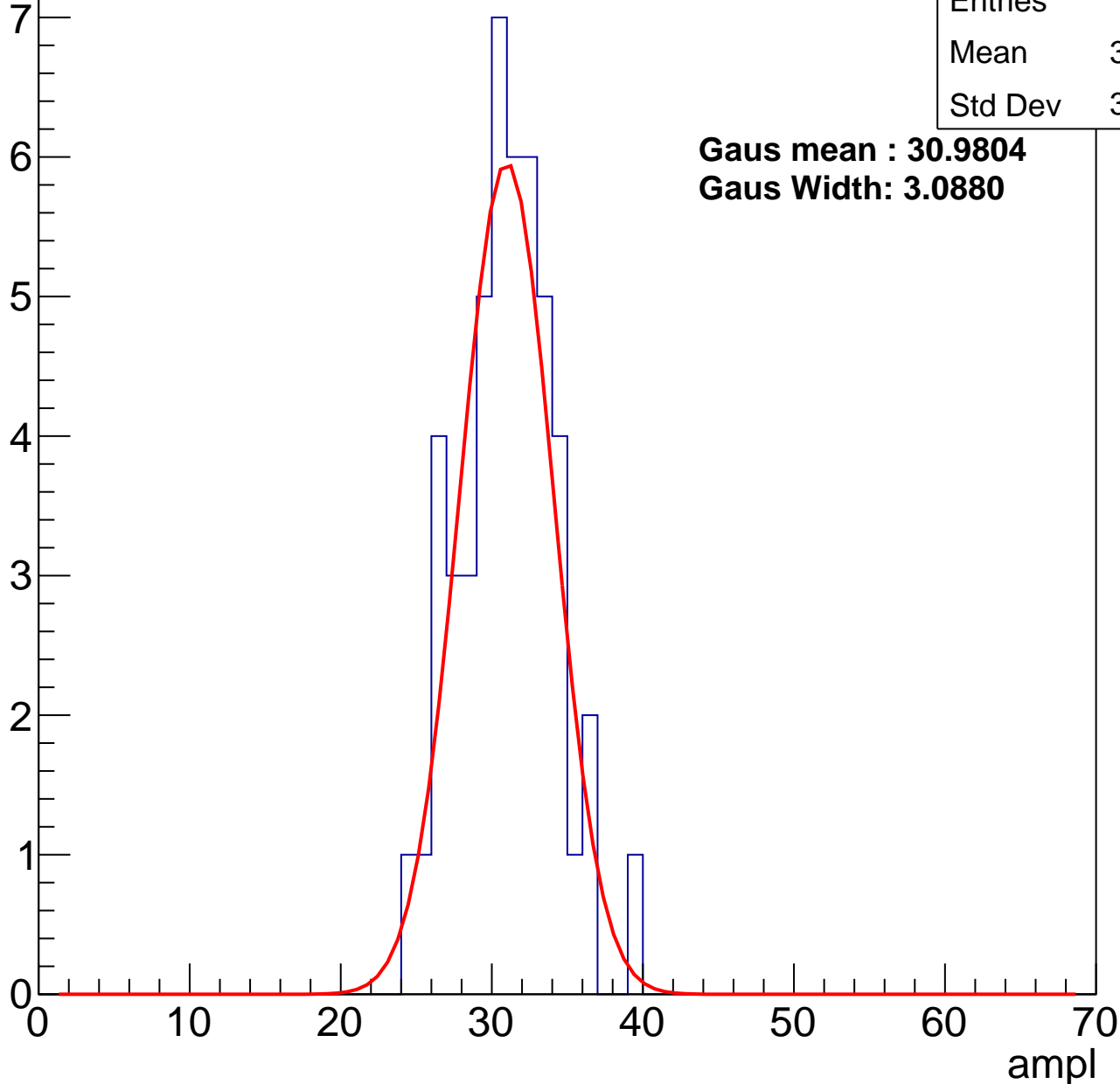
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	30.57
Std Dev	3.097

**Gaus mean : 30.9804**

**Gaus Width: 3.0880**



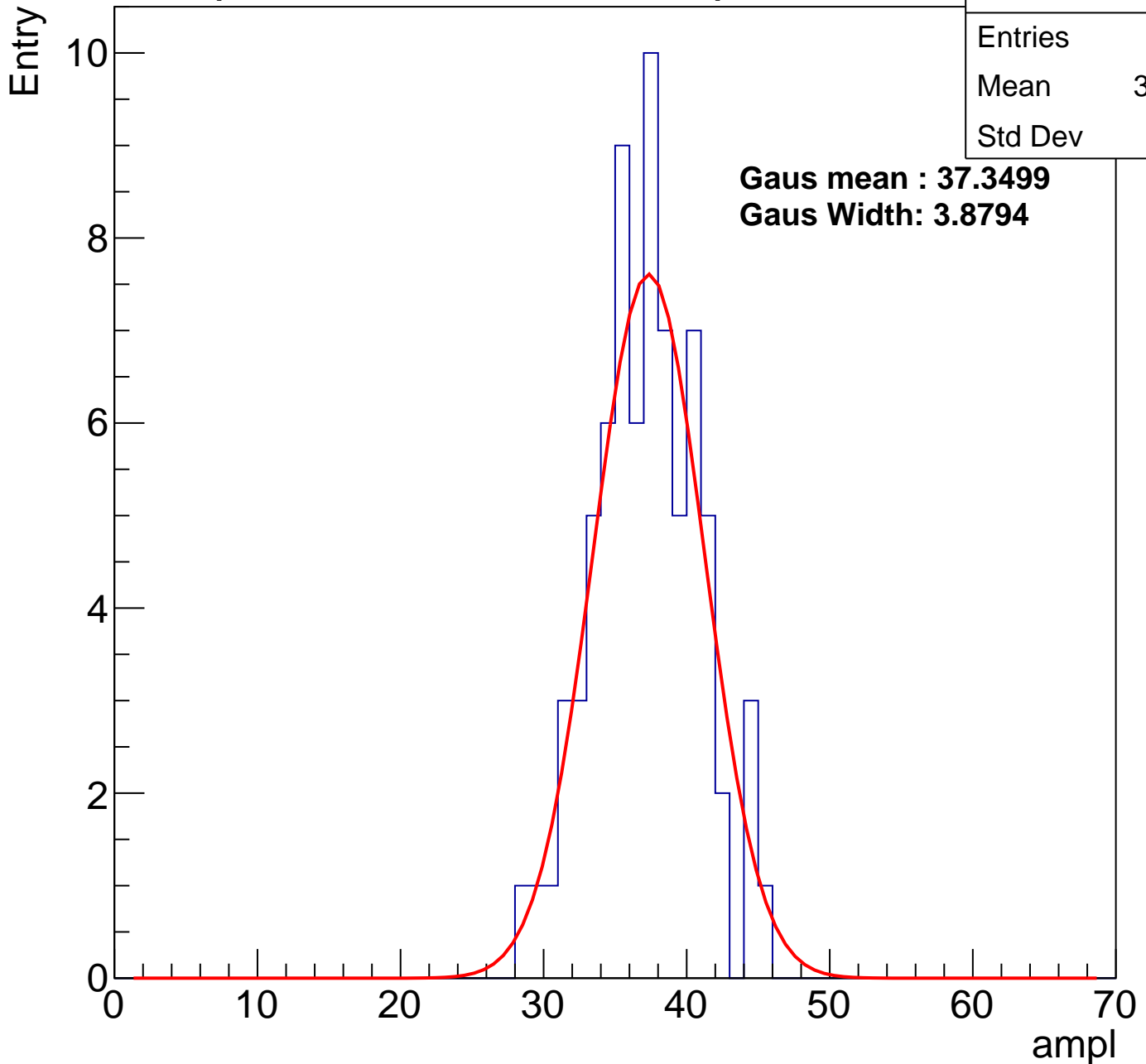
# B0L000S, U10-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	36.71
Std Dev	3.61

**Gaus mean : 37.3499**

**Gaus Width: 3.8794**



# B0L000S, U10-ch27, adc2

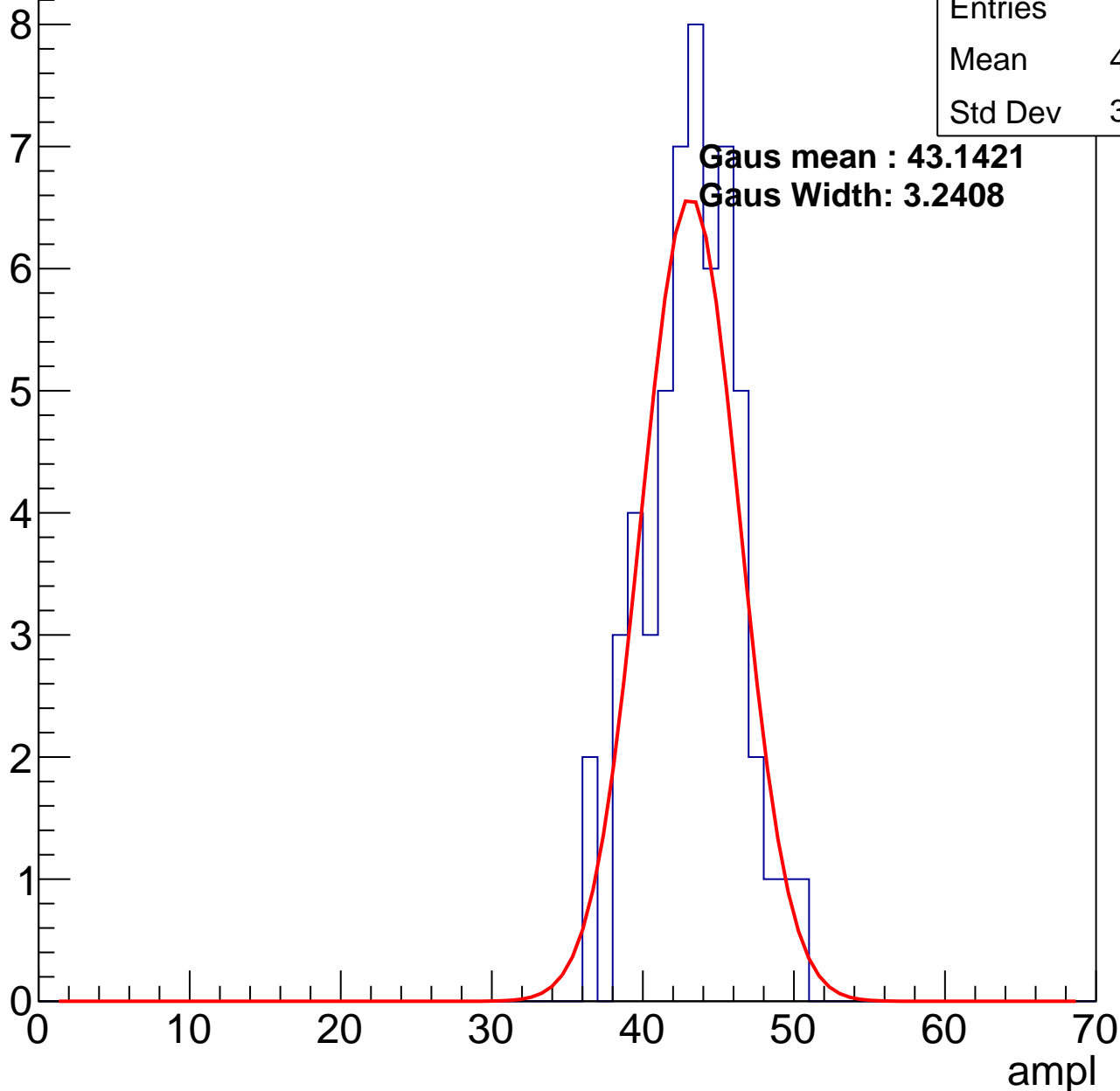
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.82
Std Dev	3.046

**Gaus mean : 43.1421**

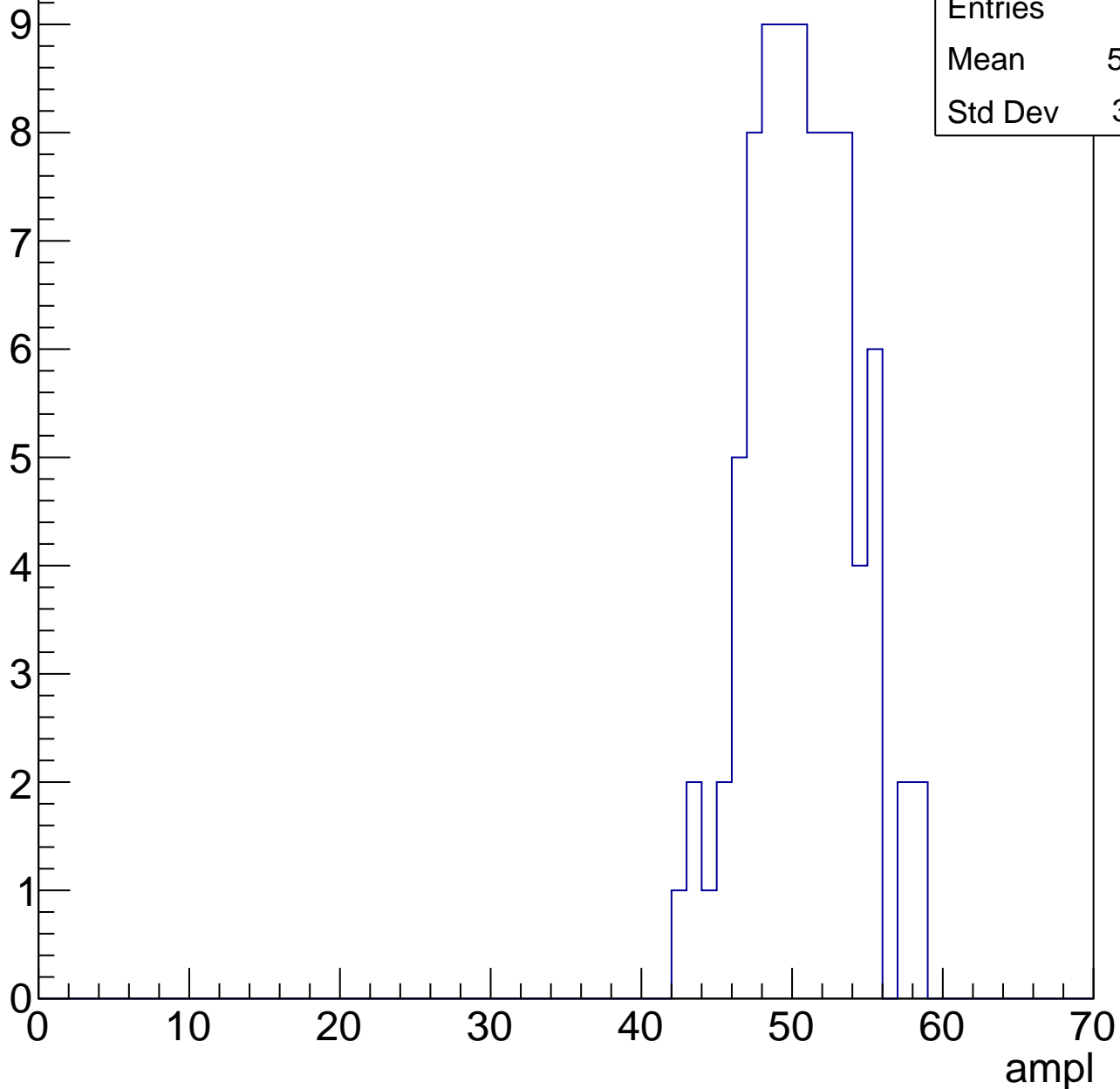
**Gaus Width: 3.2408**



# B0L000S, U10-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

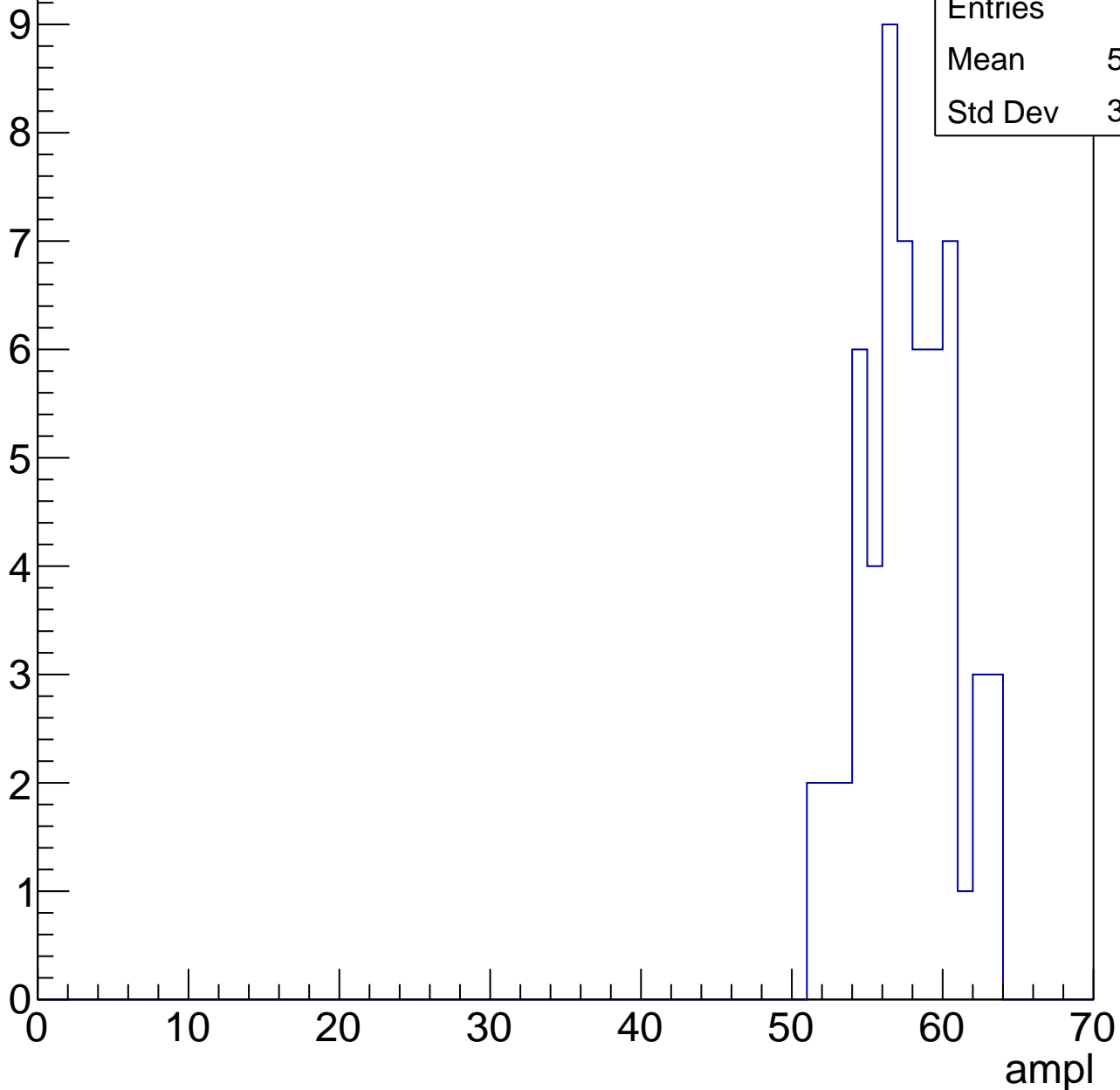


# B0L000S, U10-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	57.19
Std Dev	3.003

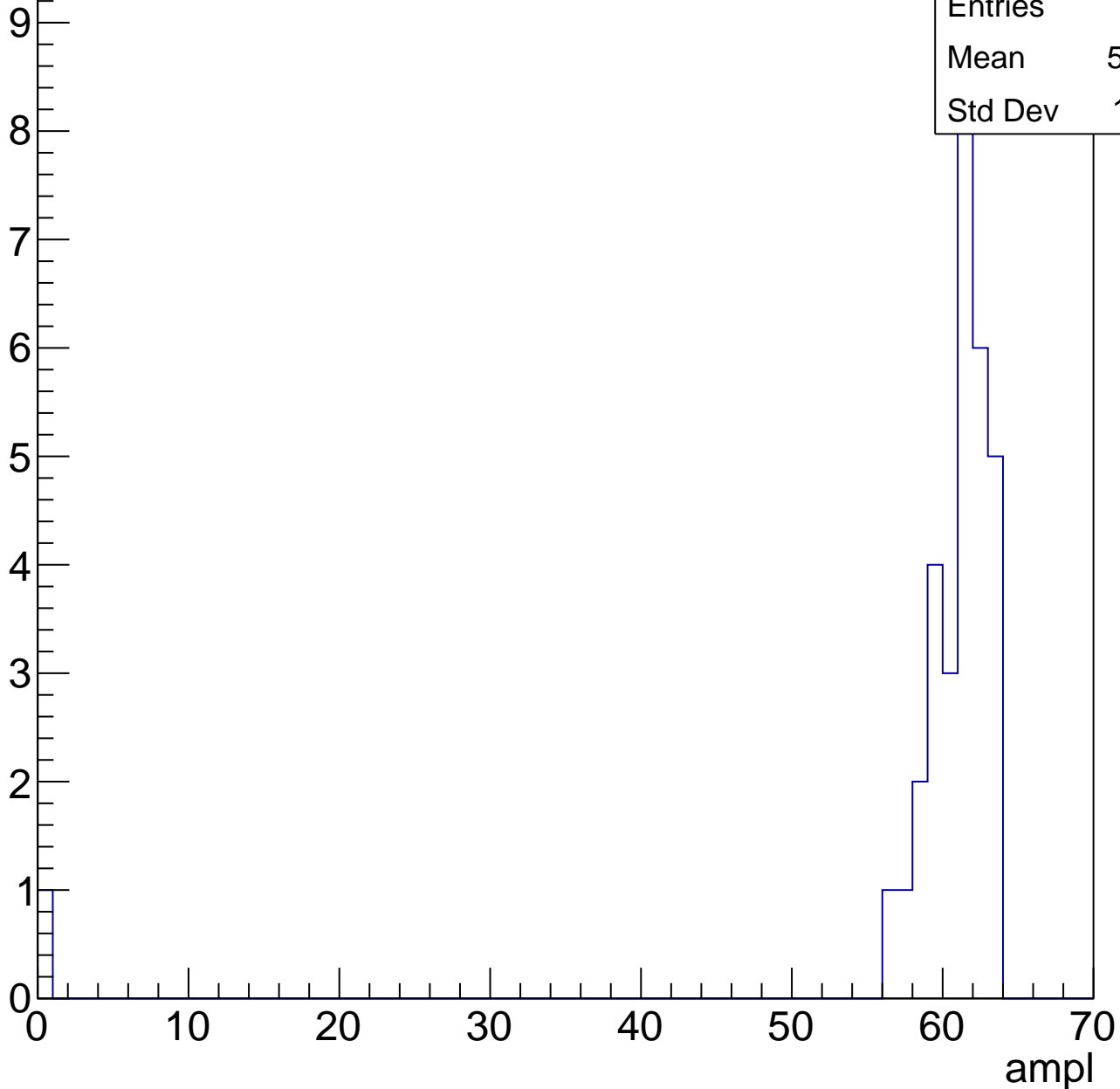


# B0L000S, U10-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.78
Std Dev	10.71



# B0L000S, U10-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L000S, U10-ch28, adc0

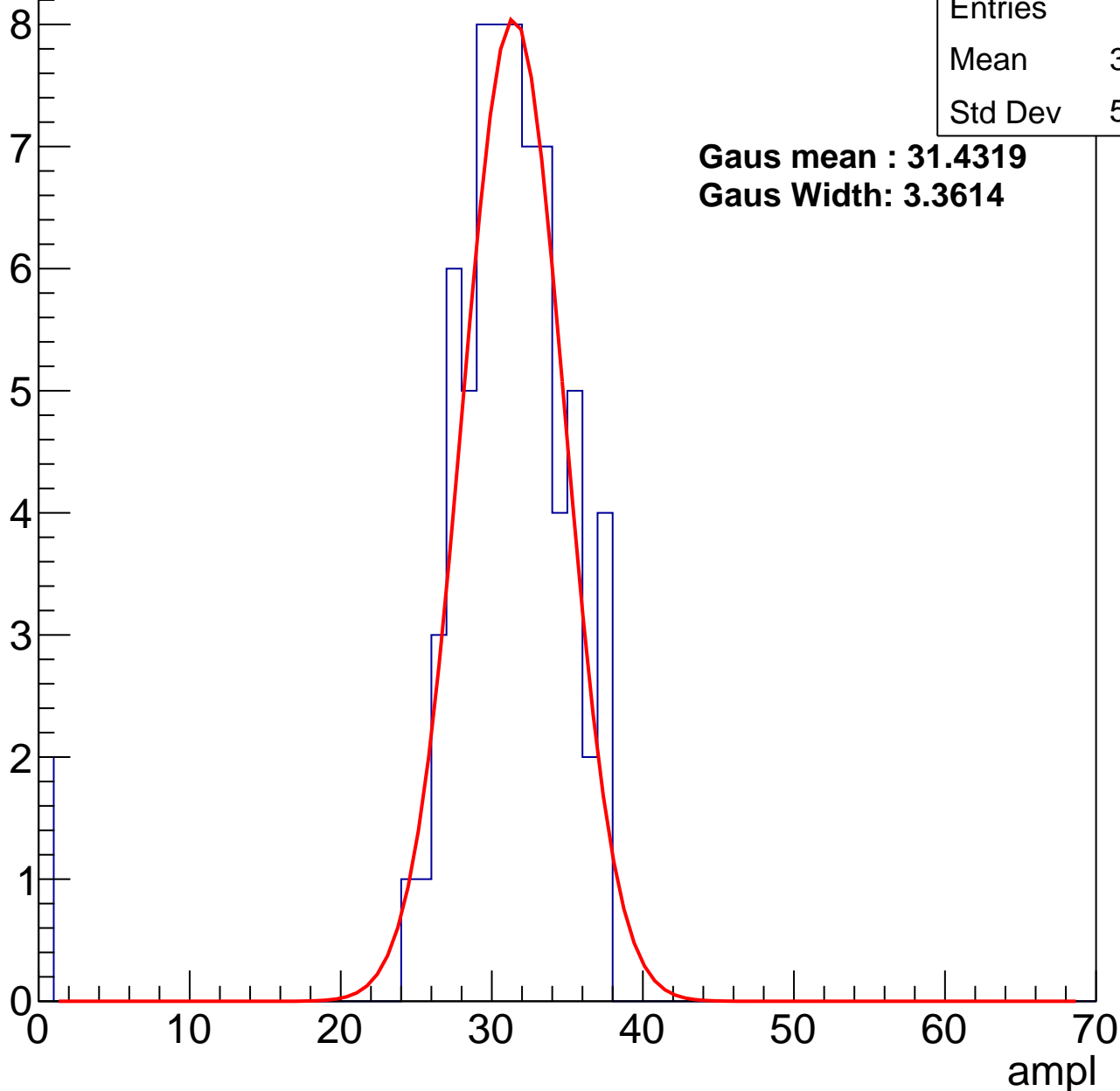
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.07
Std Dev	5.989

**Gaus mean : 31.4319**

**Gaus Width: 3.3614**



# B0L000S, U10-ch28, adc1

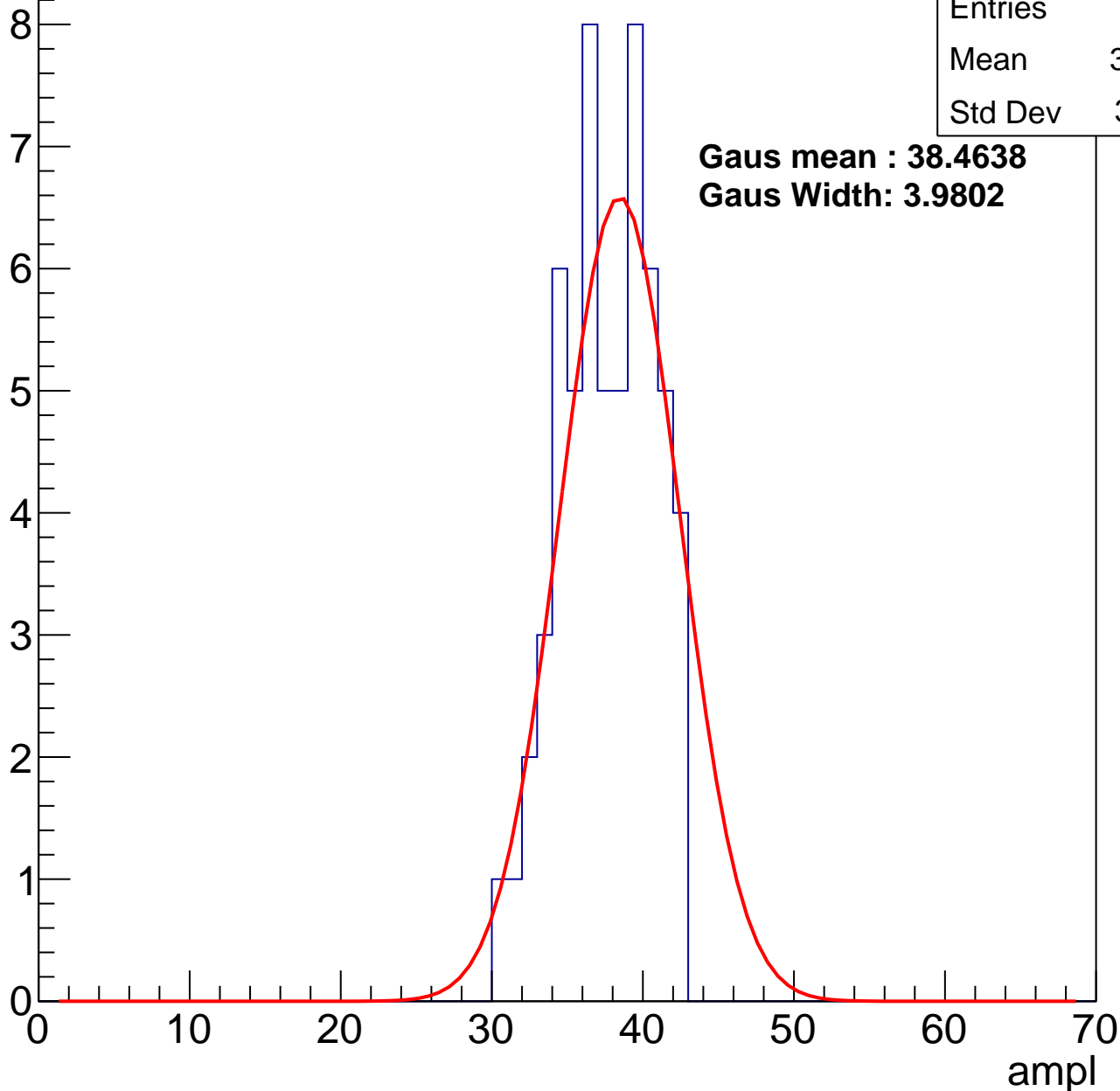
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.14
Std Dev	3.011

**Gaus mean : 38.4638**

**Gaus Width: 3.9802**



# B0L000S, U10-ch28, adc2

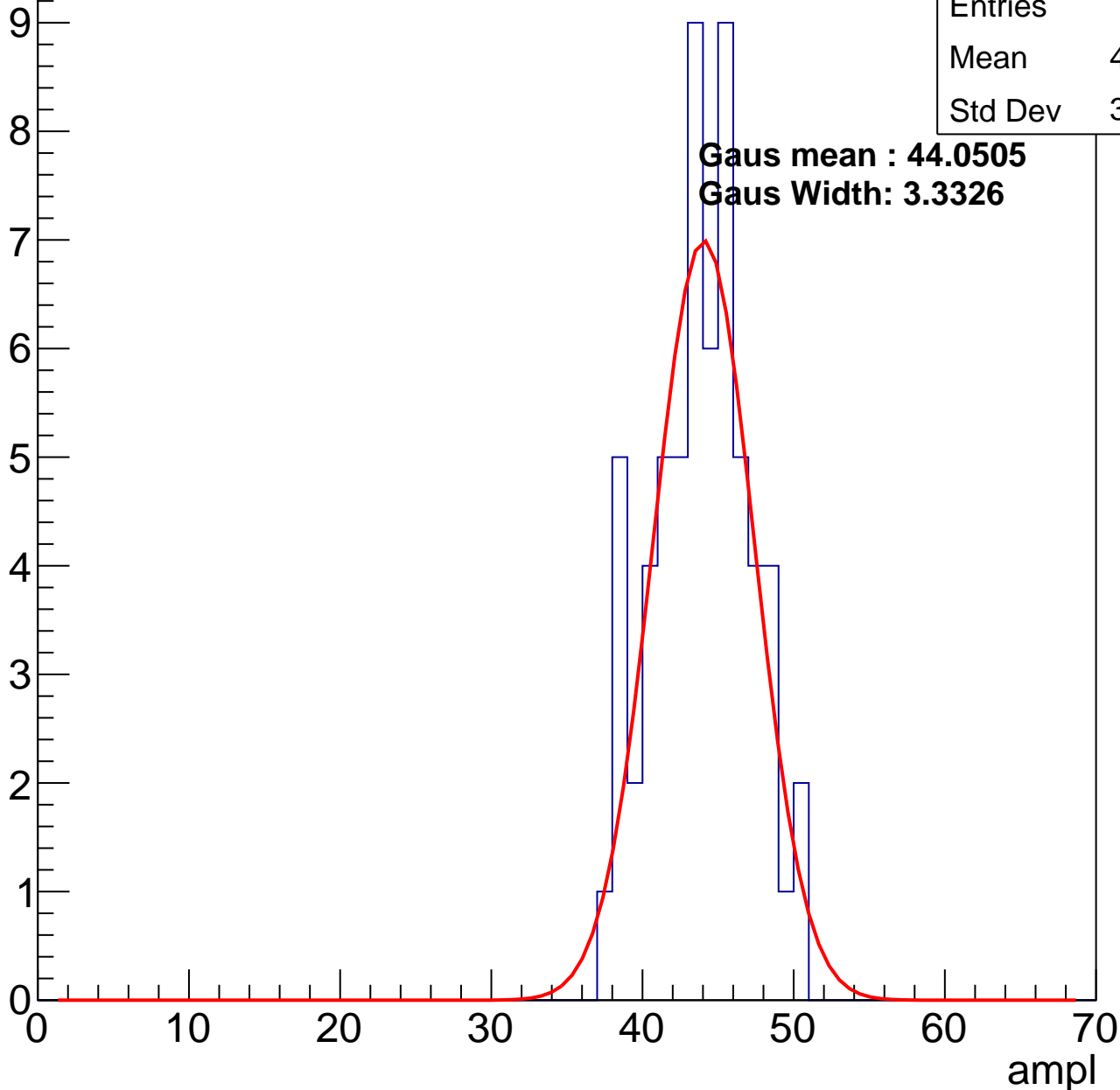
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.47
Std Dev	3.176

**Gaus mean : 44.0505**

**Gaus Width: 3.3326**

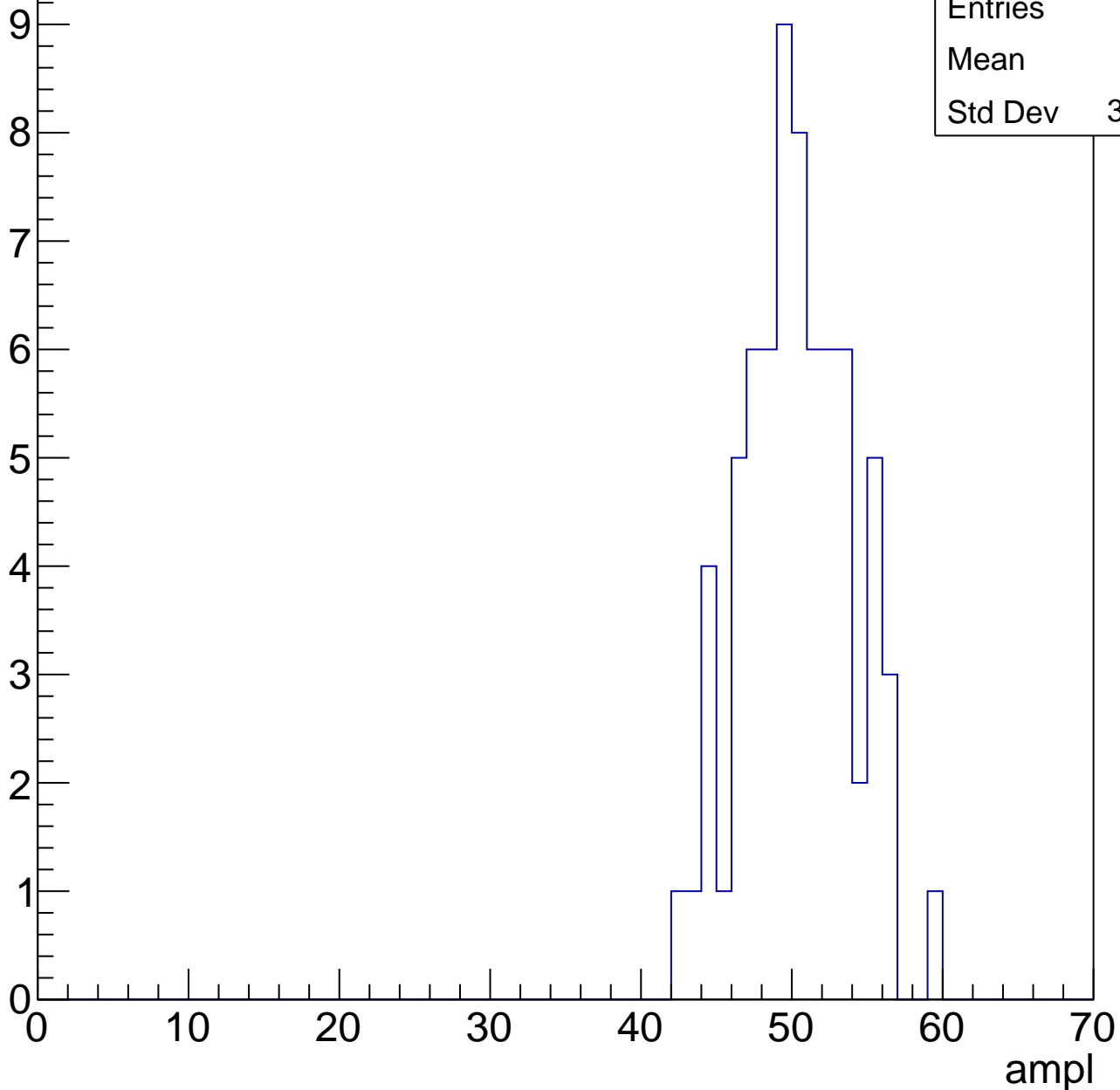


# B0L000S, U10-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.9
Std Dev	3.542

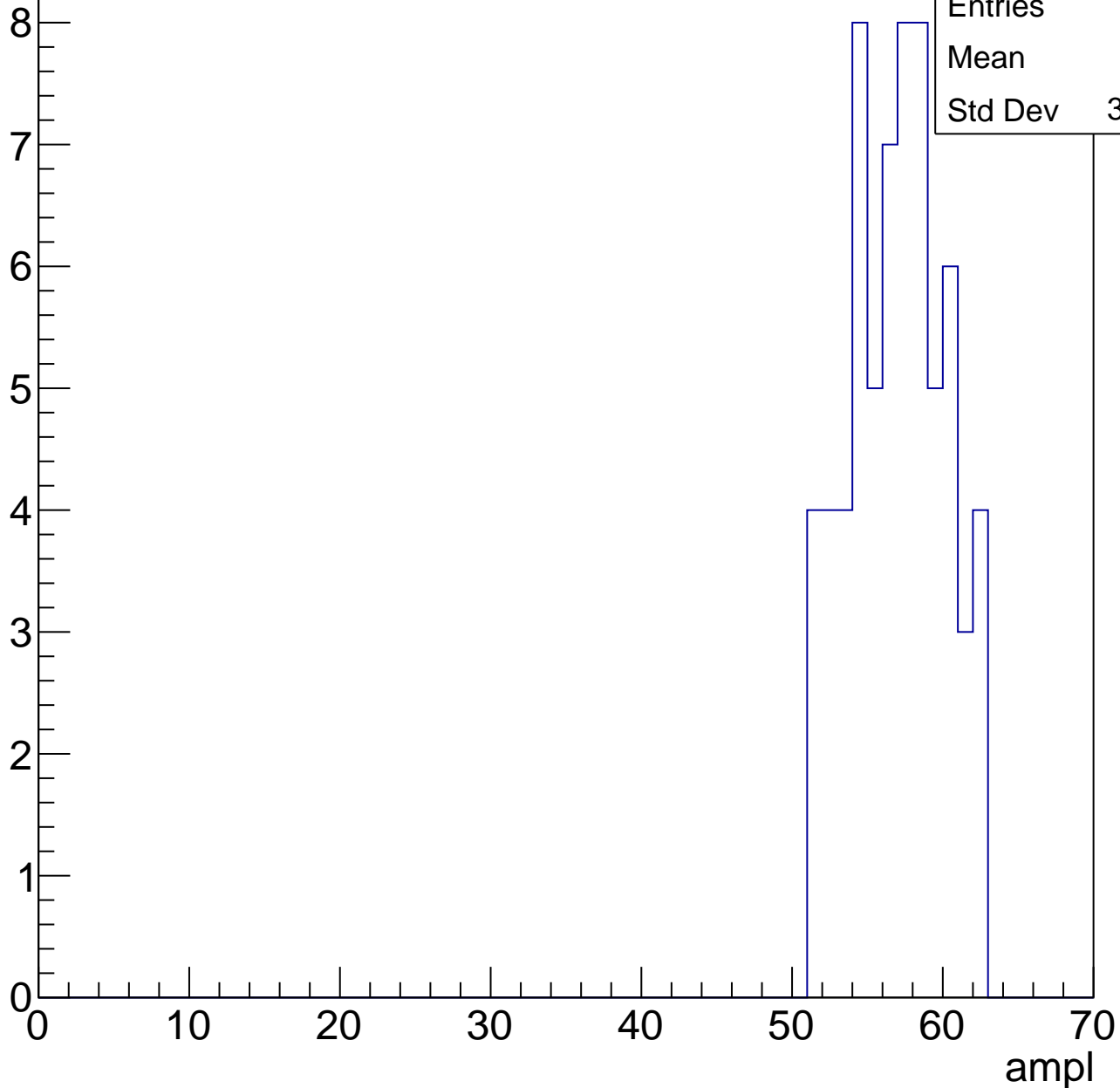


# B0L000S, U10-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.5
Std Dev	3.066

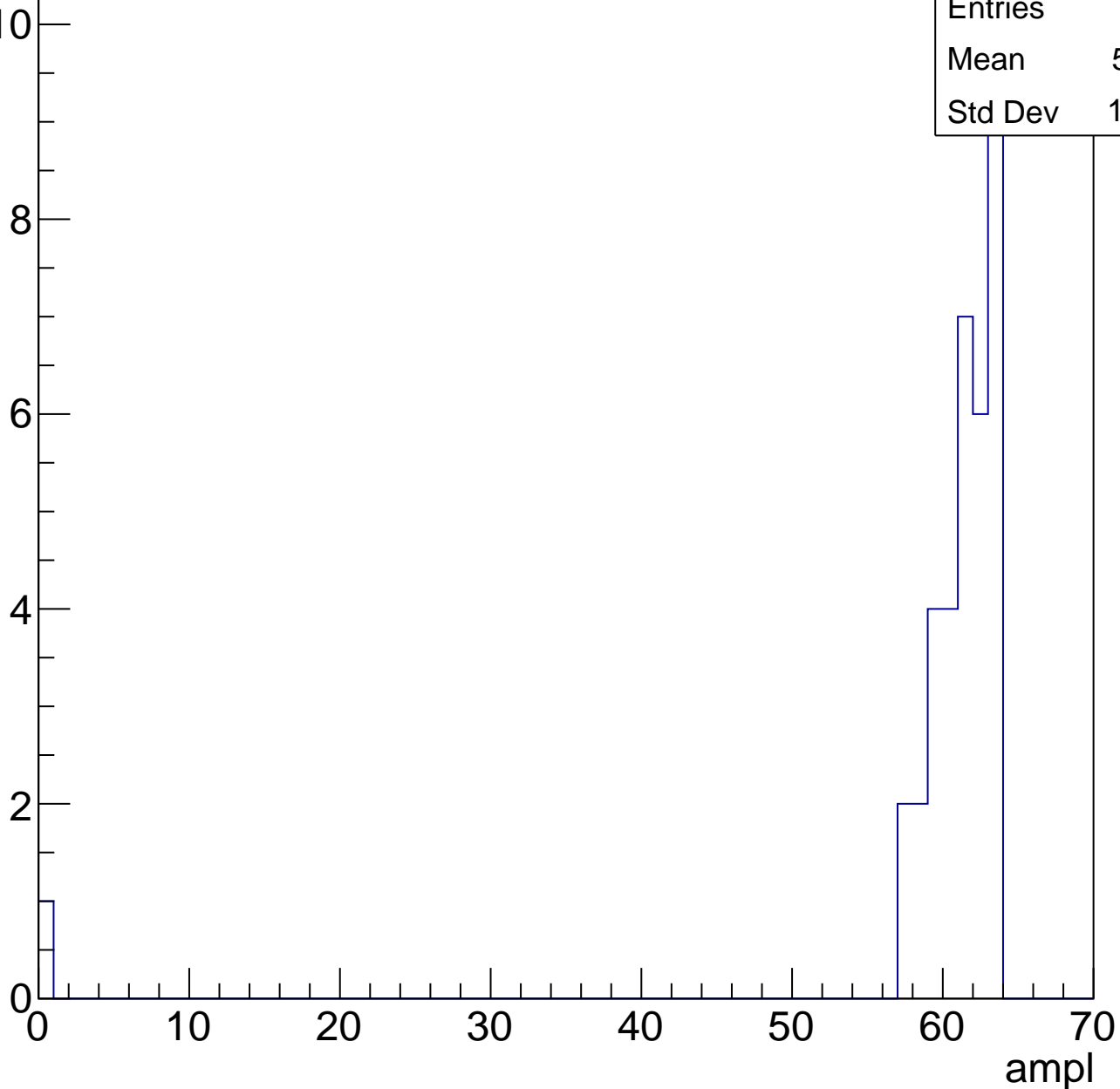


# B0L000S, U10-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	59.31
Std Dev	10.18



# B0L000S, U10-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B0L000S, U10-ch29, adc0

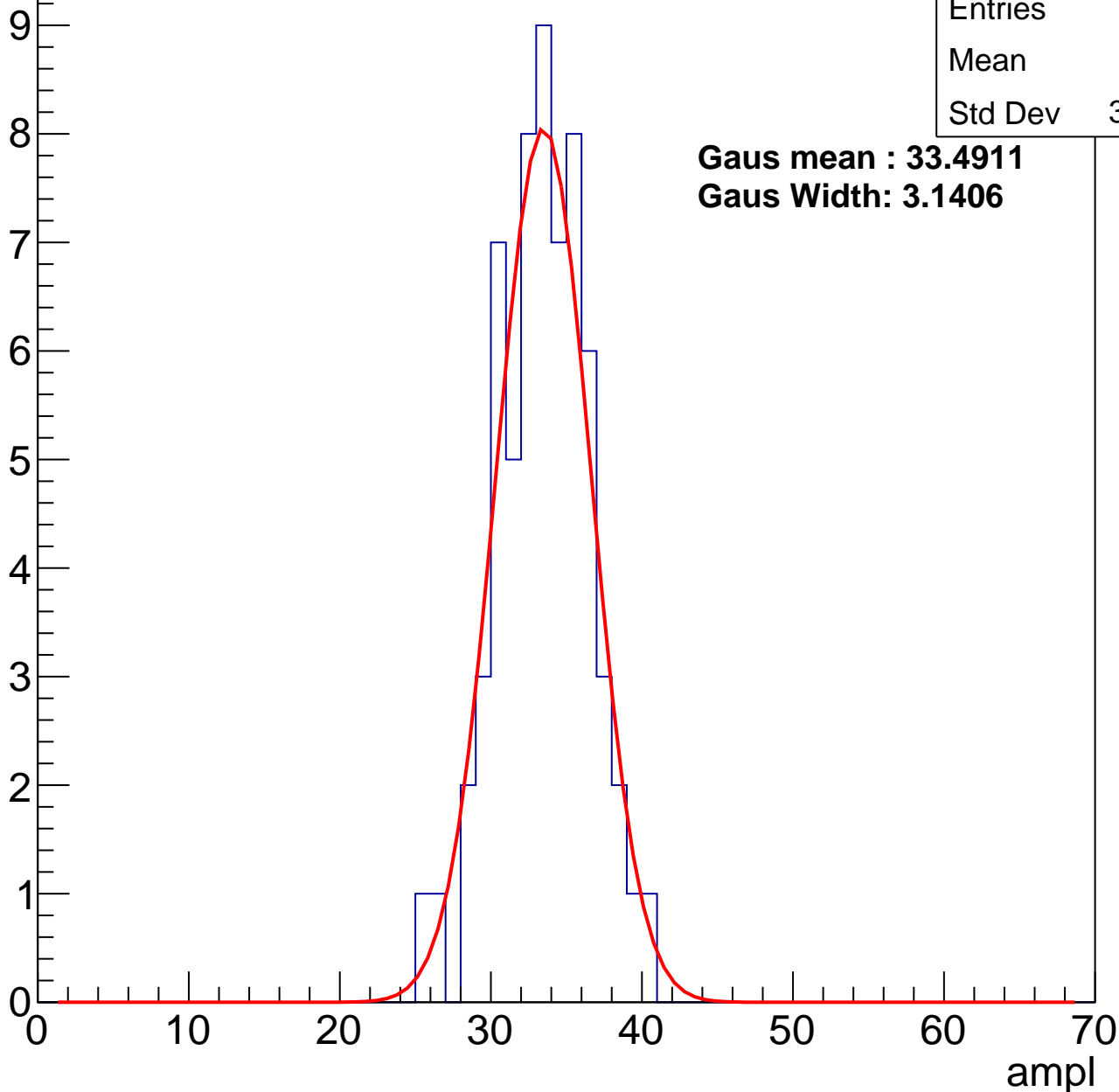
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	33
Std Dev	3.005

**Gaus mean : 33.4911**

**Gaus Width: 3.1406**



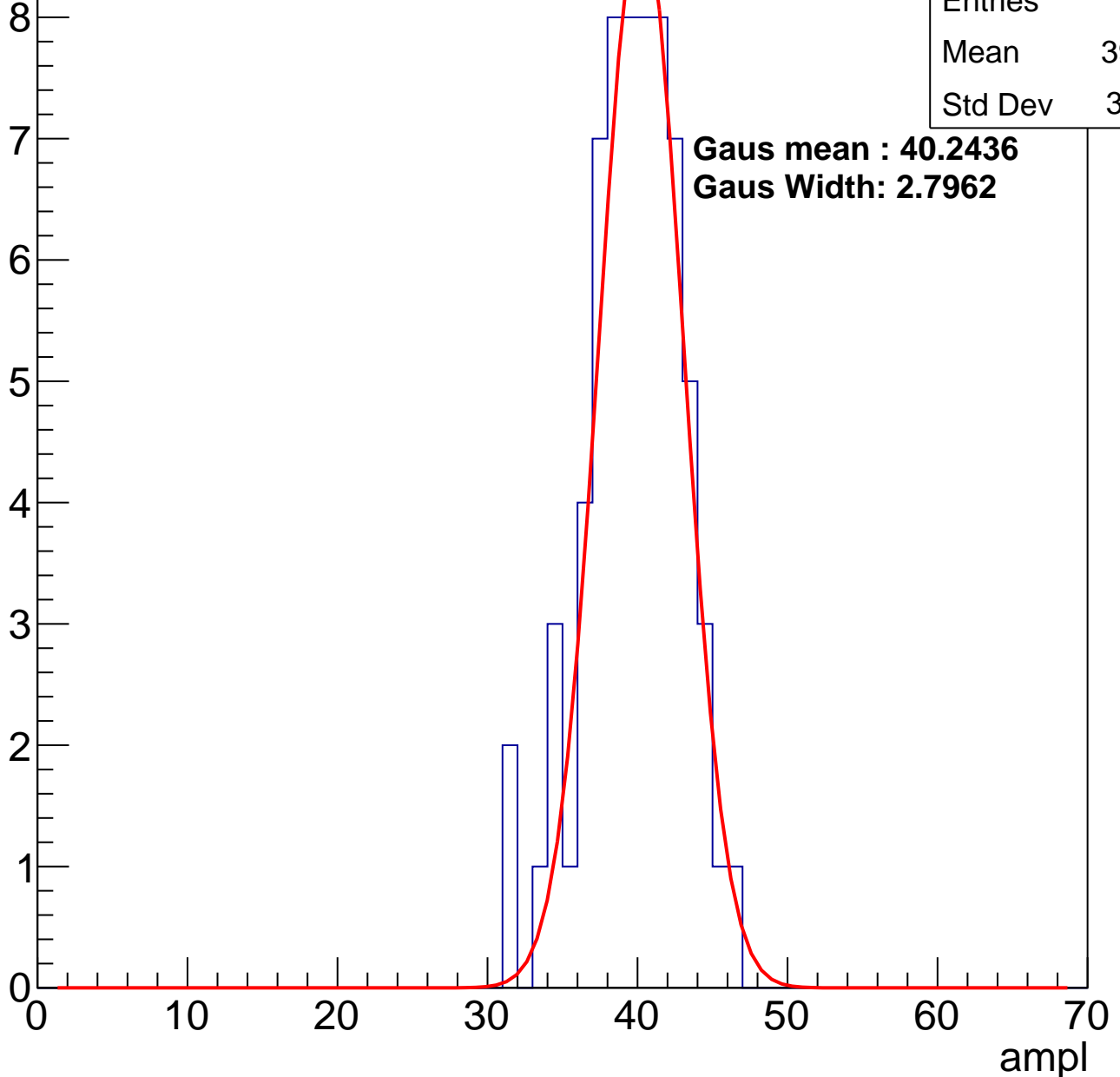
# B0L000S, U10-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	39.27
Std Dev	3.151

**Gaus mean : 40.2436**  
**Gaus Width: 2.7962**



# B0L000S, U10-ch29, adc2

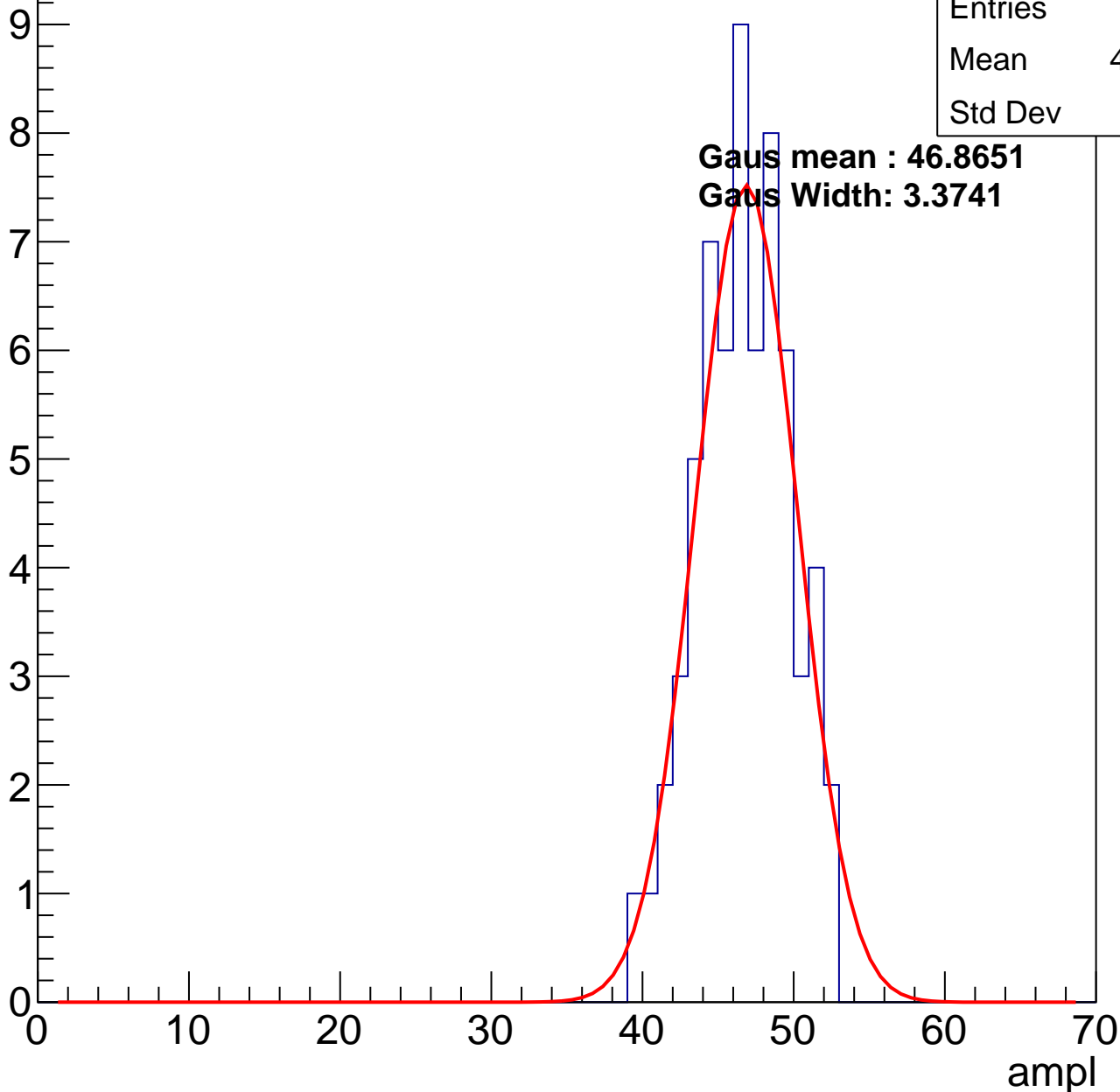
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	46.22
Std Dev	3.01

**Gaus mean : 46.8651**

**Gaus Width: 3.3741**



# B0L000S, U10-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	52.16
Std Dev	3.361

Entry

10

8

6

4

2

0

0

10

20

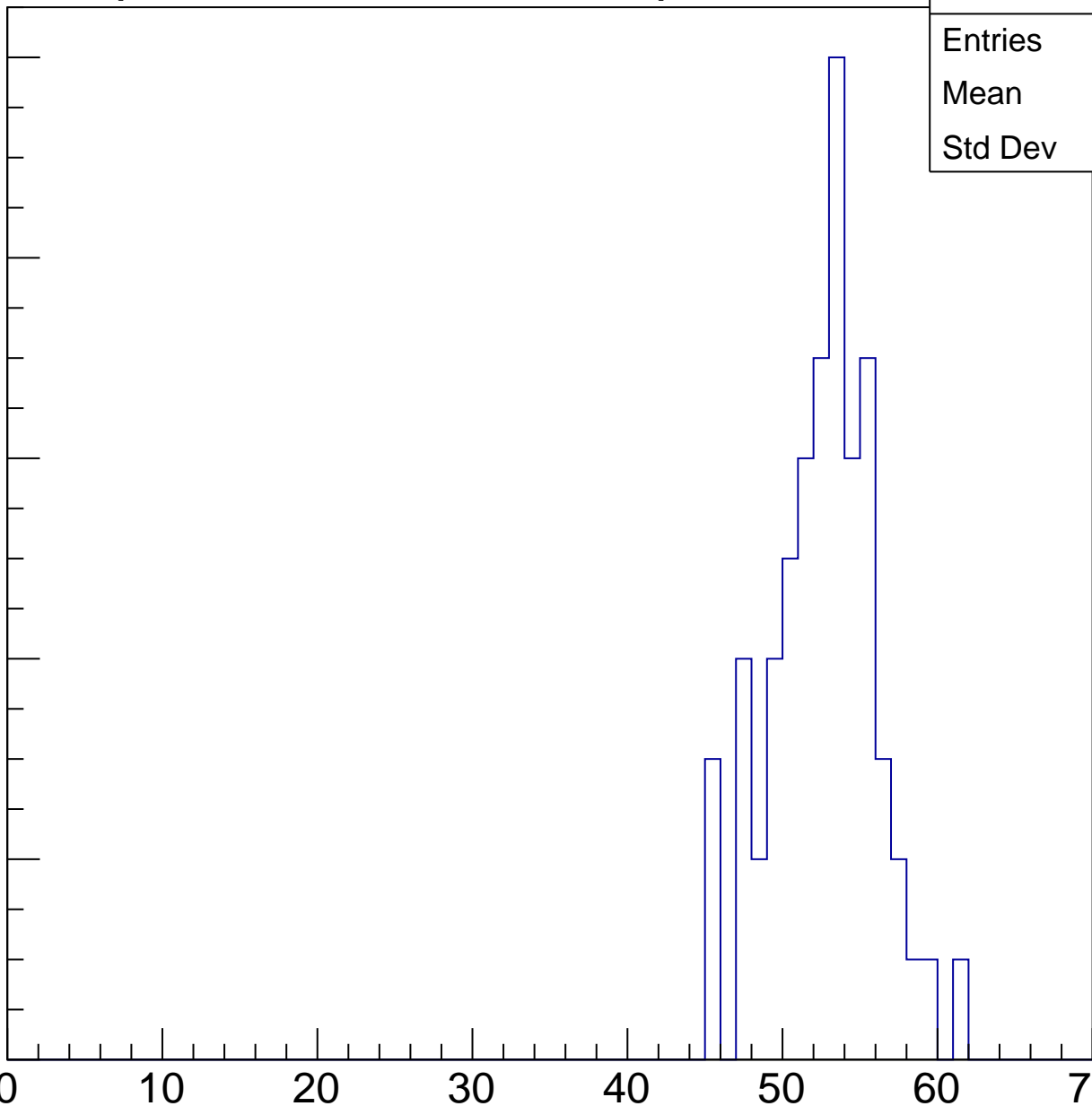
30

40

50

60

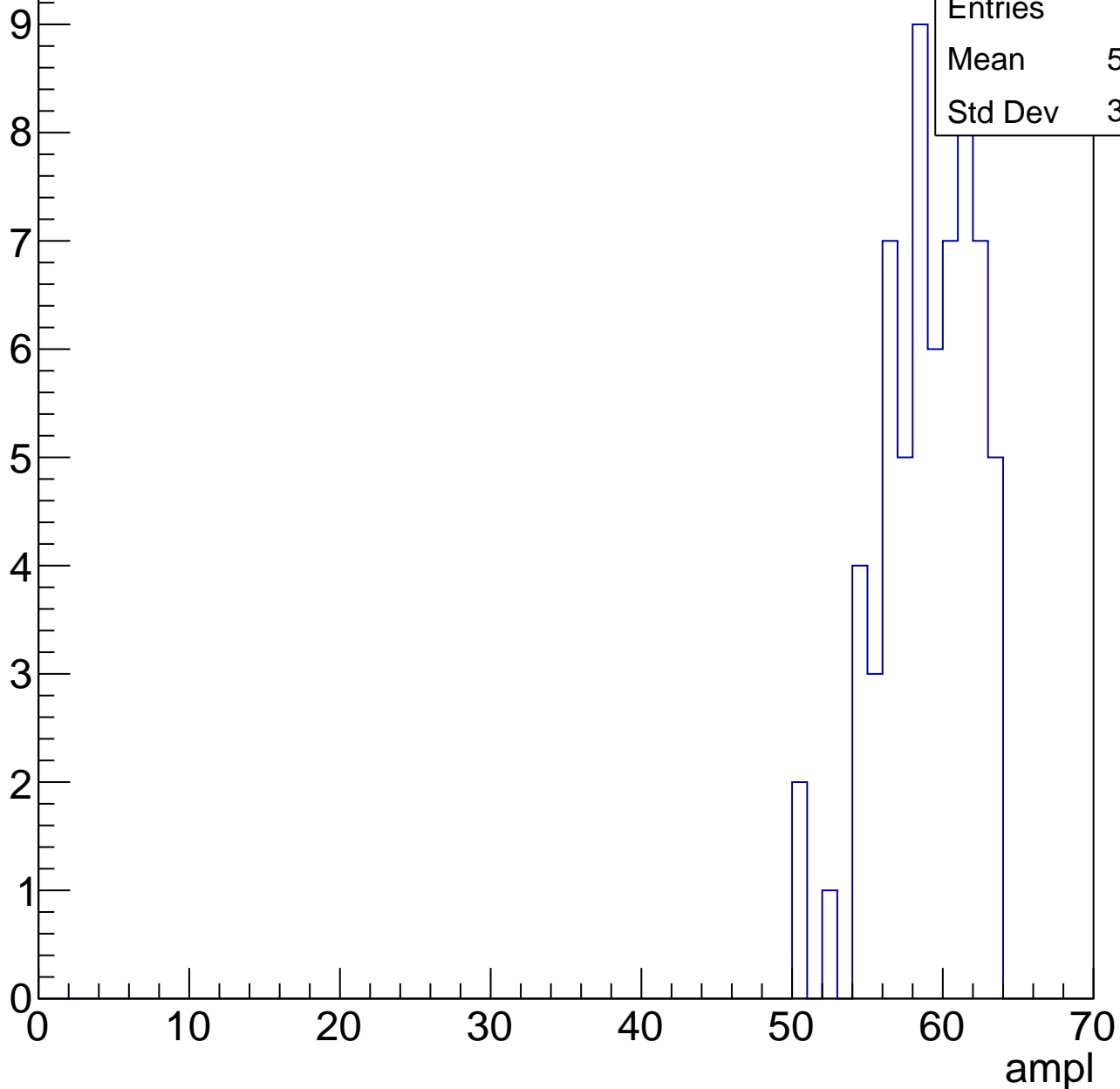
ampl



# B0L000S, U10-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

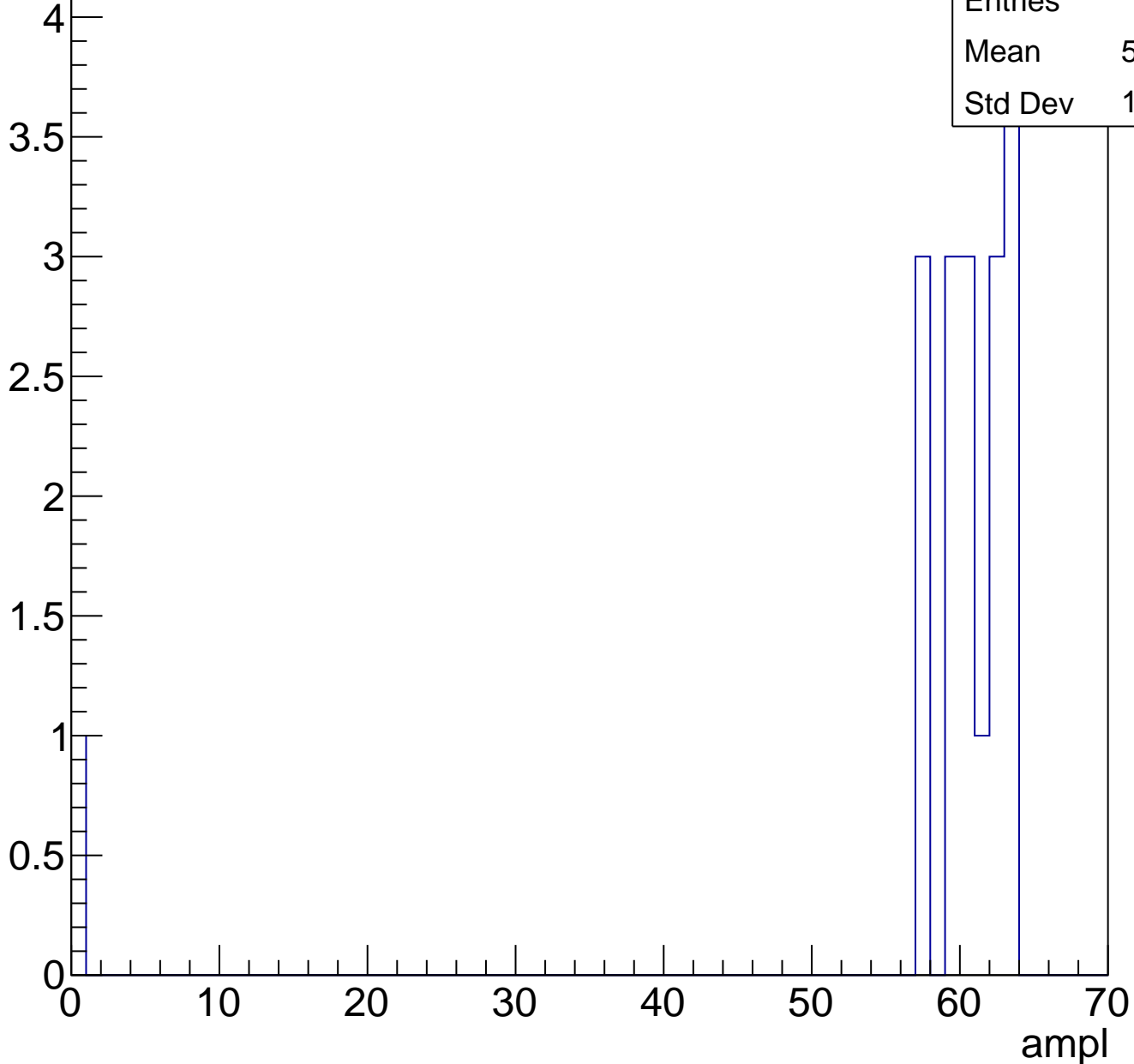
Entry



# B0L000S, U10-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U10-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U10-ch30, adc0

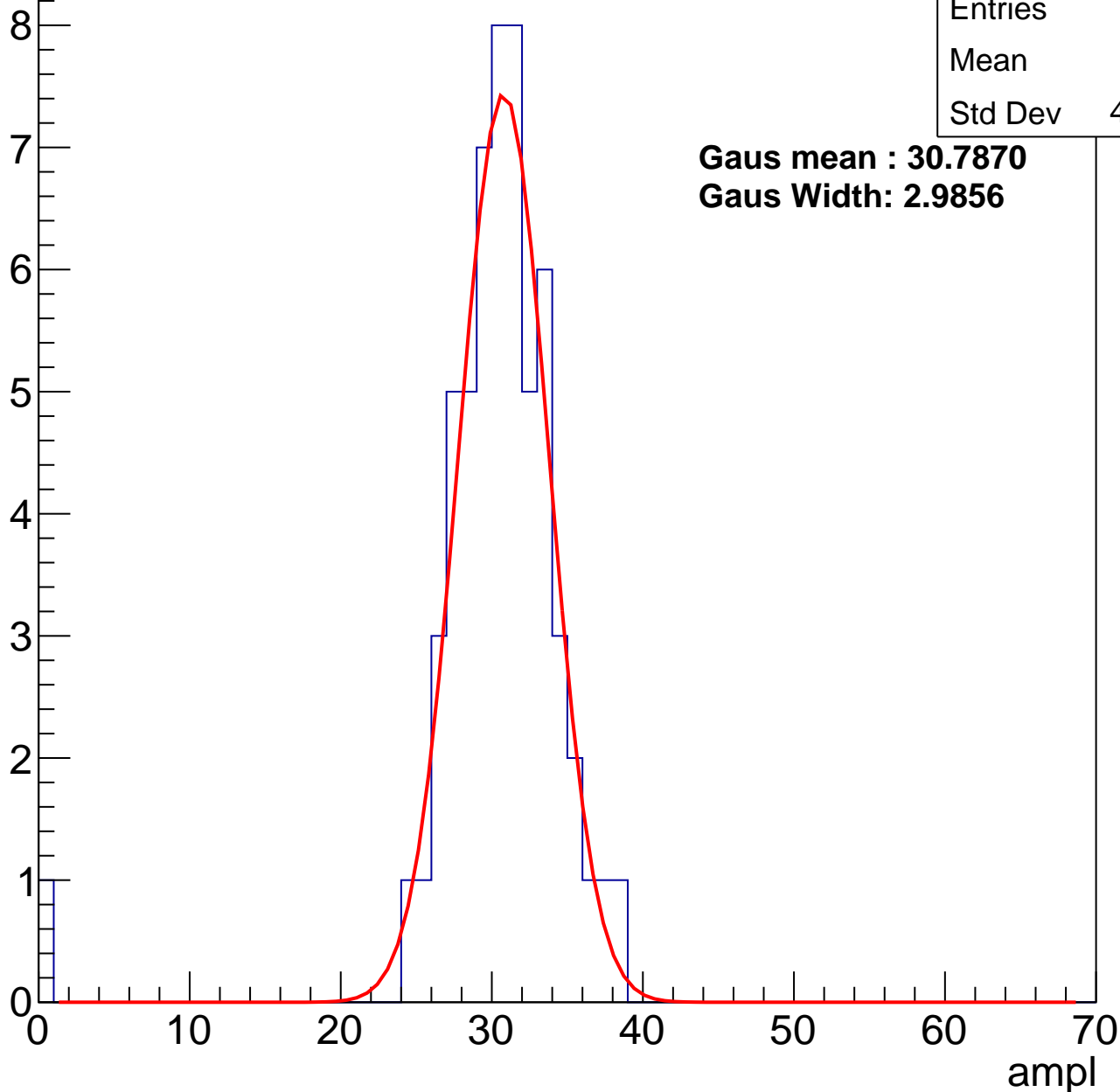
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.9
Std Dev	4.929

**Gaus mean : 30.7870**

**Gaus Width: 2.9856**



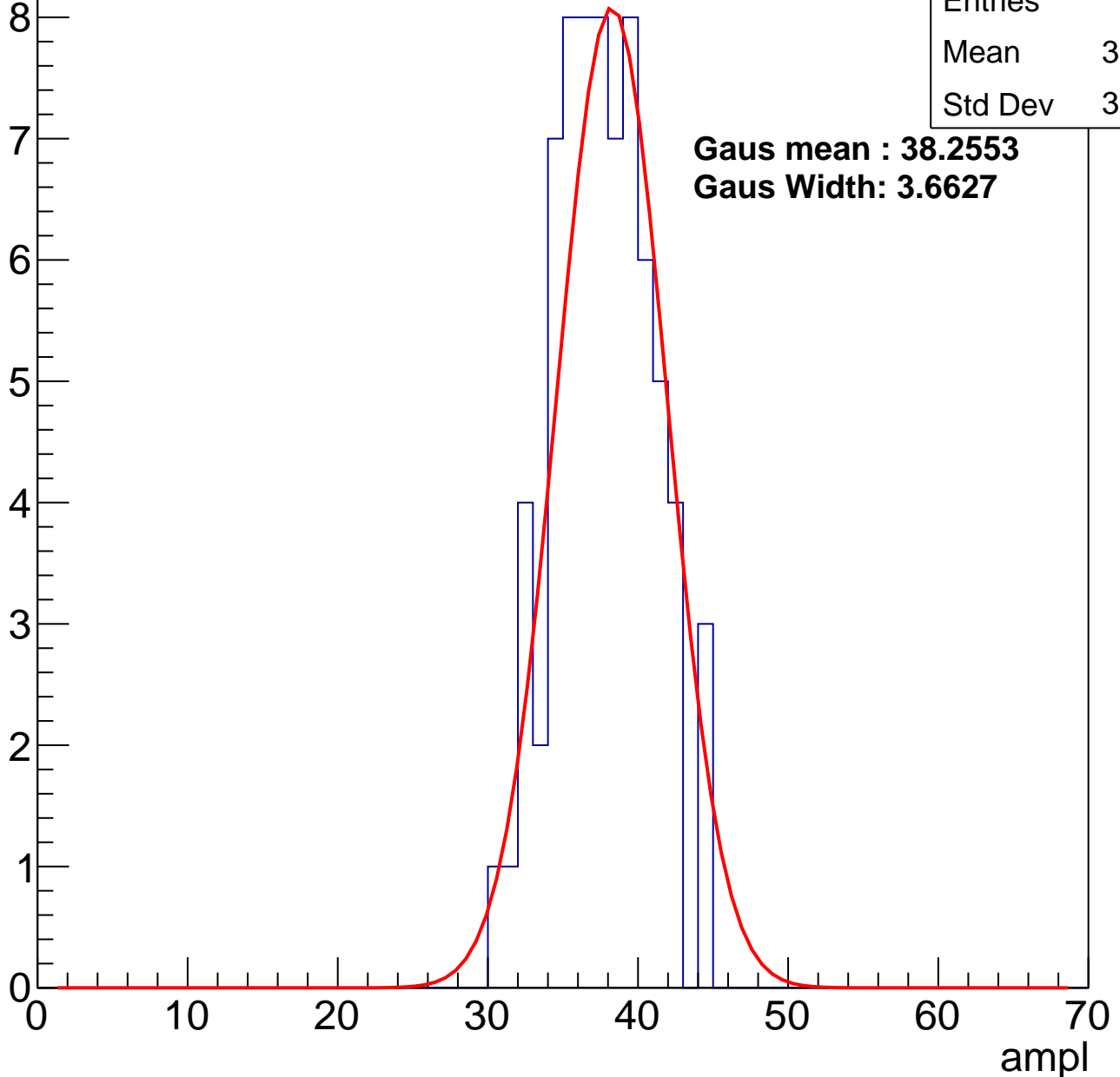
# B0L000S, U10-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	37.22
Std Dev	3.198

**Gaus mean : 38.2553**  
**Gaus Width: 3.6627**



# B0L000S, U10-ch30, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

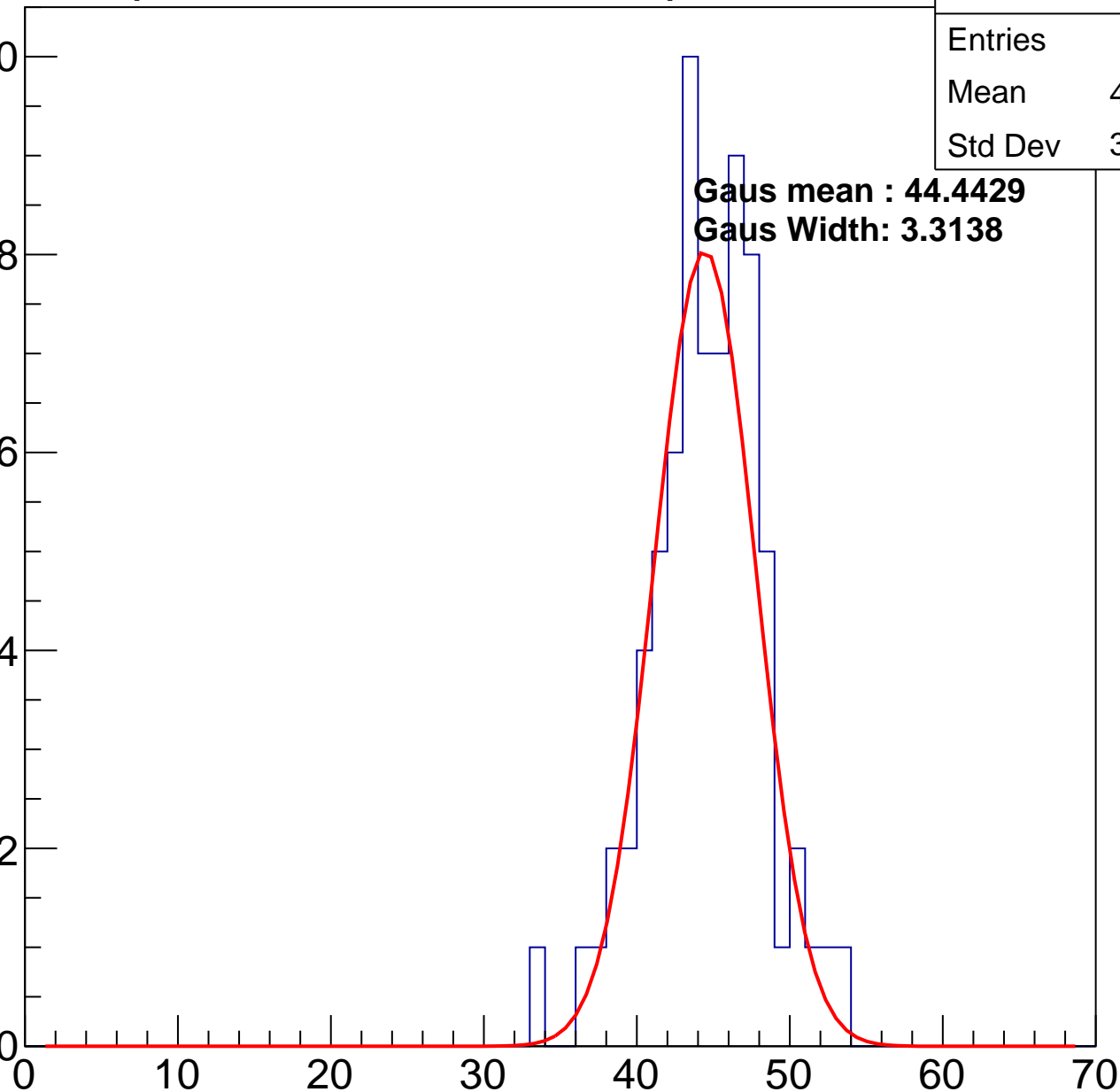
Entries	74
Mean	44.12
Std Dev	3.646

**Gaus mean : 44.4429**

**Gaus Width: 3.3138**

10  
8  
6  
4  
2  
0

ampl

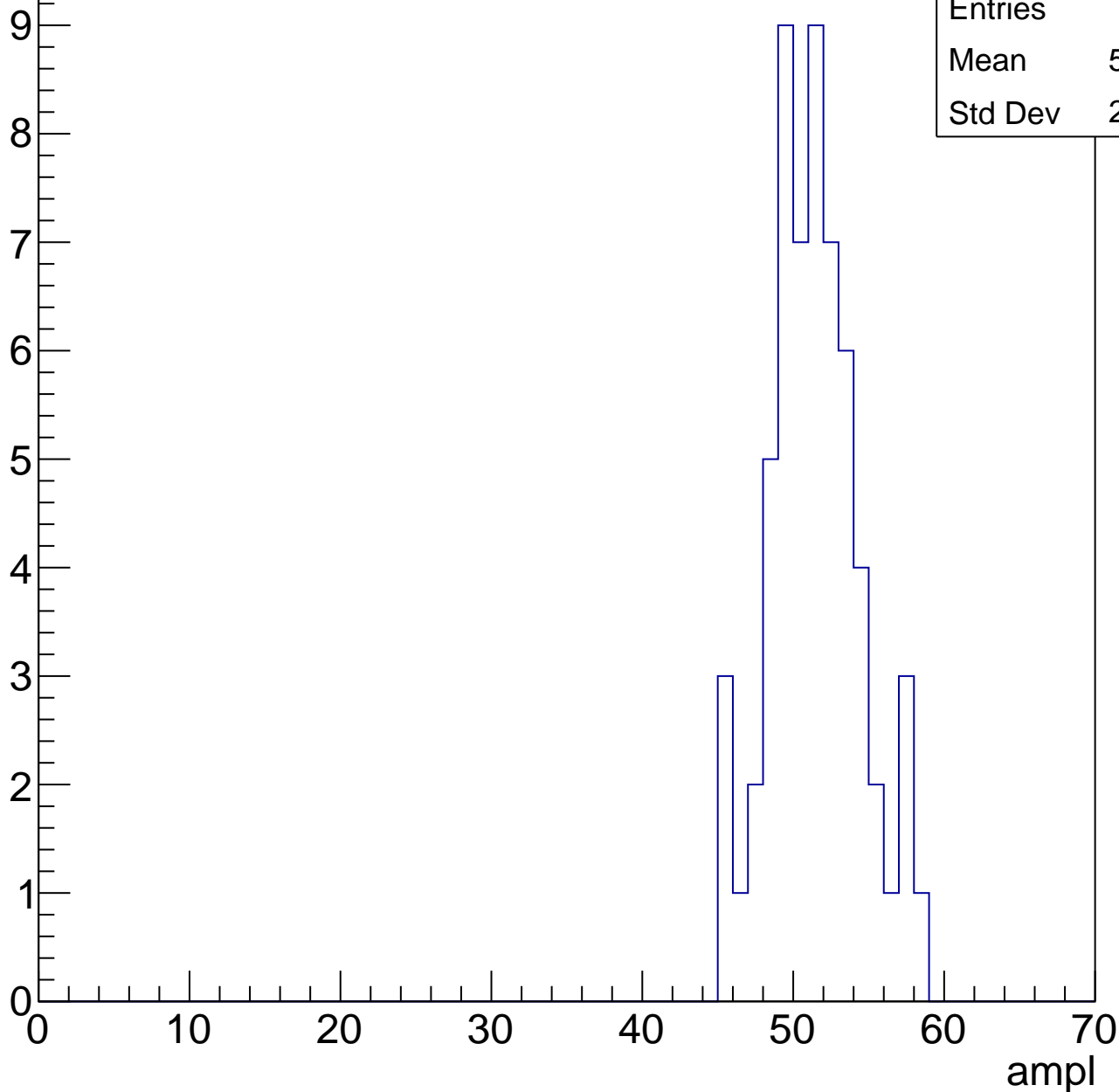


# B0L000S, U10-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.97
Std Dev	2.983

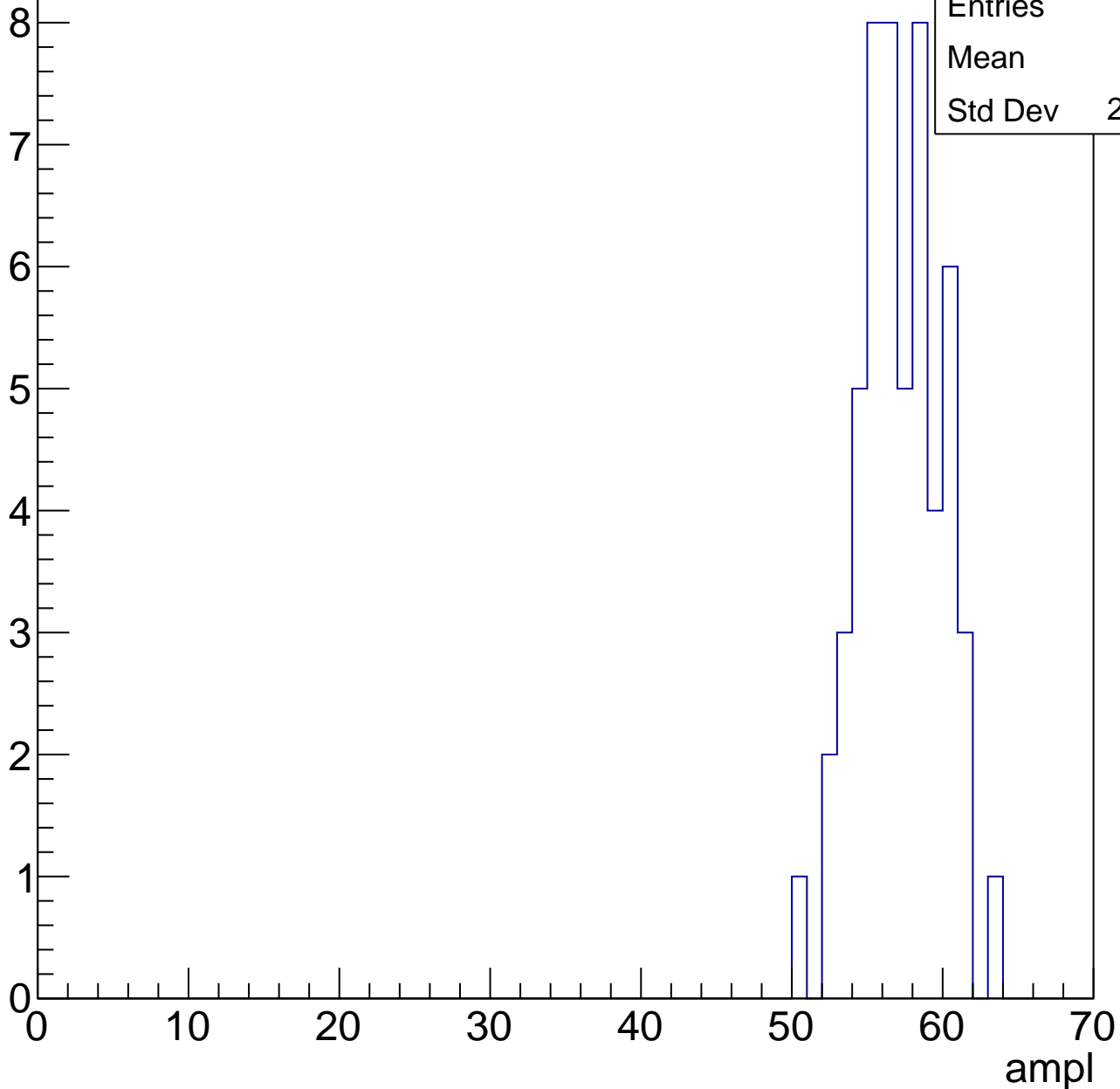


# B0L000S, U10-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	56.7
Std Dev	2.685

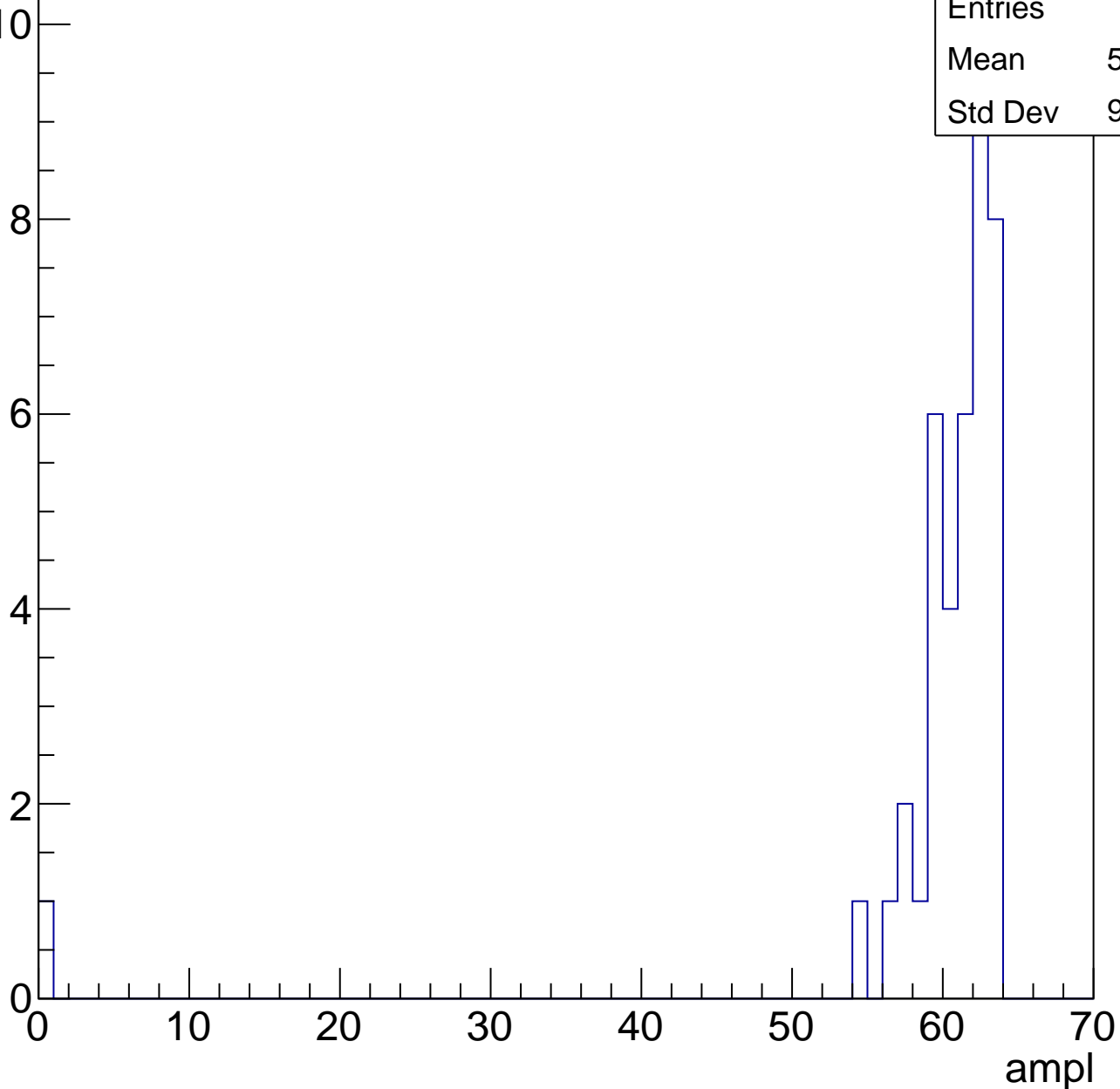


# B0L000S, U10-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	59.15
Std Dev	9.707



# B0L000S, U10-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch30, adc7

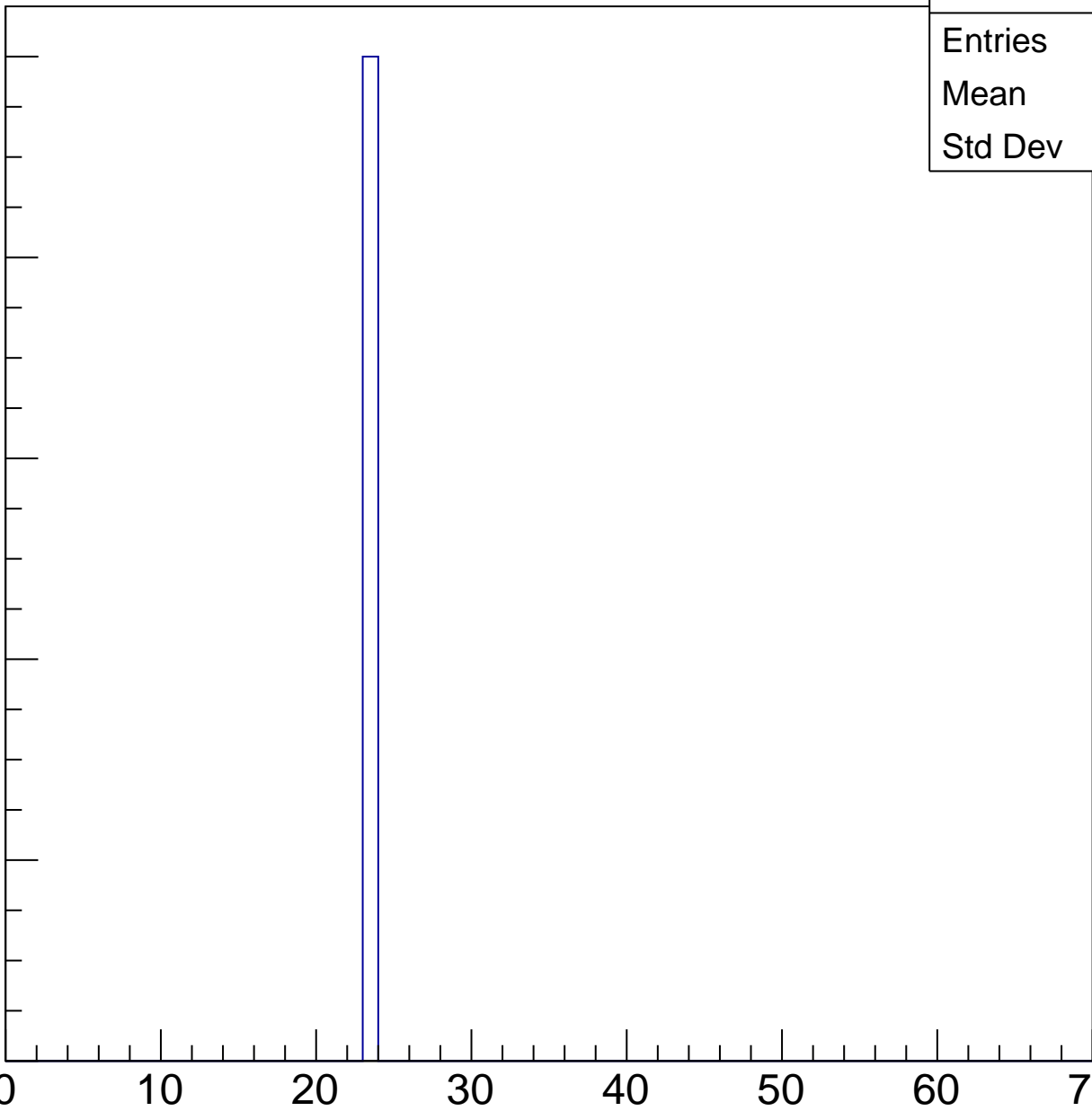
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl



# B0L000S, U10-ch31, adc0

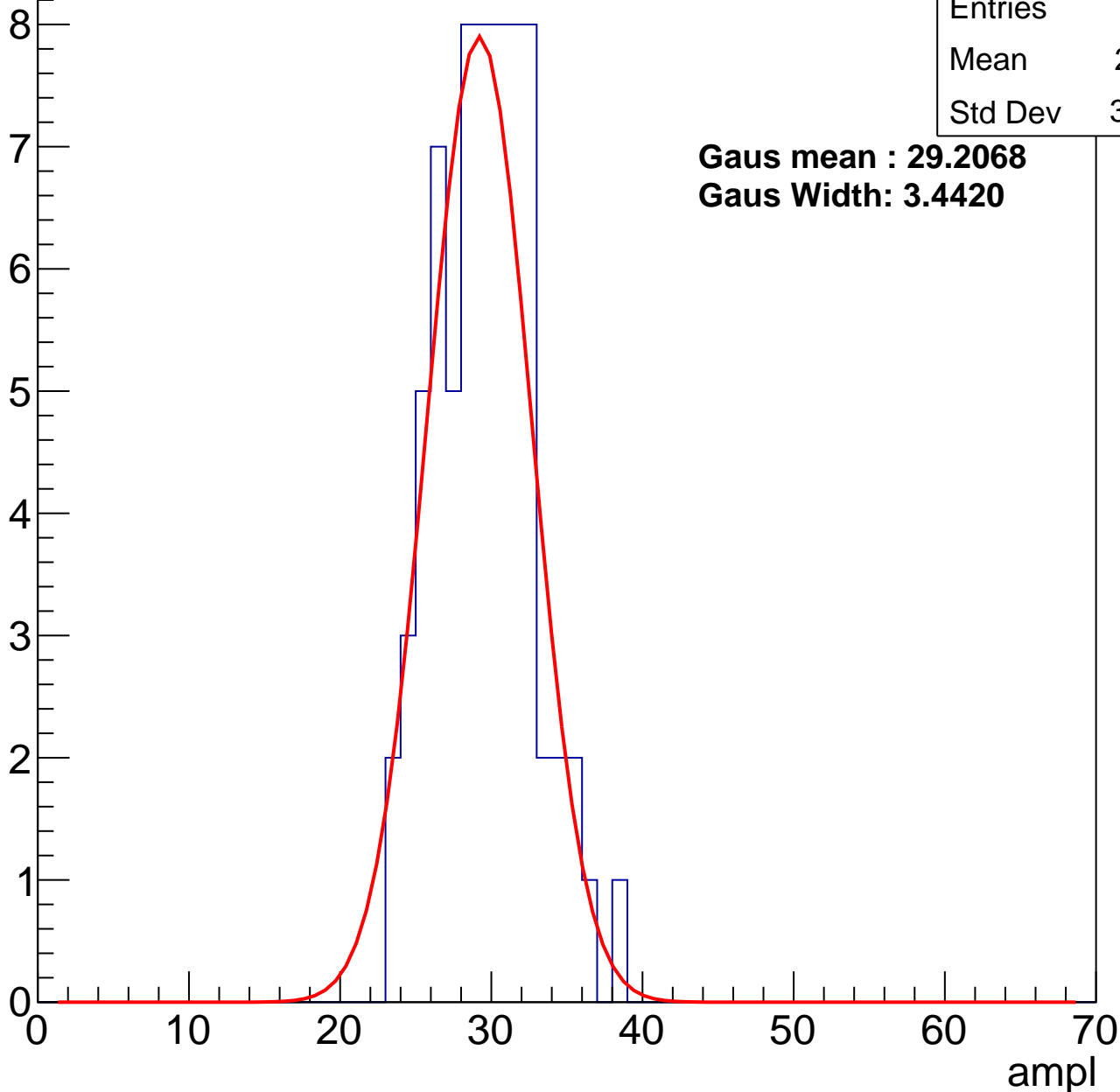
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.11
Std Dev	3.192

**Gaus mean : 29.2068**

**Gaus Width: 3.4420**



# B0L000S, U10-ch31, adc1

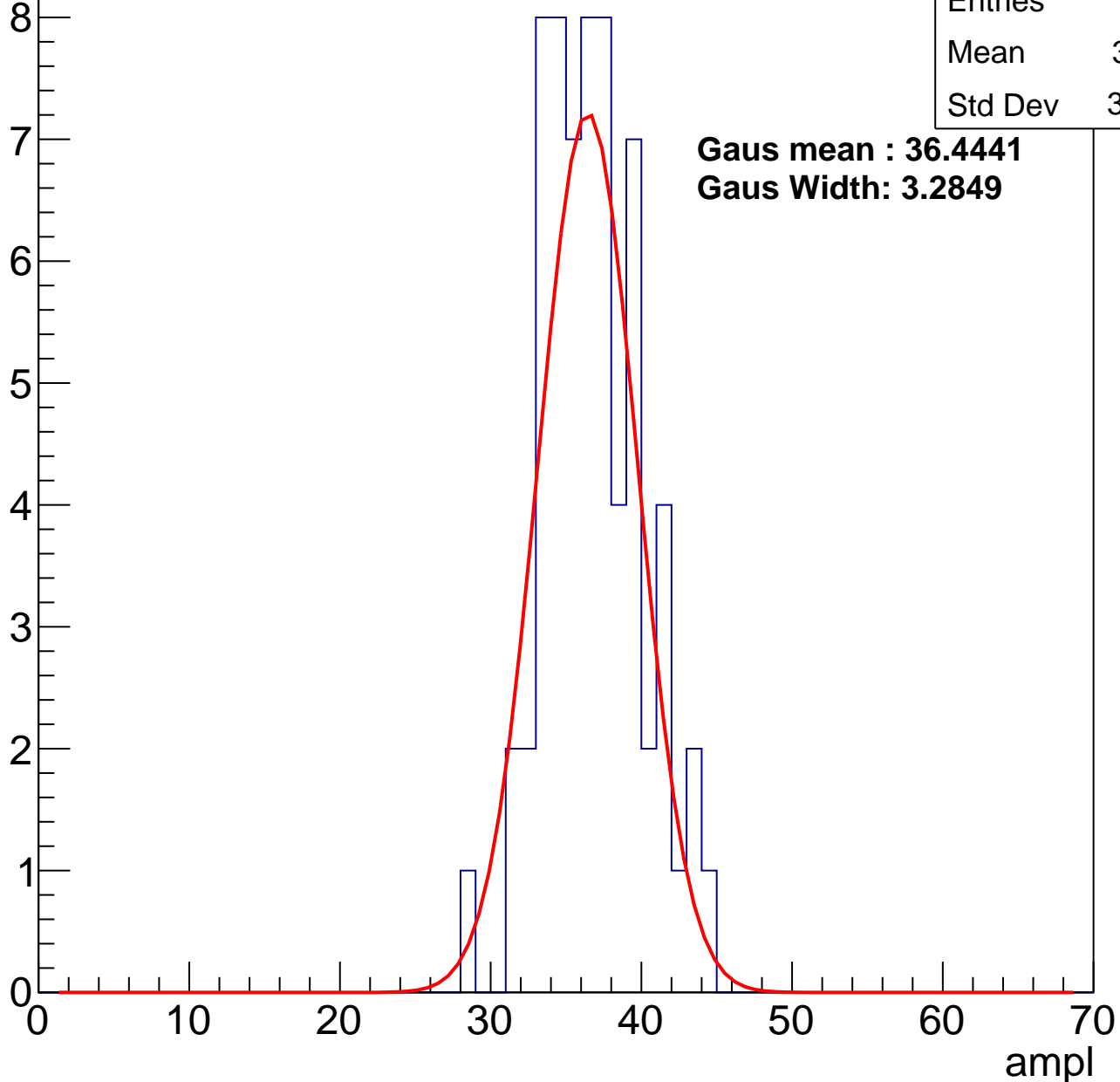
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.31
Std Dev	3.205

**Gaus mean : 36.4441**

**Gaus Width: 3.2849**



# B0L000S, U10-ch31, adc2

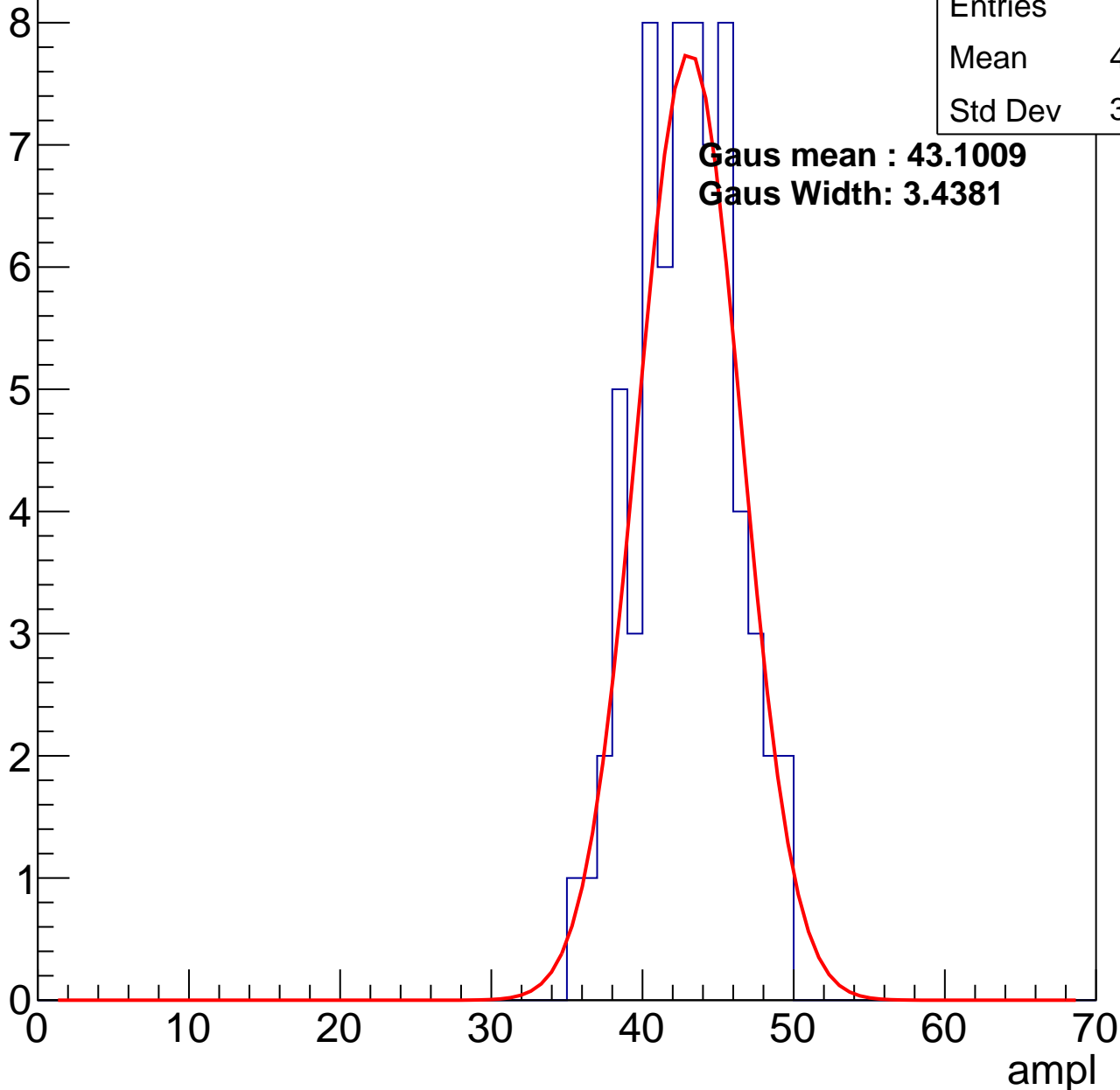
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.43
Std Dev	3.173

**Gaus mean : 43.1009**

**Gaus Width: 3.4381**

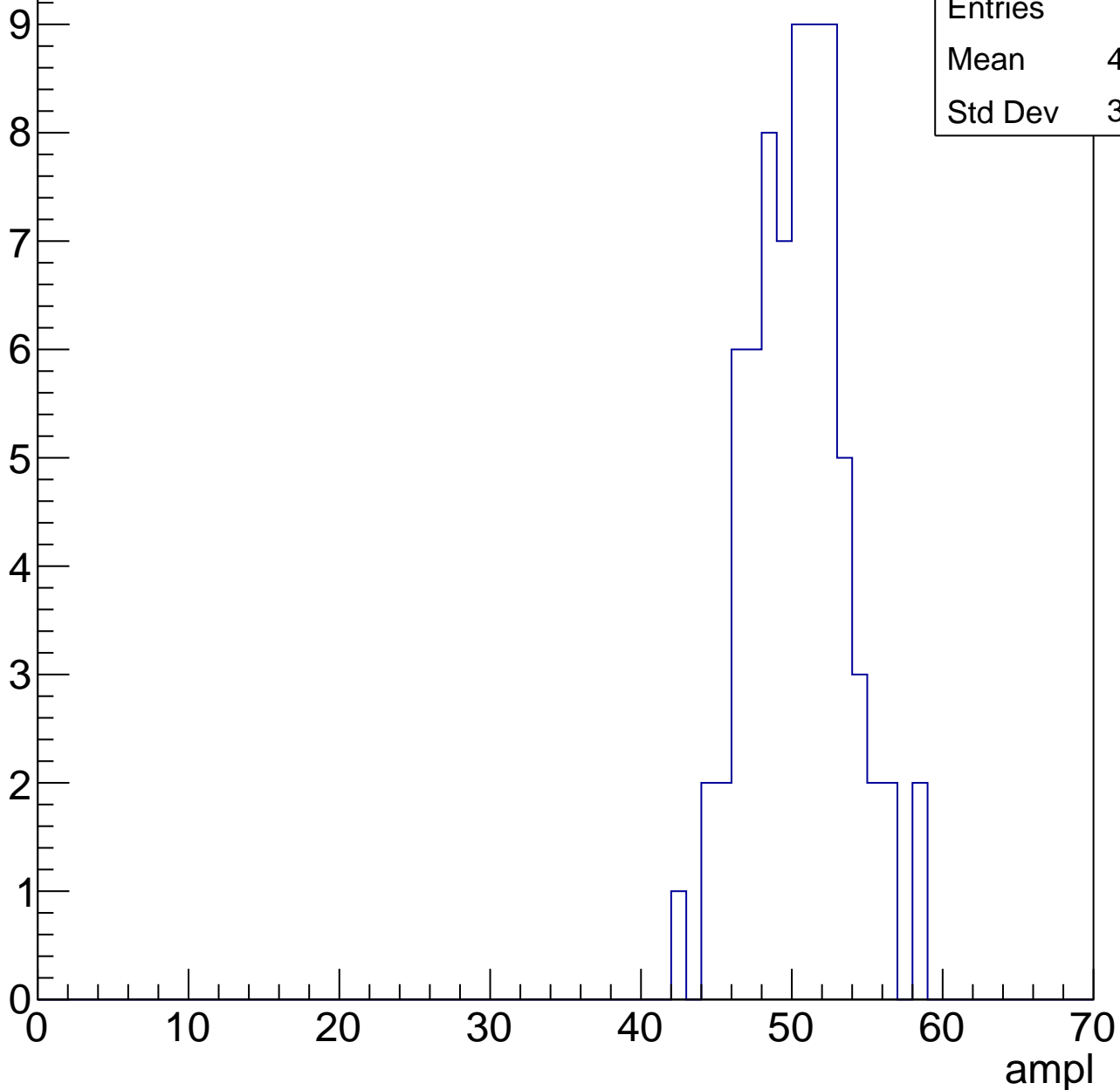


# B0L000S, U10-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.96
Std Dev	3.233

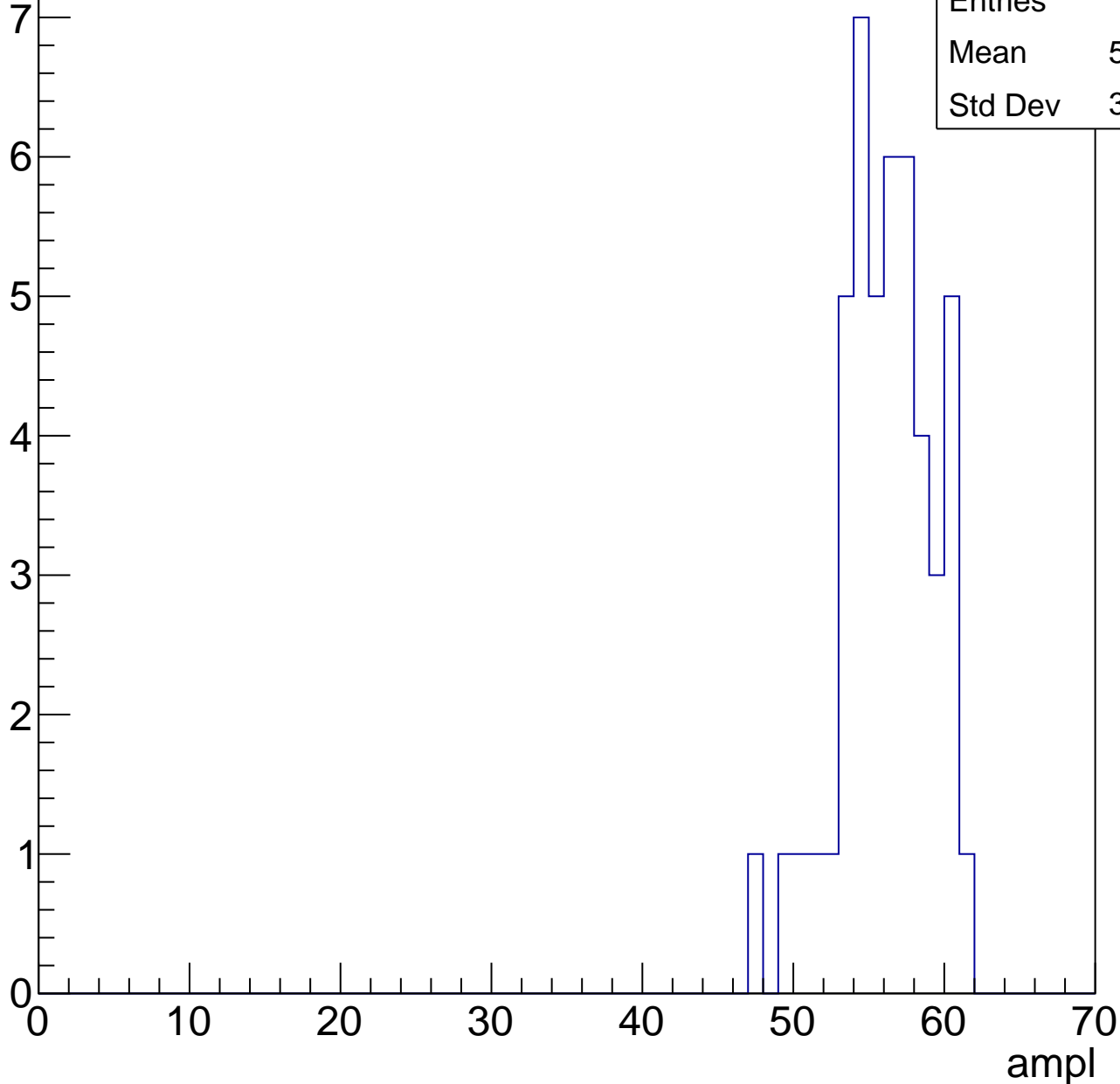


# B0L000S, U10-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	55.64
Std Dev	3.035

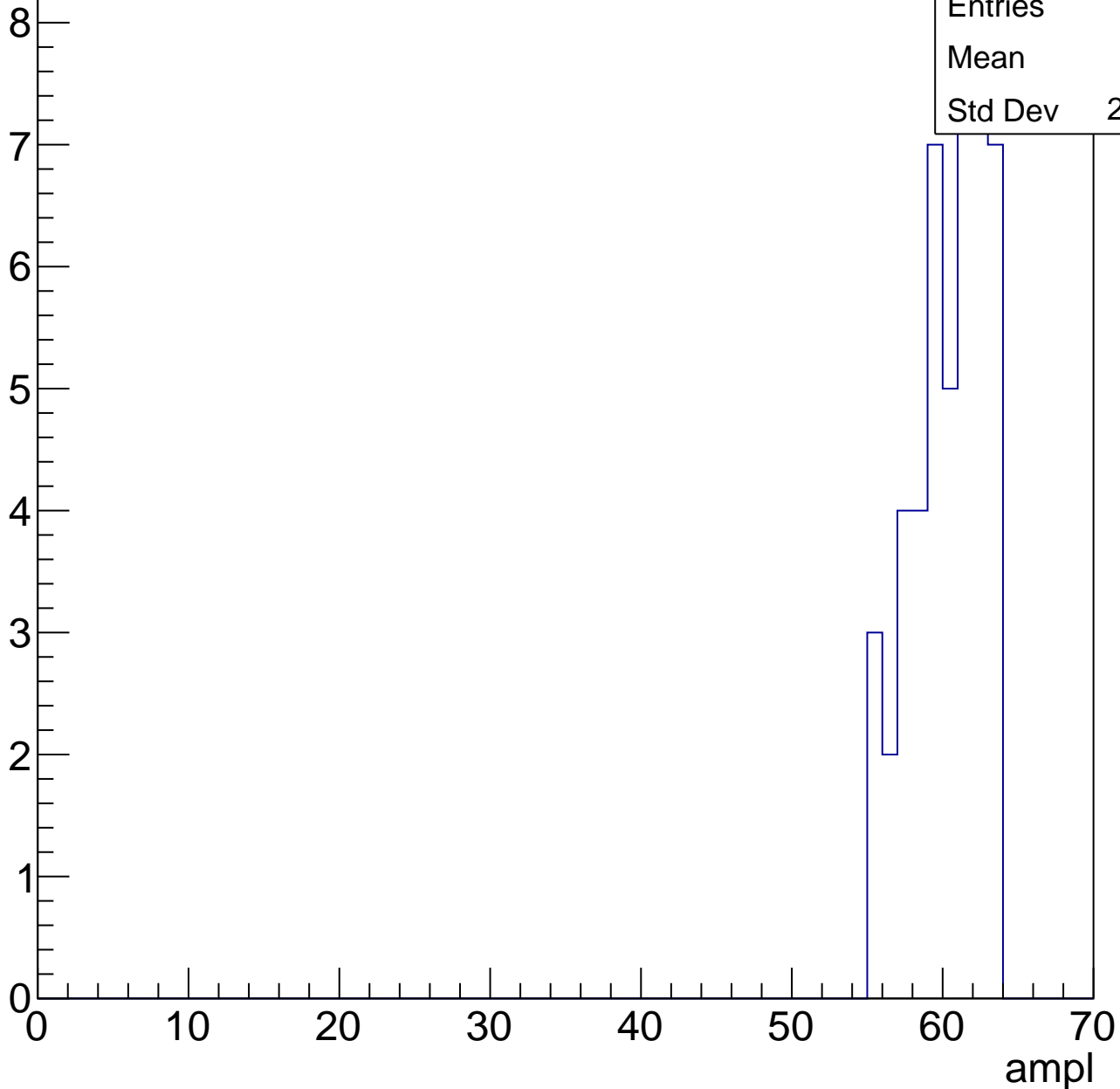


# B0L000S, U10-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	59.9
Std Dev	2.365



# B0L000S, U10-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

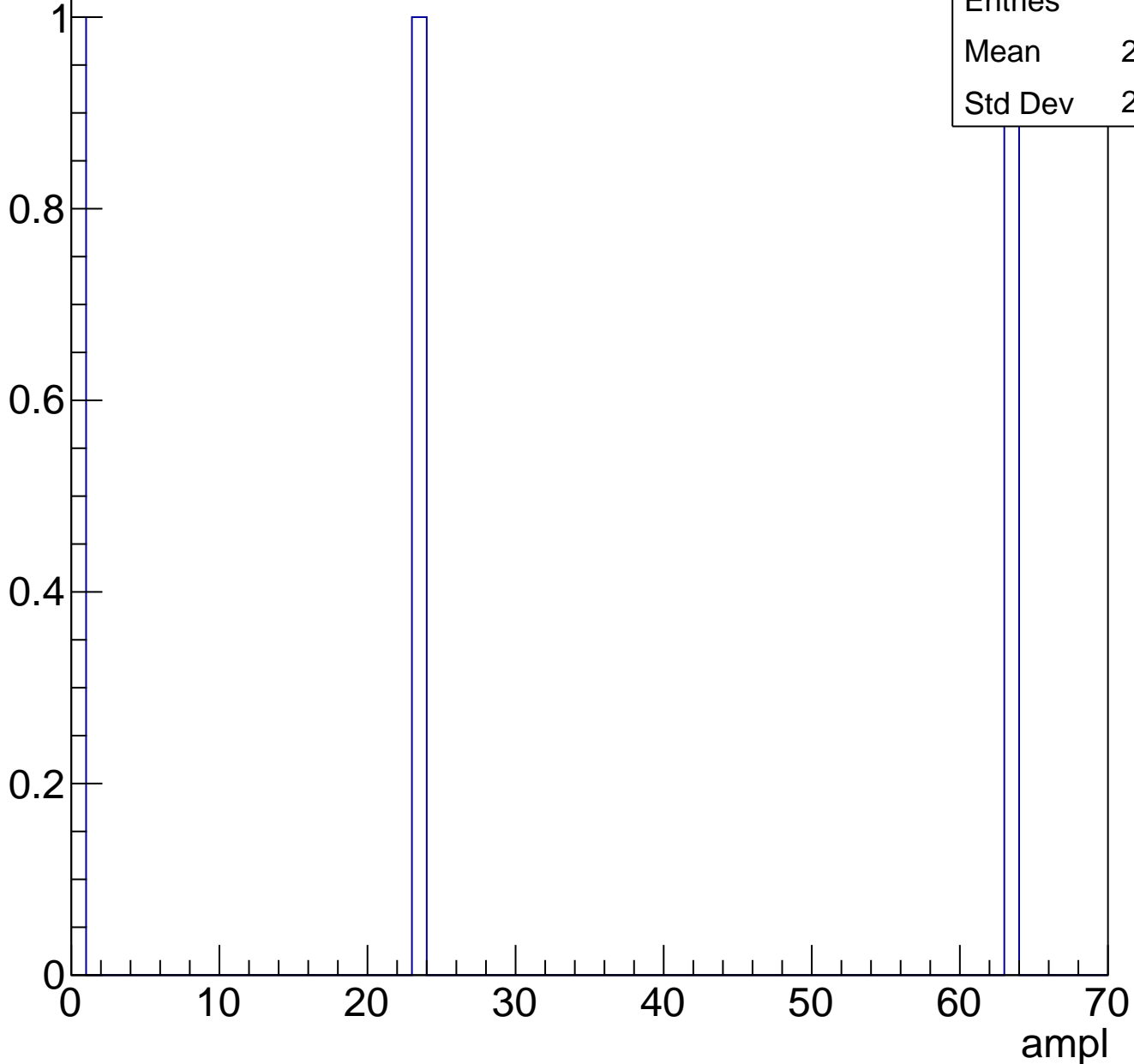




# B0L000S, U10-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B0L000S, U10-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.42
Std Dev	5.031

**Gaus mean : 30.5634**

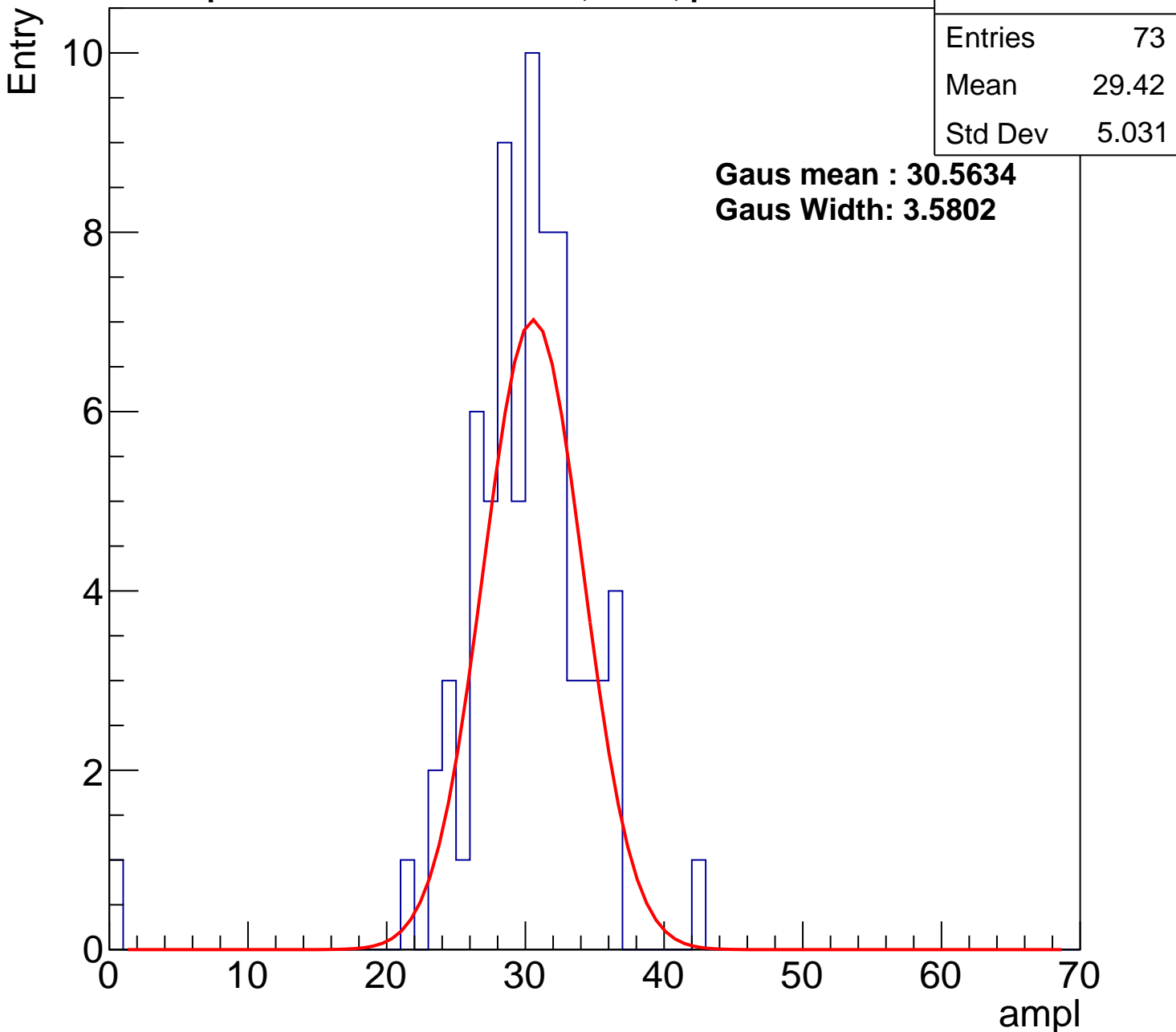
**Gaus Width: 3.5802**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch32, adc1

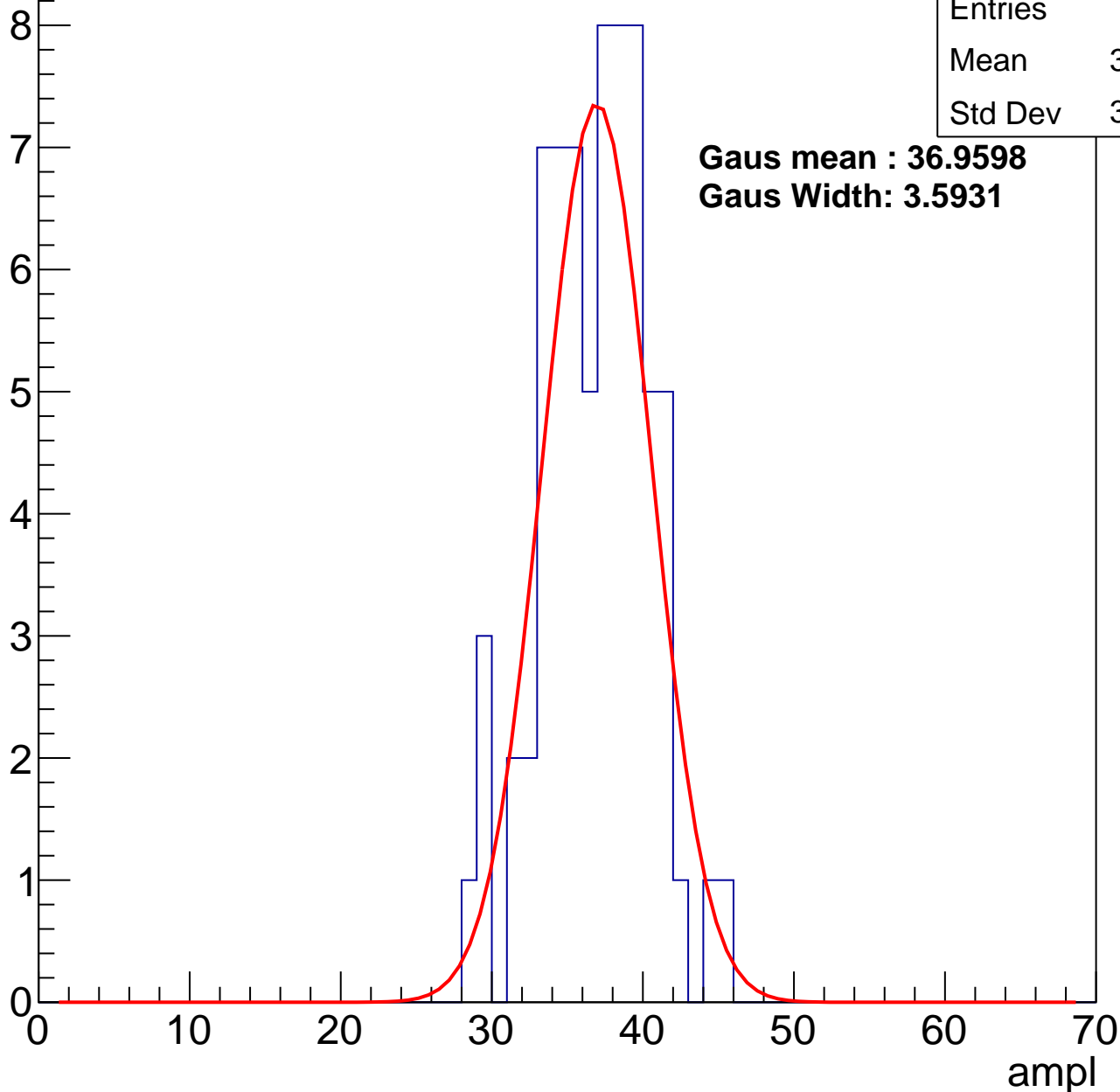
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.38
Std Dev	3.518

**Gaus mean : 36.9598**

**Gaus Width: 3.5931**



# B0L000S, U10-ch32, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	43.06
Std Dev	2.884

**Gaus mean : 44.0759**

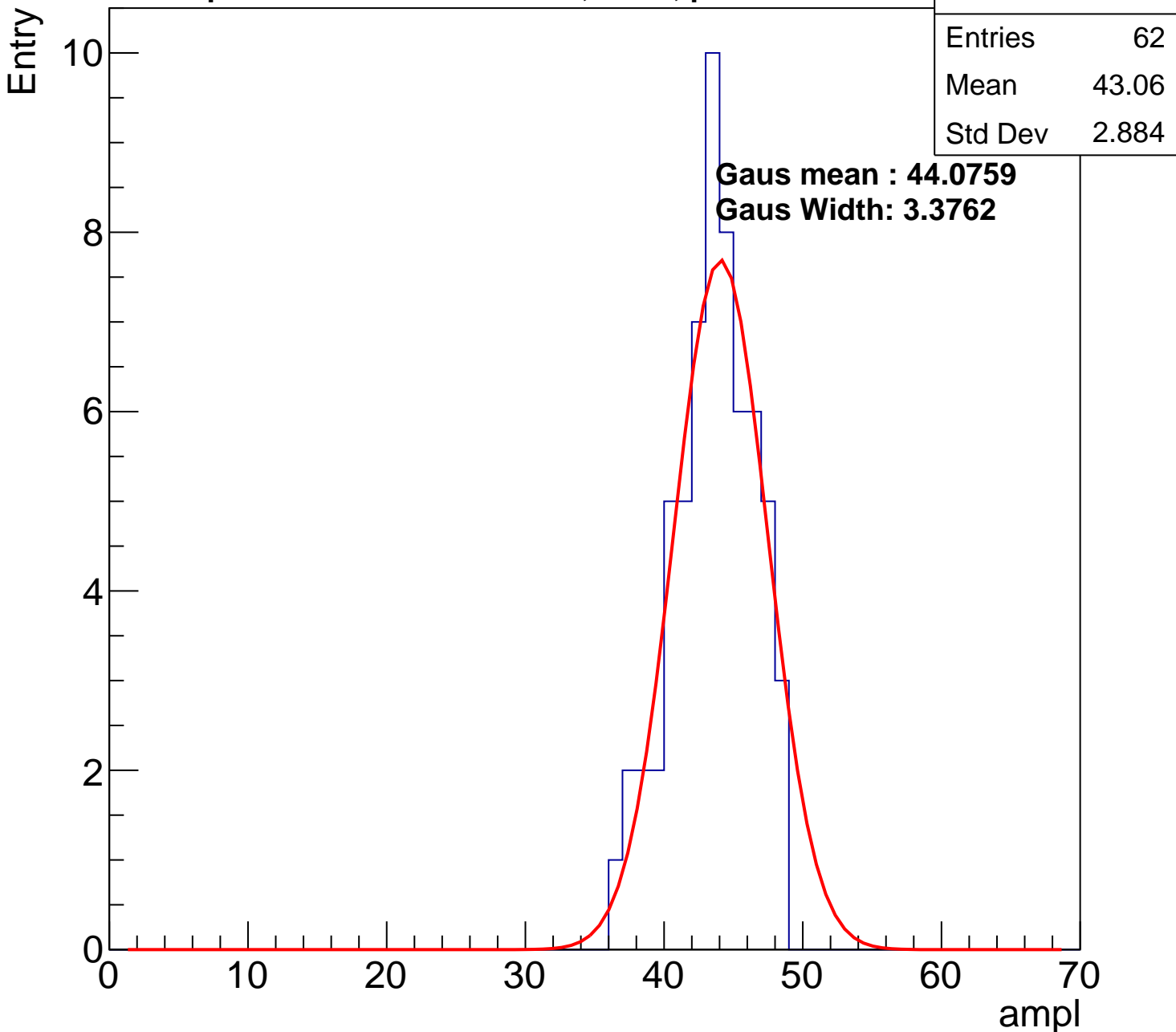
**Gaus Width: 3.3762**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

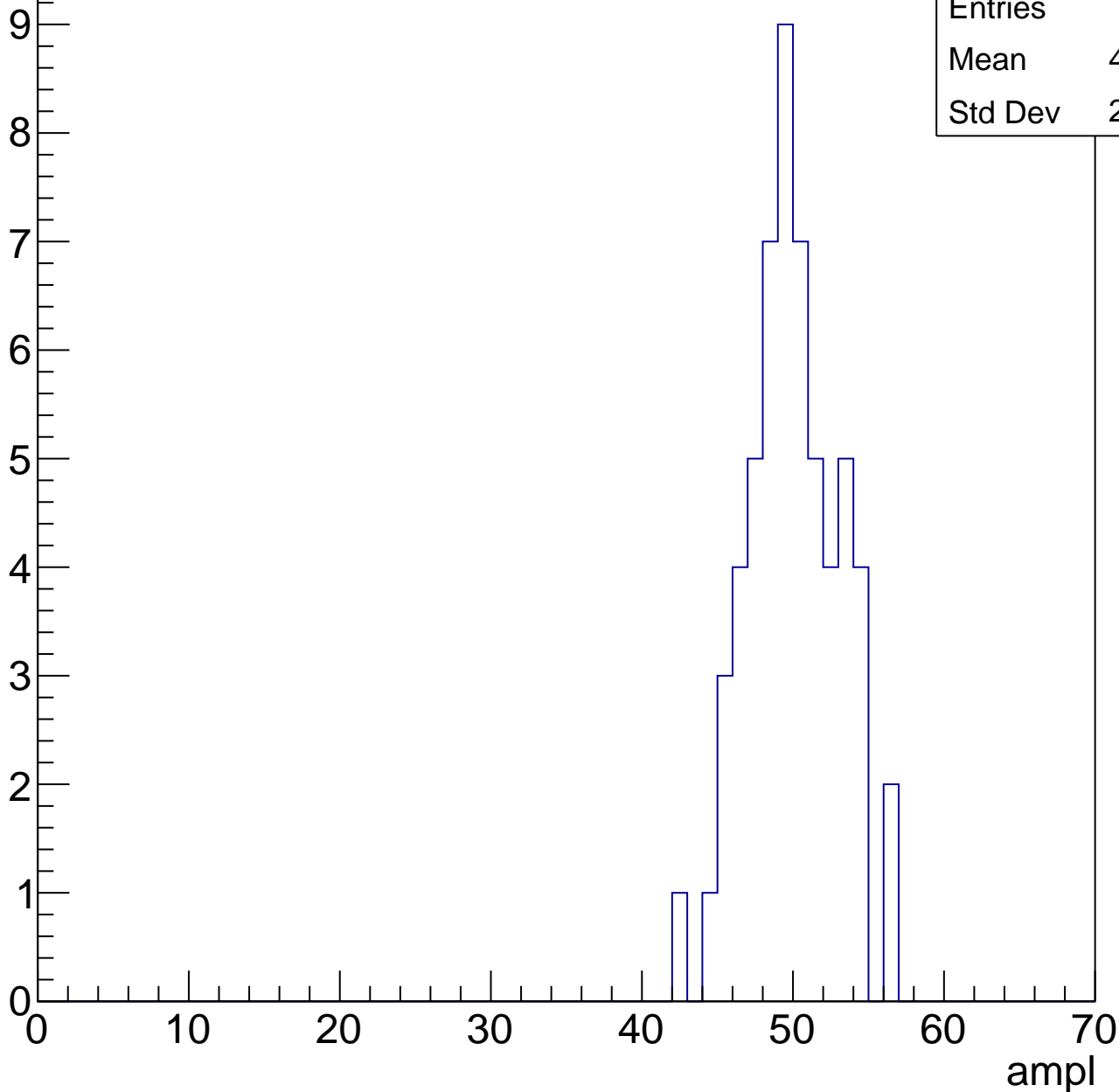


# B0L000S, U10-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.53
Std Dev	2.992



# B0L000S, U10-ch32, adc4

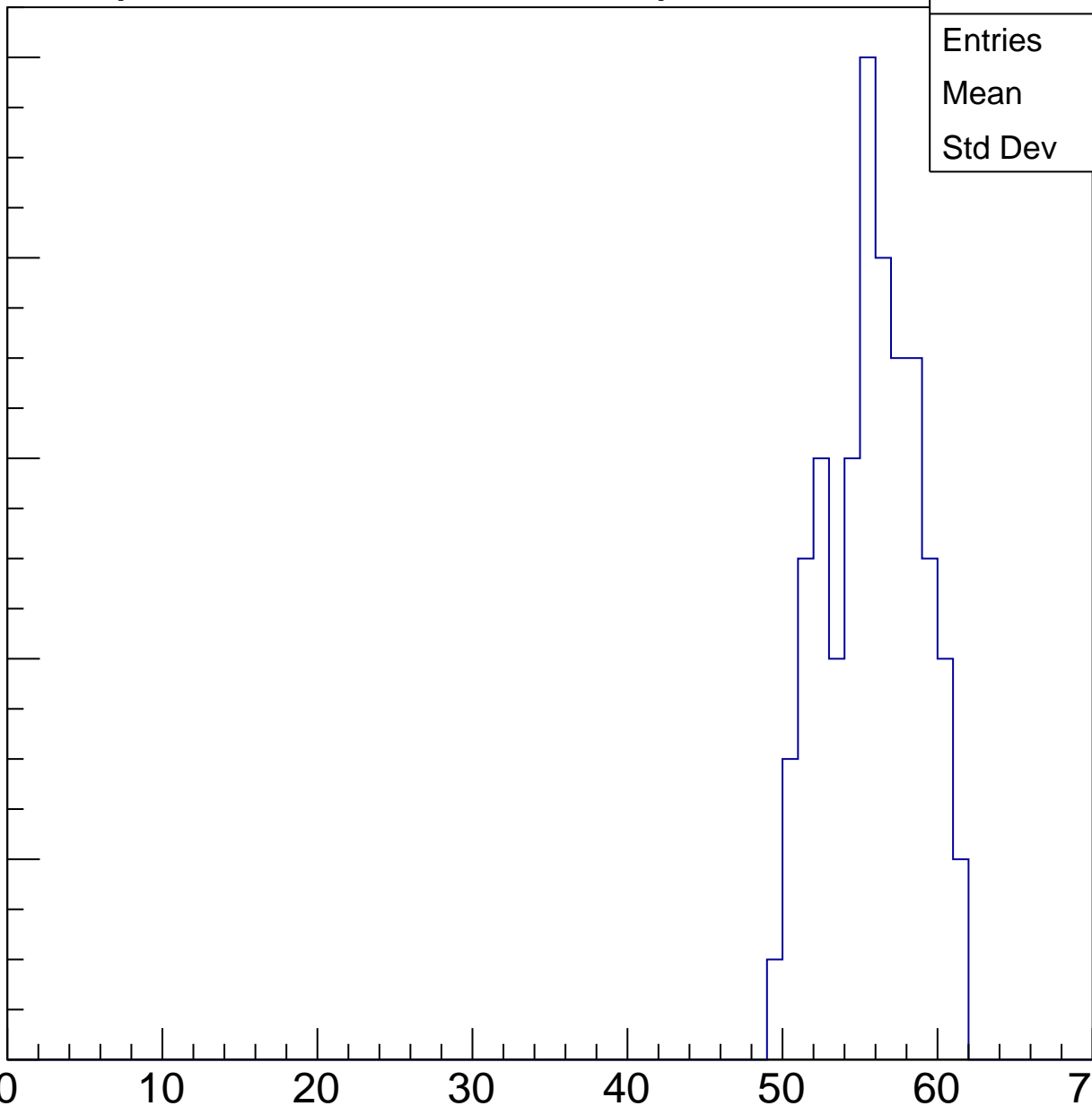
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10  
8  
6  
4  
2  
0

Entries	68
Mean	55.32
Std Dev	2.997

ampl

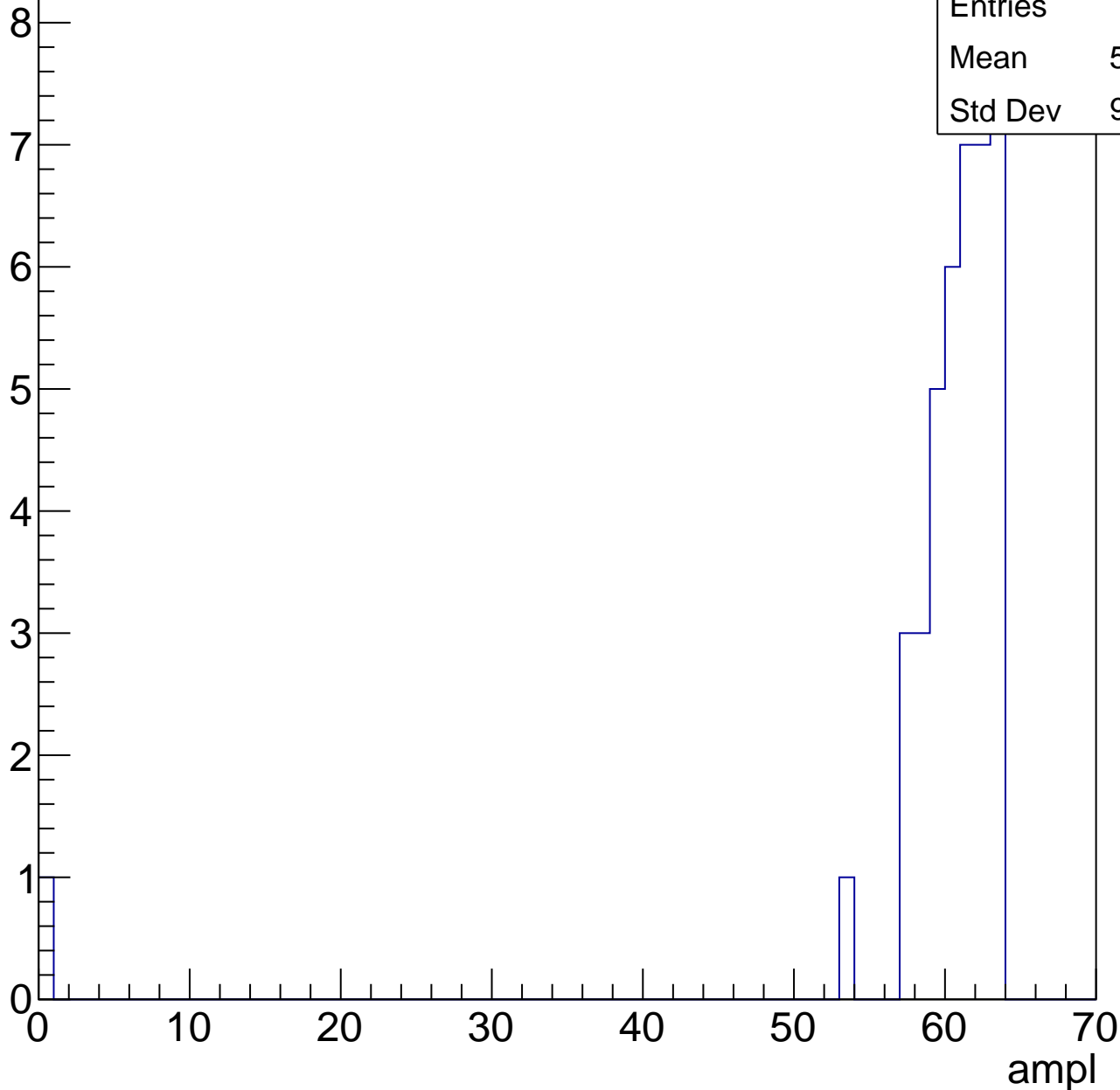


# B0L000S, U10-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.98
Std Dev	9.573



# B0L000S, U10-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch33, adc0

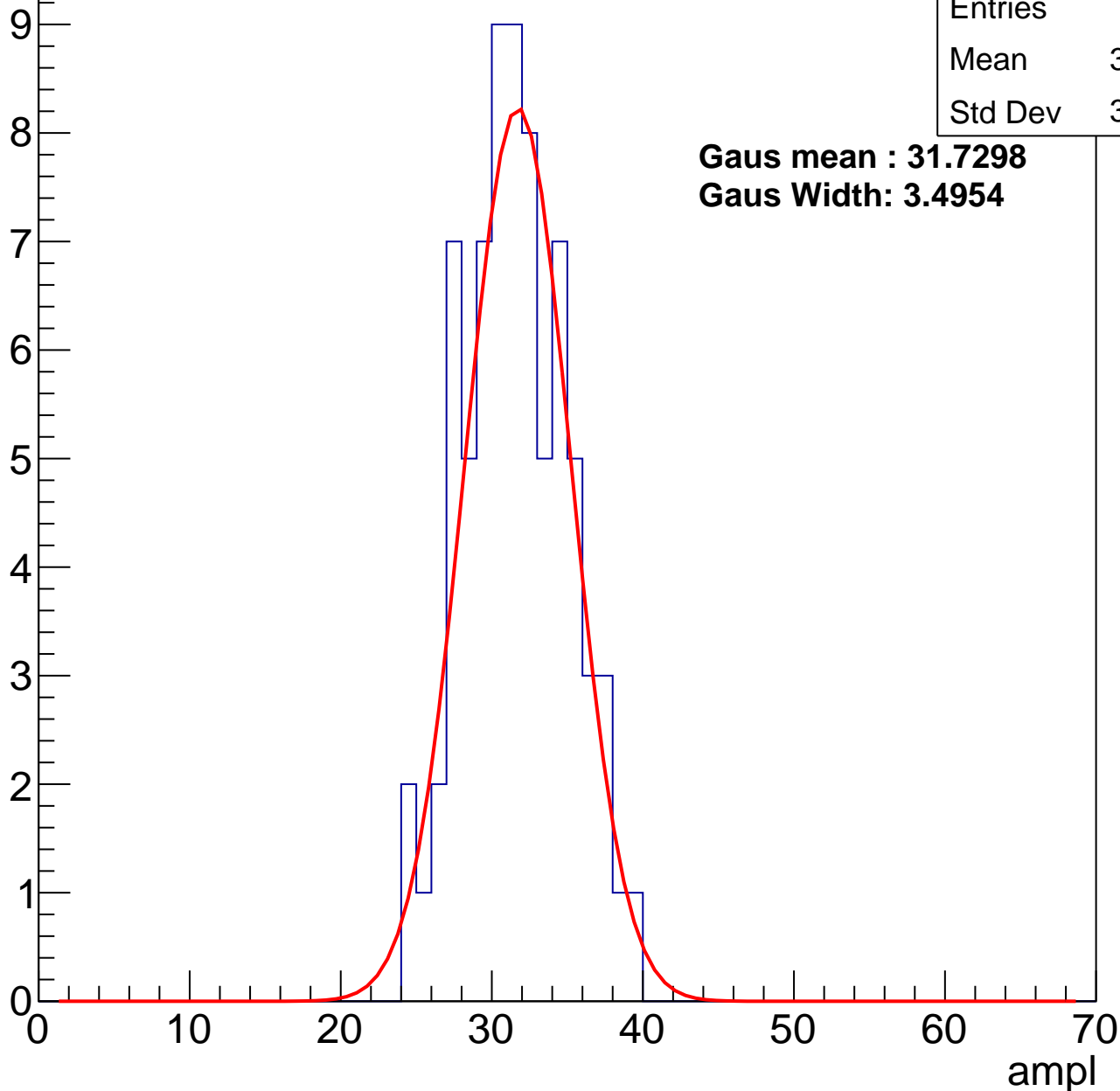
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	31.15
Std Dev	3.353

**Gaus mean : 31.7298**

**Gaus Width: 3.4954**



# B0L000S, U10-ch33, adc1

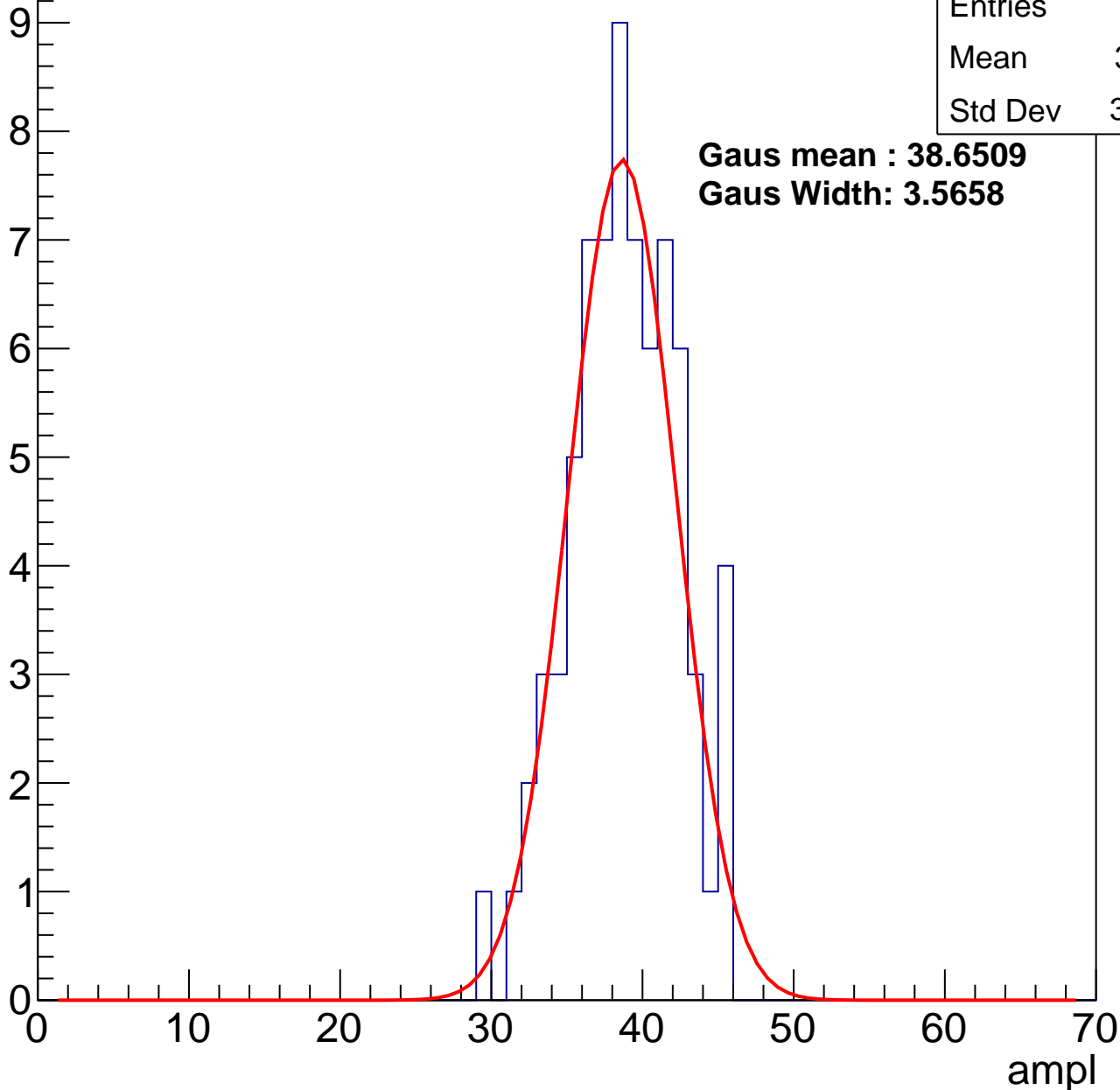
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	38.31
Std Dev	3.526

**Gaus mean : 38.6509**

**Gaus Width: 3.5658**



# B0L000S, U10-ch33, adc2

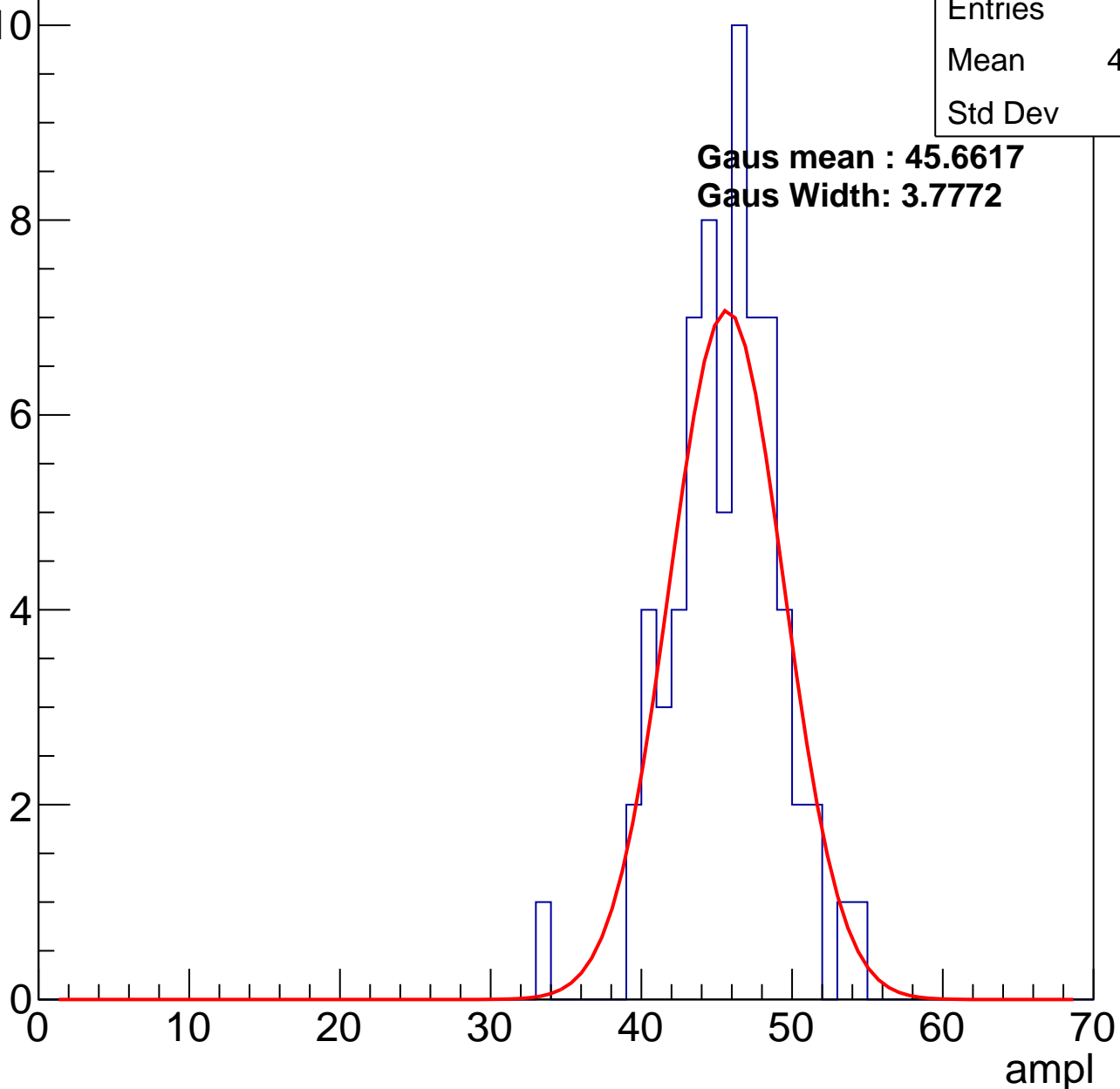
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	45.15
Std Dev	3.57

Entry

**Gaus mean : 45.6617**

**Gaus Width: 3.7772**

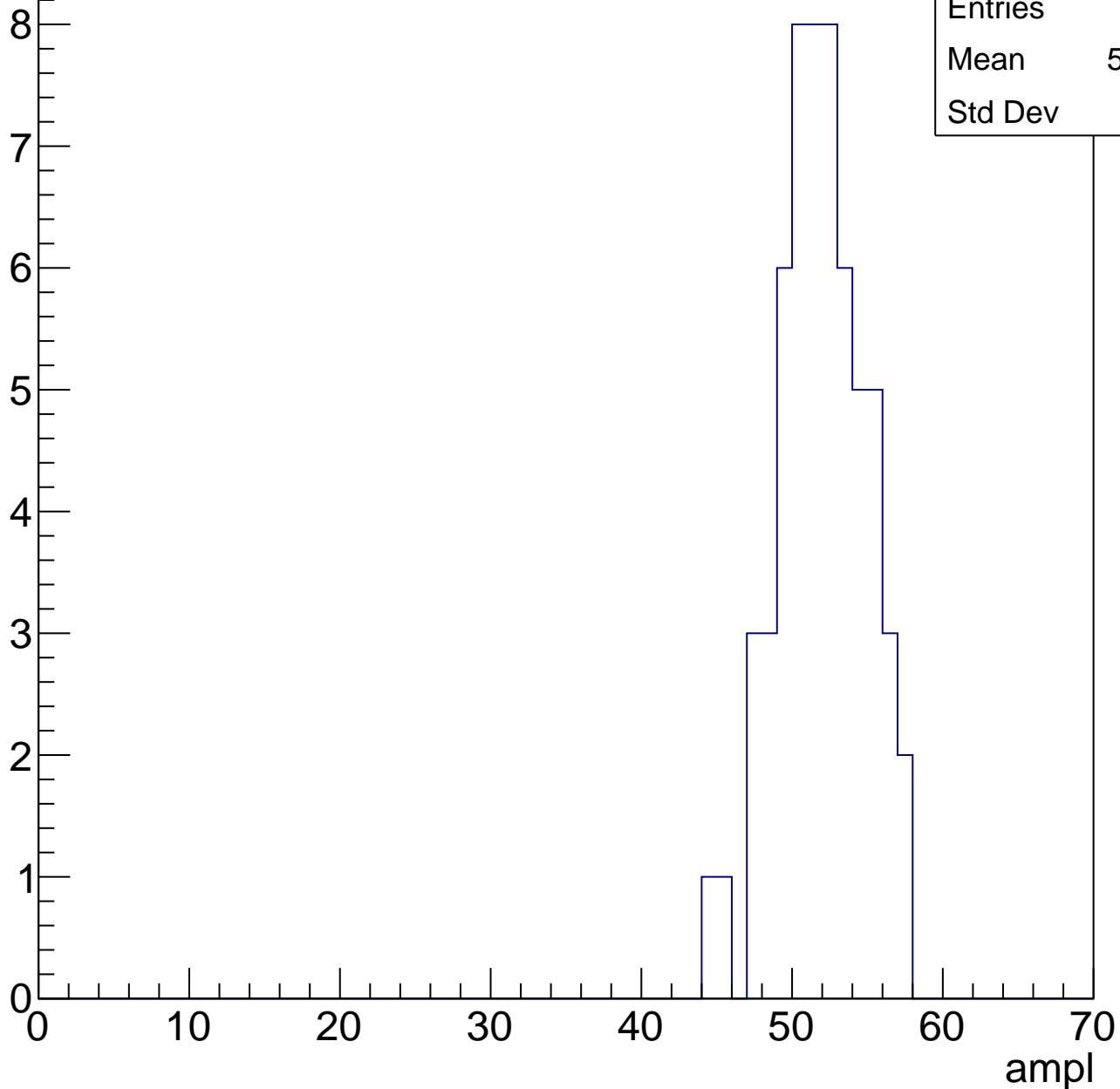


# B0L000S, U10-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

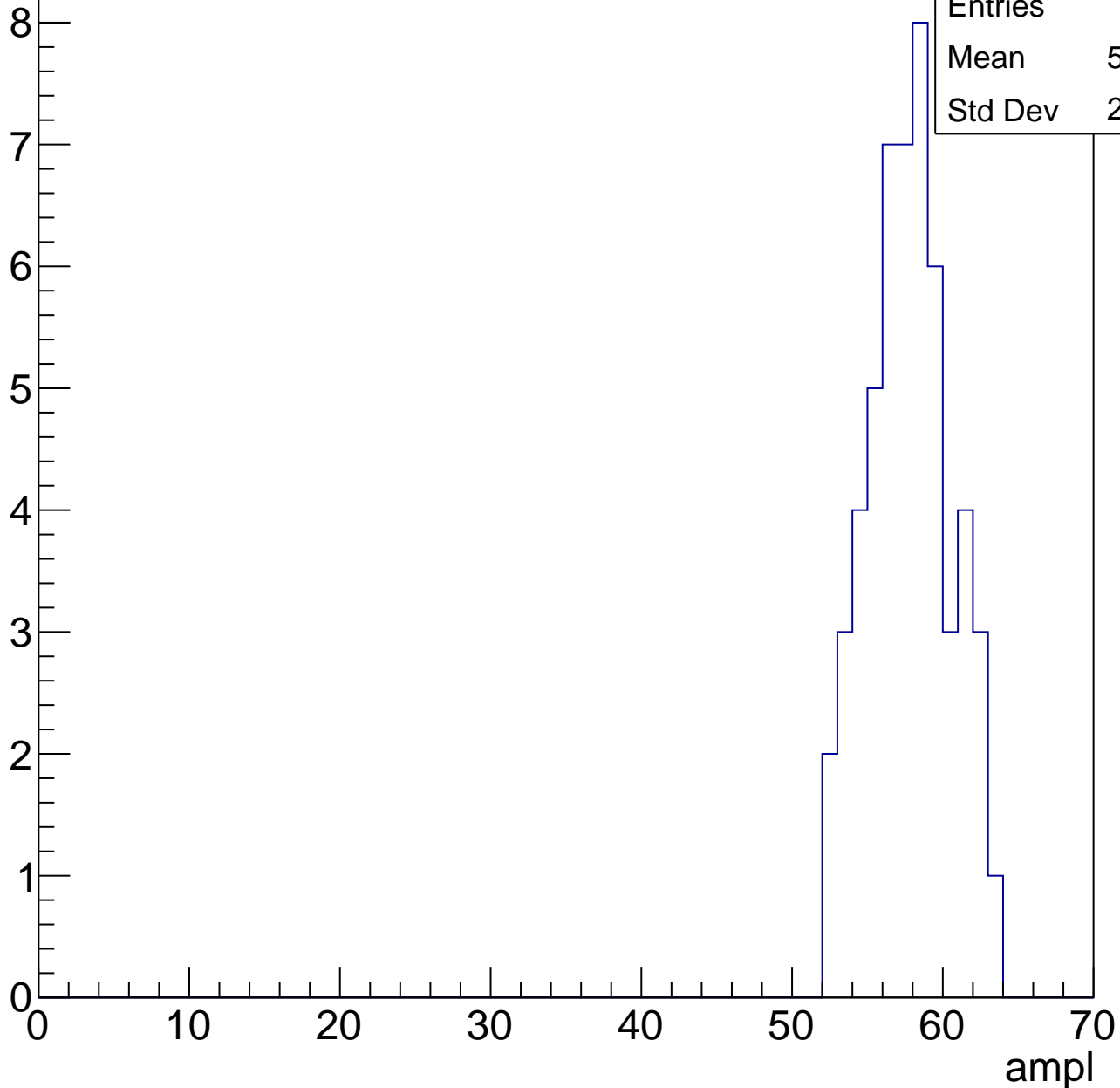
Entries	59
Mean	51.47
Std Dev	2.86



# B0L000S, U10-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



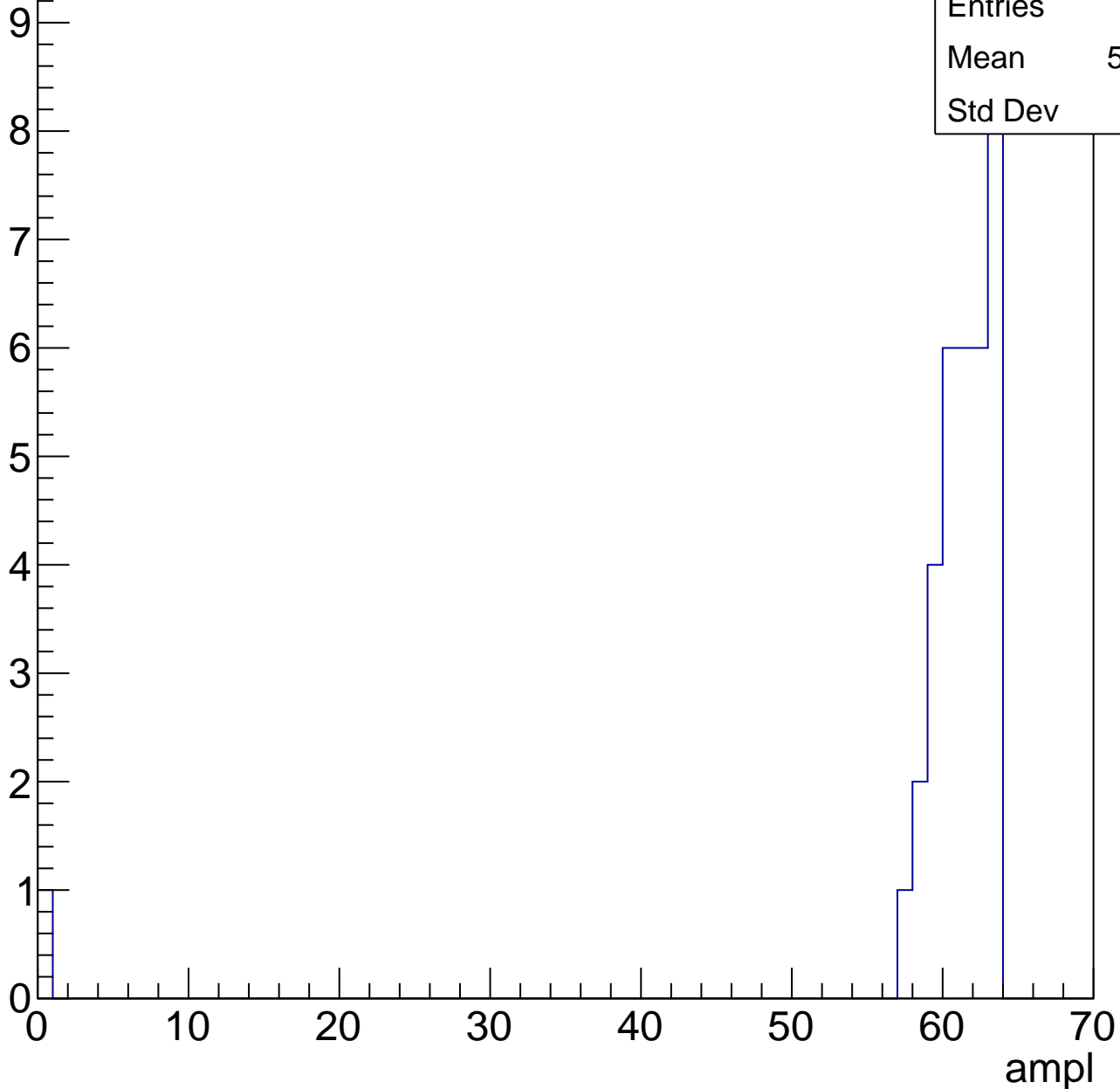
Entries	53
Mean	57.28
Std Dev	2.715

# B0L000S, U10-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	59.26
Std Dev	10.3



# B0L000S, U10-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12
Std Dev	12

# B0L000S, U10-ch34, adc0

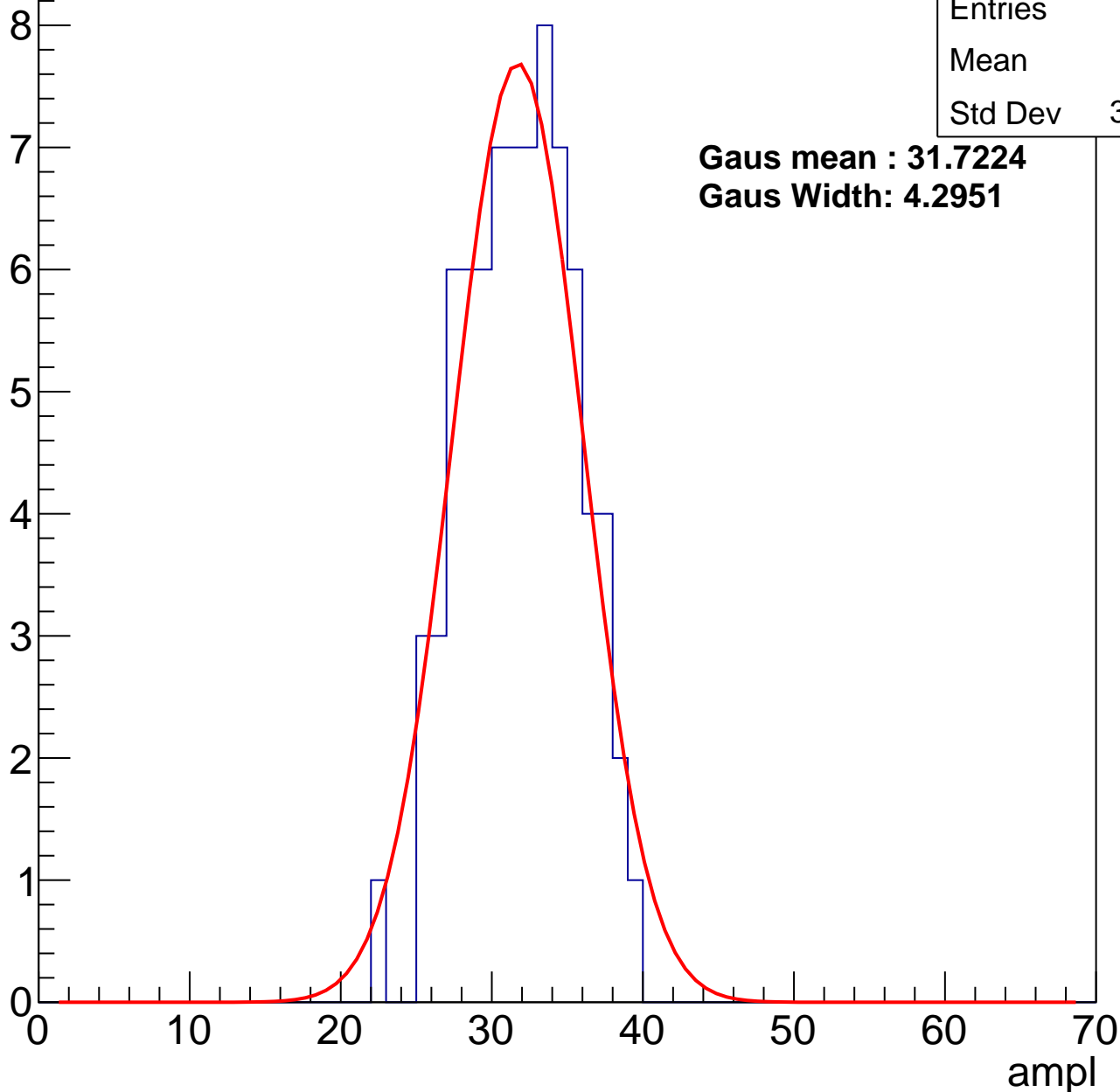
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	31.4
Std Dev	3.646

**Gaus mean : 31.7224**

**Gaus Width: 4.2951**



# B0L000S, U10-ch34, adc1

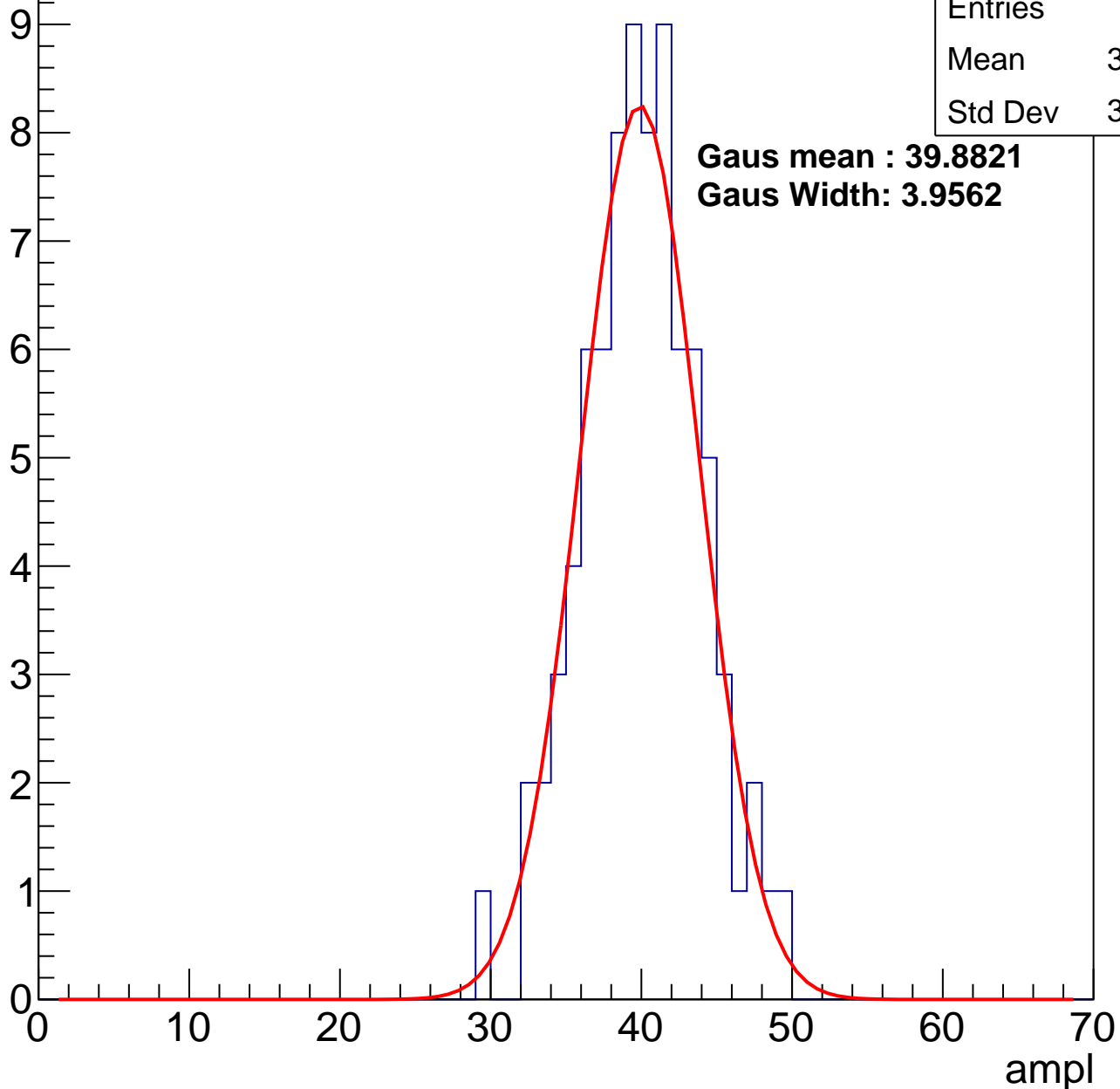
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	39.58
Std Dev	3.884

**Gaus mean : 39.8821**

**Gaus Width: 3.9562**



# B0L000S, U10-ch34, adc2

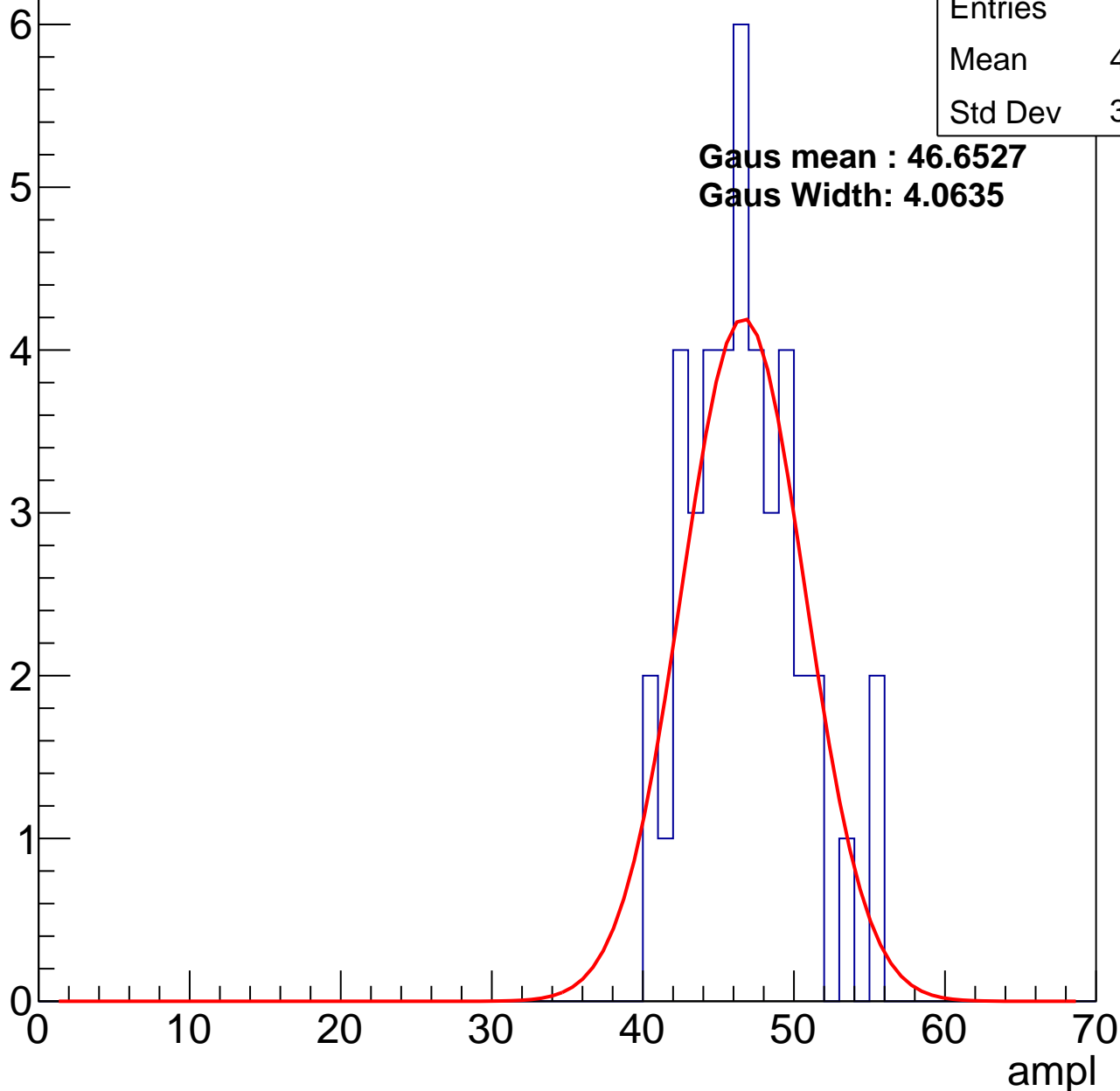
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	46.26
Std Dev	3.619

**Gaus mean : 46.6527**

**Gaus Width: 4.0635**

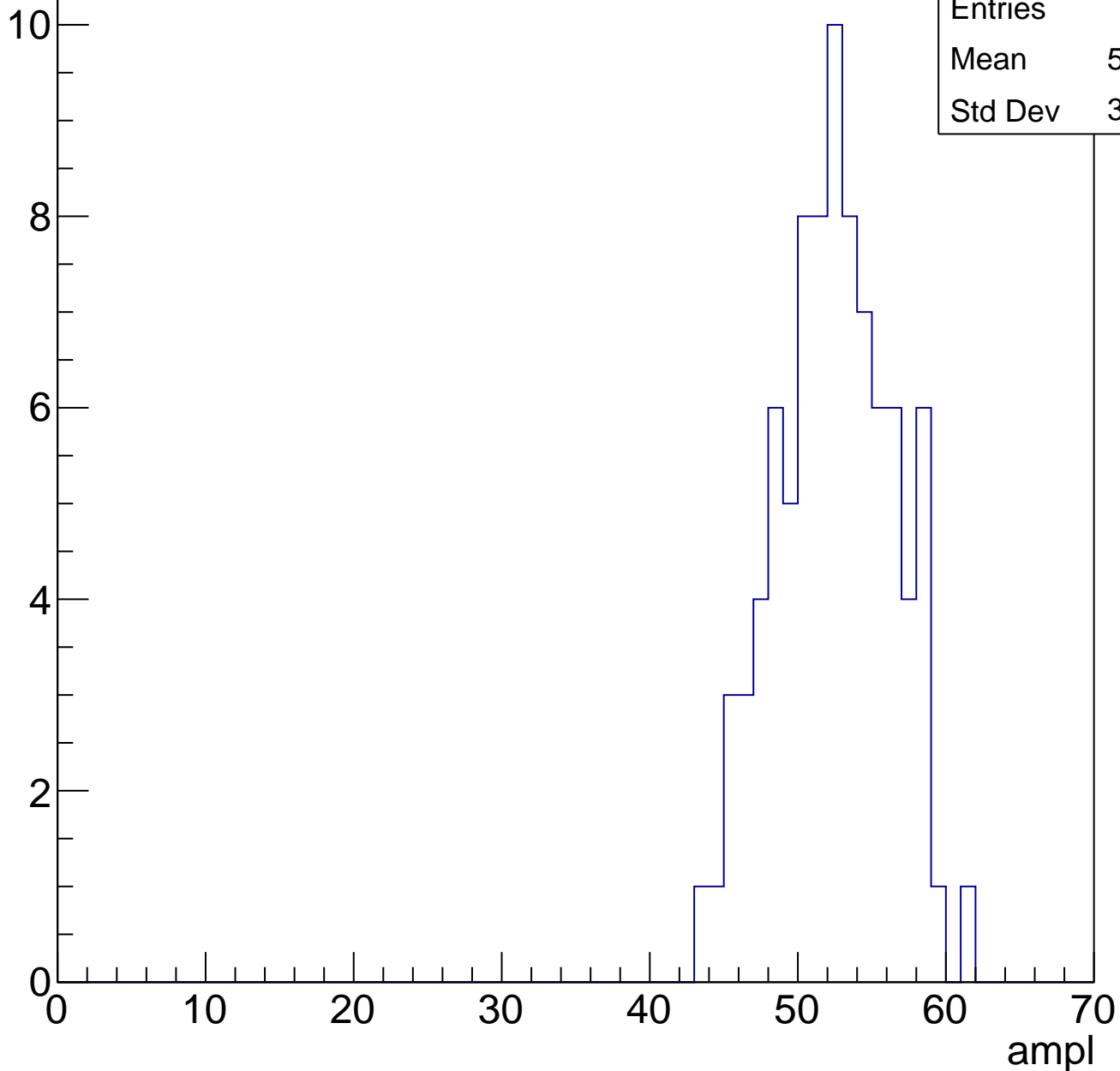


# B0L000S, U10-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	51.97
Std Dev	3.863

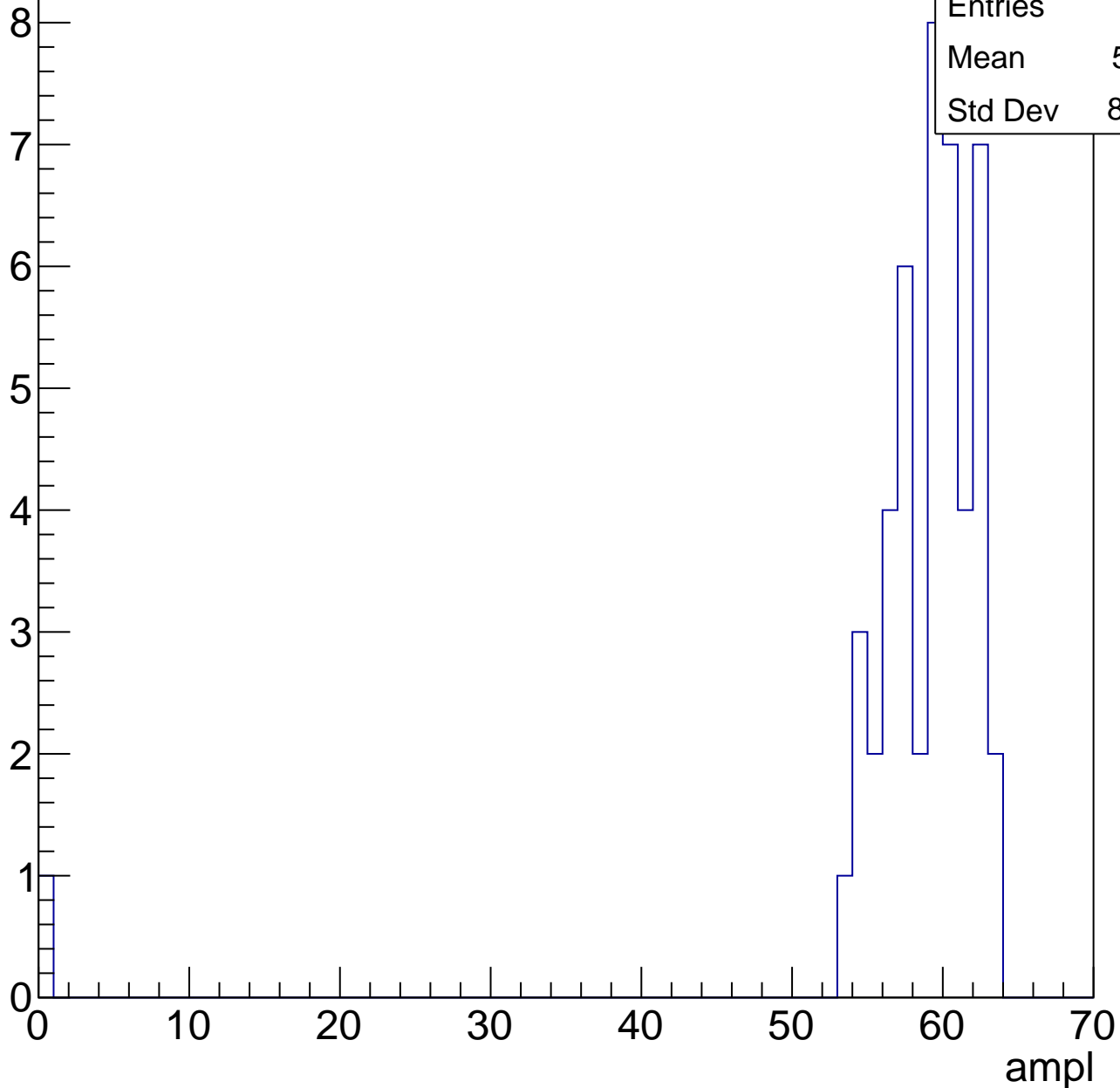
Entry



# B0L000S, U10-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

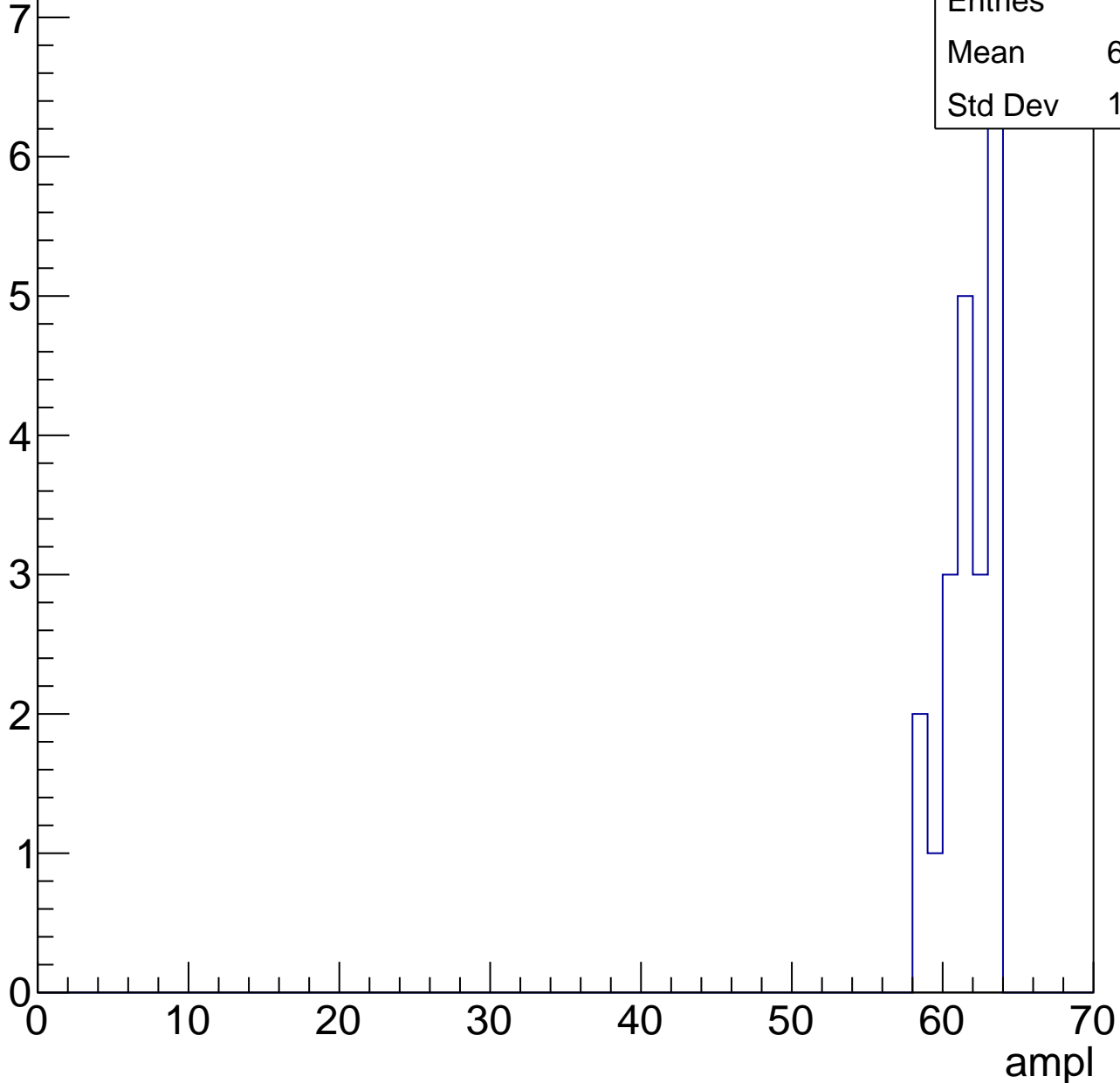


# B0L000S, U10-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	61.29
Std Dev	1.608



# B0L000S, U10-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch35, adc0

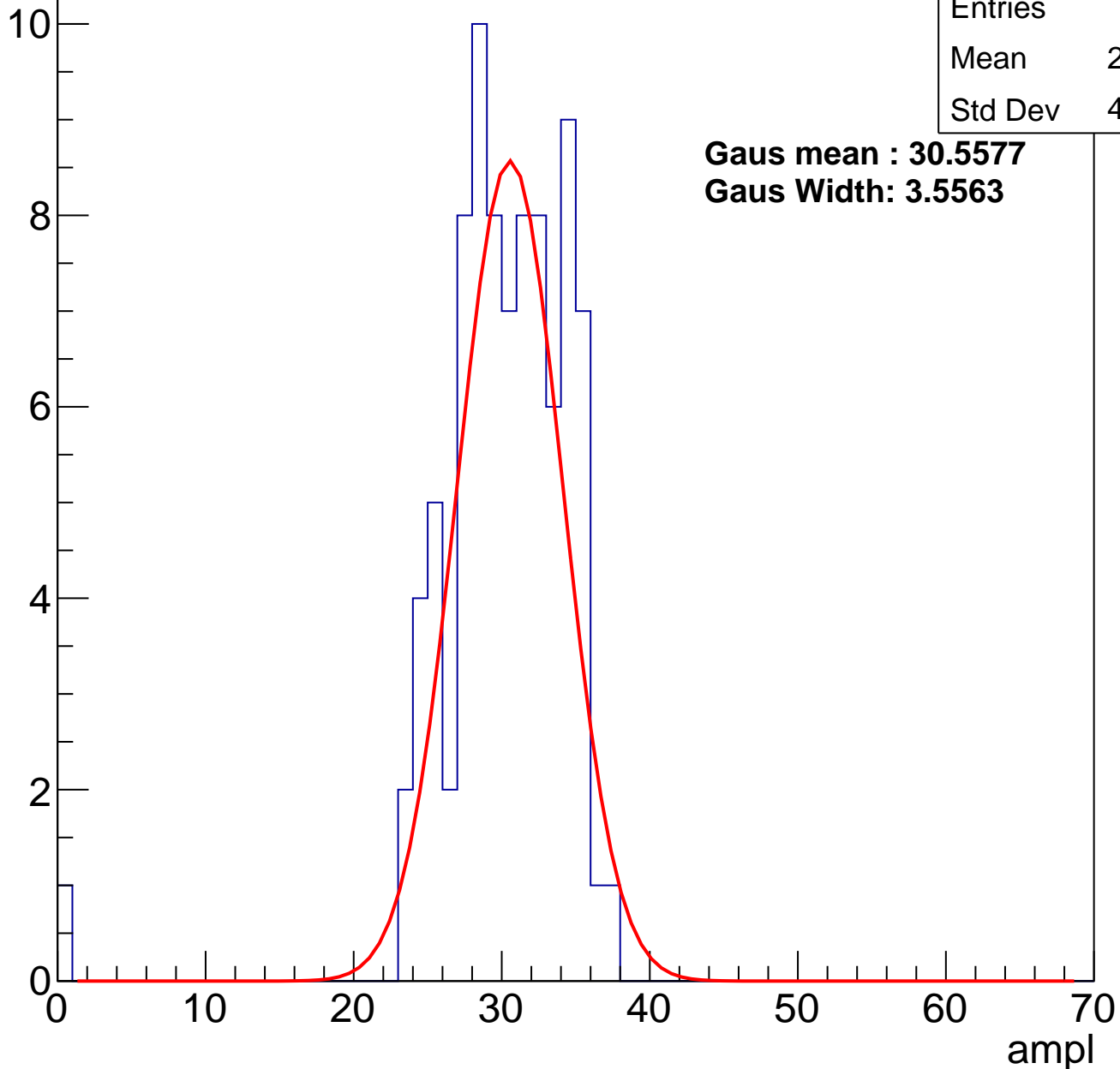
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	29.69
Std Dev	4.684

**Gaus mean : 30.5577**

**Gaus Width: 3.5563**

Entry



# B0L000S, U10-ch35, adc1

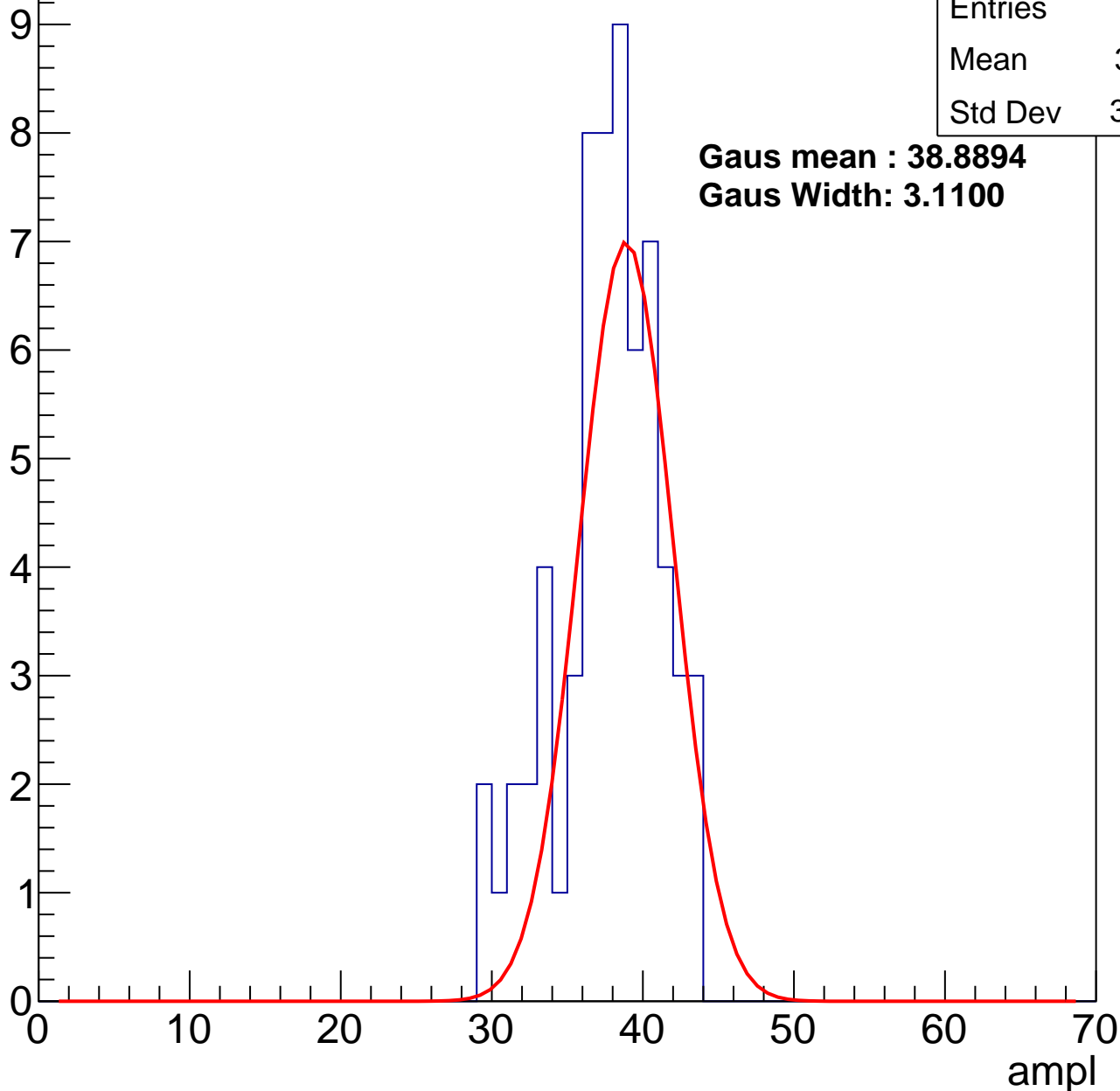
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.21
Std Dev	3.409

**Gaus mean : 38.8894**

**Gaus Width: 3.1100**



# B0L000S, U10-ch35, adc2

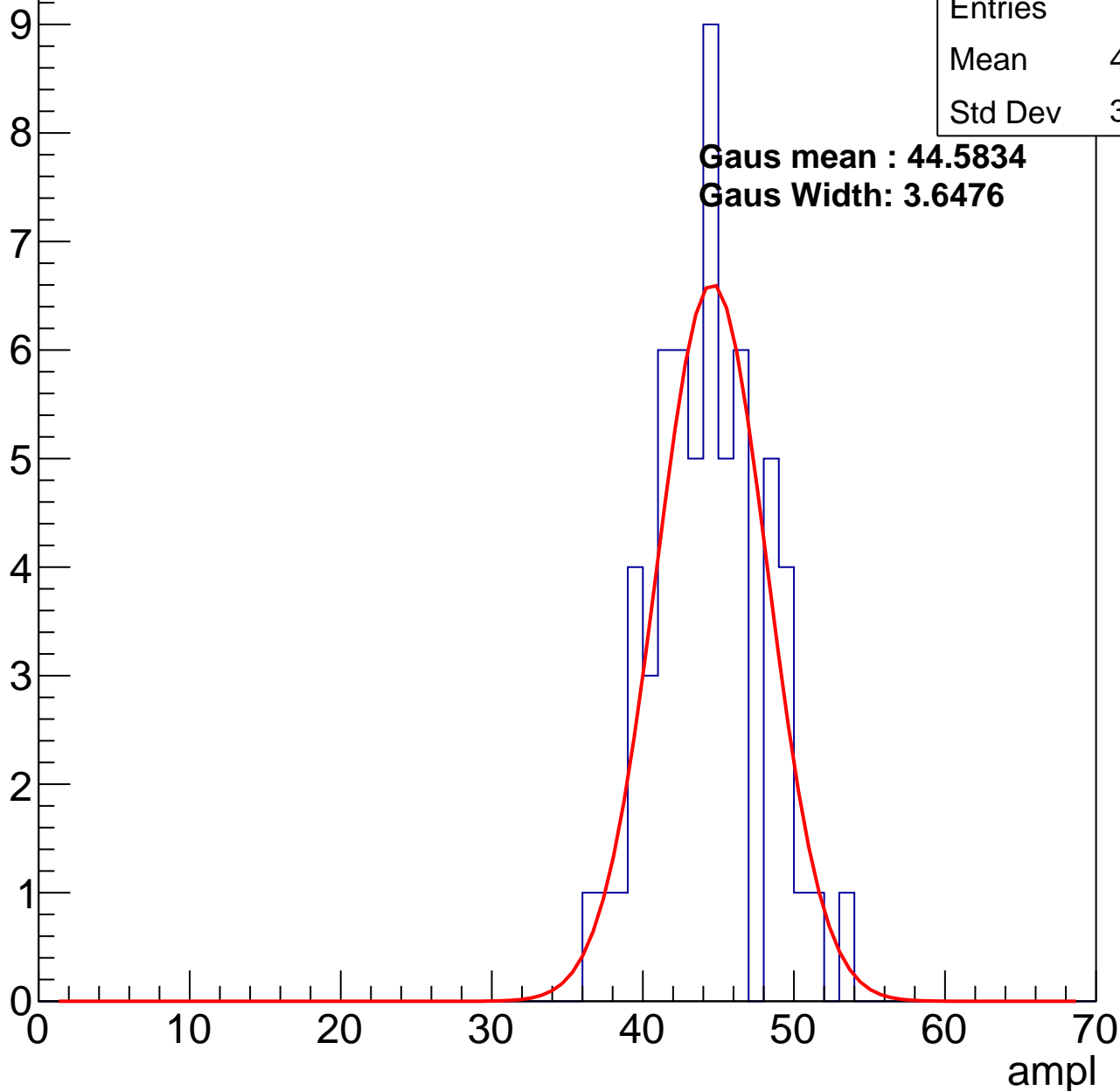
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.85
Std Dev	3.574

**Gaus mean : 44.5834**

**Gaus Width: 3.6476**

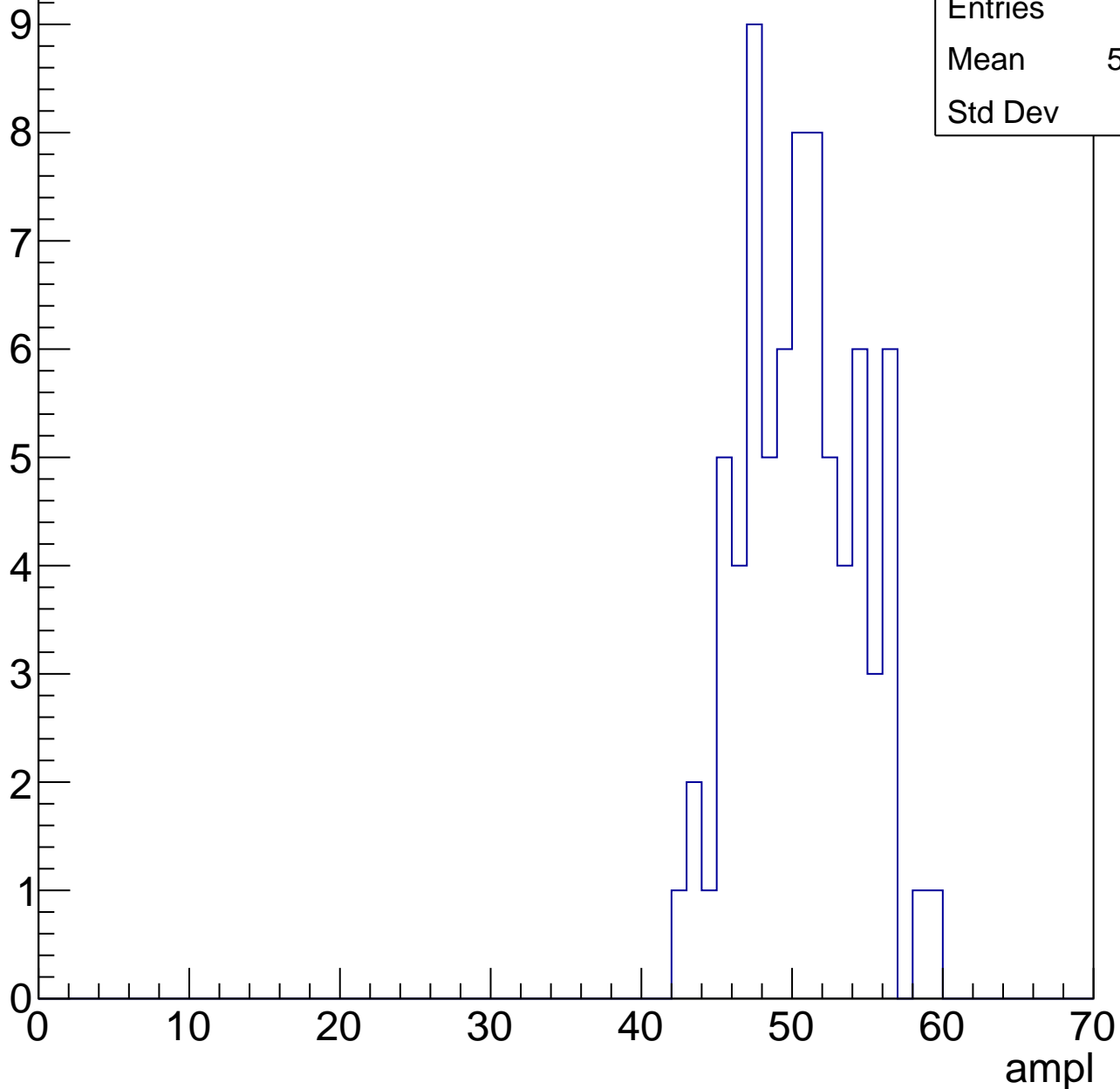


# B0L000S, U10-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	50.13
Std Dev	3.81

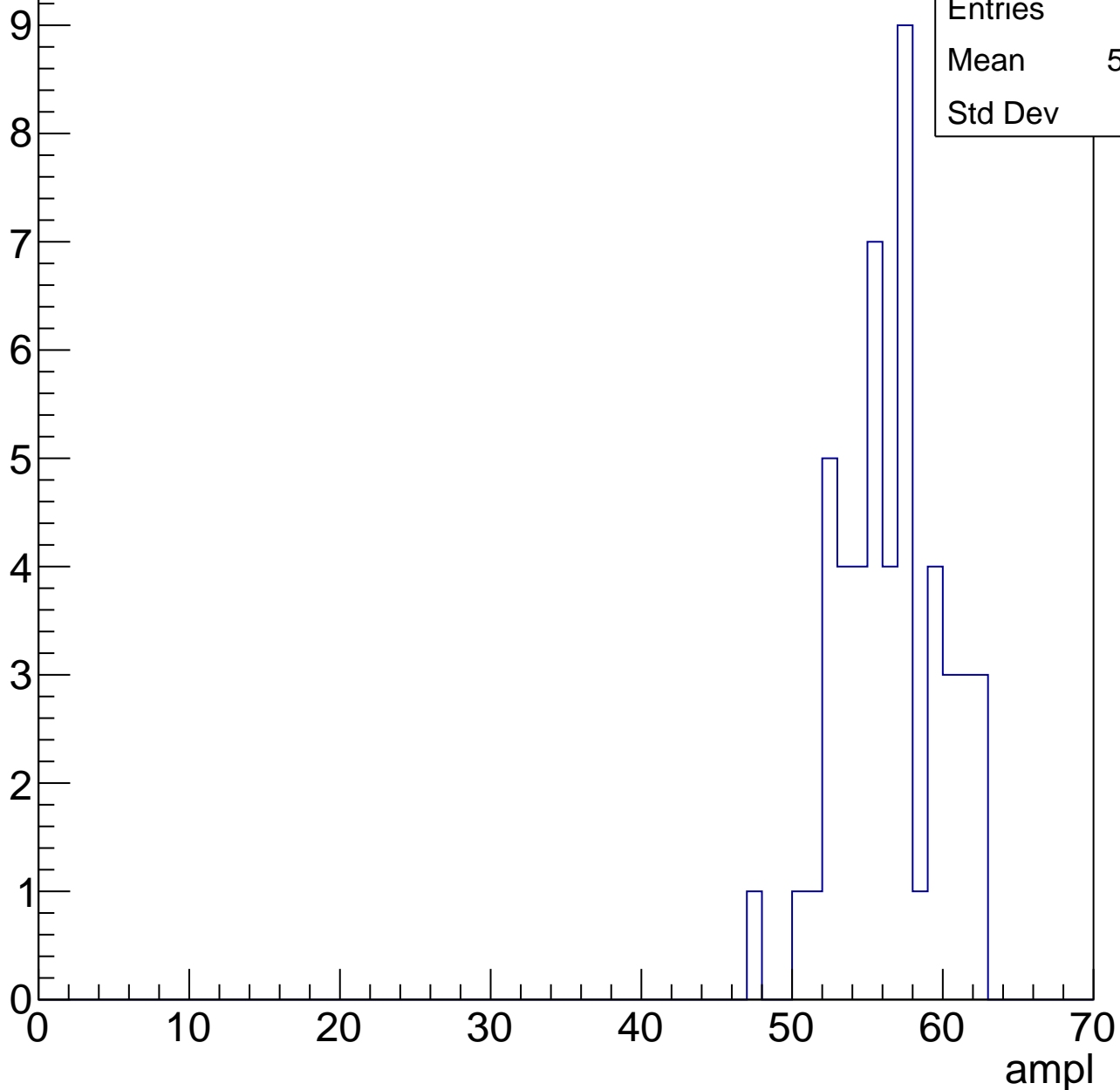


# B0L000S, U10-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

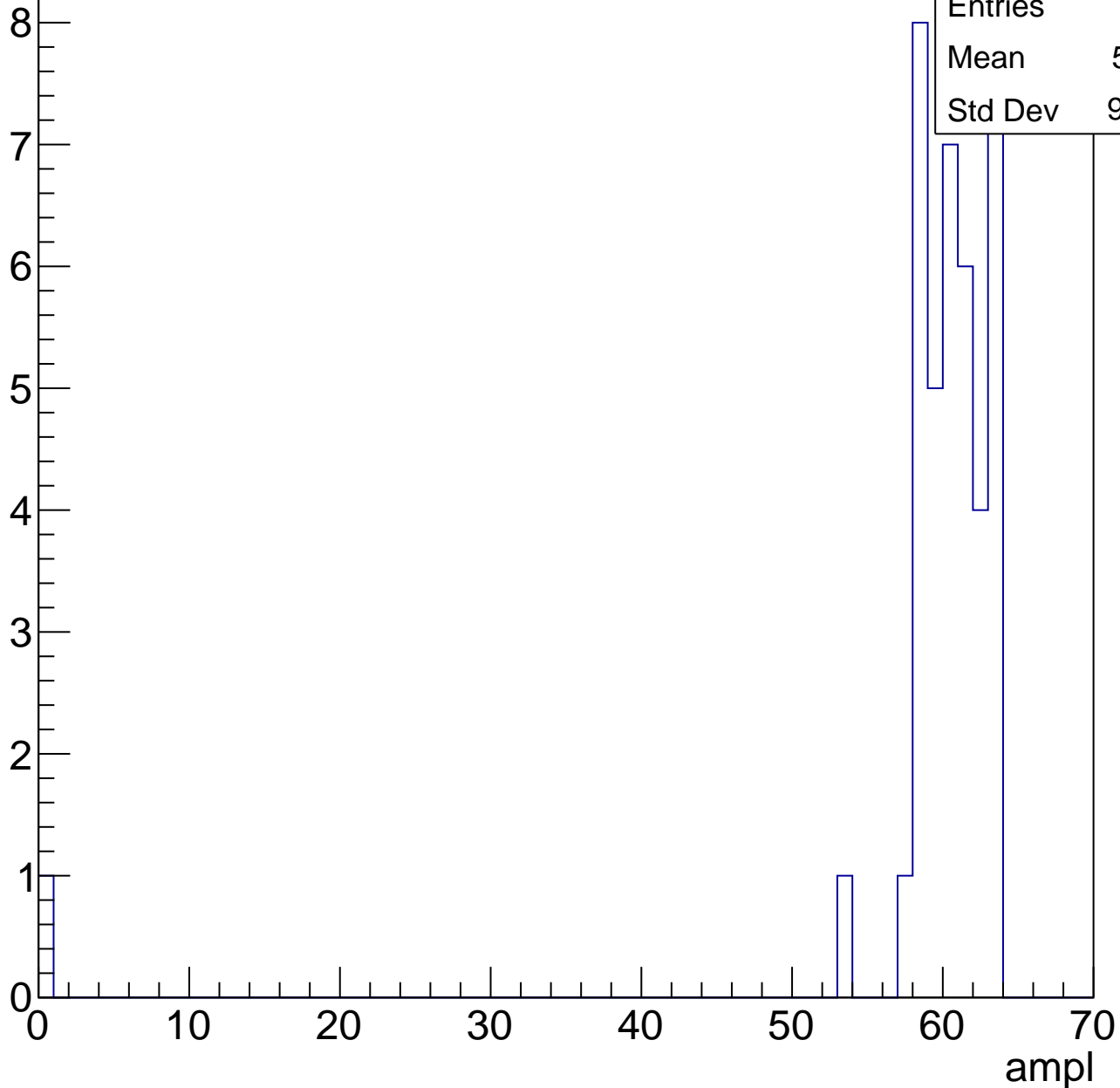
Entries	50
Mean	56.02
Std Dev	3.35



# B0L000S, U10-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	6
Mean	62.17
Std Dev	0.6872



# B0L000S, U10-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L000S, U10-ch36, adc0

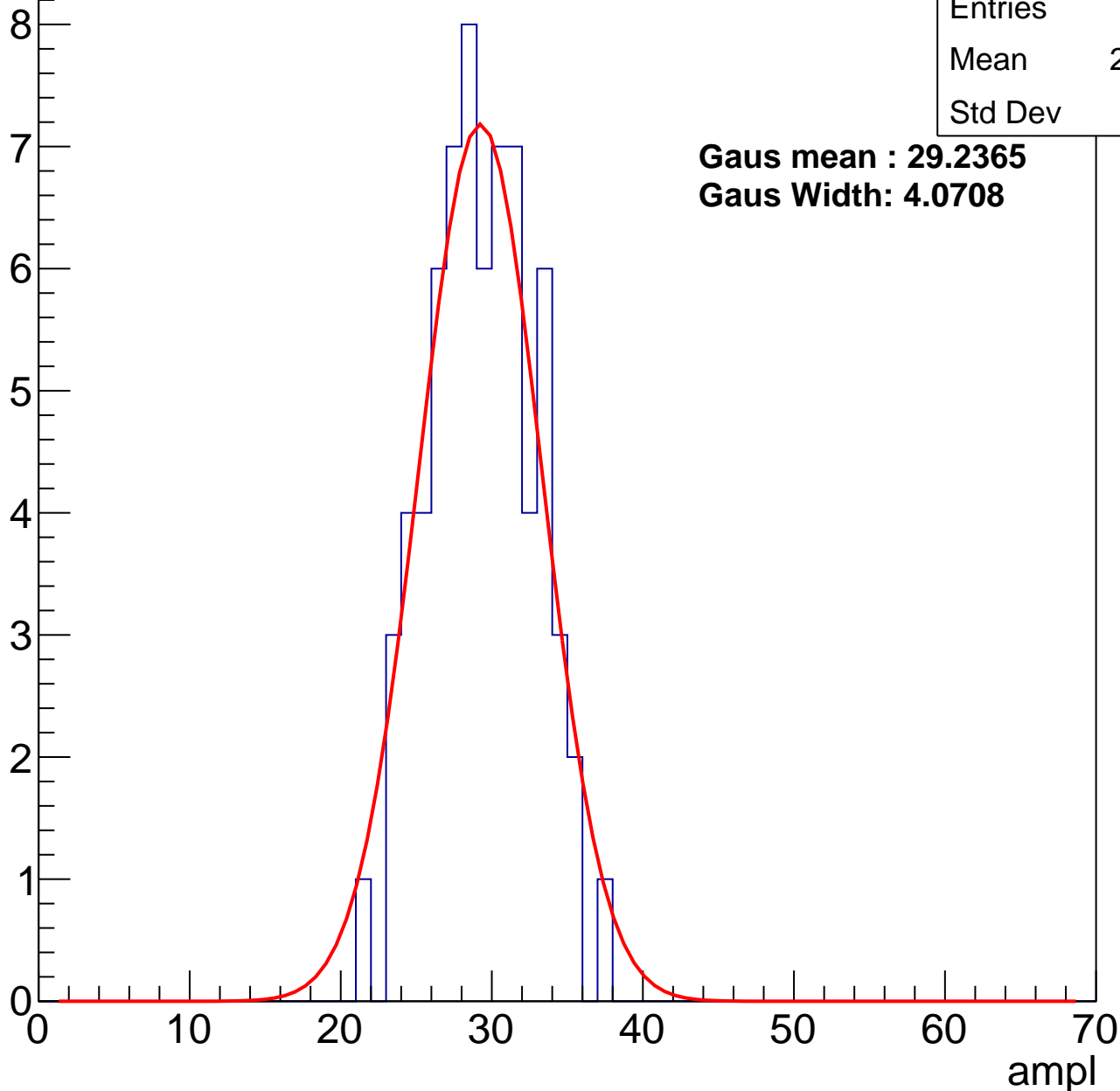
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	28.86
Std Dev	3.41

**Gaus mean : 29.2365**

**Gaus Width: 4.0708**



# B0L000S, U10-ch36, adc1

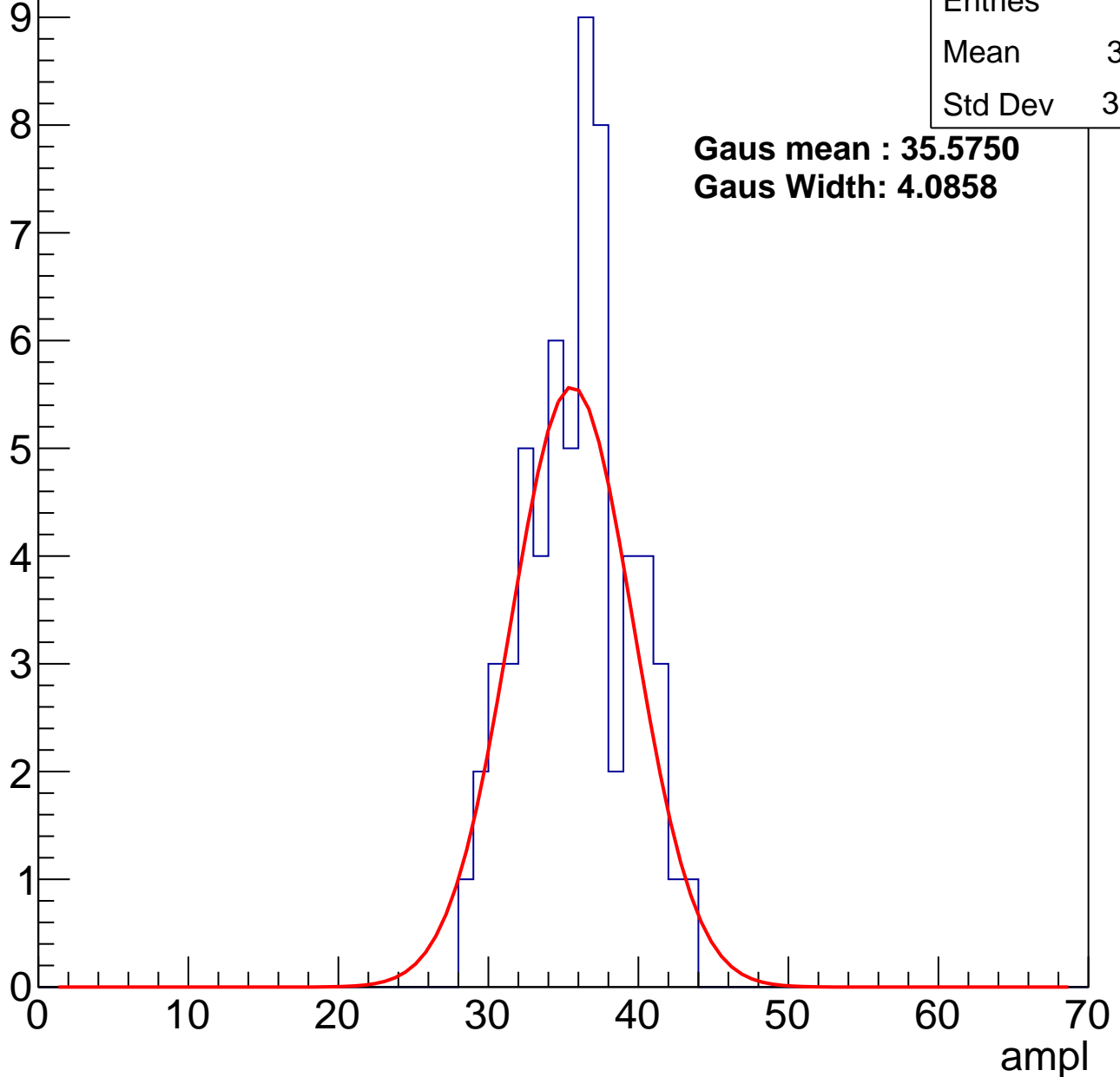
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.41
Std Dev	3.494

**Gaus mean : 35.5750**

**Gaus Width: 4.0858**



# B0L000S, U10-ch36, adc2

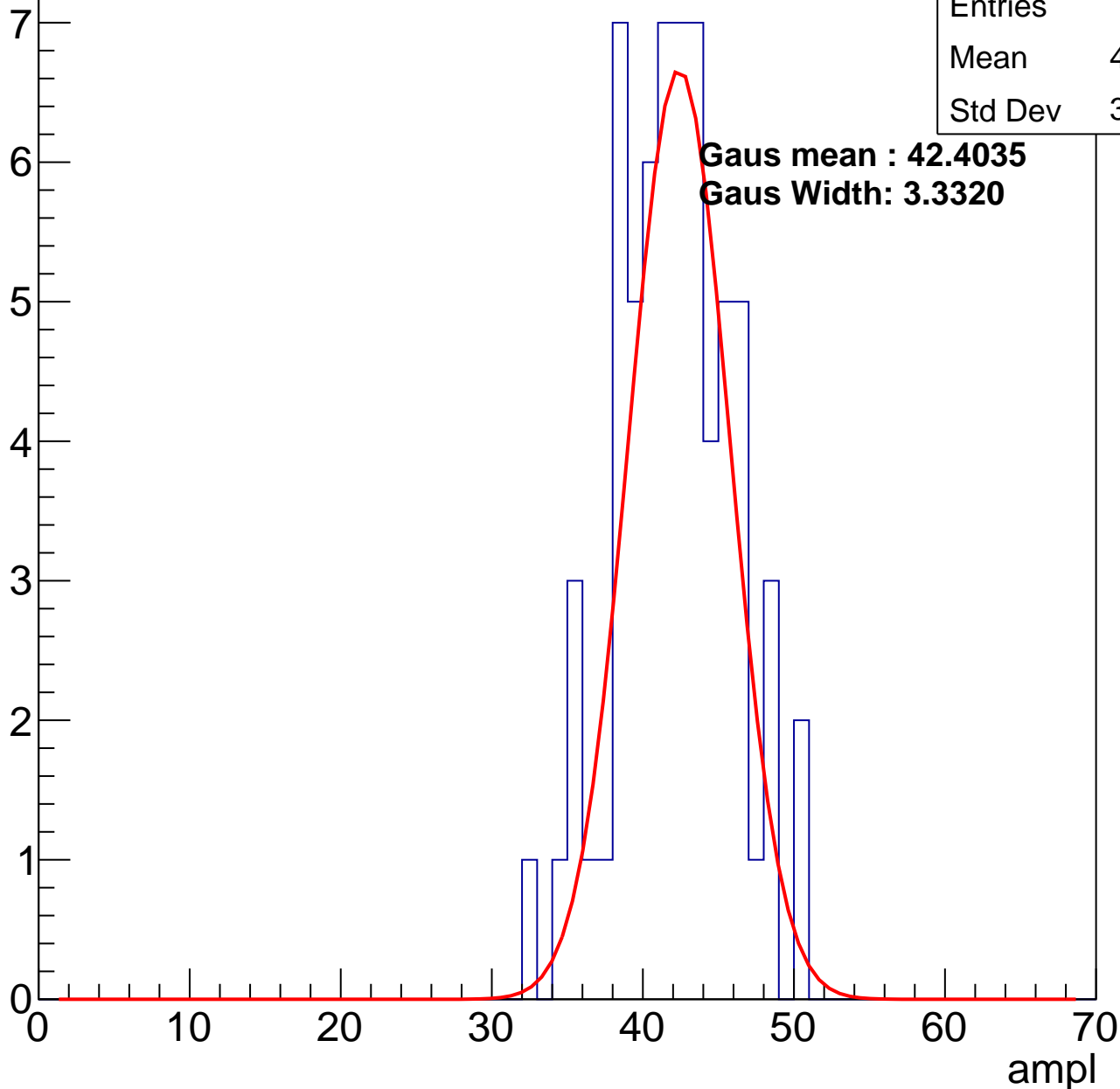
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	41.65
Std Dev	3.816

**Gaus mean : 42.4035**

**Gaus Width: 3.3320**

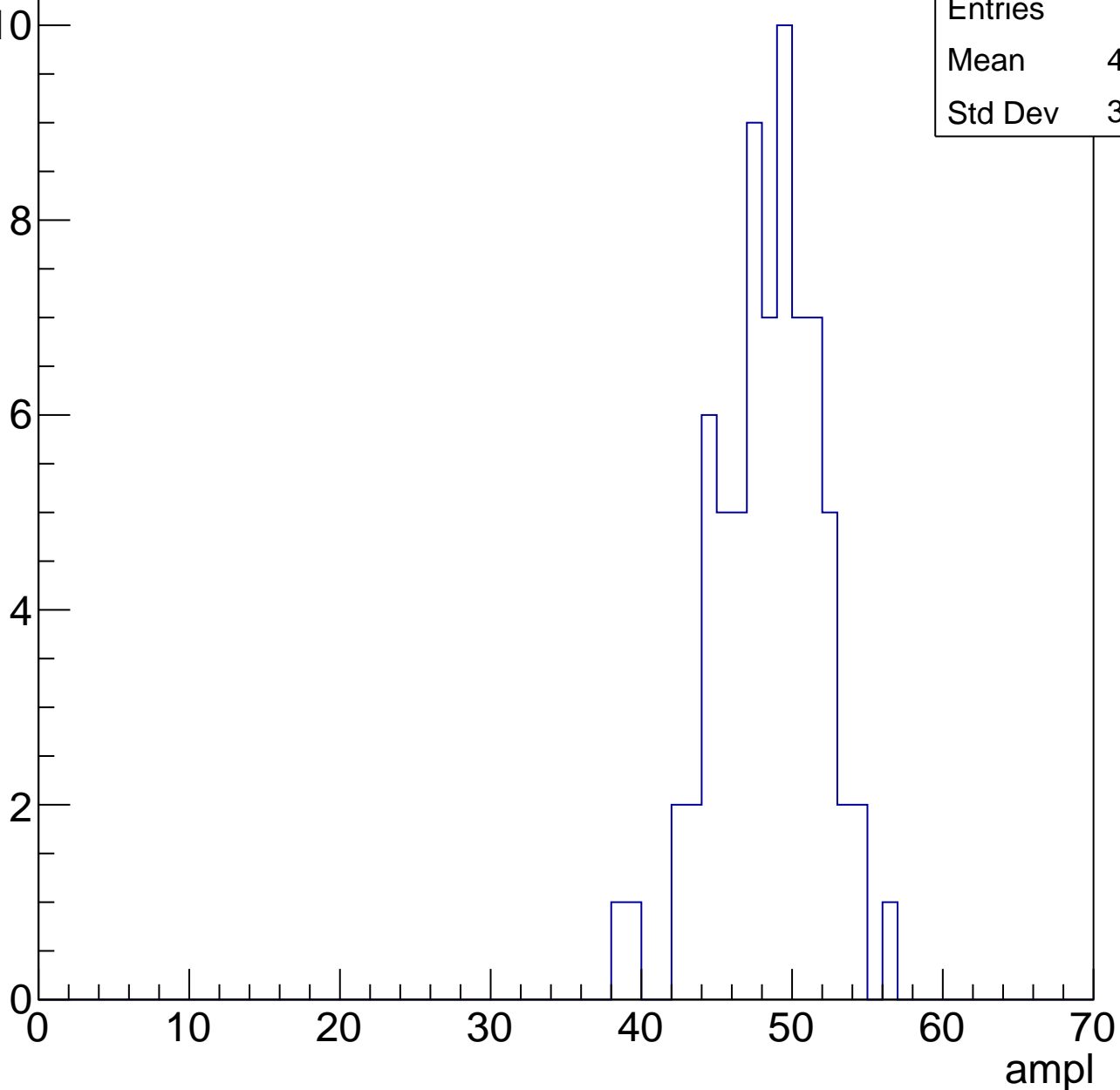


# B0L000S, U10-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	47.94
Std Dev	3.415



# B0L000S, U10-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

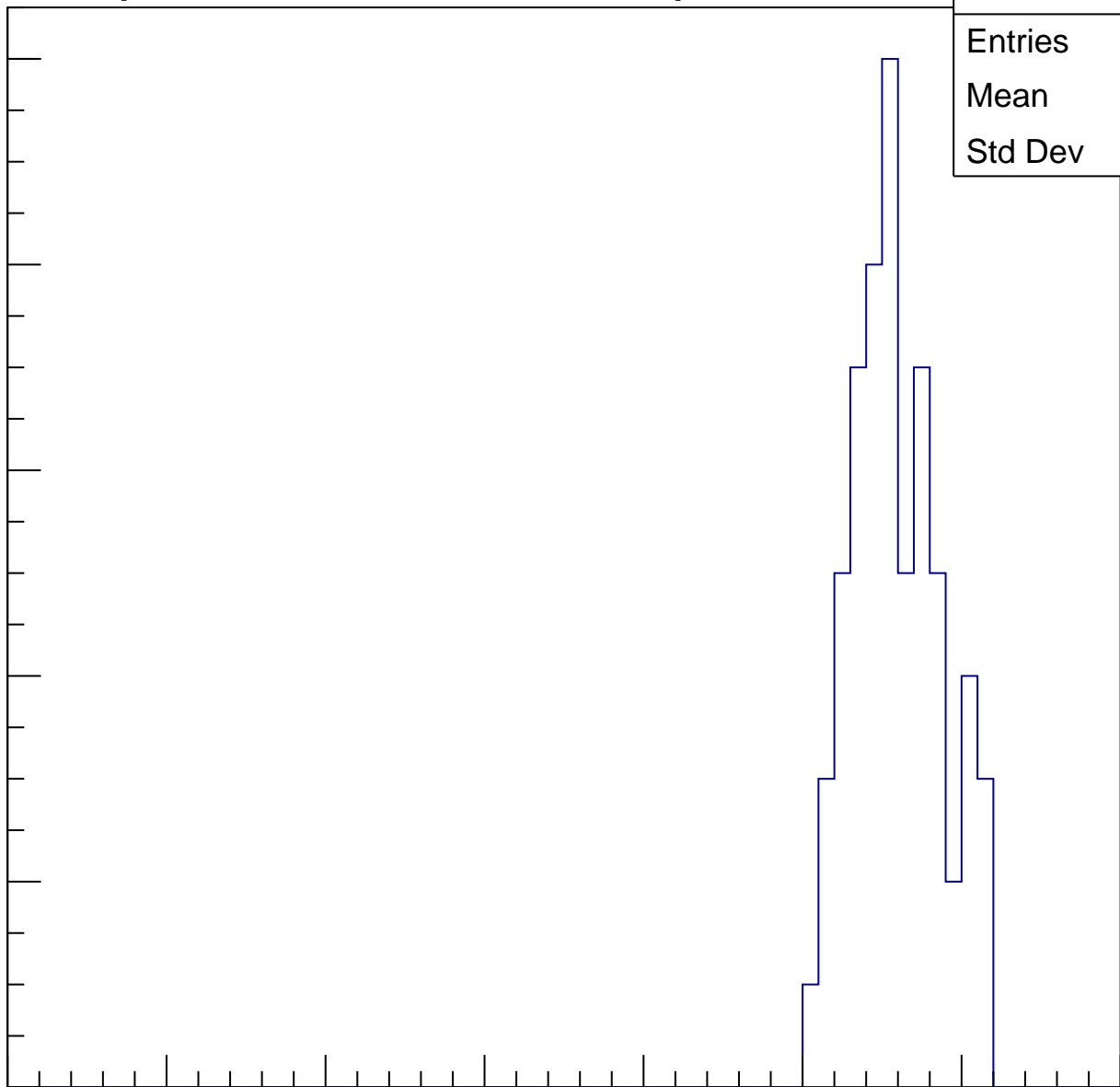
Entries	60
Mean	55.43
Std Dev	2.771

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

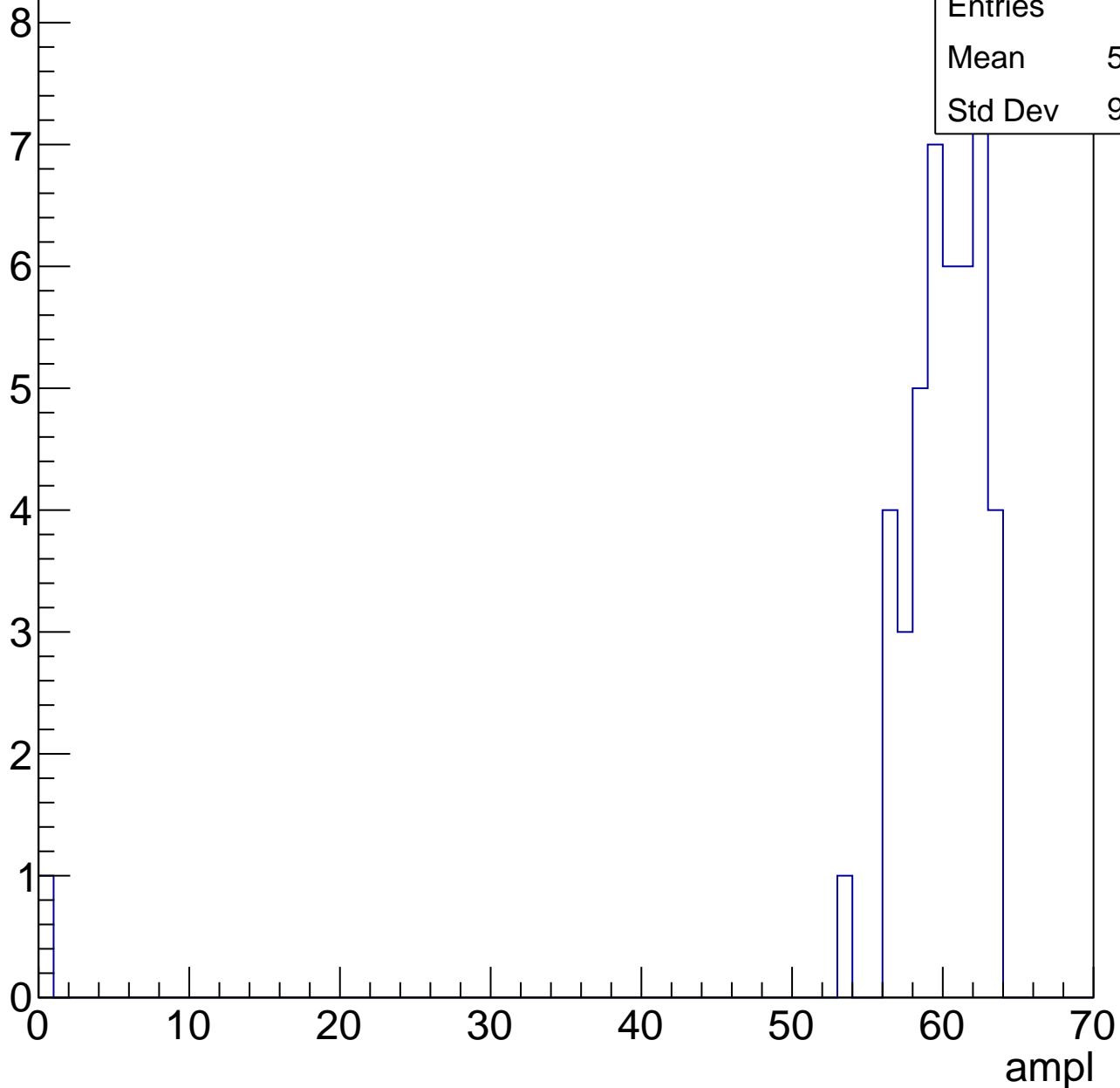


# B0L000S, U10-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.33
Std Dev	9.087

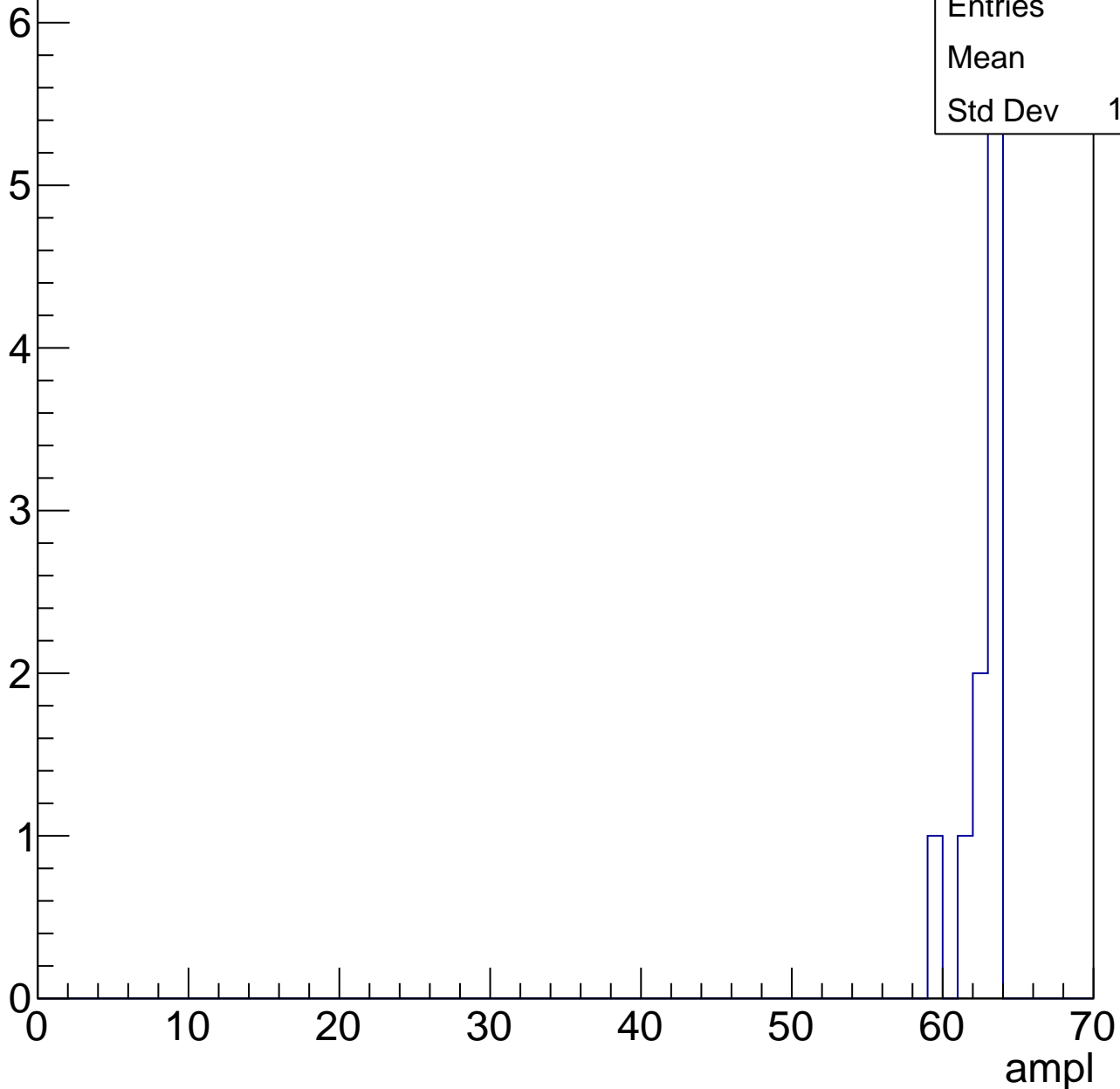


# B0L000S, U10-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.2
Std Dev	1.249





# B0L000S, U10-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch37, adc0

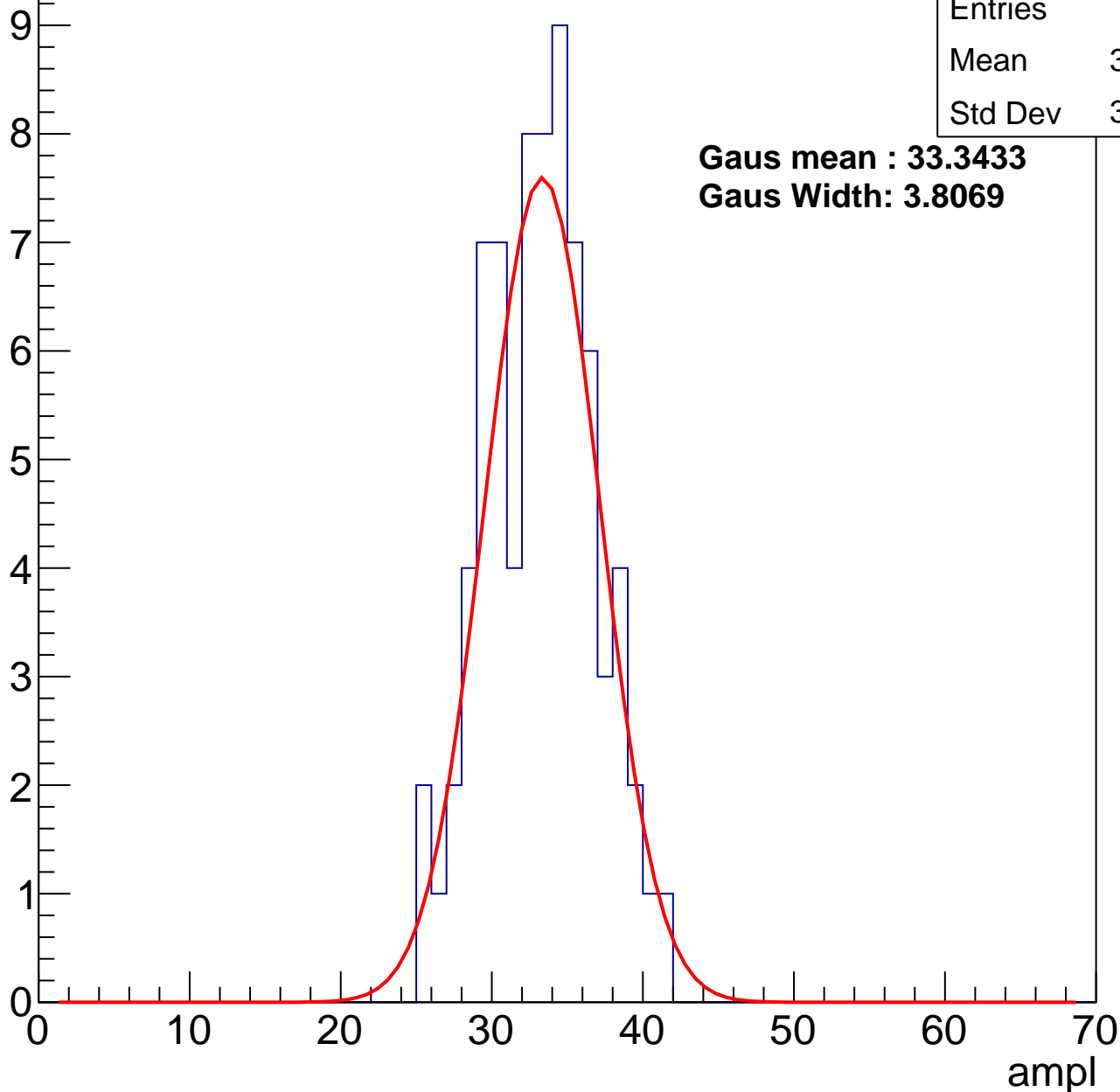
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	32.74
Std Dev	3.567

**Gaus mean : 33.3433**

**Gaus Width: 3.8069**



# B0L000S, U10-ch37, adc1

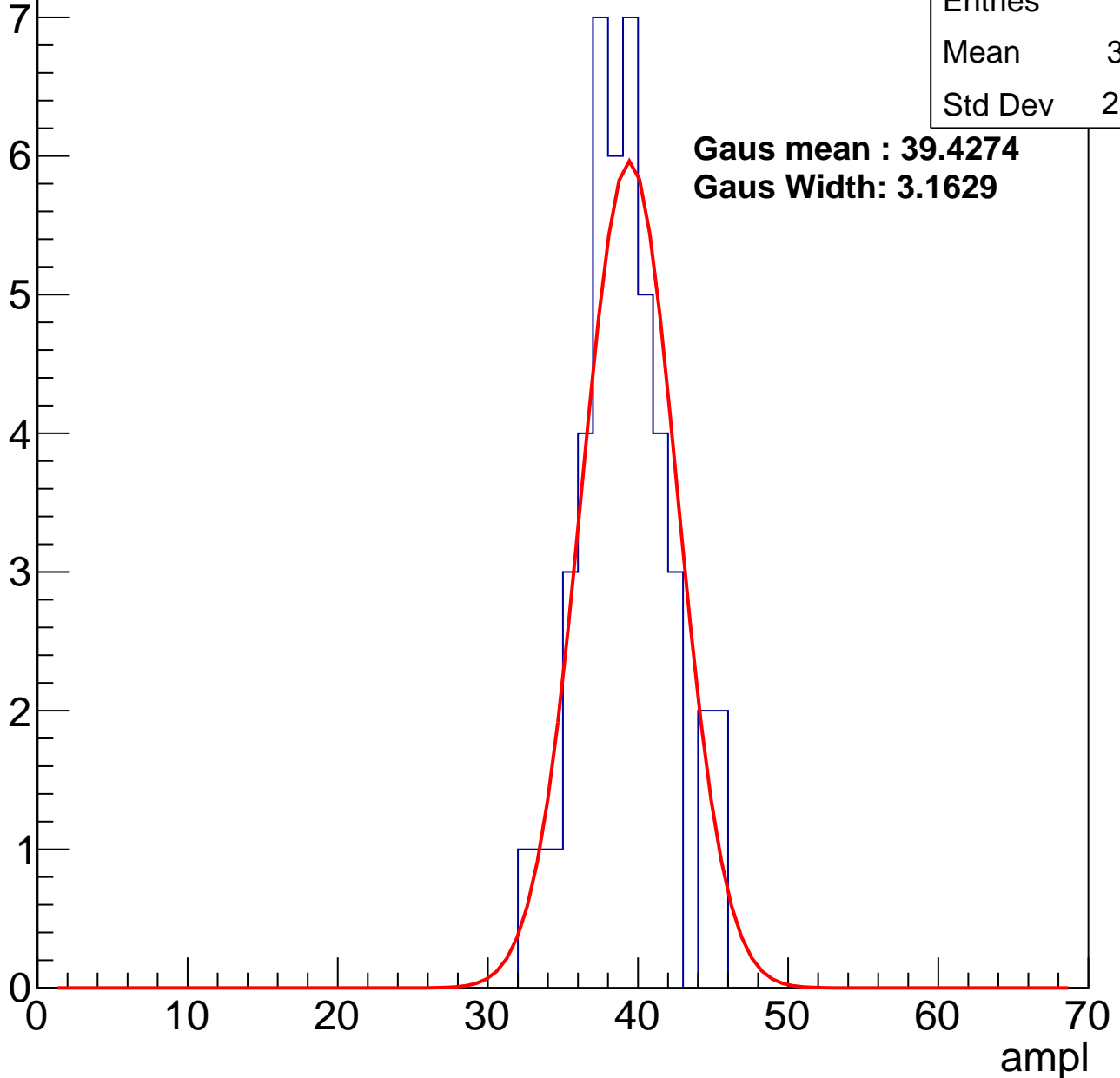
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	38.61
Std Dev	2.915

**Gaus mean : 39.4274**

**Gaus Width: 3.1629**



# B0L000S, U10-ch37, adc2

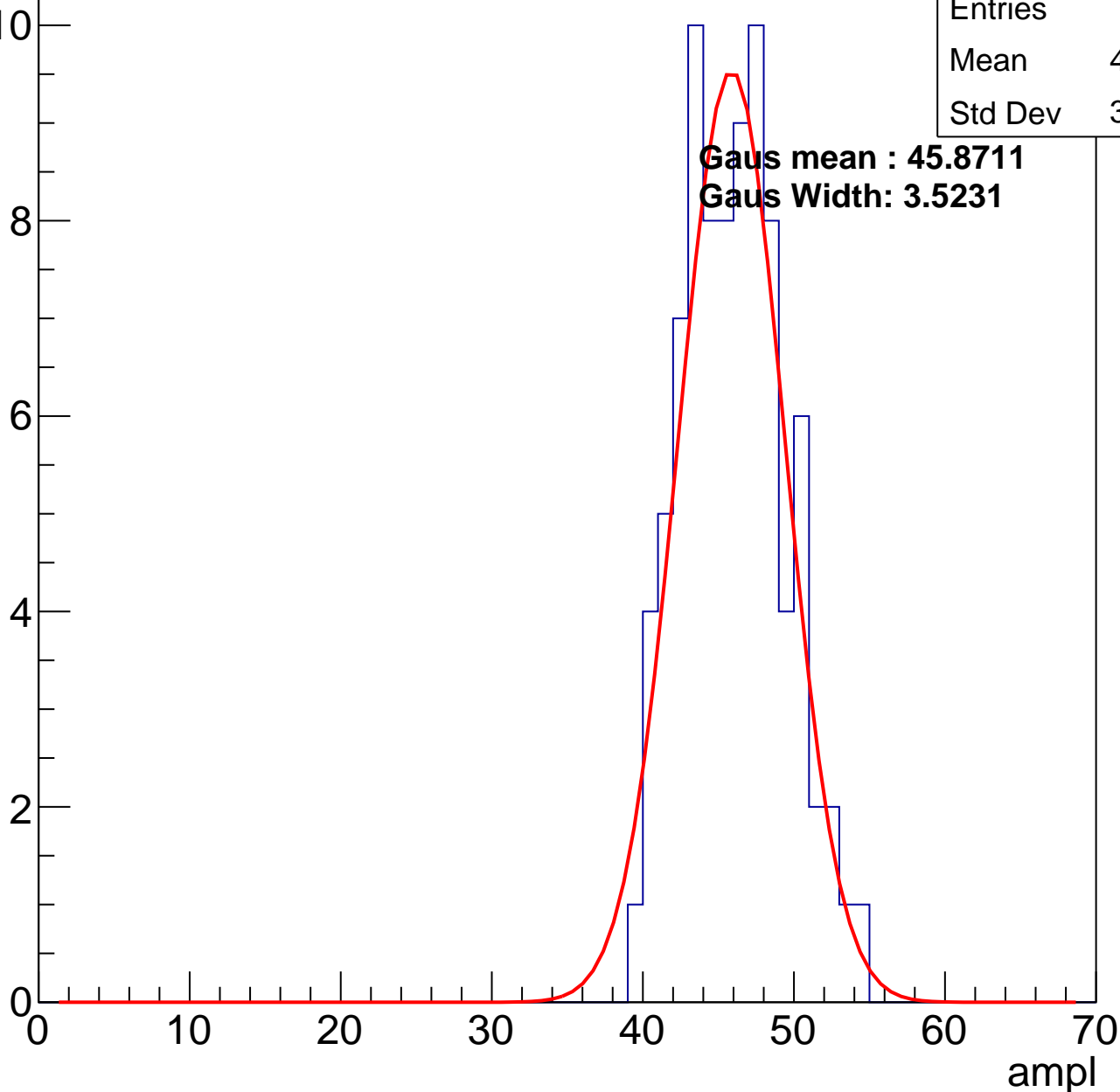
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	45.55
Std Dev	3.326

**Gaus mean : 45.8711**

**Gaus Width: 3.5231**

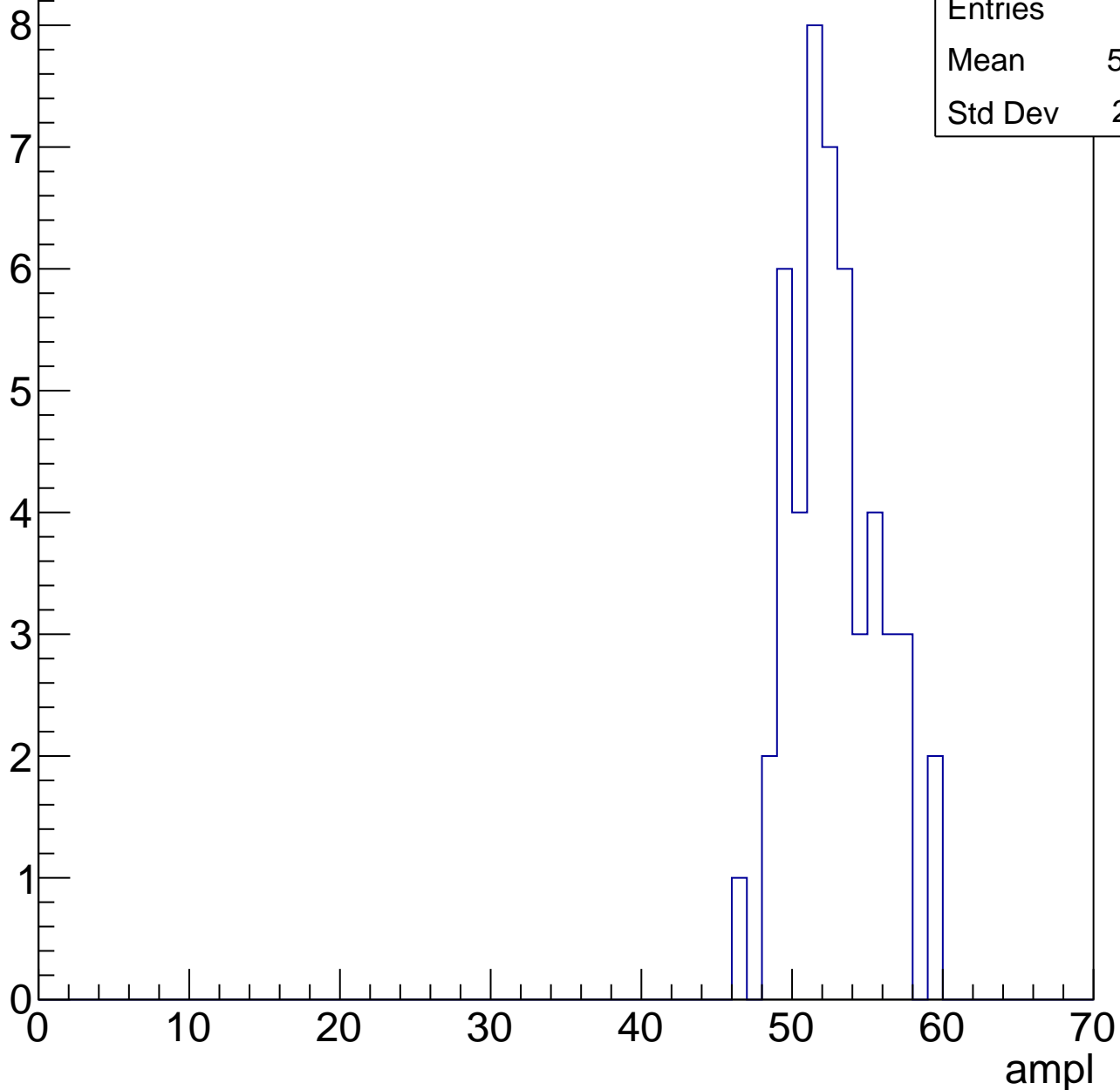


# B0L000S, U10-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	52.35
Std Dev	2.911

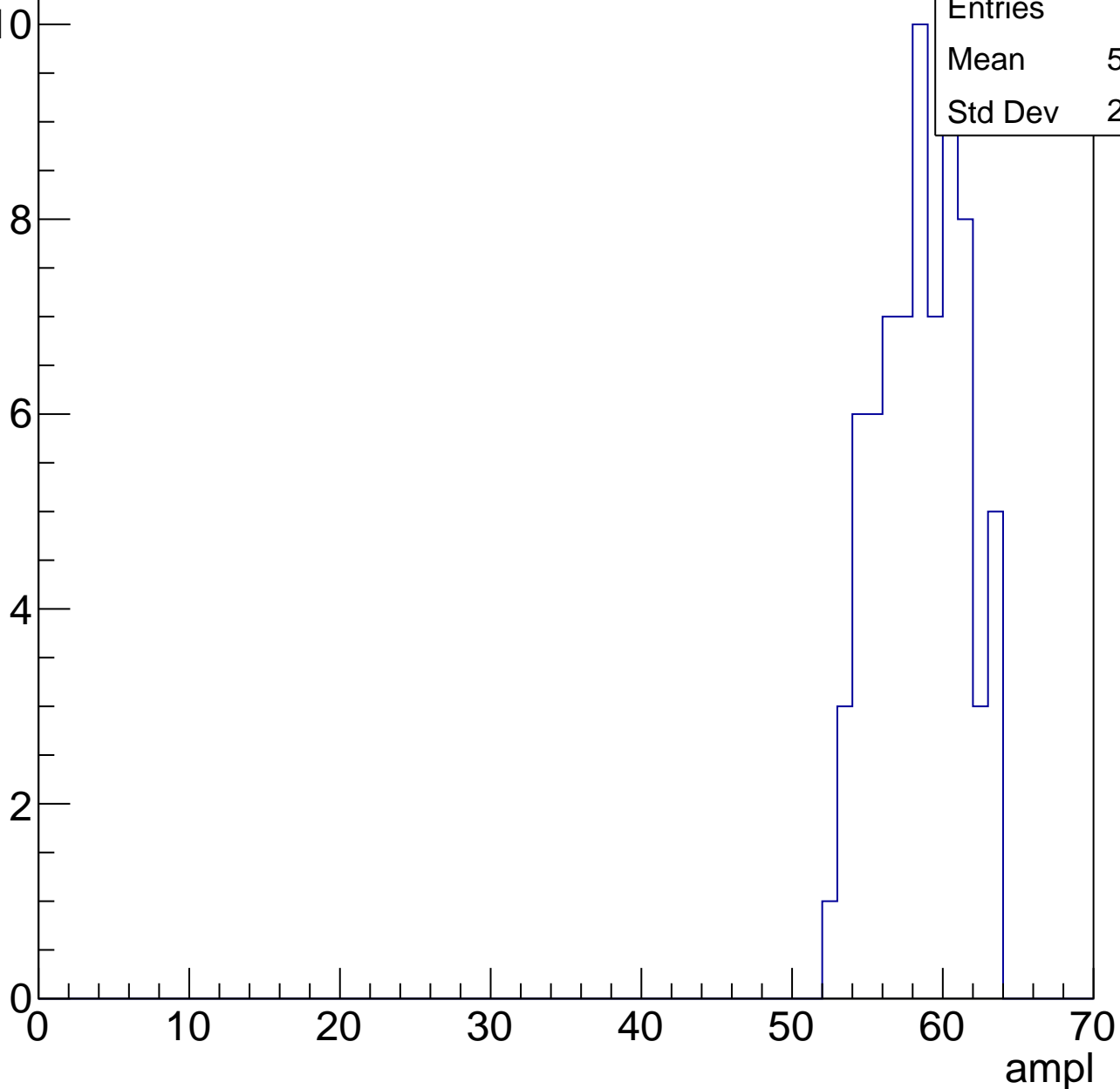


# B0L000S, U10-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

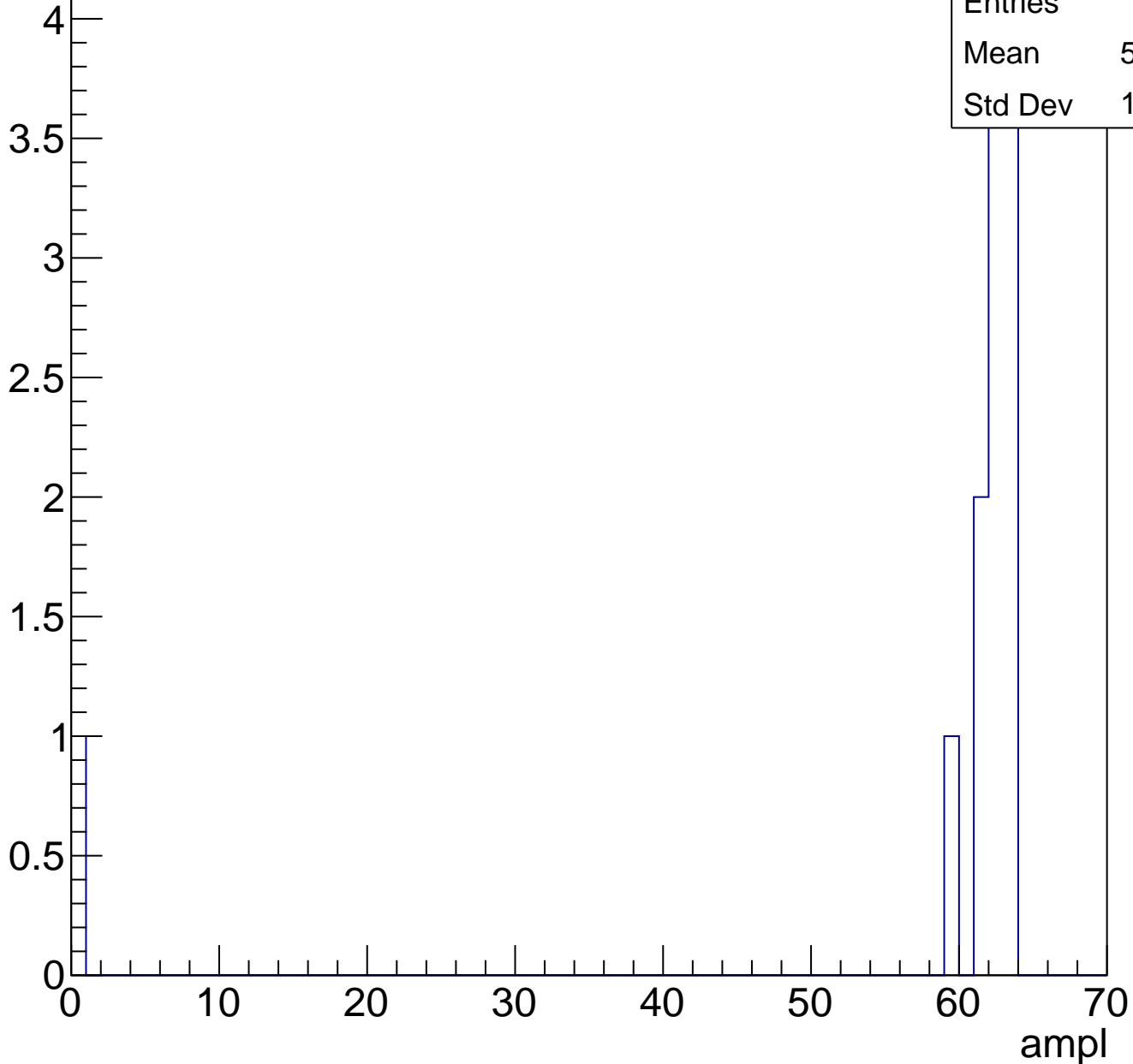
Entries	72
Mean	58.03
Std Dev	2.848



# B0L000S, U10-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

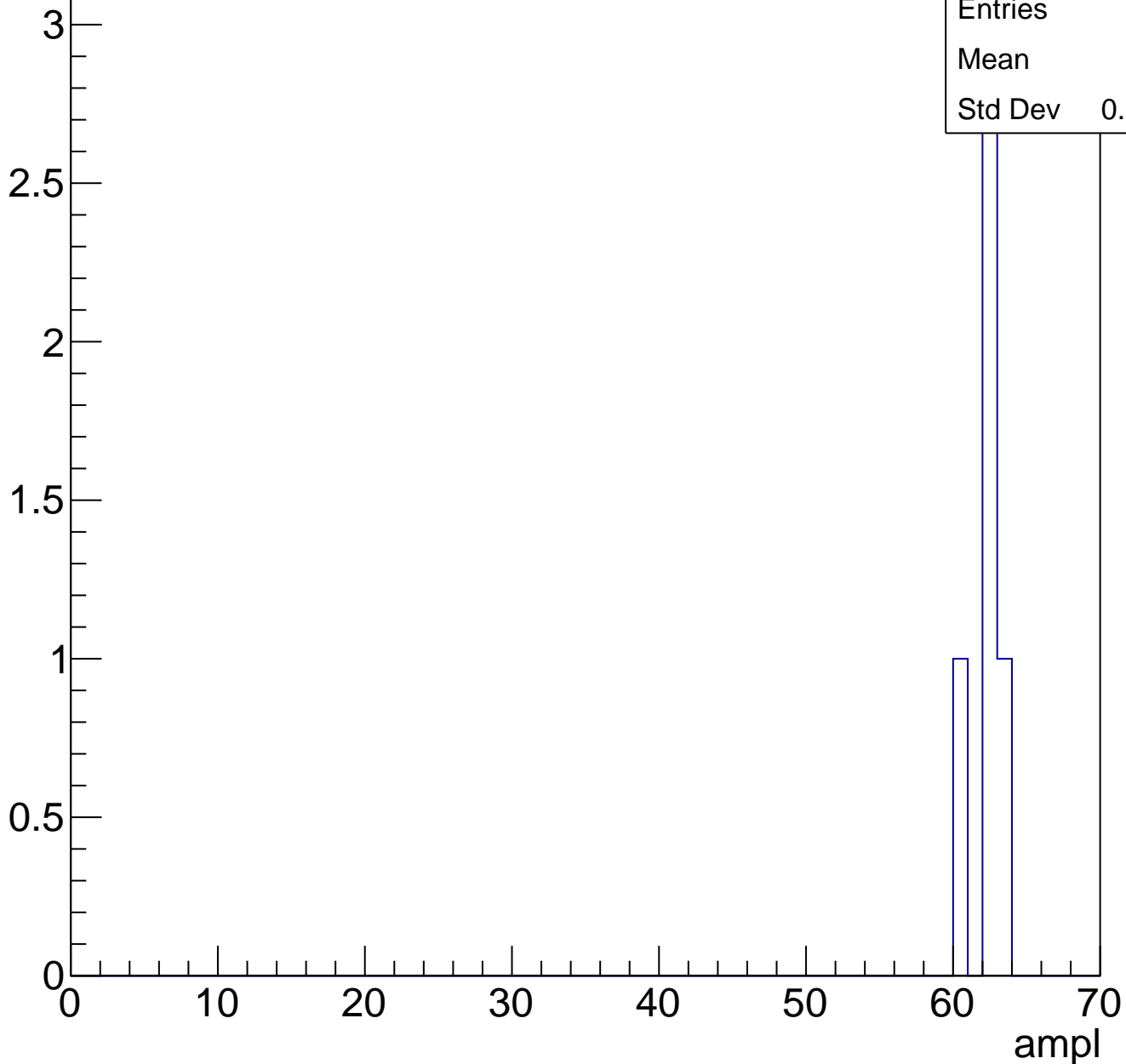
Entry



# B0L000S, U10-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U10-ch38, adc0

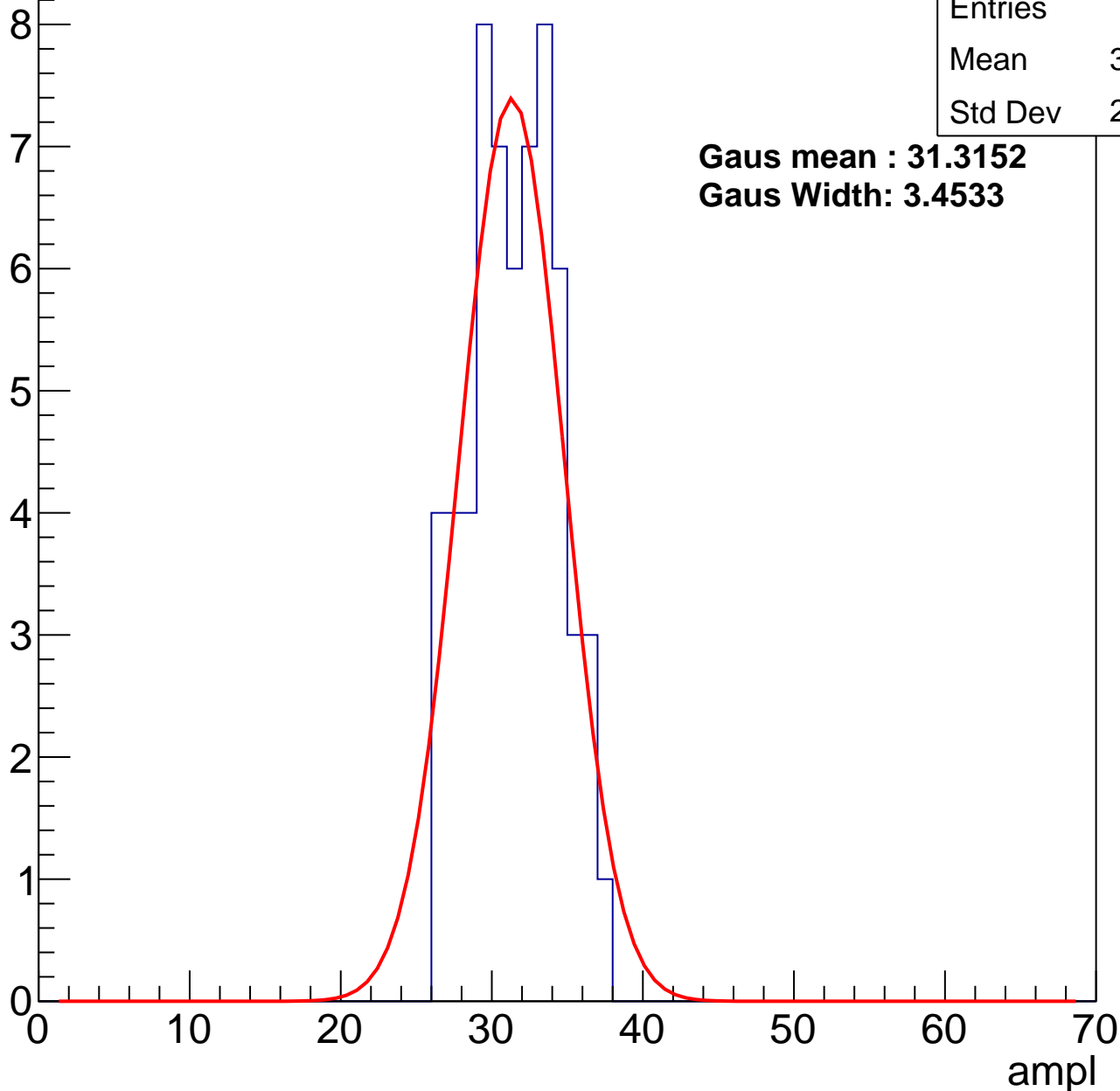
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	31.05
Std Dev	2.837

**Gaus mean : 31.3152**

**Gaus Width: 3.4533**



# B0L000S, U10-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	38.19
Std Dev	3.413

**Gaus mean : 38.9720**

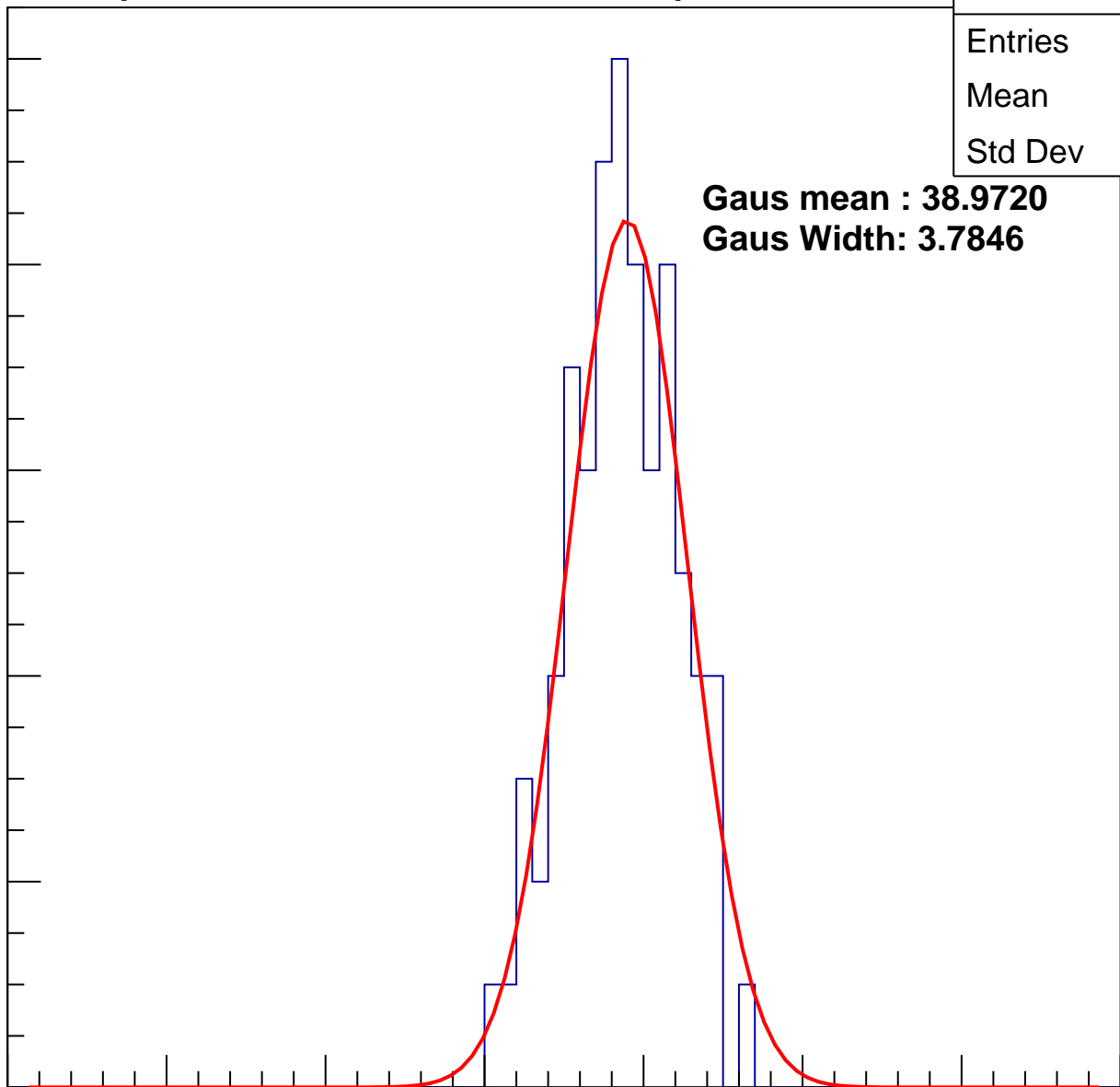
**Gaus Width: 3.7846**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch38, adc2

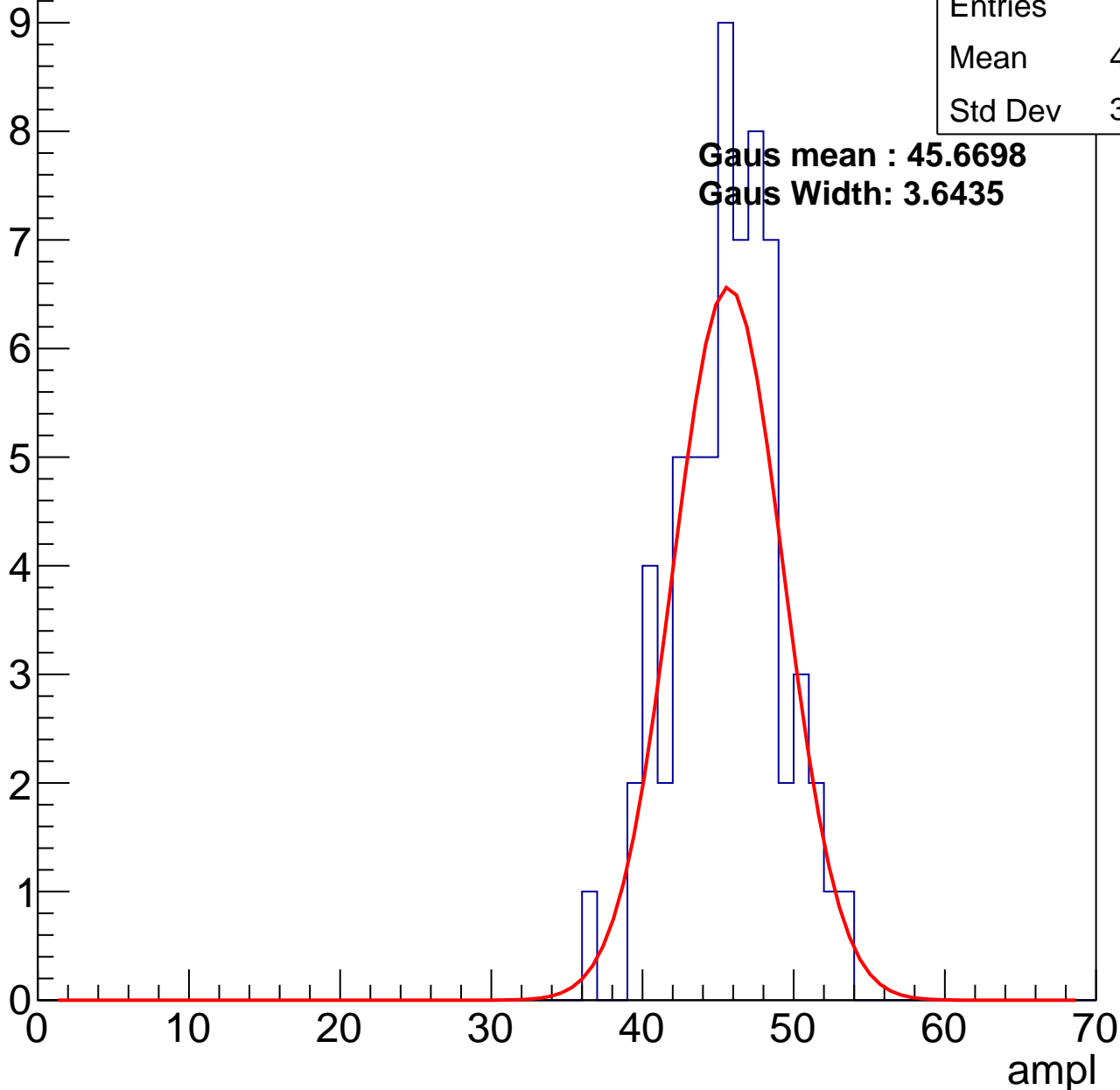
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	45.23
Std Dev	3.413

**Gaus mean : 45.6698**

**Gaus Width: 3.6435**

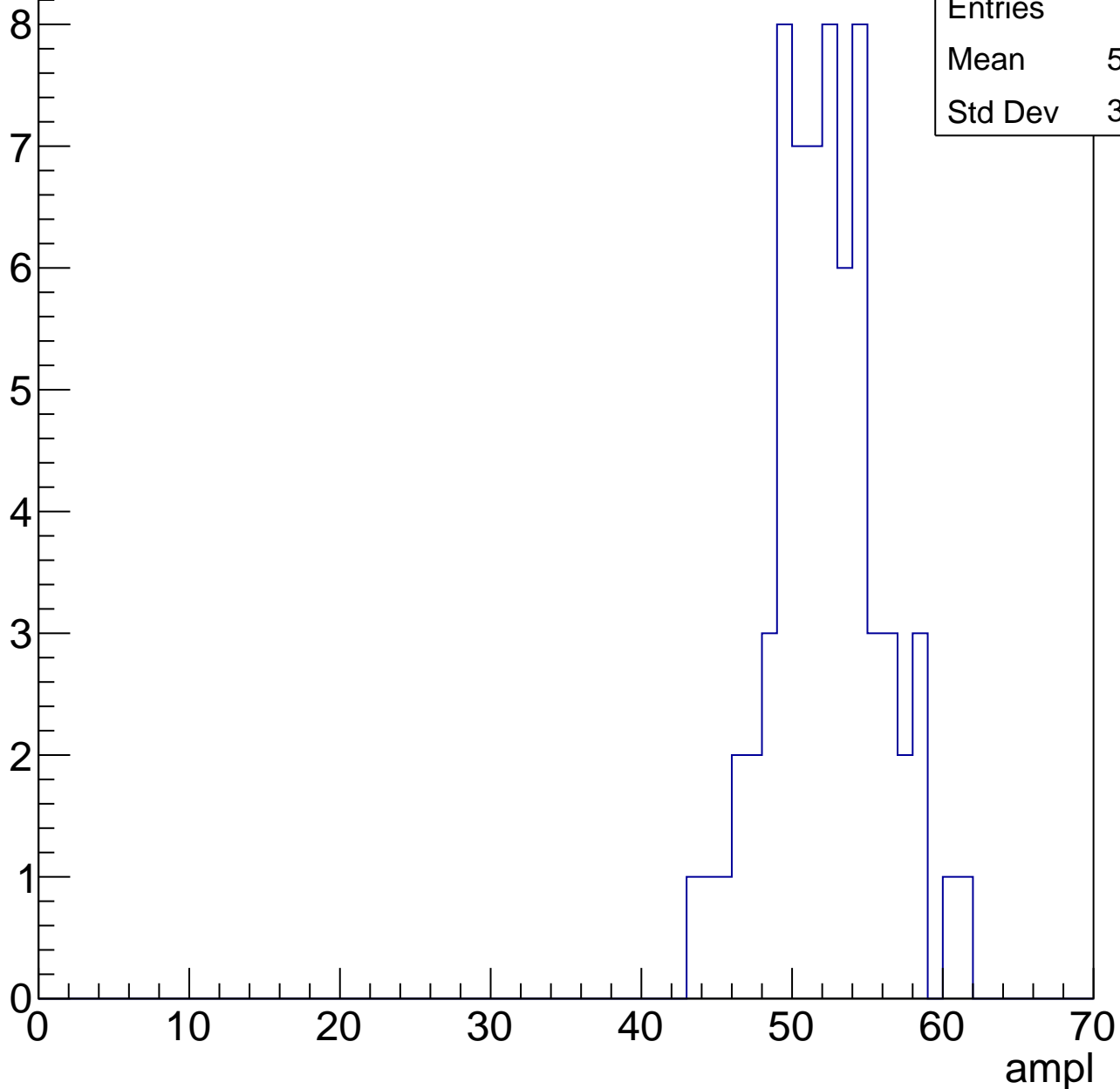


# B0L000S, U10-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	51.78
Std Dev	3.632

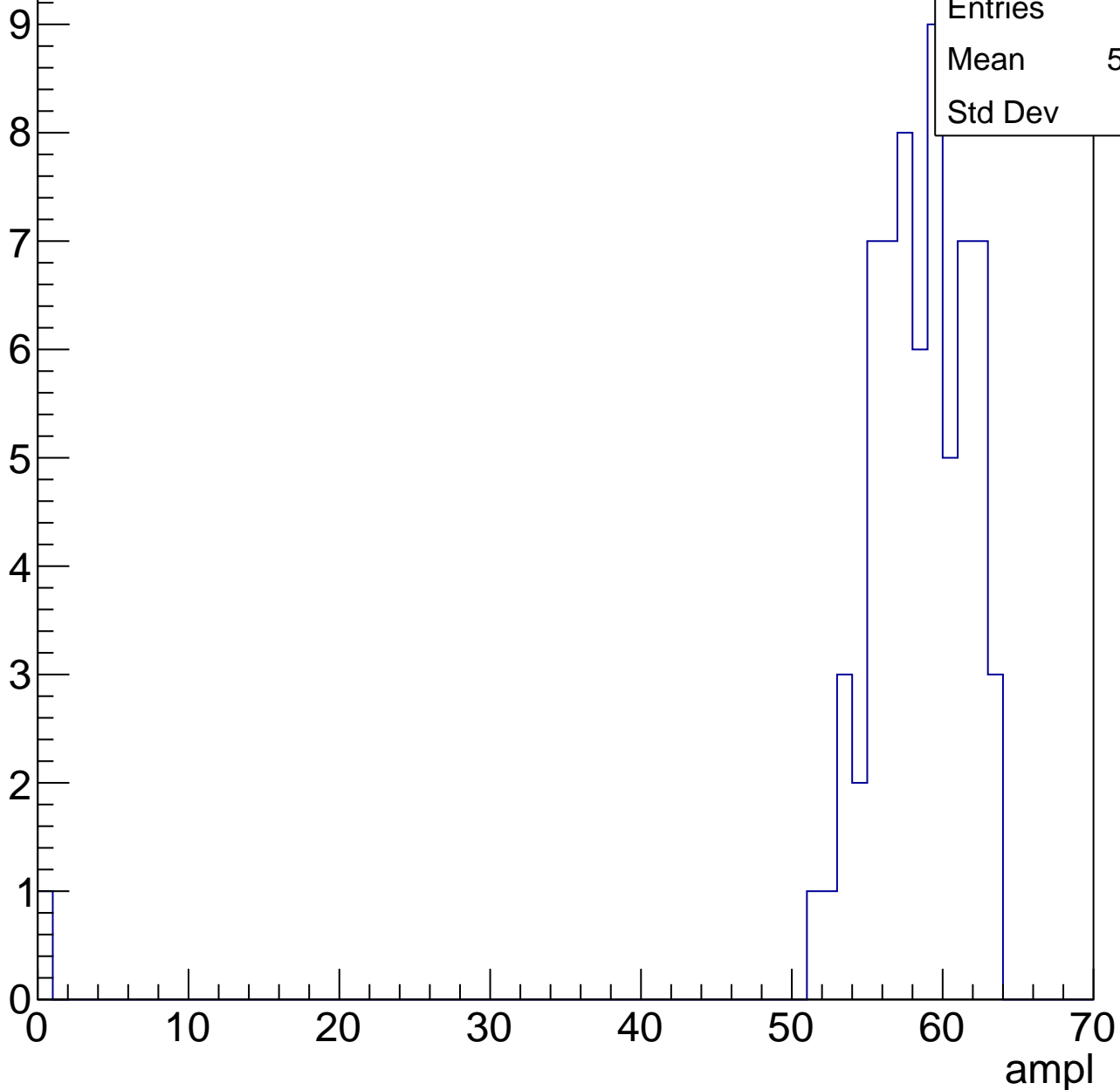


# B0L000S, U10-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	57.19
Std Dev	7.62

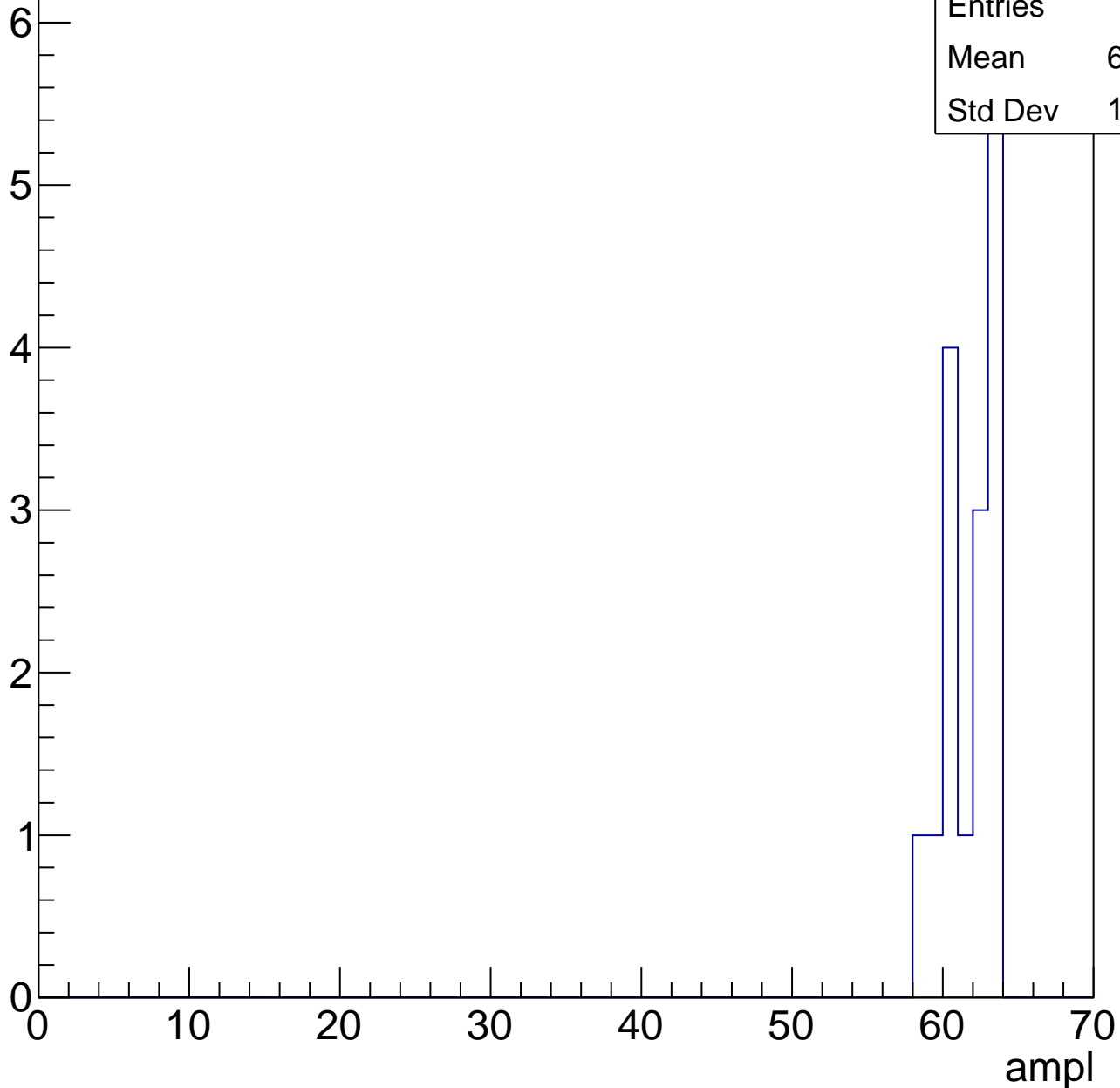


# B0L000S, U10-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.38
Std Dev	1.615



# B0L000S, U10-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

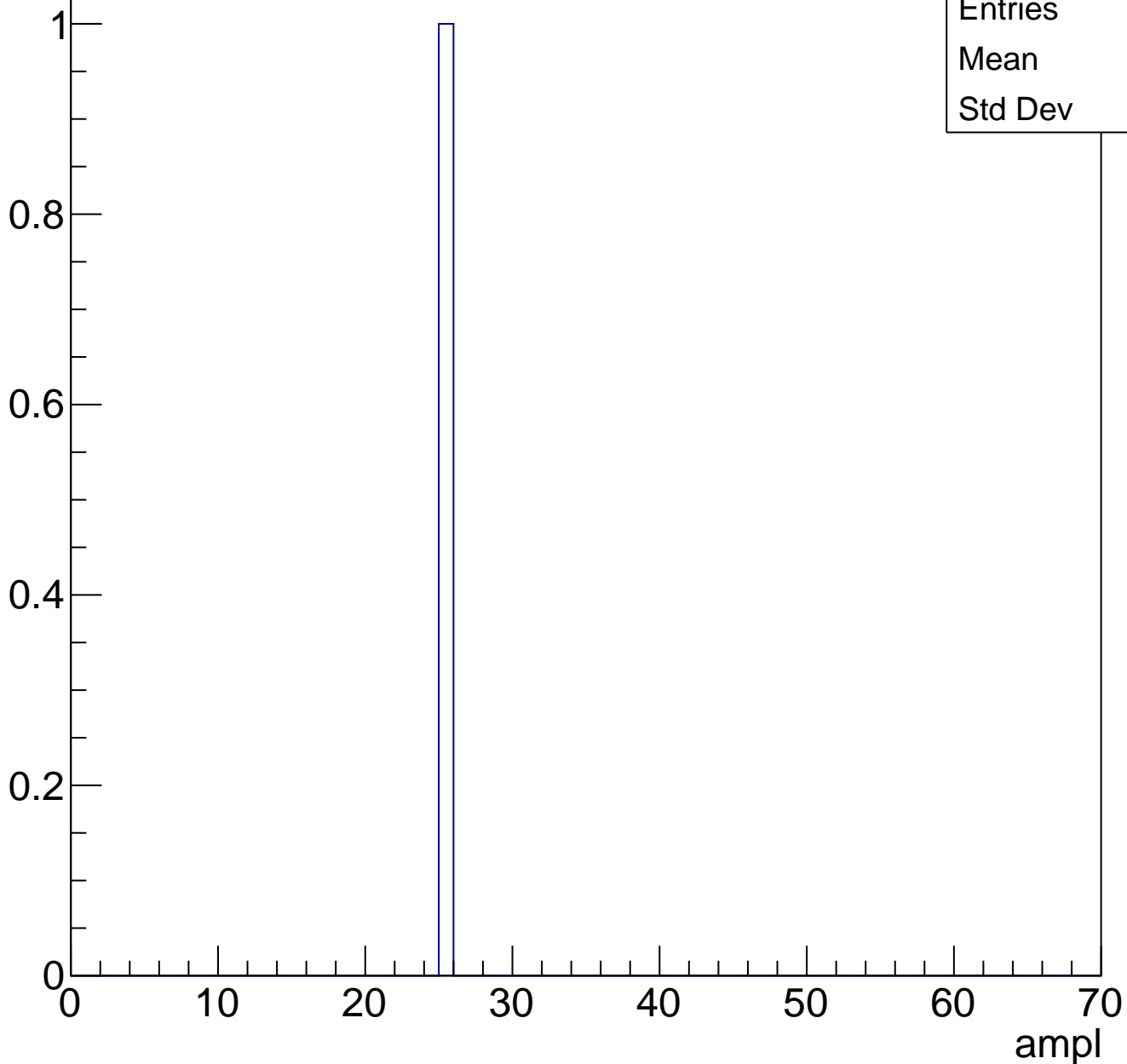




# B0L000S, U10-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L000S, U10-ch39, adc0

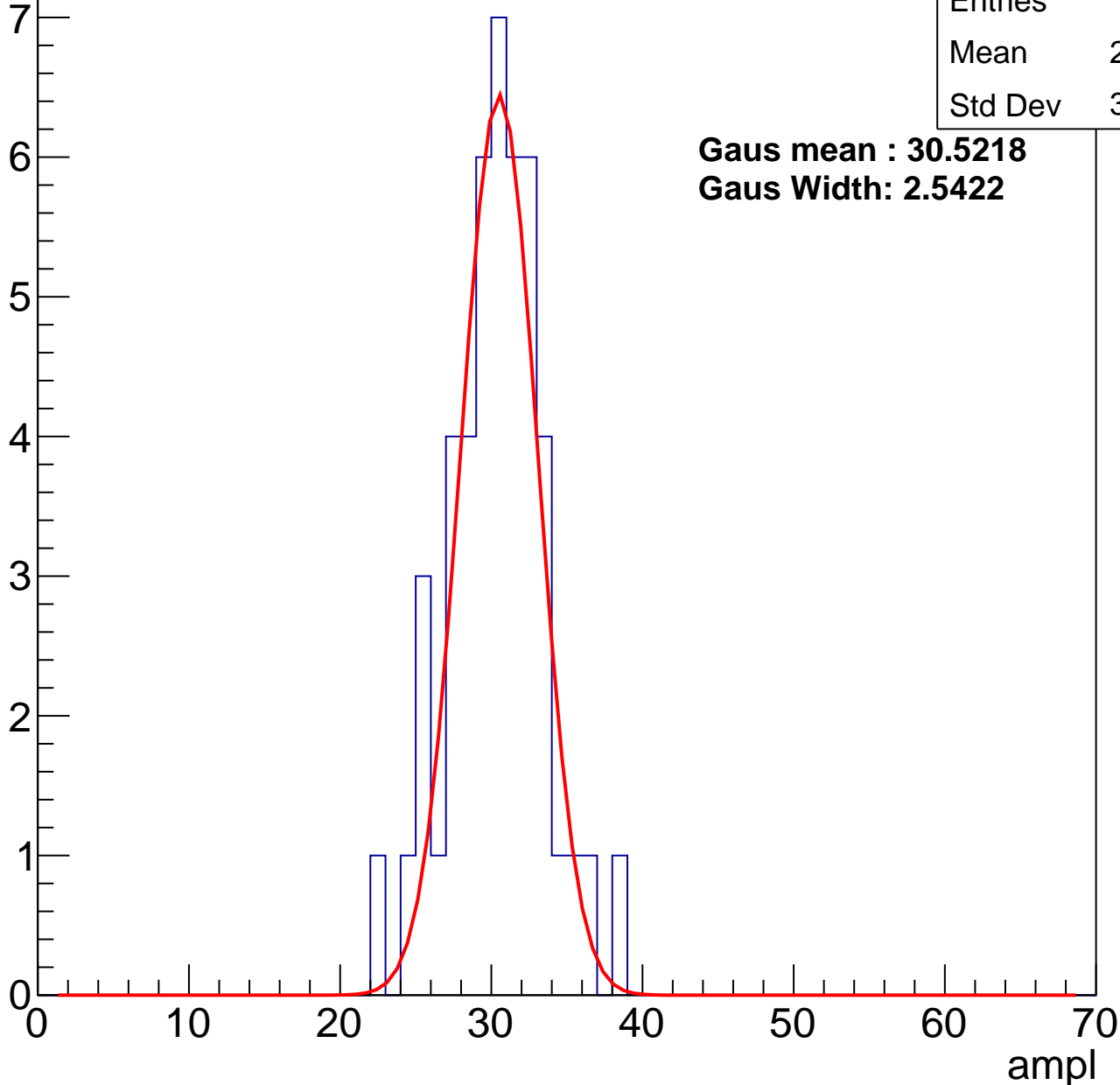
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	29.87
Std Dev	3.112

**Gaus mean : 30.5218**

**Gaus Width: 2.5422**



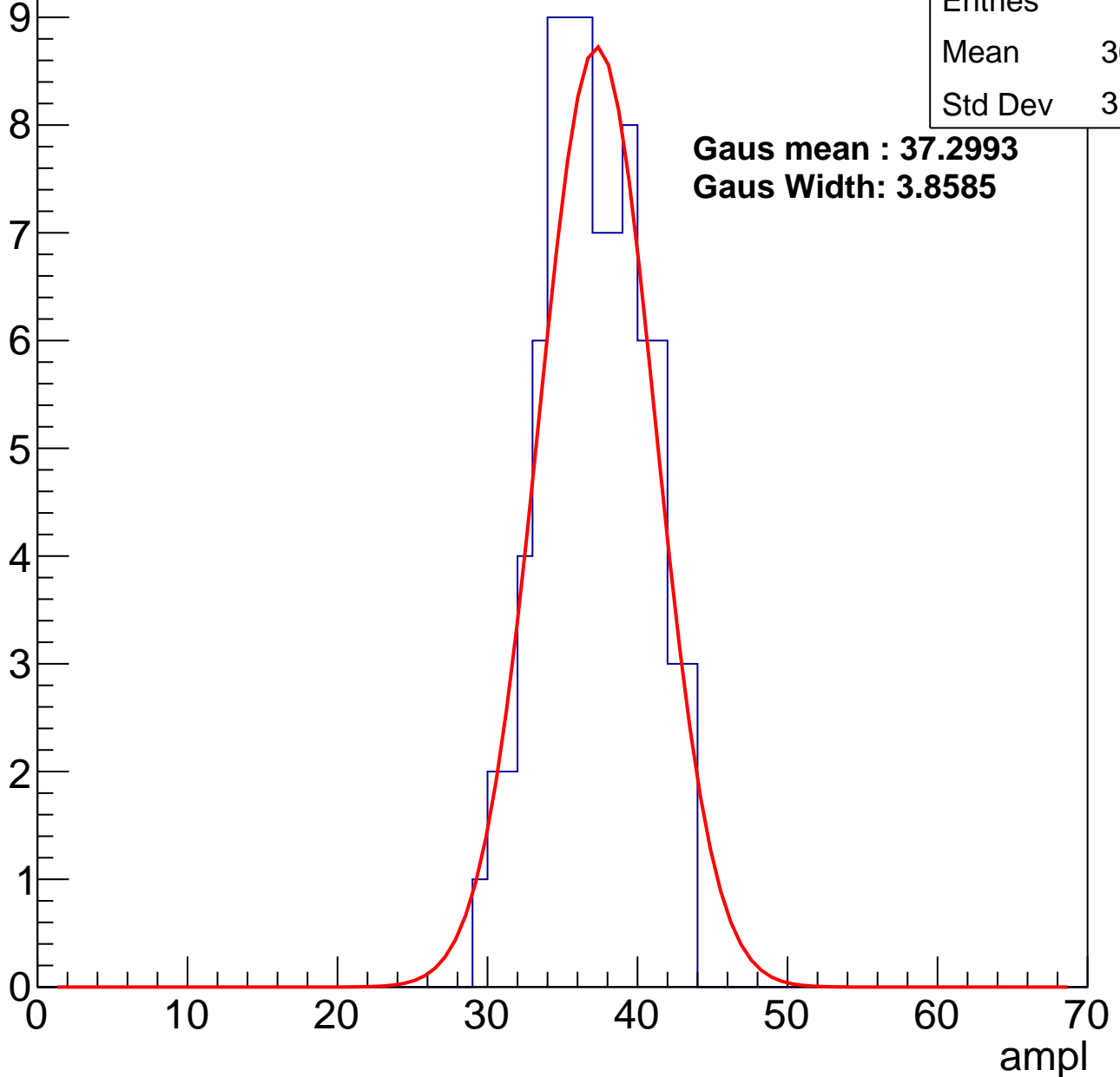
# B0L000S, U10-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	36.59
Std Dev	3.338

**Gaus mean : 37.2993**  
**Gaus Width: 3.8585**



# B0L000S, U10-ch39, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	44.67
Std Dev	3.68

**Gaus mean : 45.2758**

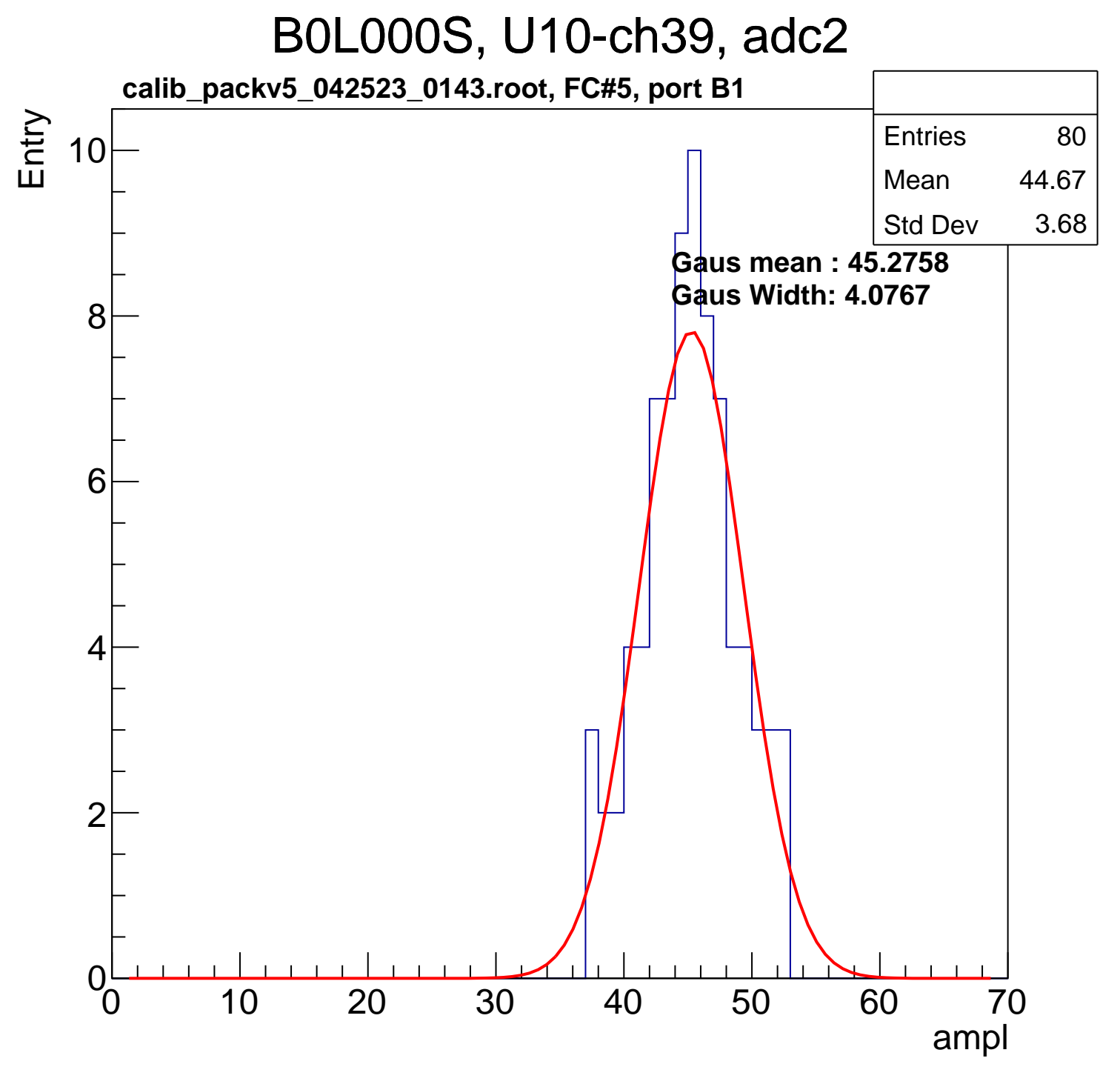
**Gaus Width: 4.0767**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

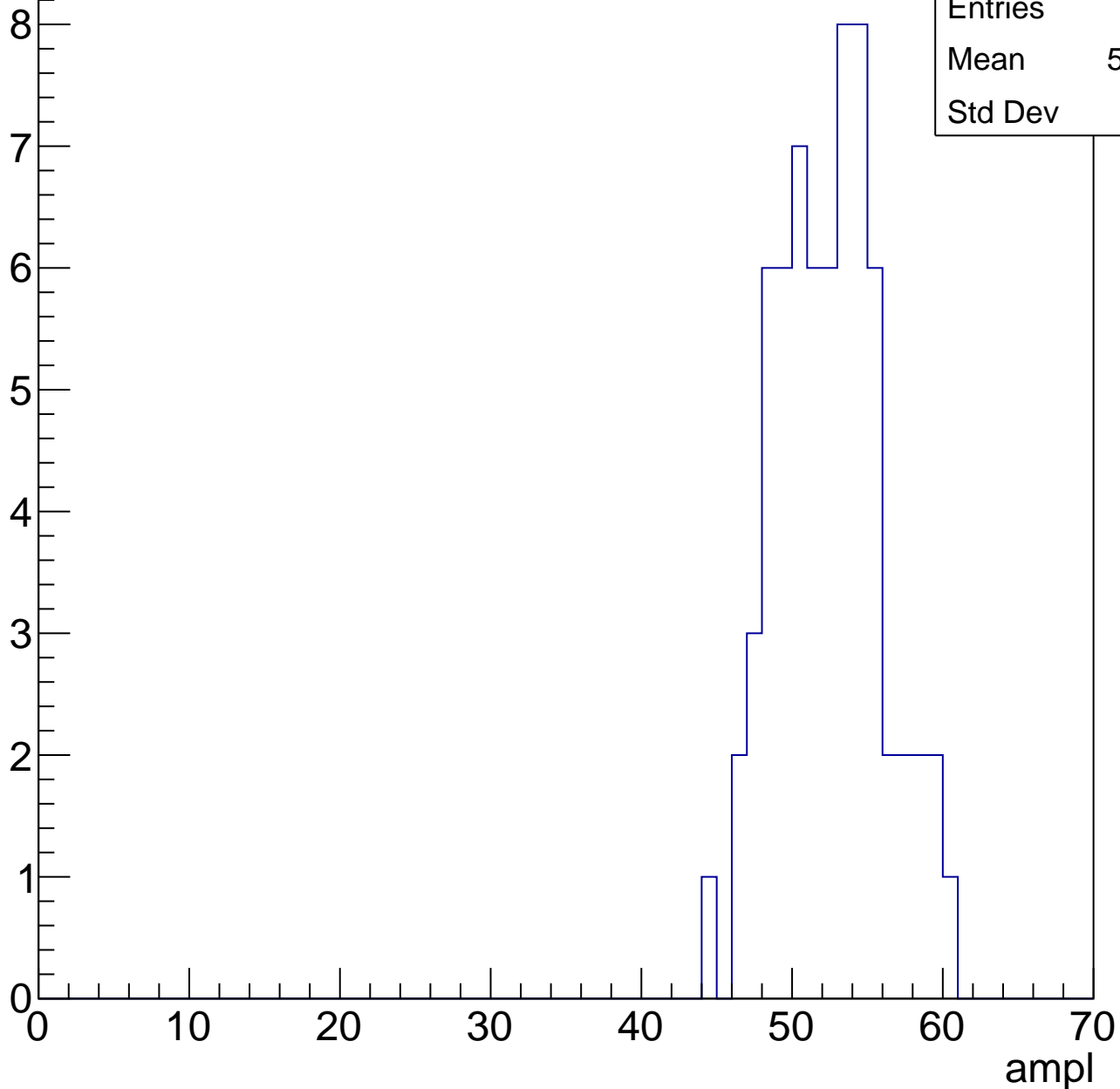


# B0L000S, U10-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

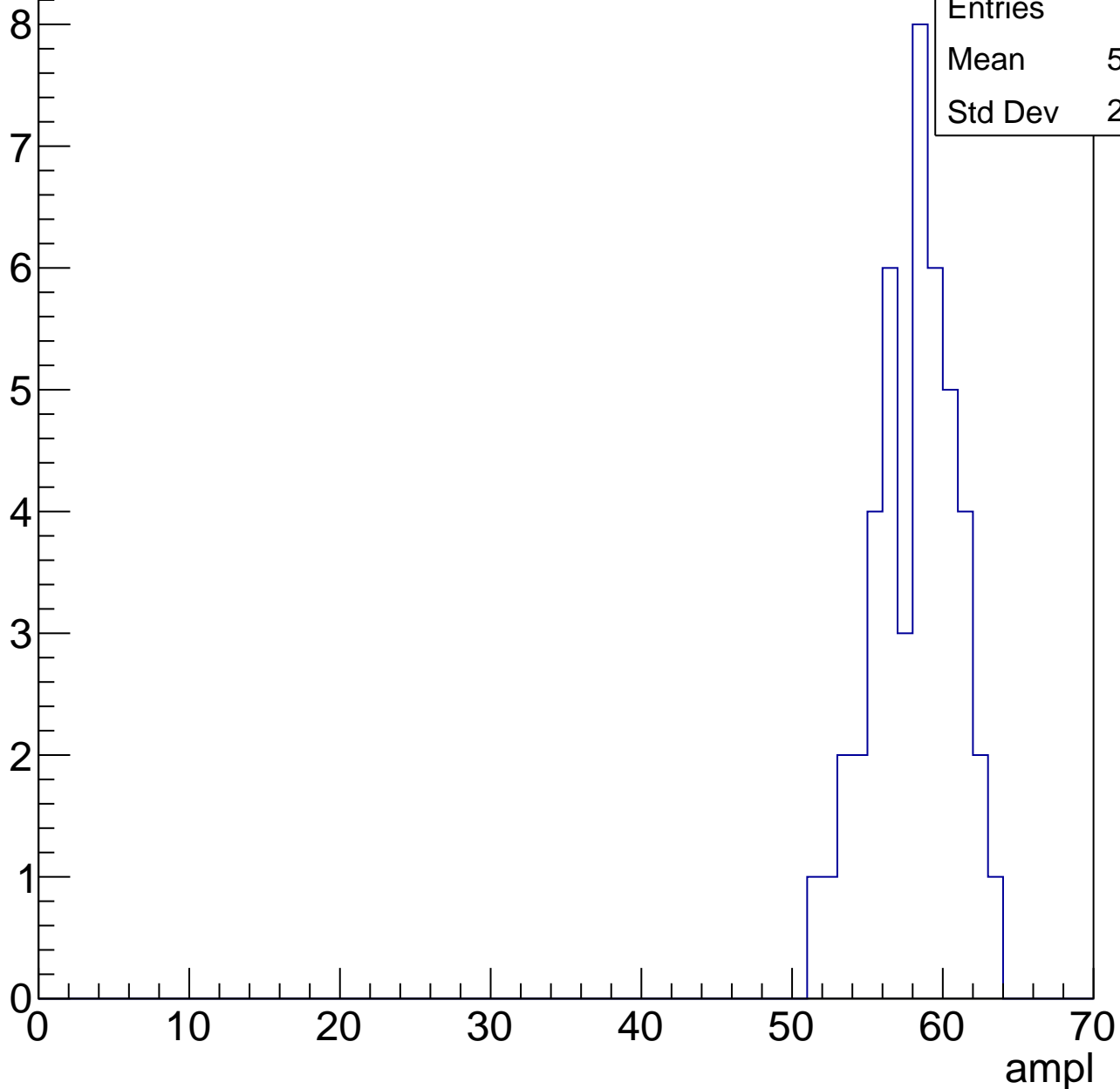
Entries	68
Mean	51.96
Std Dev	3.44



# B0L000S, U10-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



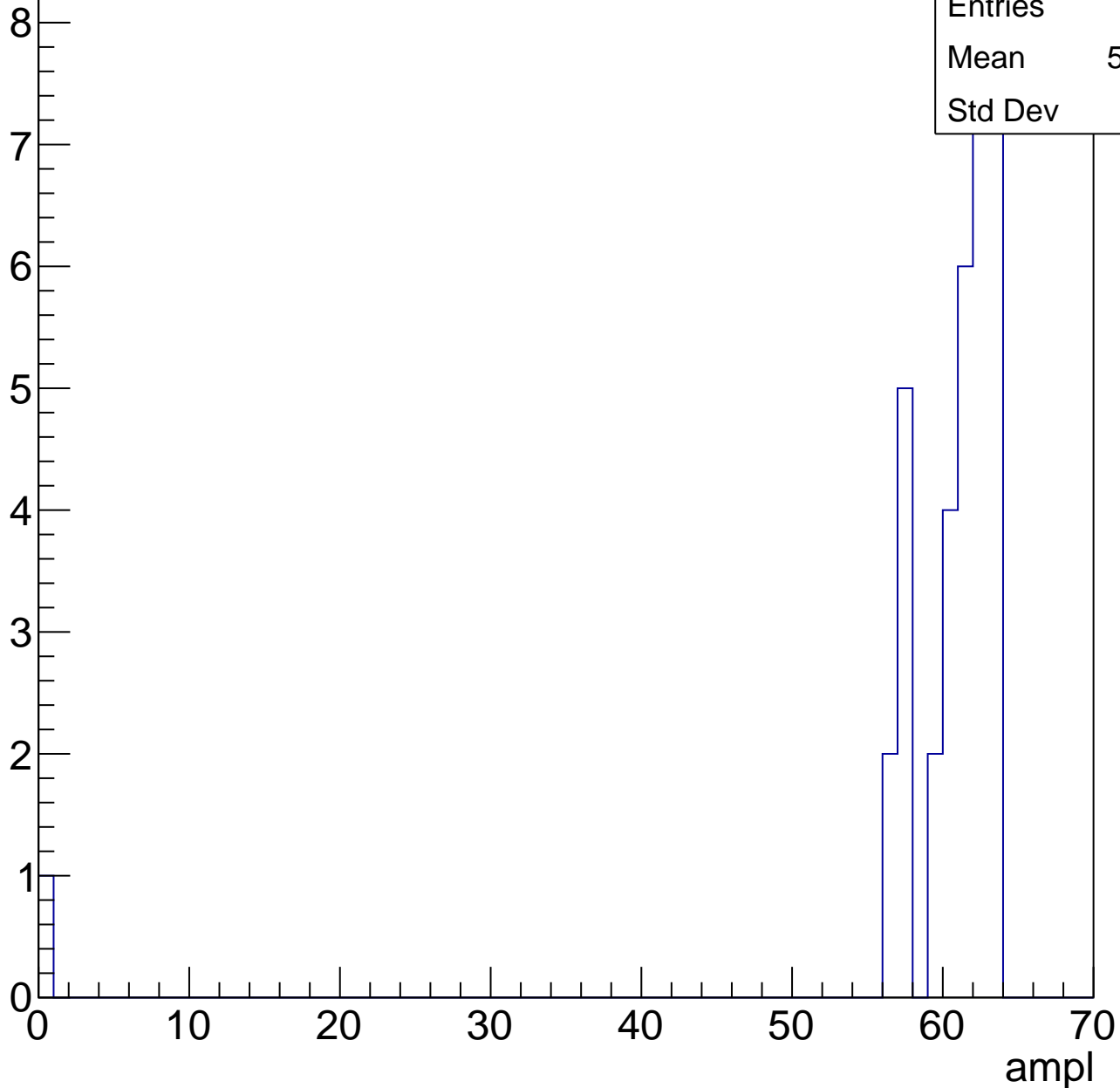
Entries	45
Mean	57.62
Std Dev	2.759

# B0L000S, U10-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	58.92
Std Dev	10.2



# B0L000S, U10-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U10-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch40, adc0

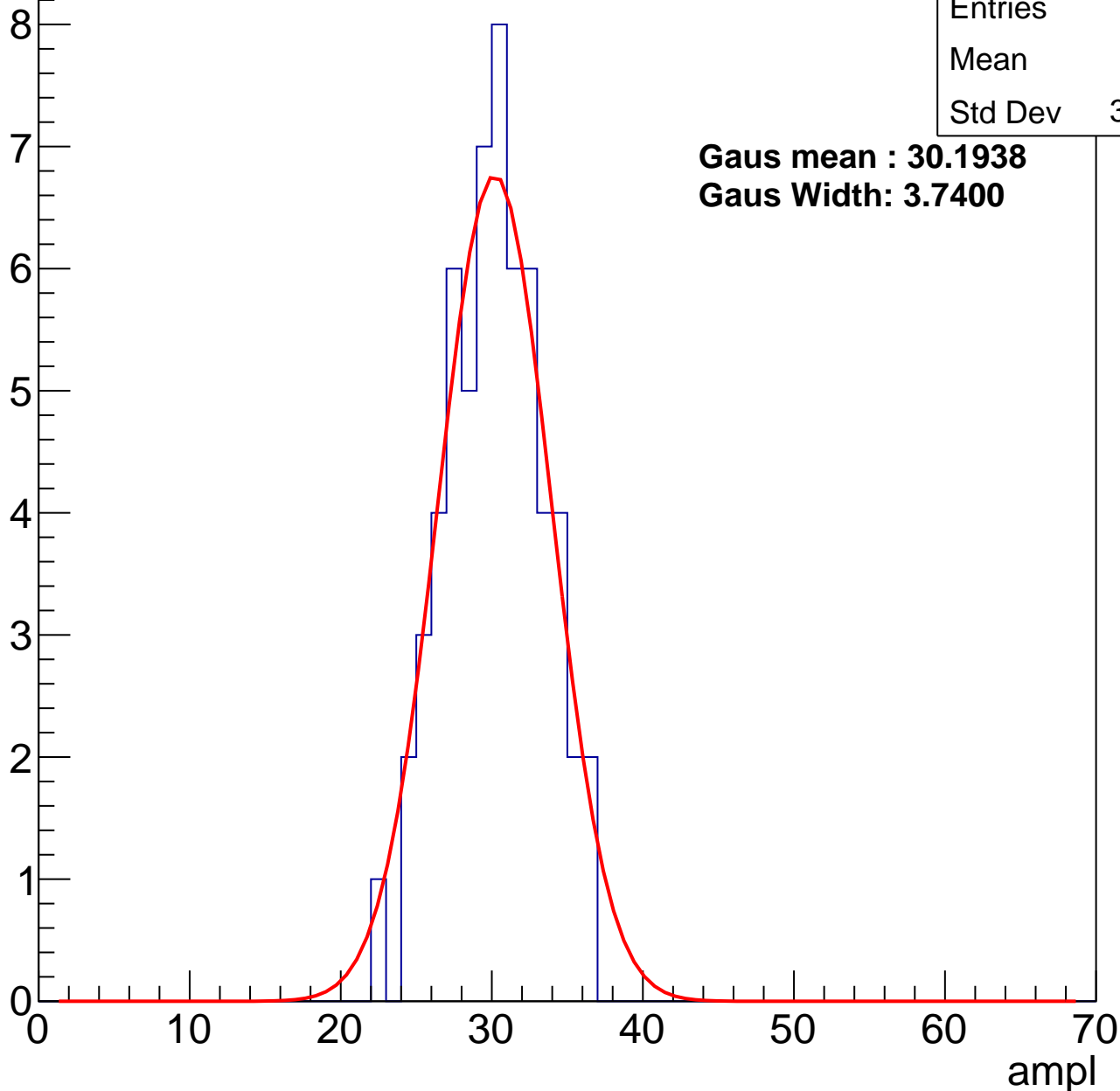
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.7
Std Dev	3.169

**Gaus mean : 30.1938**

**Gaus Width: 3.7400**



# B0L000S, U10-ch40, adc1

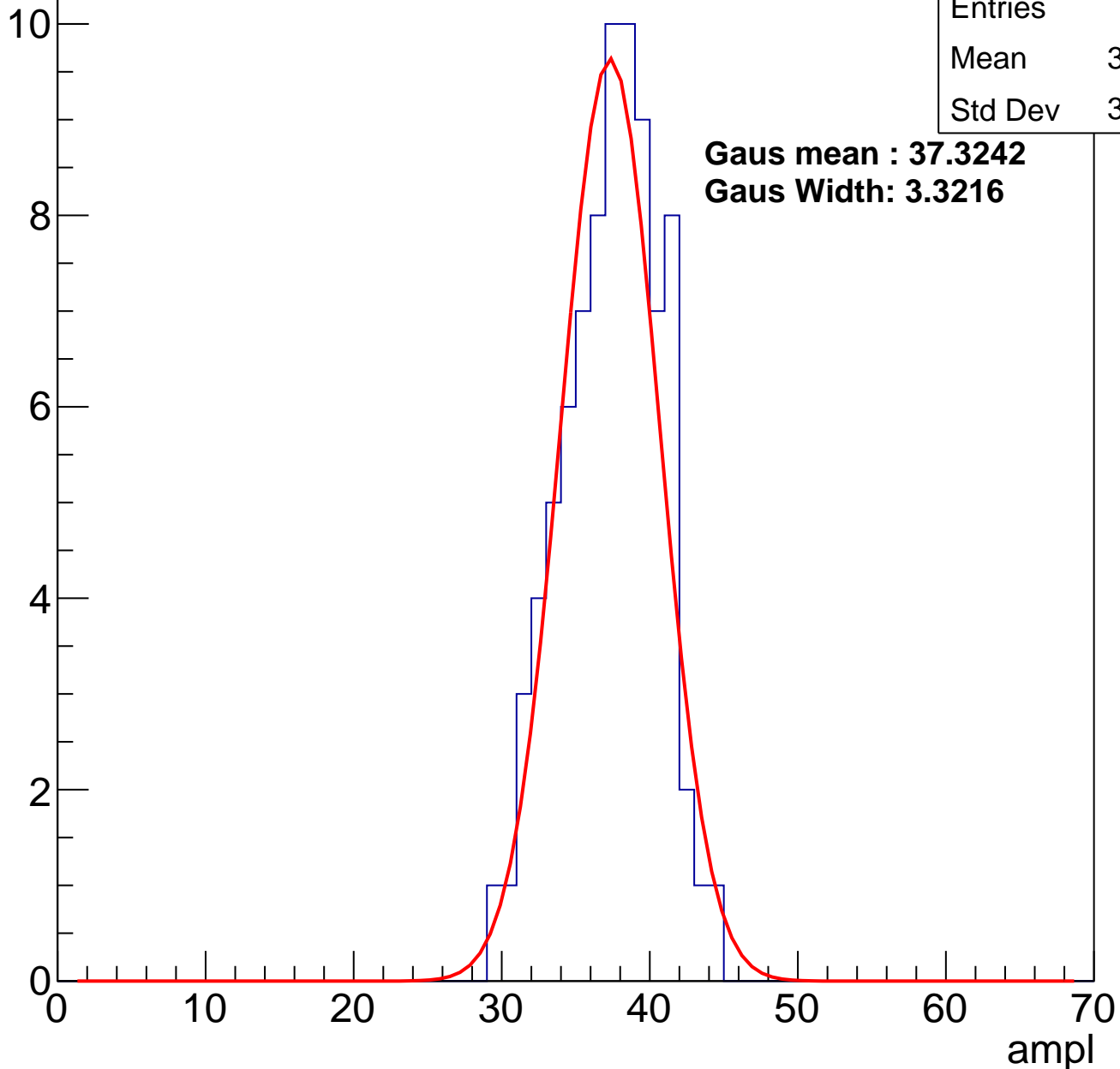
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	36.89
Std Dev	3.223

**Gaus mean : 37.3242**

**Gaus Width: 3.3216**

Entry



# B0L000S, U10-ch40, adc2

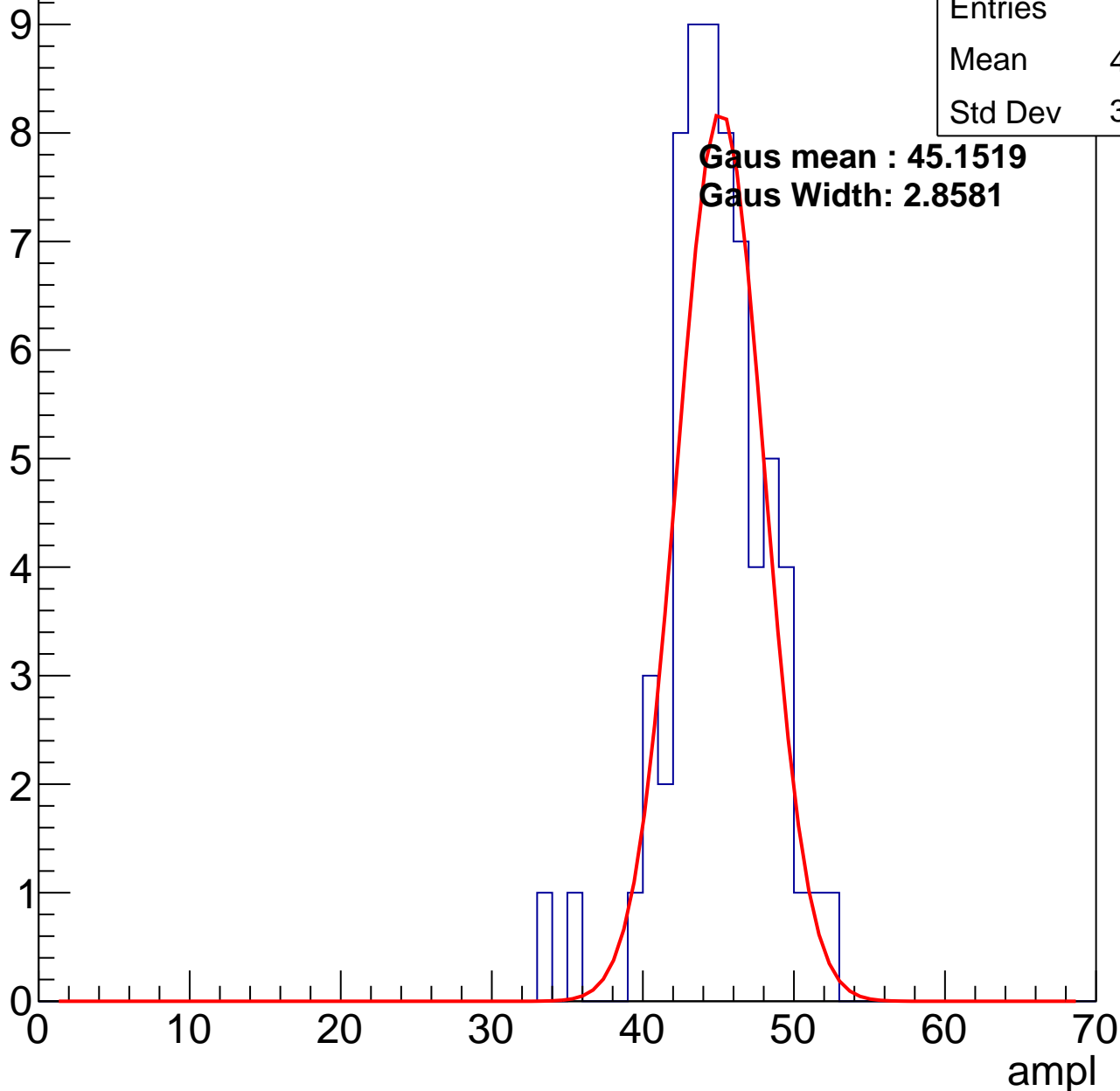
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.42
Std Dev	3.337

**Gaus mean : 45.1519**

**Gaus Width: 2.8581**

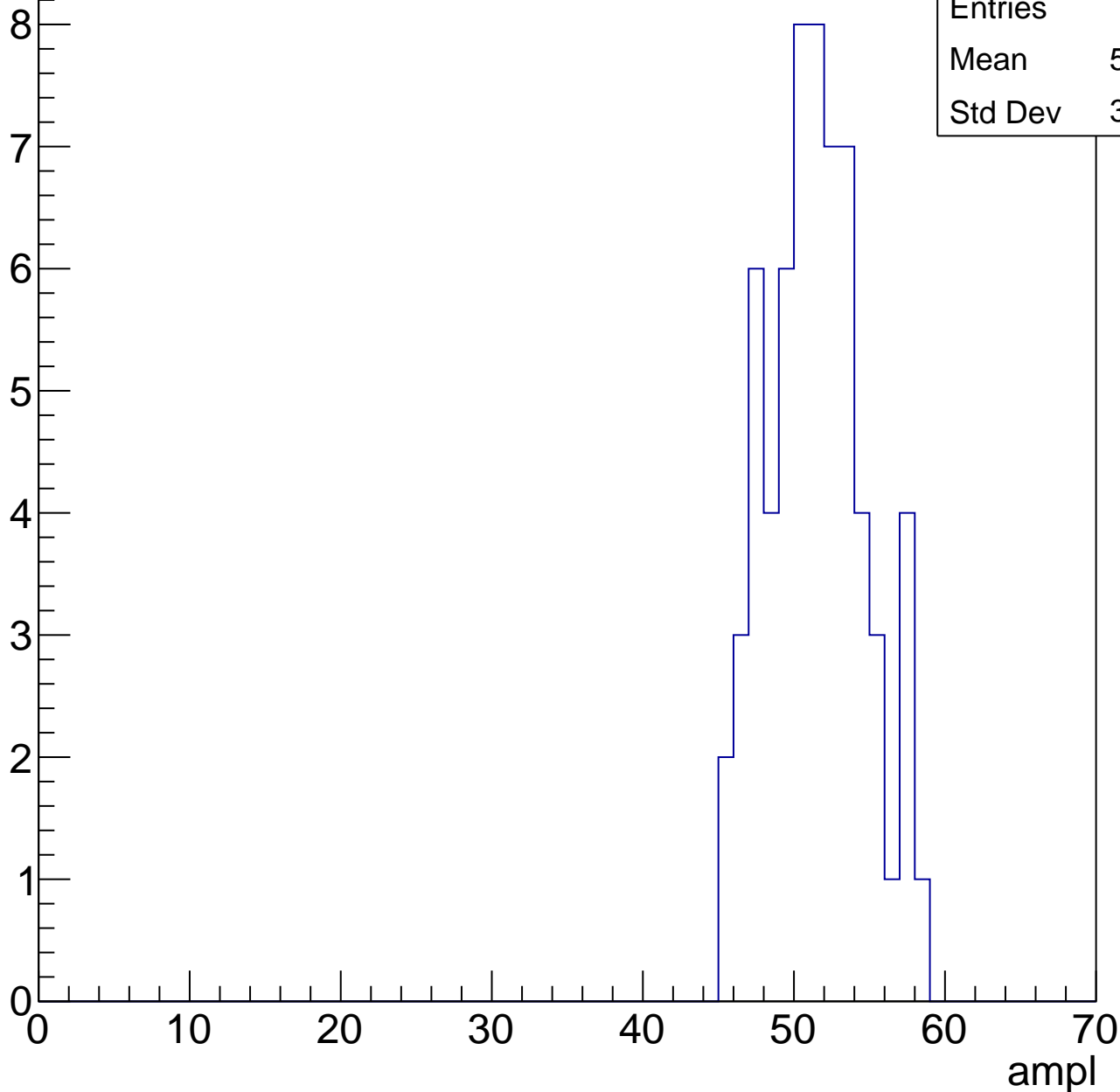


# B0L000S, U10-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	50.97
Std Dev	3.182

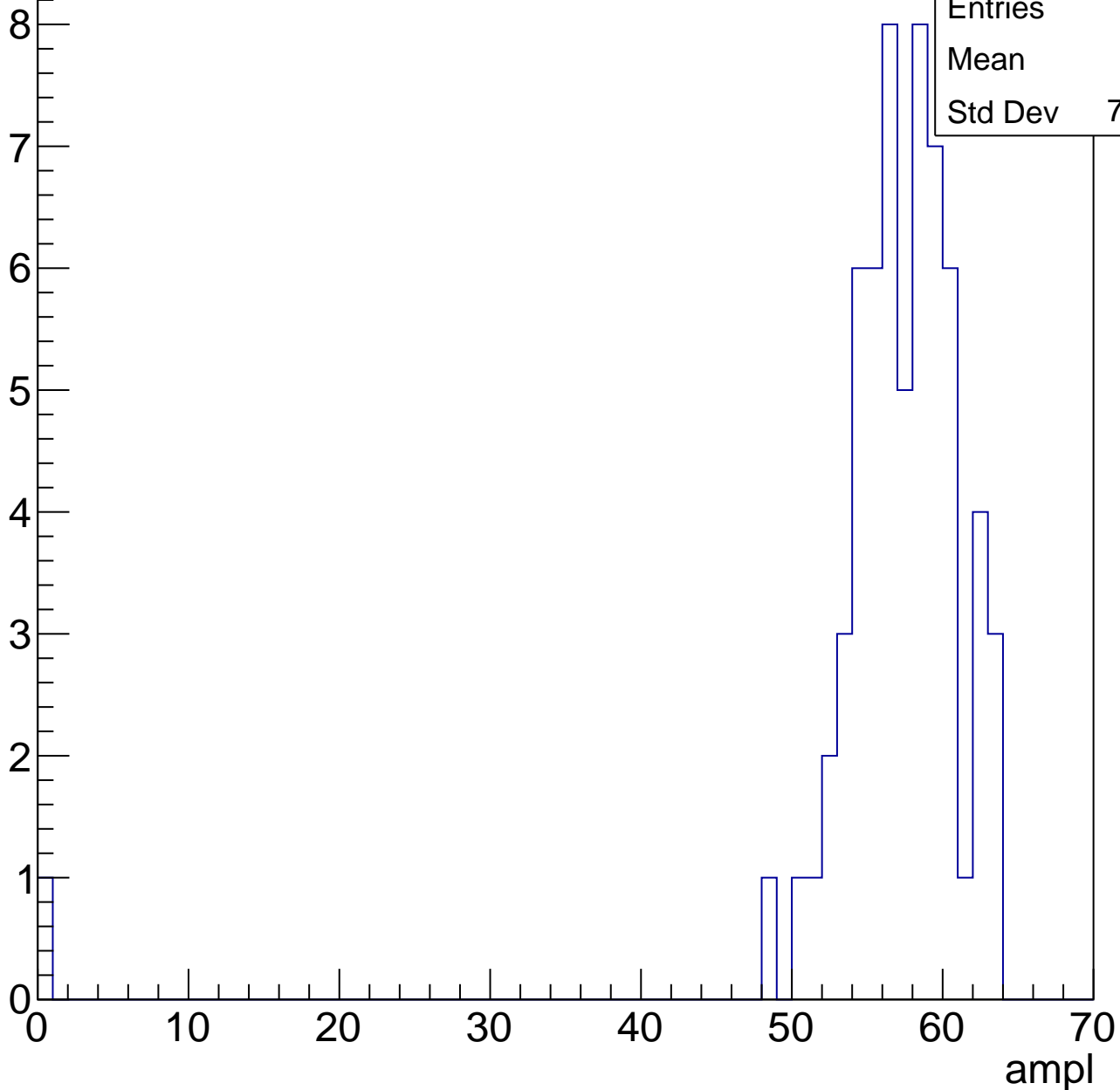


# B0L000S, U10-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	56.1
Std Dev	7.829

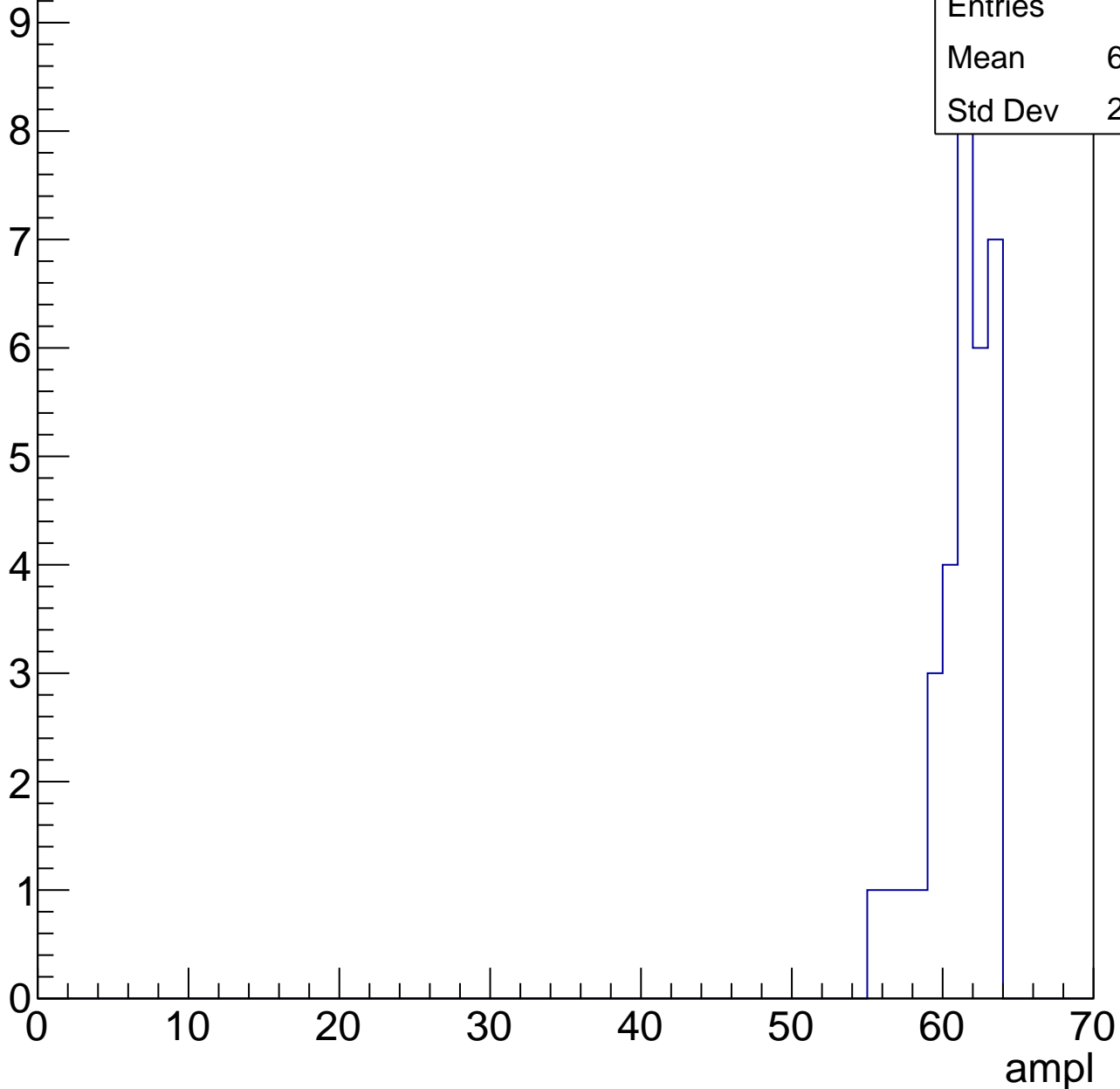


# B0L000S, U10-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	60.76
Std Dev	2.016



# B0L000S, U10-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch41, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	32.37
Std Dev	3.508

**Gaus mean : 32.4614**

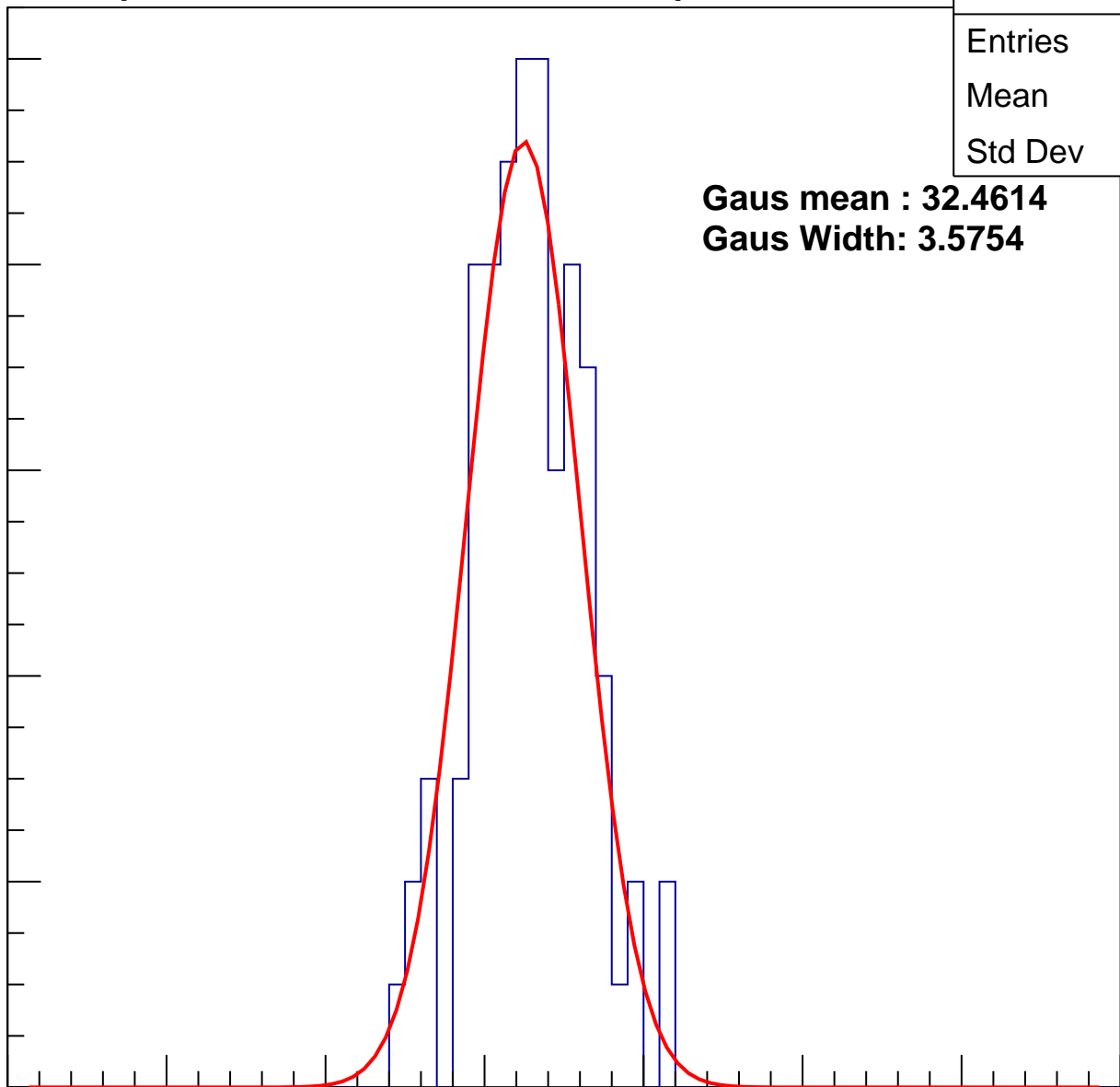
**Gaus Width: 3.5754**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch41, adc1

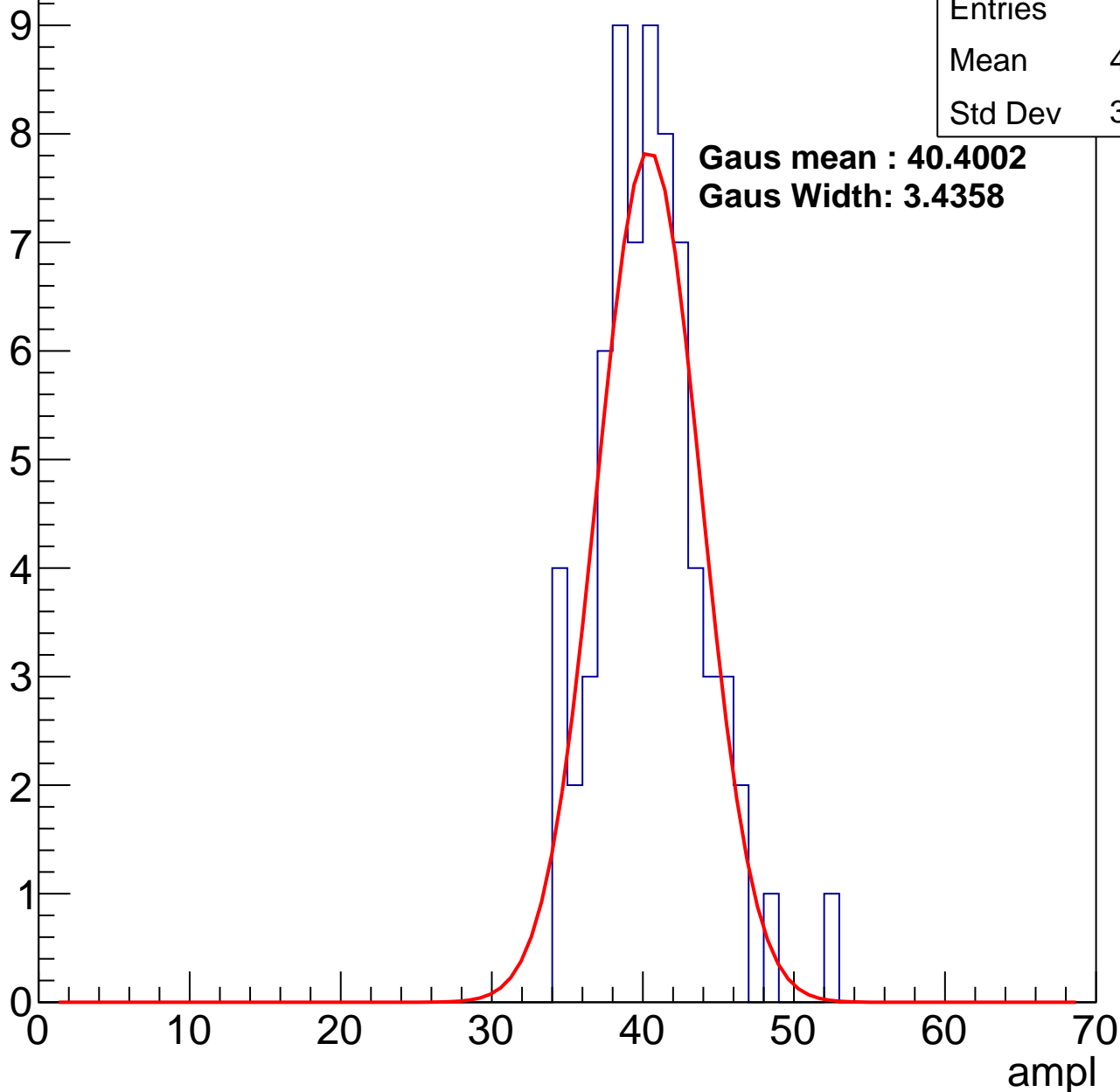
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	40.06
Std Dev	3.434

**Gaus mean : 40.4002**

**Gaus Width: 3.4358**



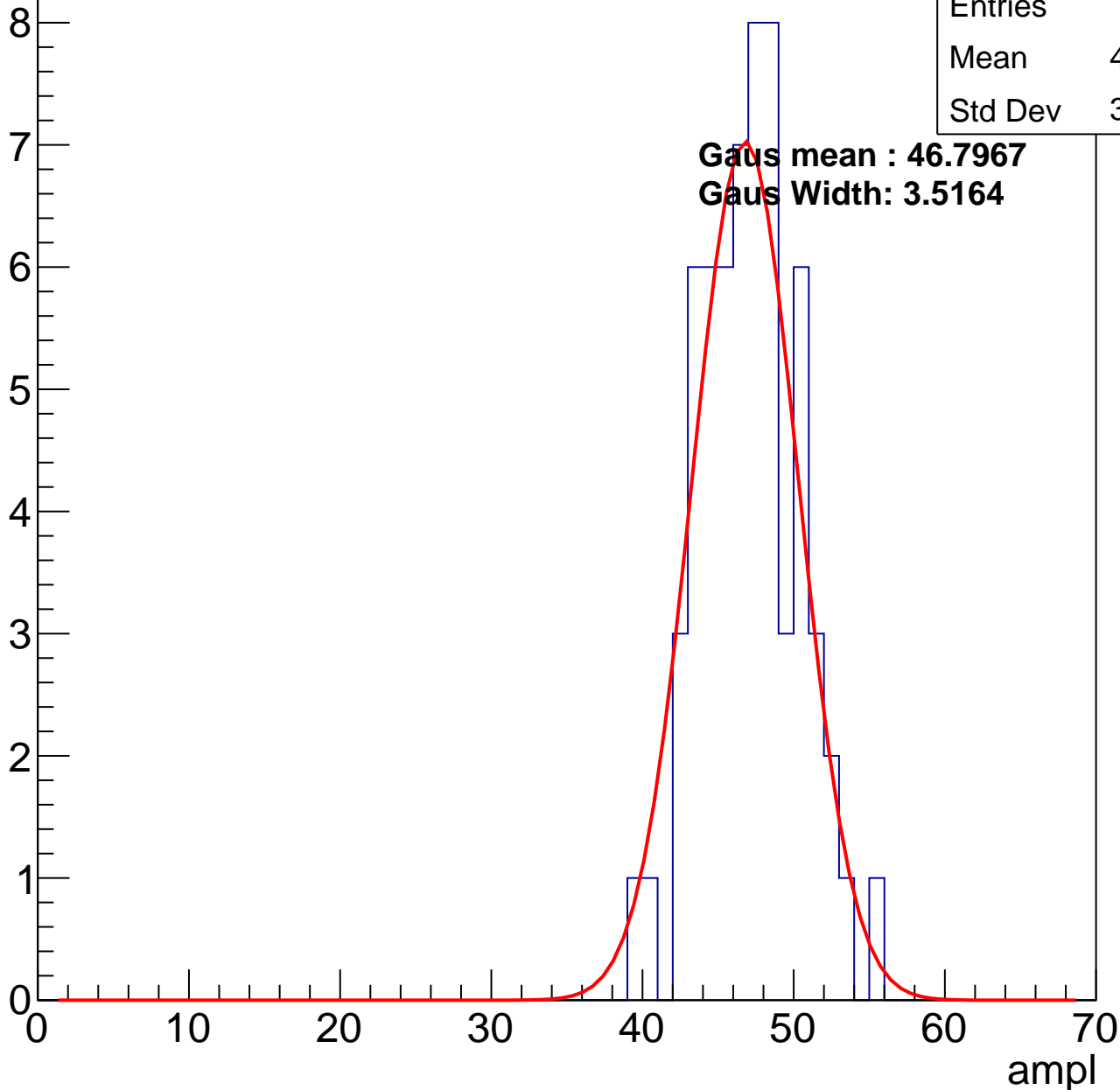
# B0L000S, U10-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	46.63
Std Dev	3.194

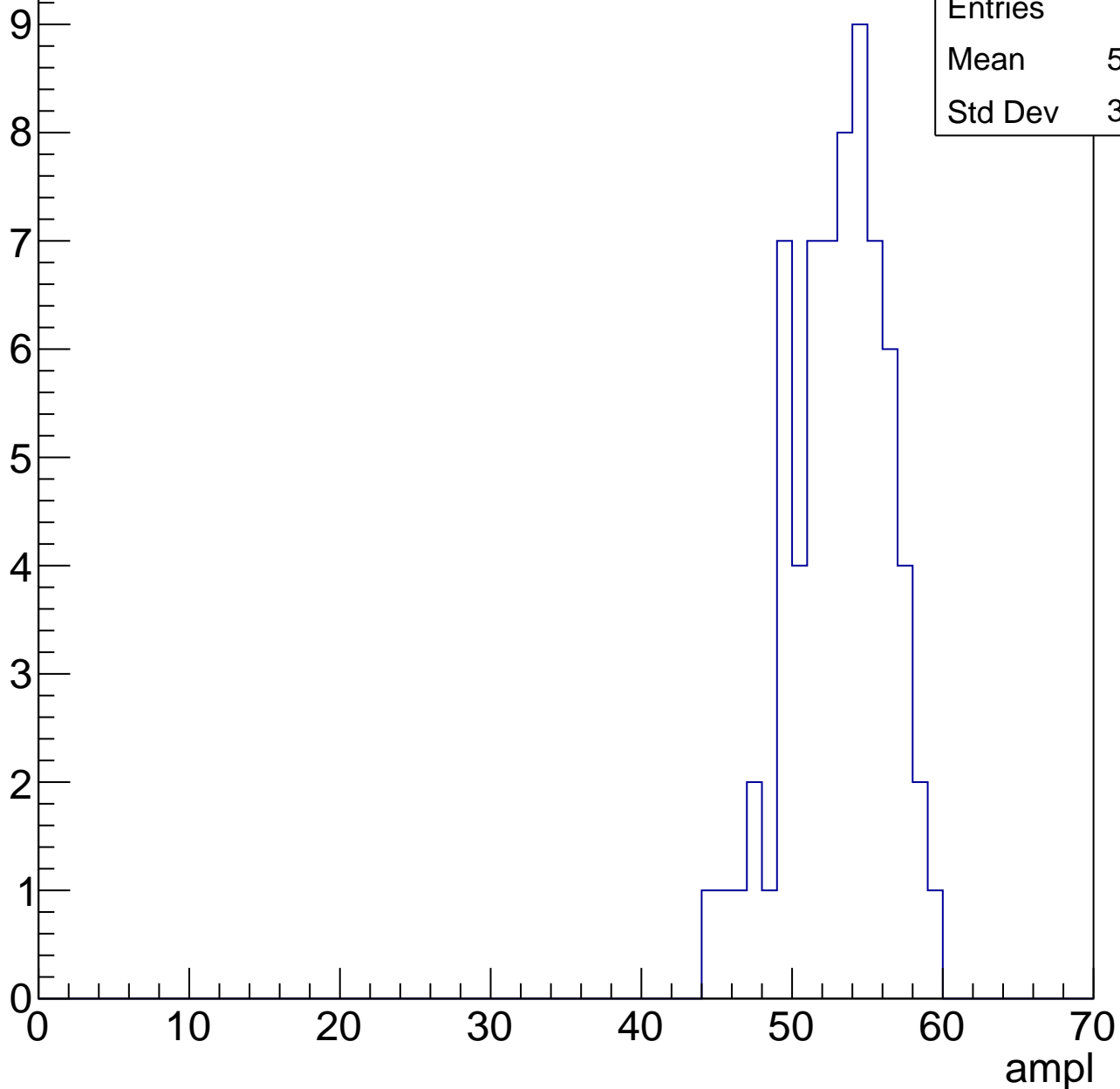
**Gaus mean : 46.7967**  
**Gaus Width: 3.5164**



# B0L000S, U10-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

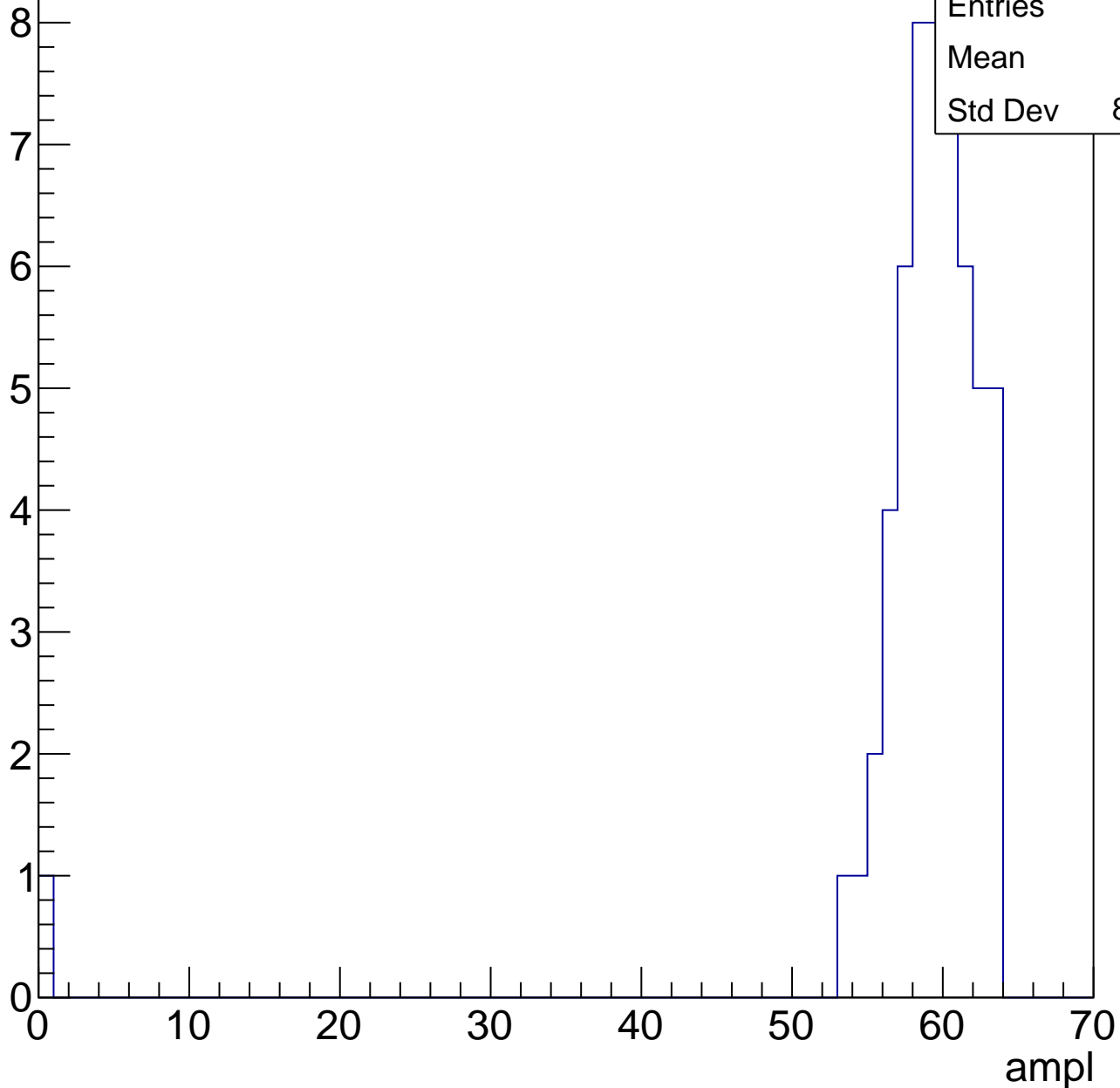


Entries	68
Mean	52.57
Std Dev	3.214

# B0L000S, U10-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

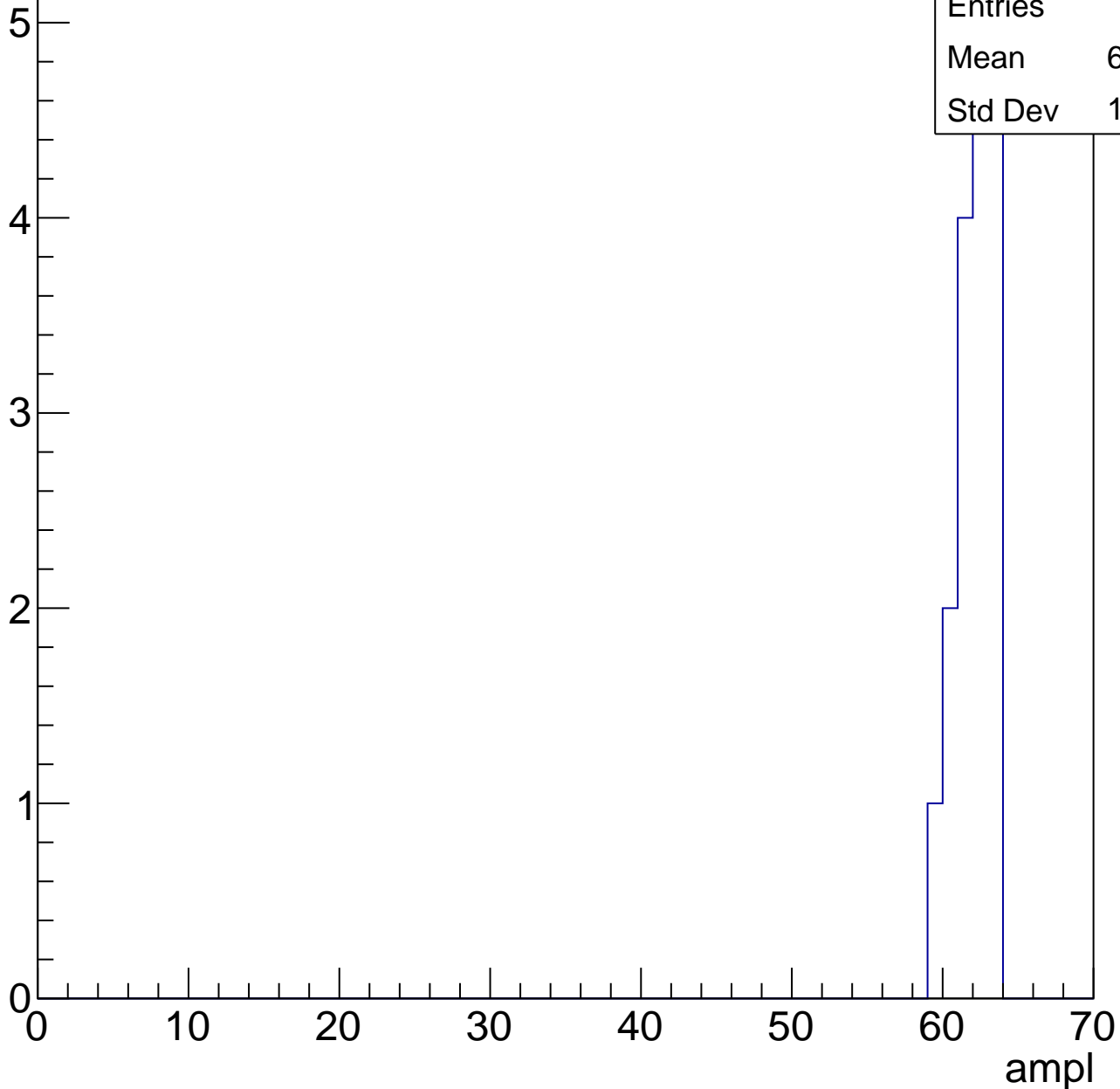


# B0L000S, U10-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.65
Std Dev	1.185



# B0L000S, U10-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U10-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	32.51
Std Dev	5.339

**Gaus mean : 34.0781**

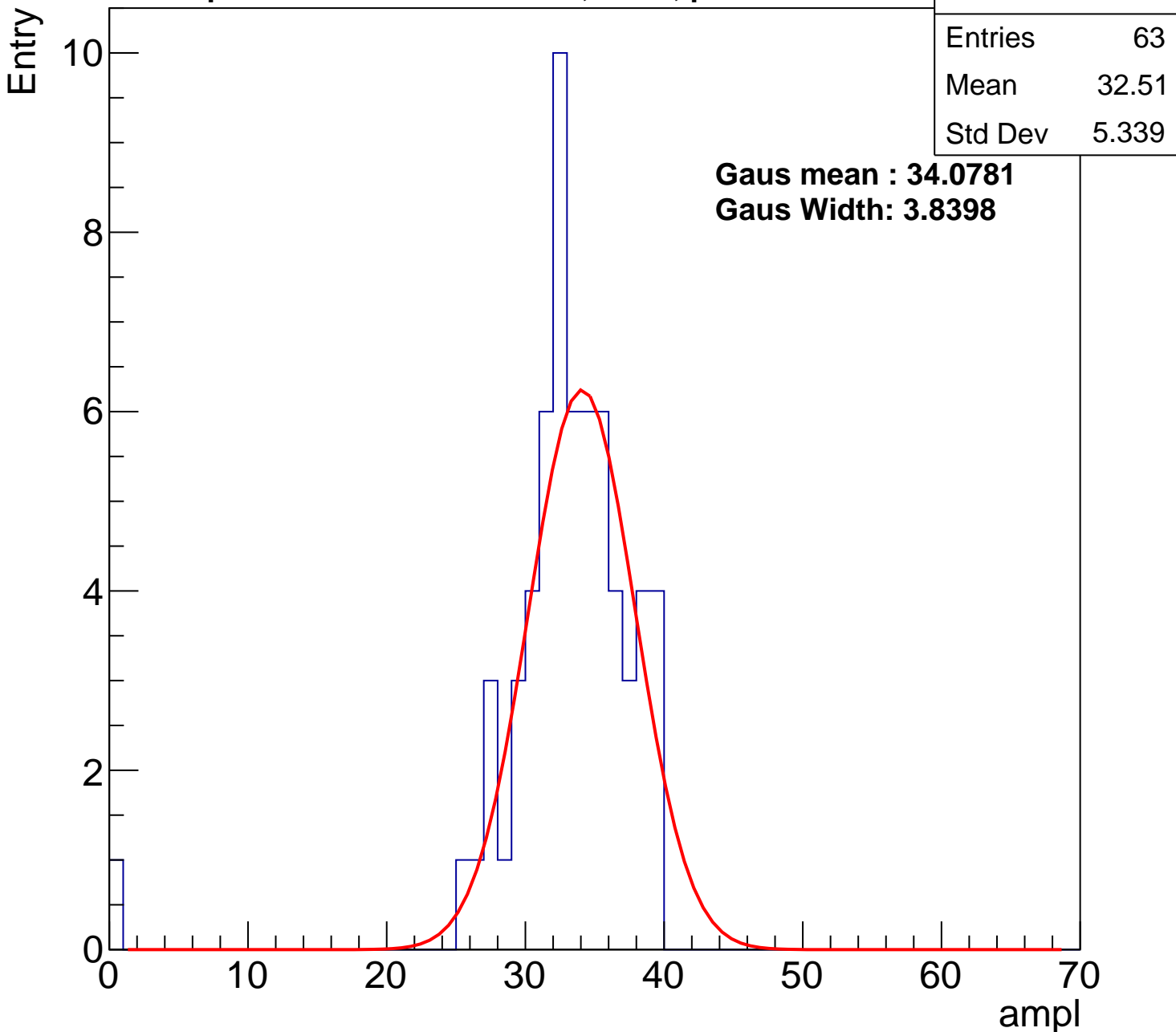
**Gaus Width: 3.8398**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch42, adc1

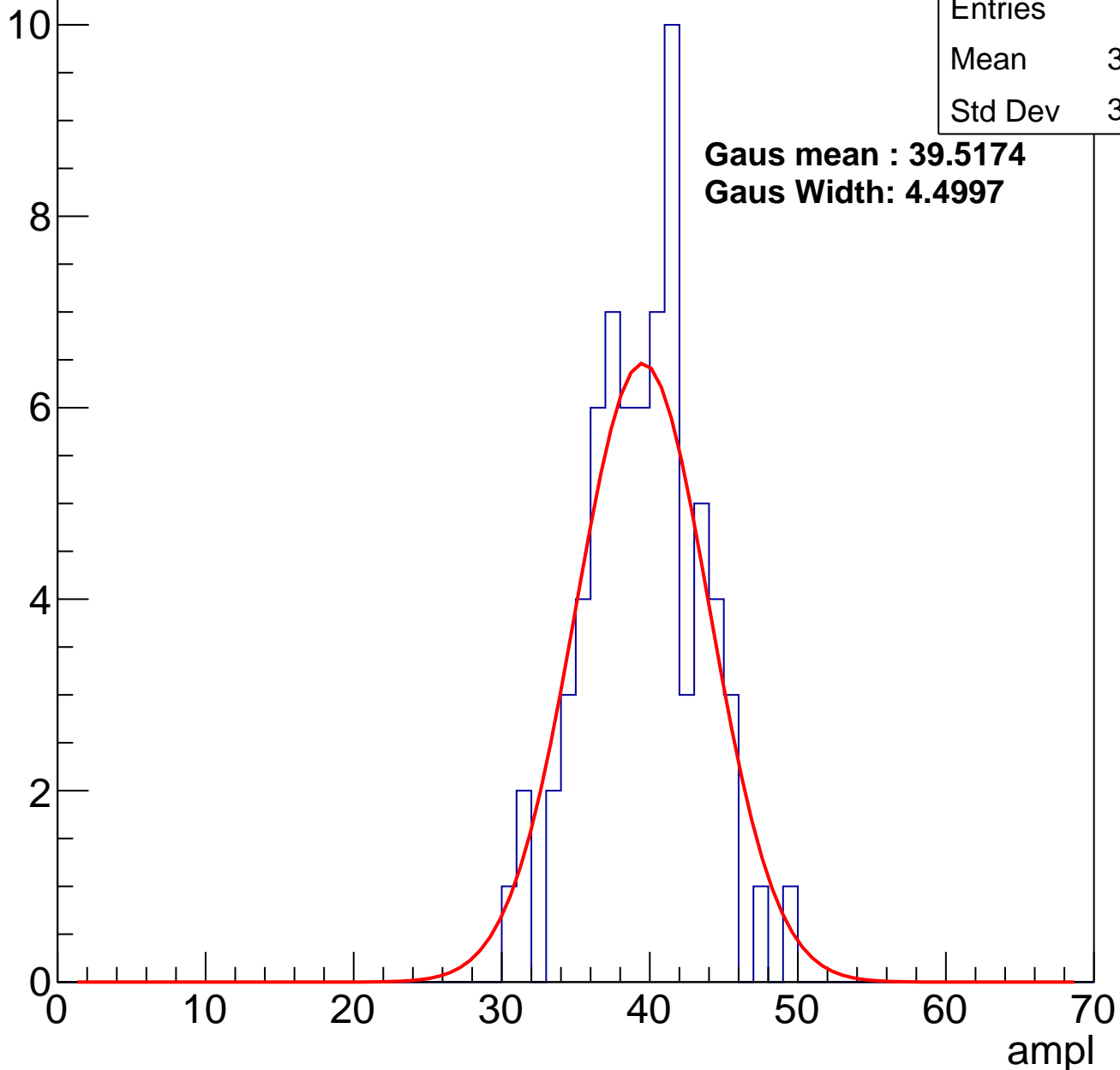
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	39.08
Std Dev	3.815

**Gaus mean : 39.5174**

**Gaus Width: 4.4997**

Entry



# B0L000S, U10-ch42, adc2

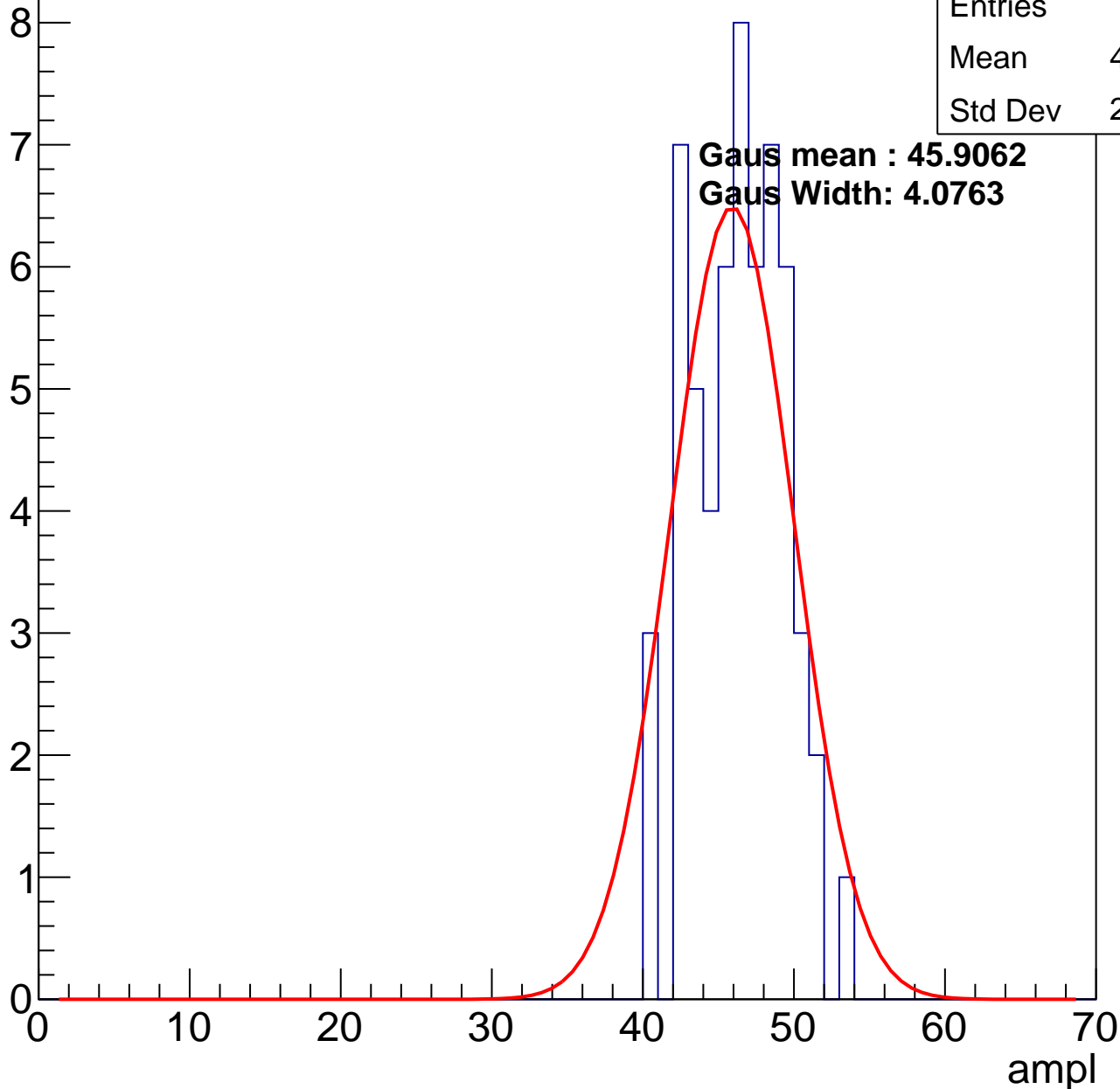
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.86
Std Dev	2.997

**Gaus mean : 45.9062**

**Gaus Width: 4.0763**

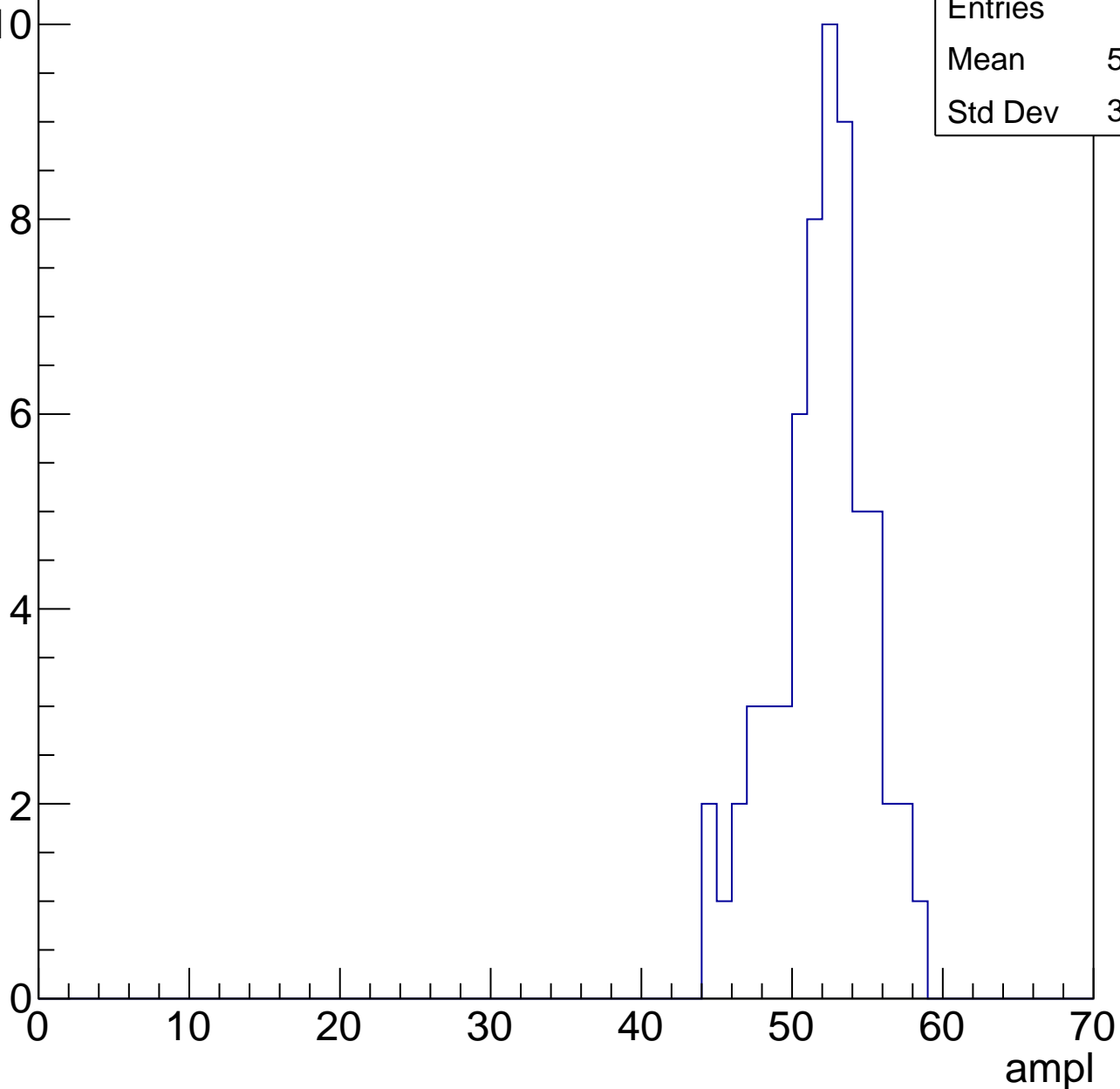


# B0L000S, U10-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	51.47
Std Dev	3.125



# B0L000S, U10-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	58.49
Std Dev	2.83

10

8

6

4

2

0

0

10

20

30

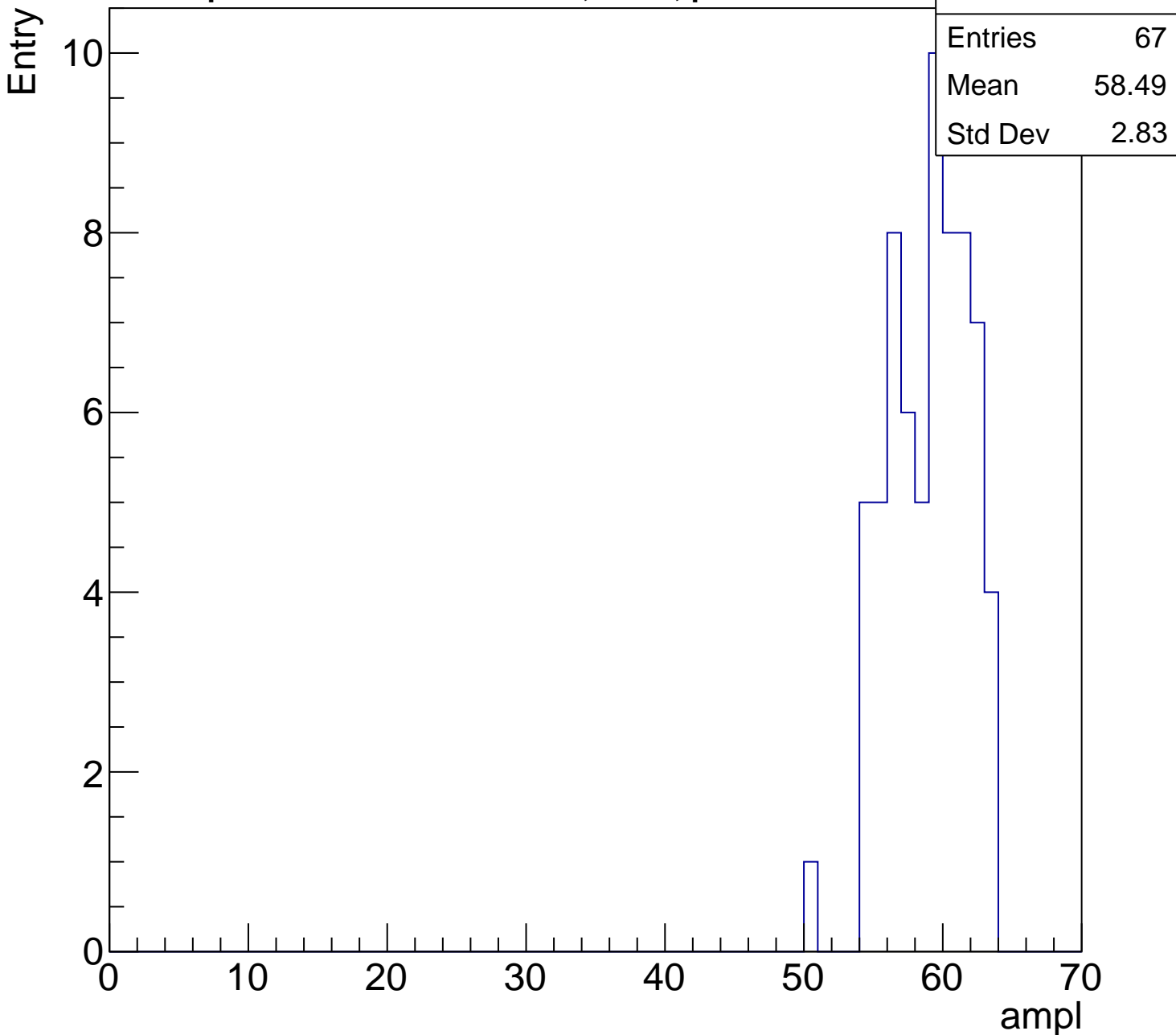
40

50

60

70

ampl

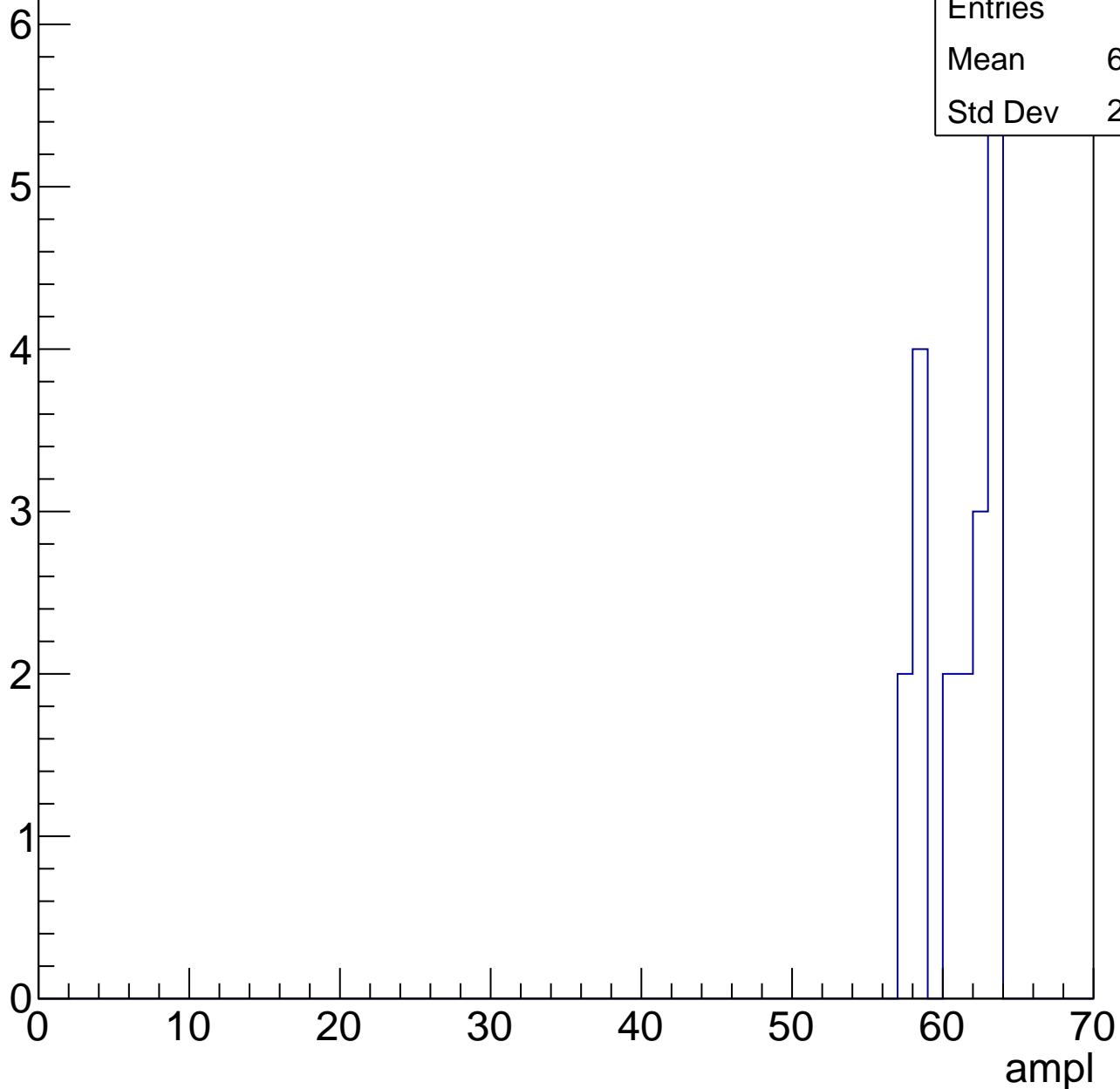


# B0L000S, U10-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	60.63
Std Dev	2.229



# B0L000S, U10-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U10-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	101
Mean	30.27
Std Dev	3.747

**Gaus mean : 31.0862**

**Gaus Width: 4.0409**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L000S, U10-ch43, adc1

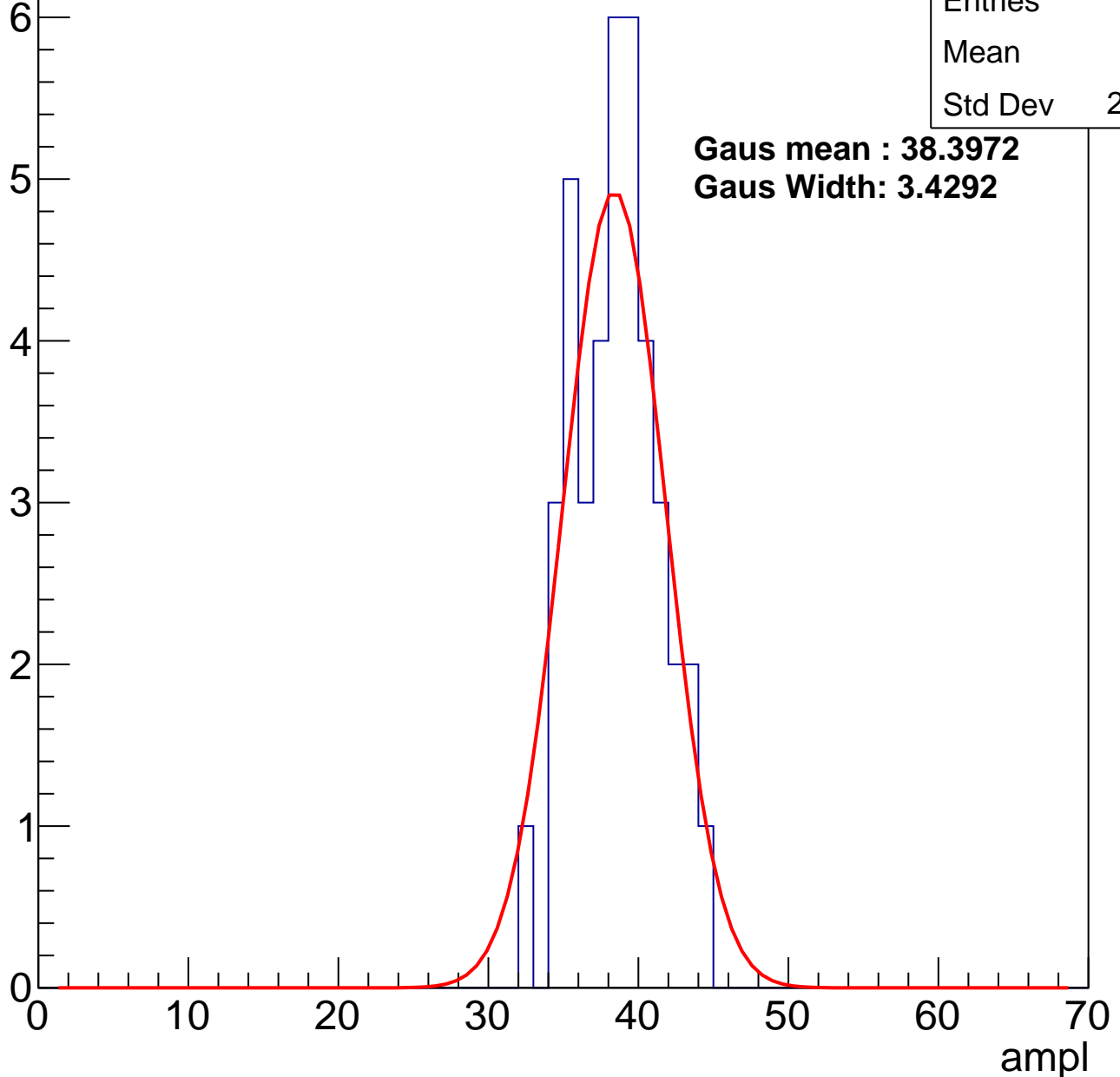
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	38.1
Std Dev	2.791

**Gaus mean : 38.3972**

**Gaus Width: 3.4292**



# B0L000S, U10-ch43, adc2

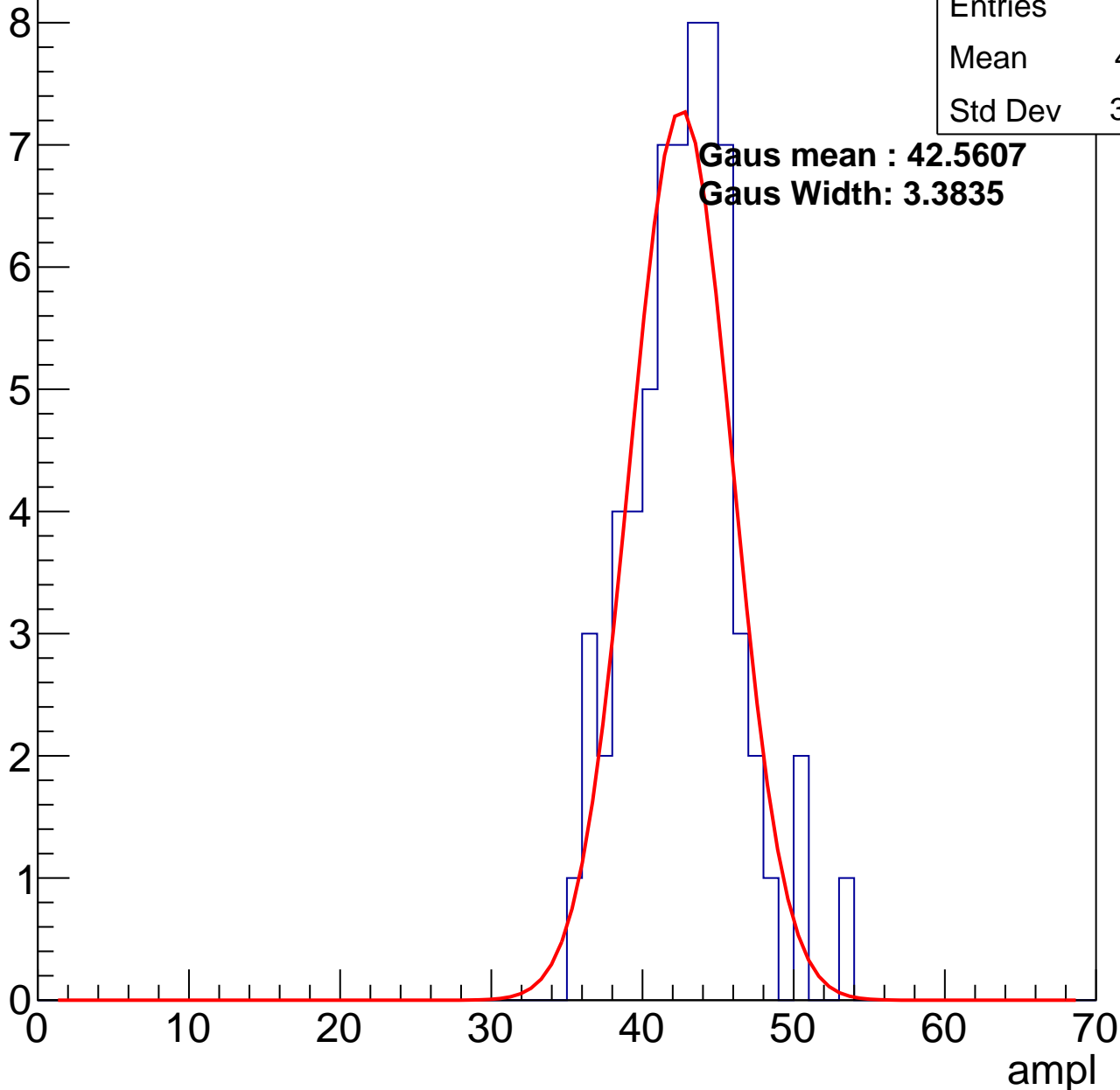
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.31
Std Dev	3.538

**Gaus mean : 42.5607**

**Gaus Width: 3.3835**



# B0L000S, U10-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

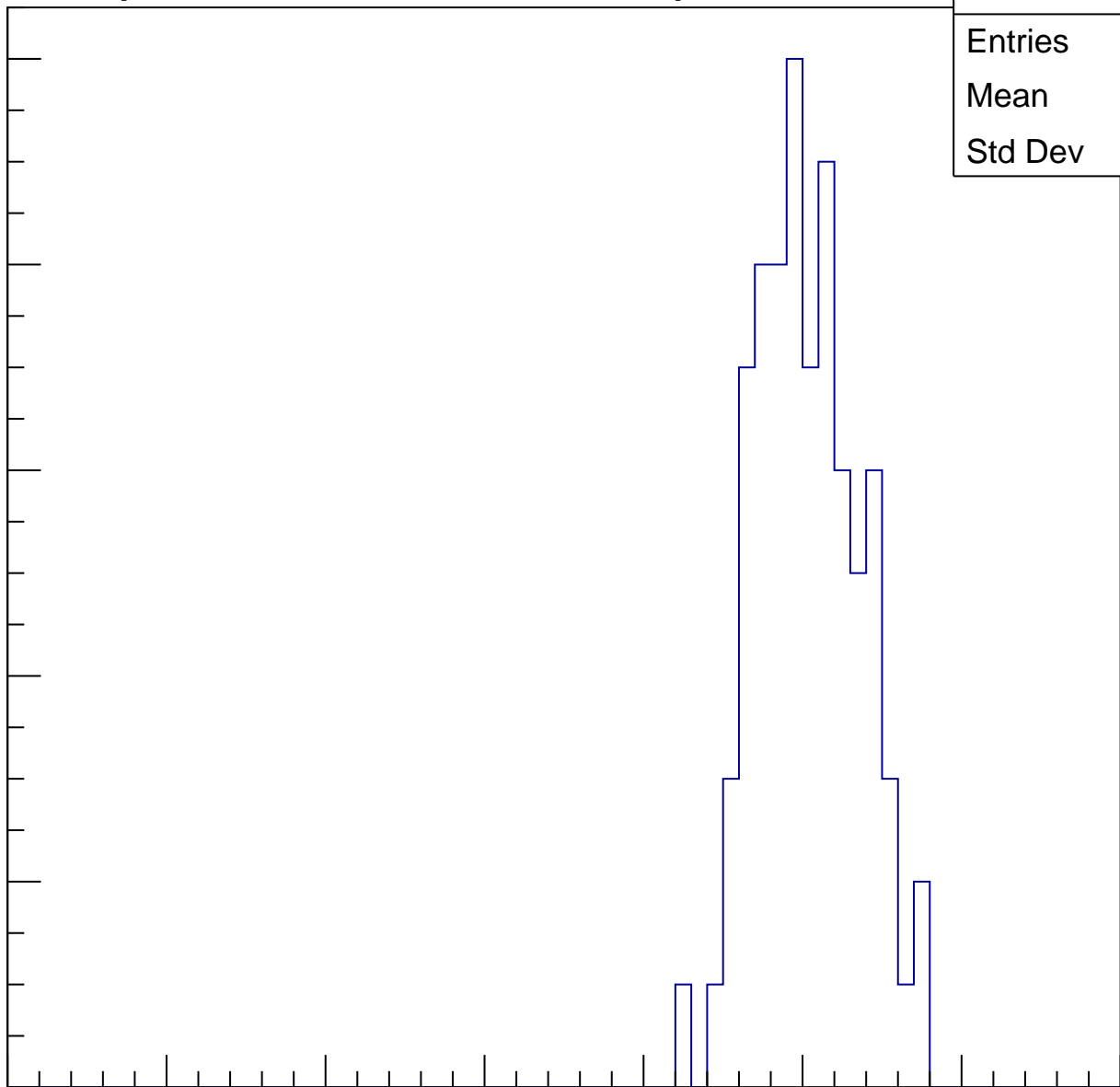
Entries	77
Mean	49.84
Std Dev	3.187

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

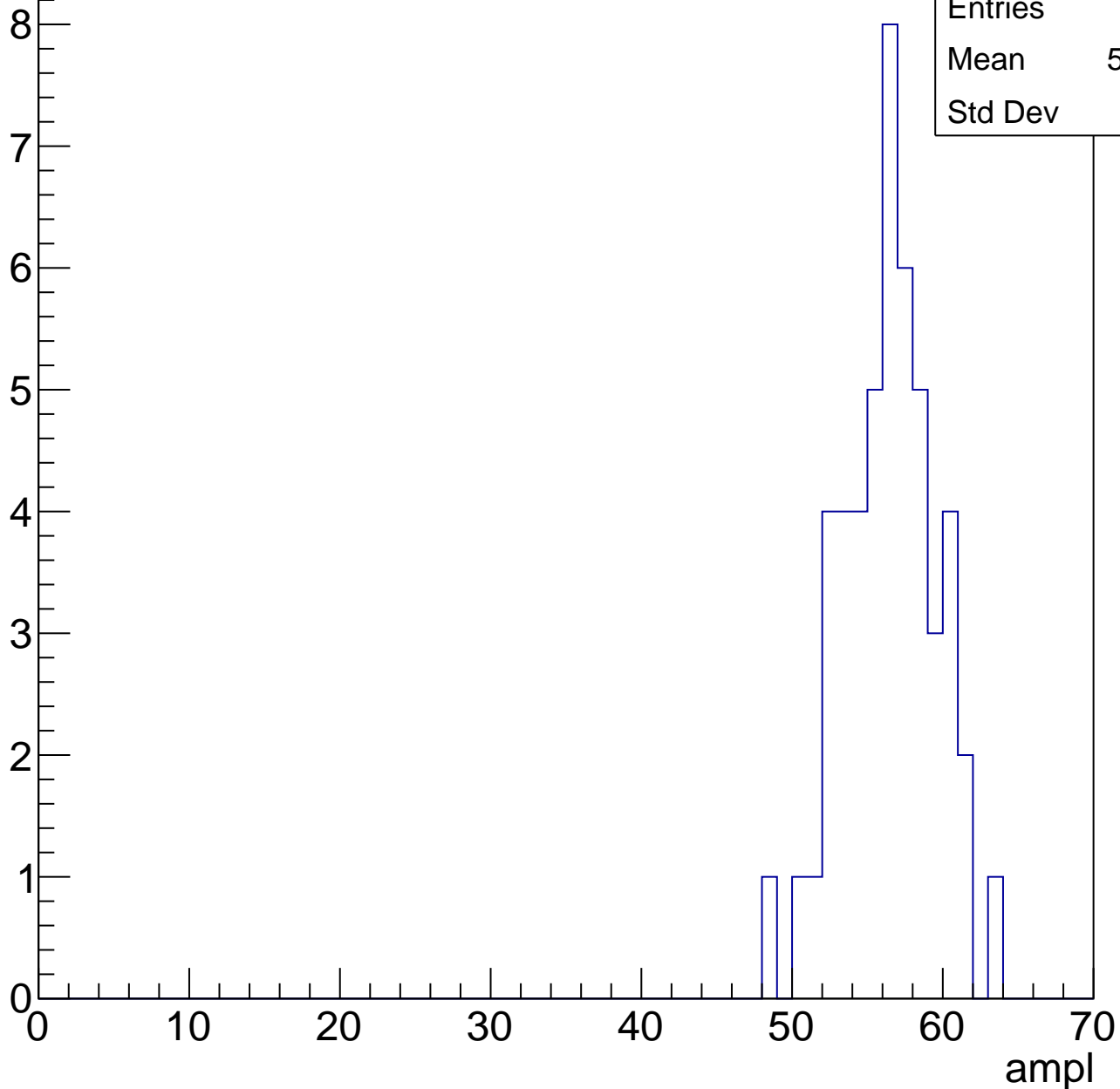


# B0L000S, U10-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.96
Std Dev	3.07

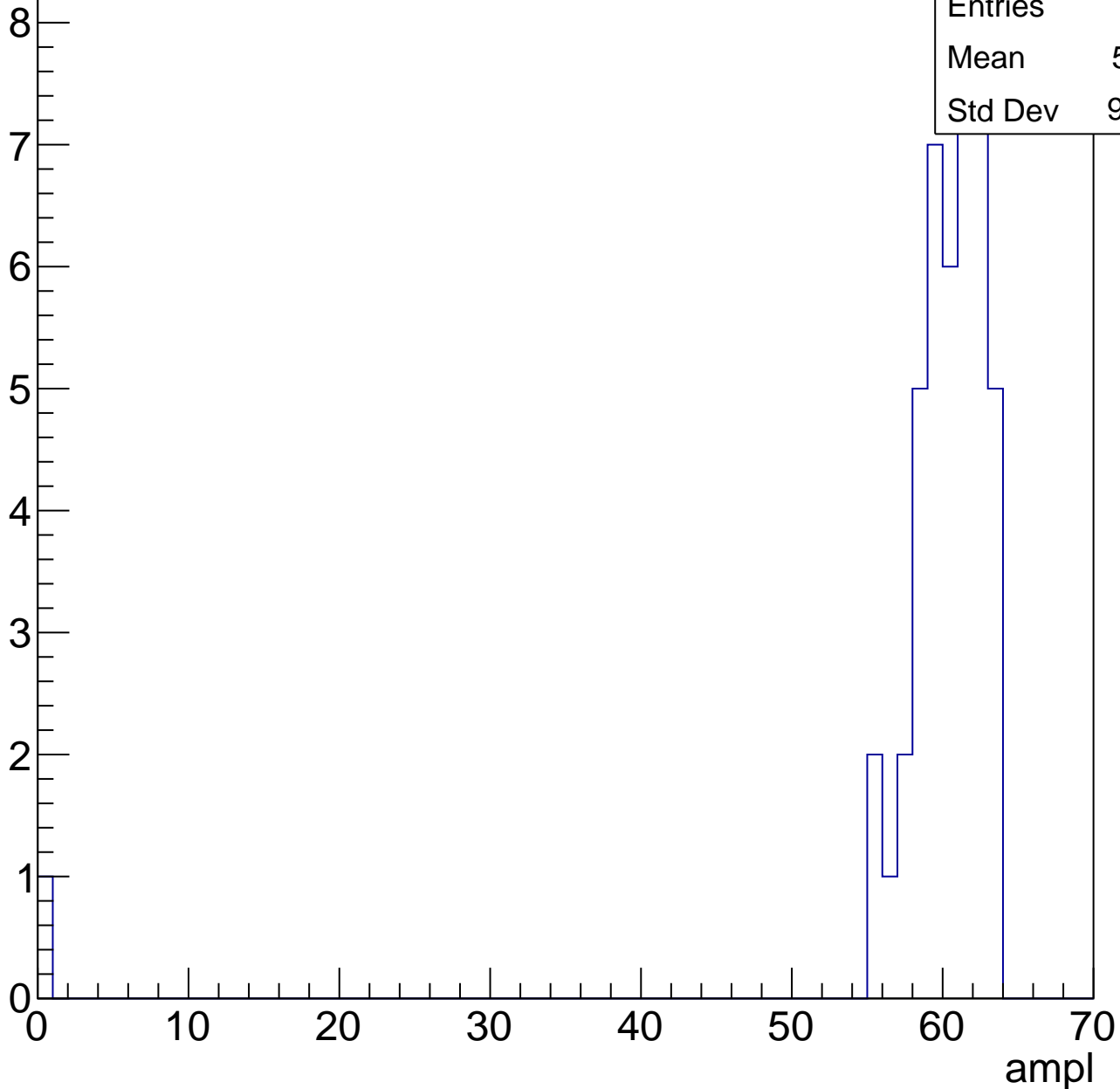


# B0L000S, U10-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.71
Std Dev	9.094



# B0L000S, U10-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch44, adc0

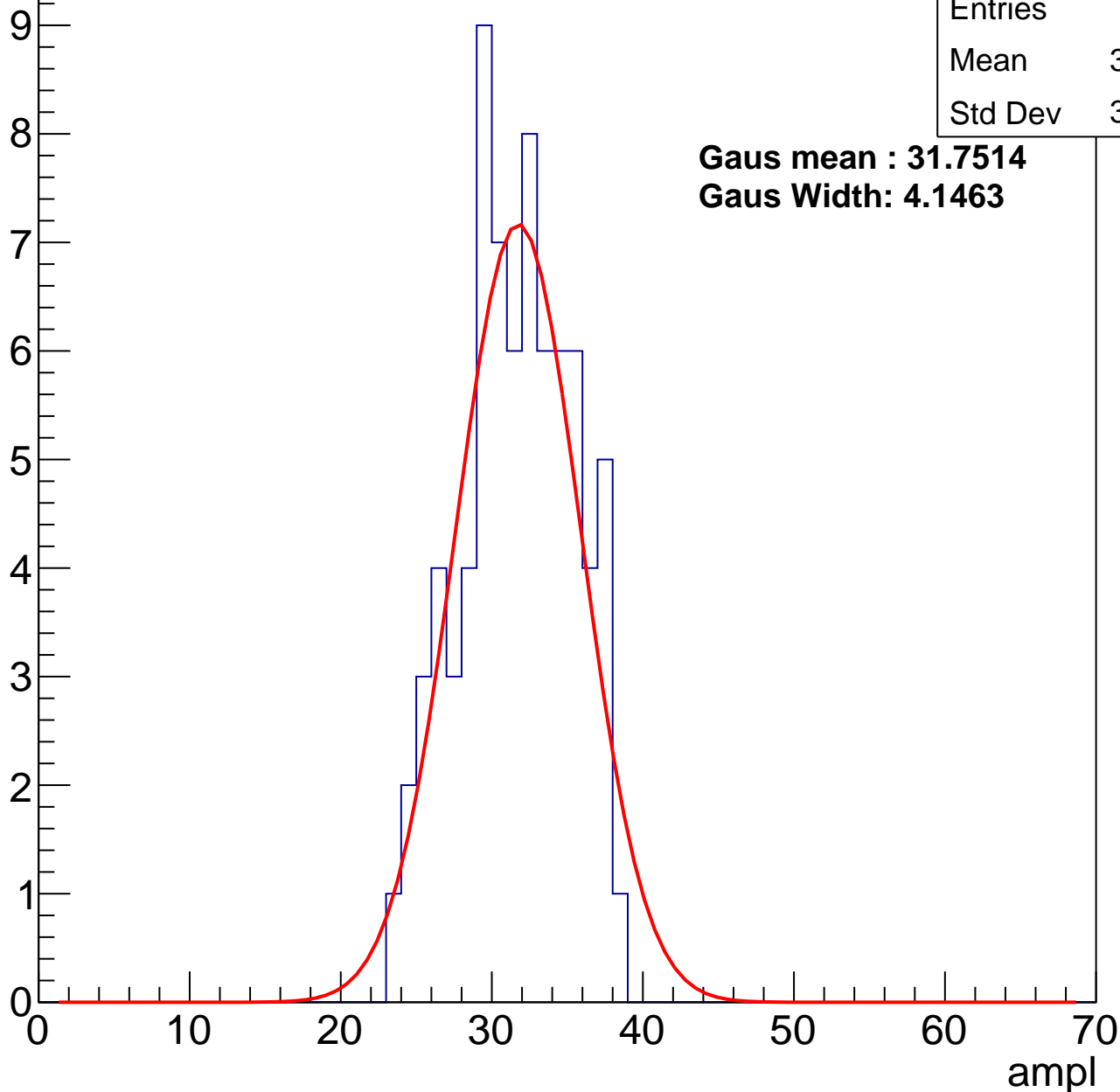
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	31.13
Std Dev	3.664

**Gaus mean : 31.7514**

**Gaus Width: 4.1463**



# B0L000S, U10-ch44, adc1

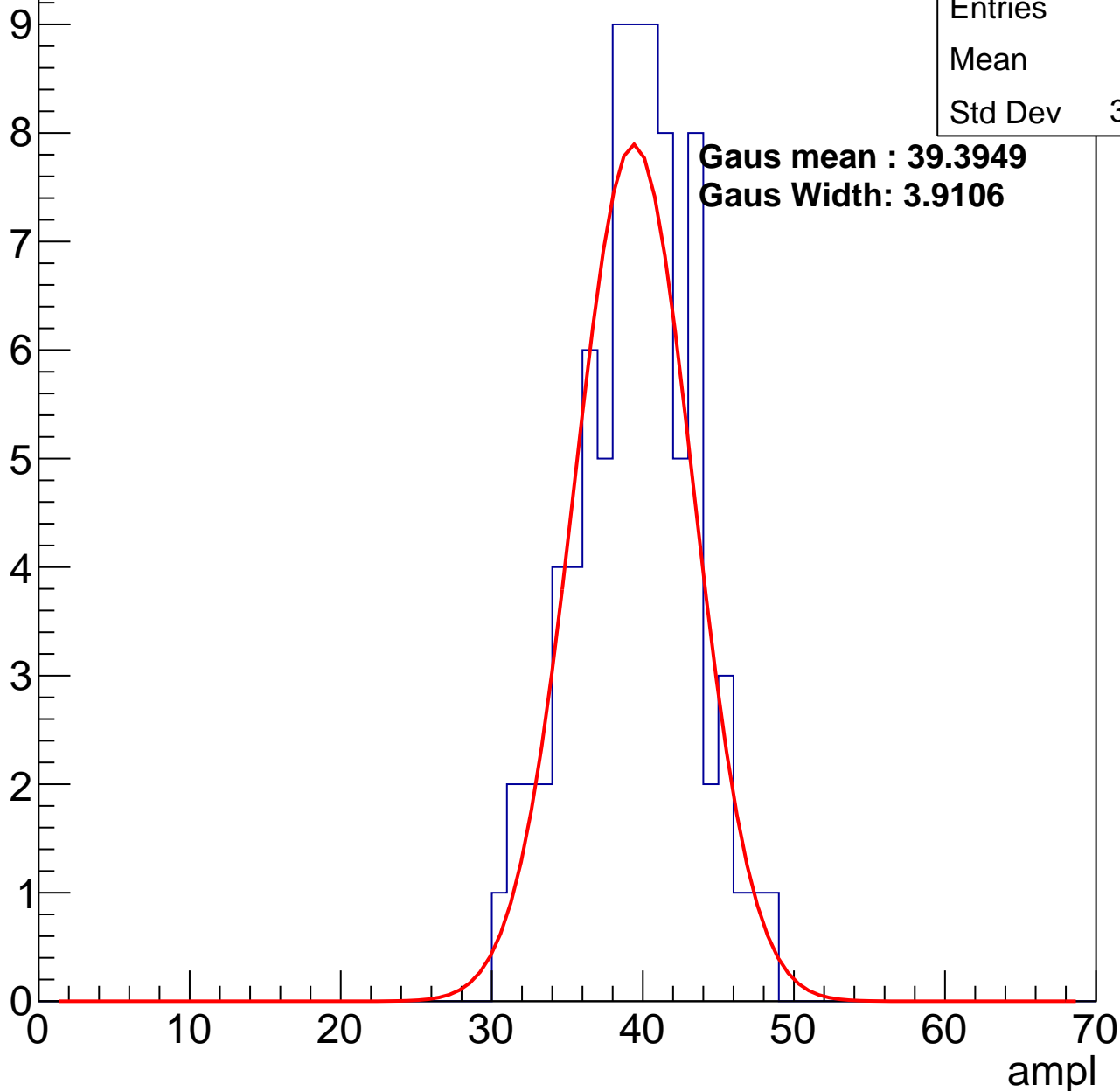
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	39
Std Dev	3.813

**Gaus mean : 39.3949**

**Gaus Width: 3.9106**



# B0L000S, U10-ch44, adc2

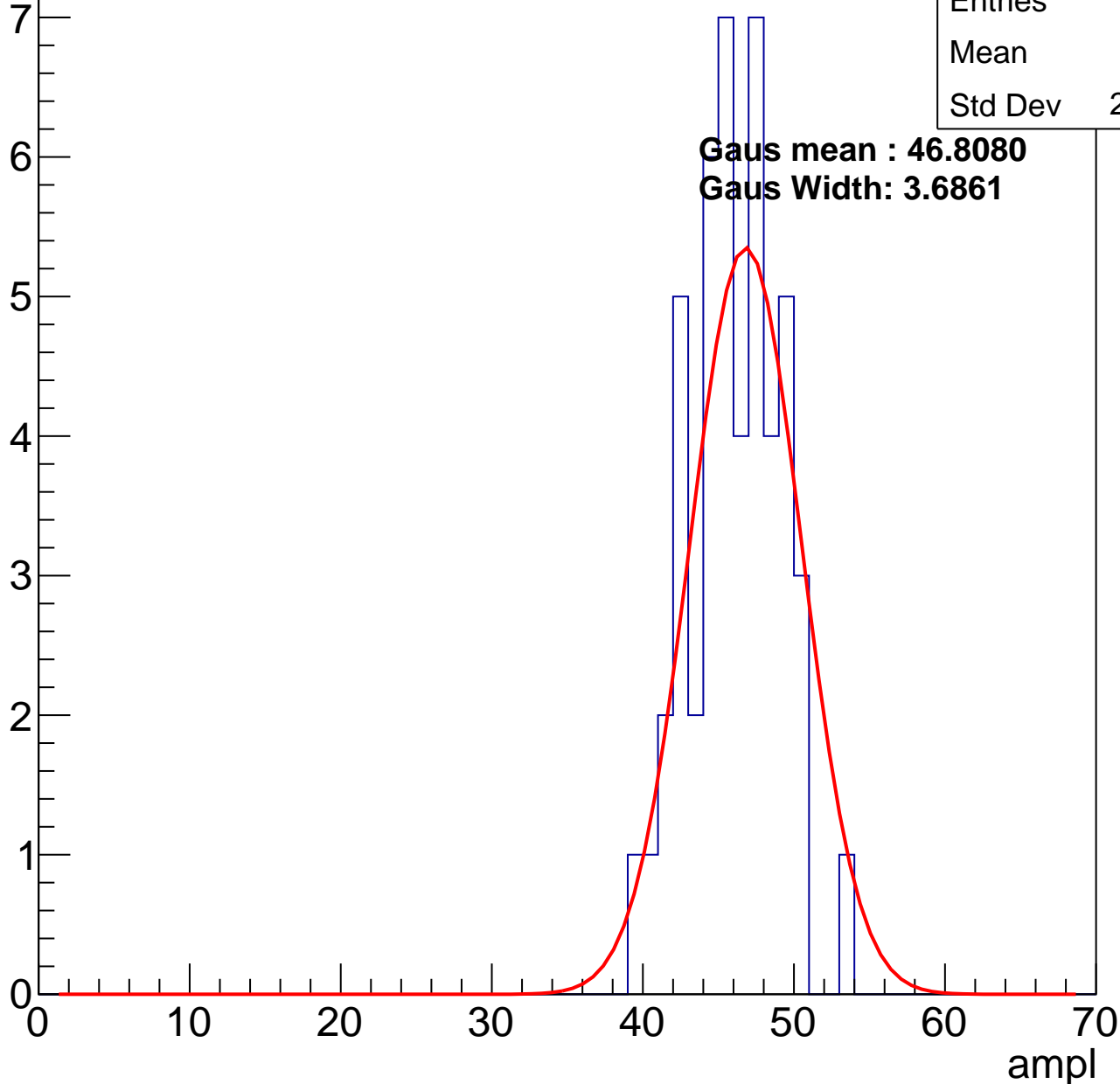
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	45.6
Std Dev	2.963

**Gaus mean : 46.8080**

**Gaus Width: 3.6861**

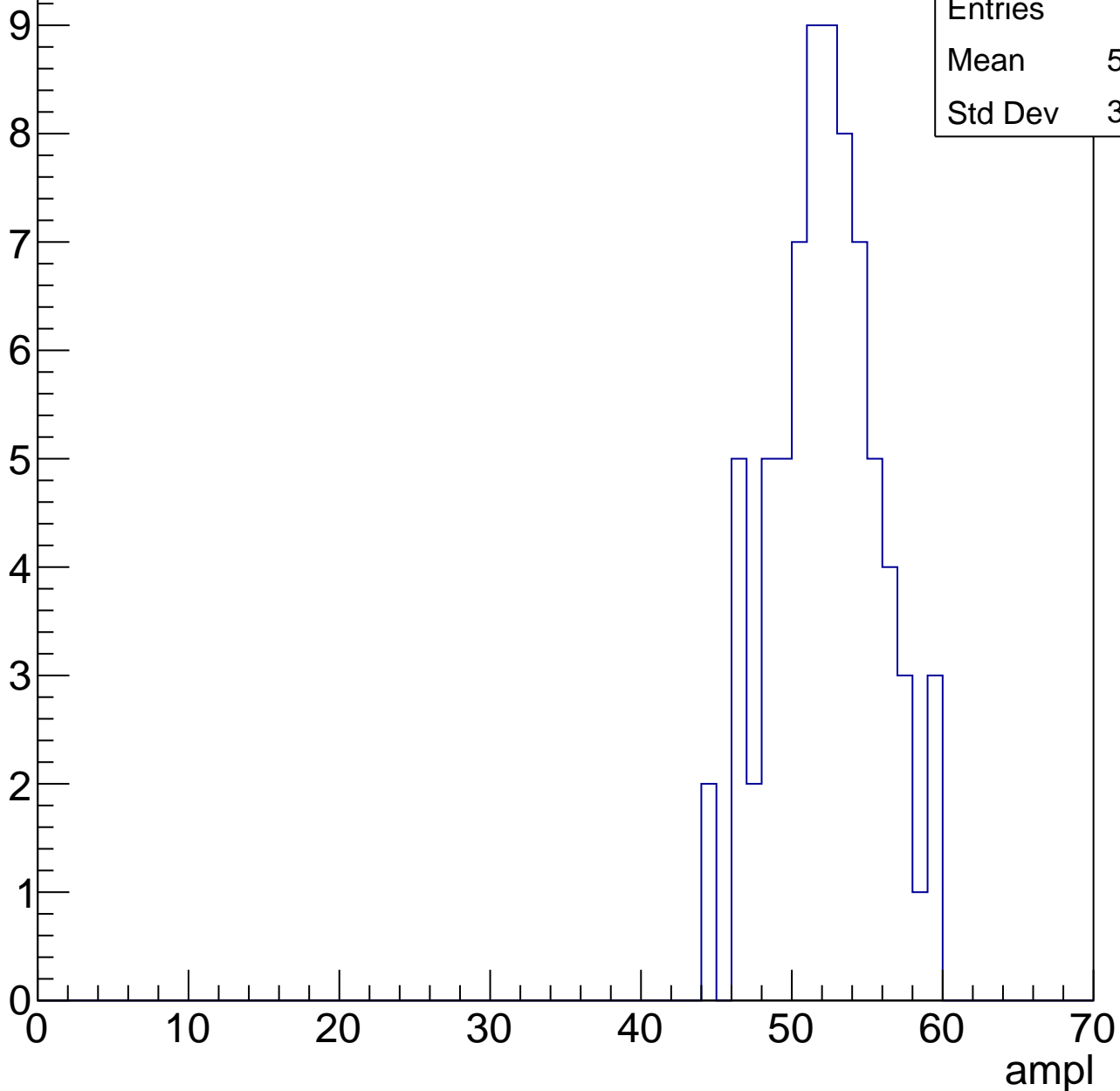


# B0L000S, U10-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	51.75
Std Dev	3.499

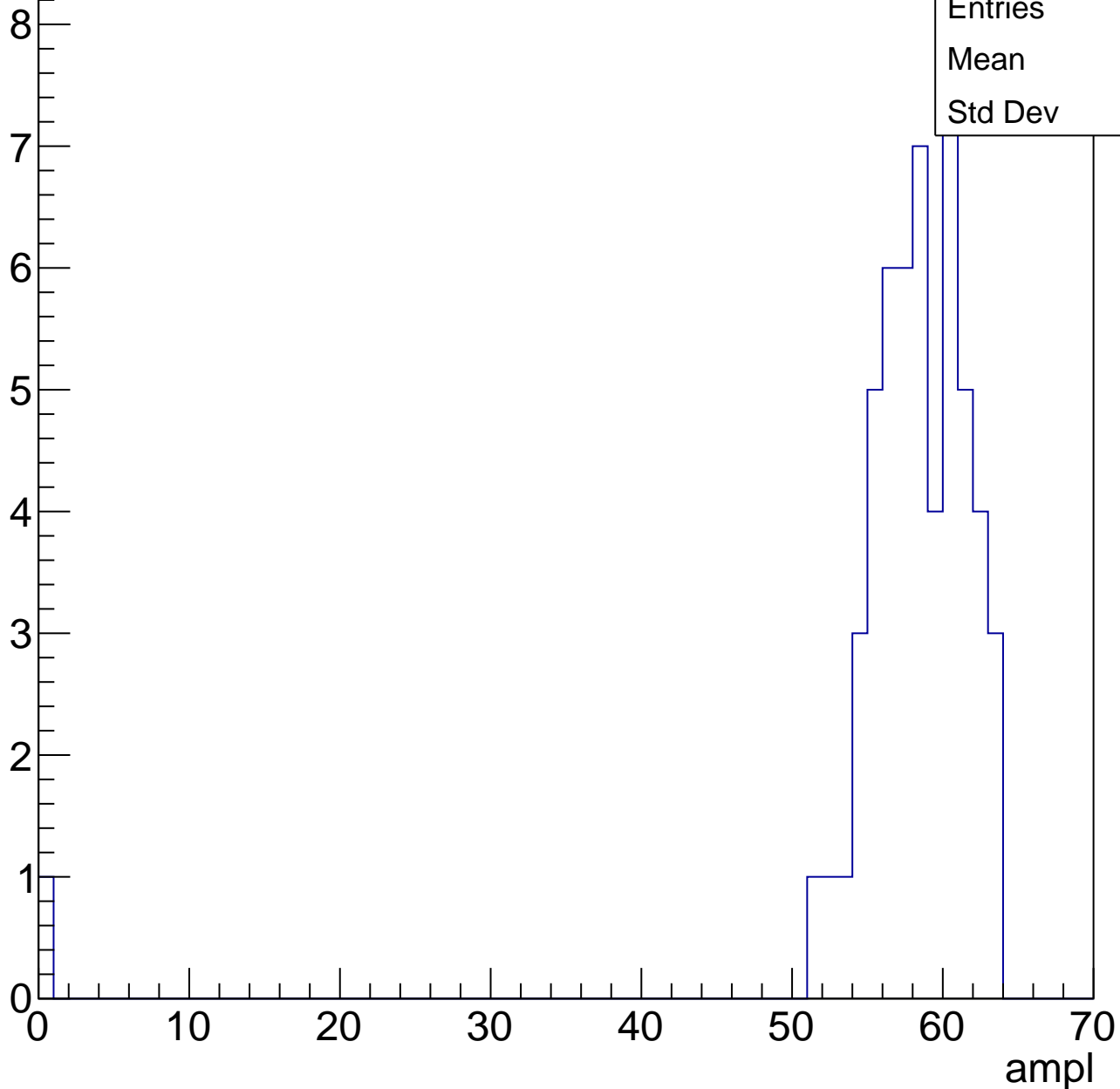


# B0L000S, U10-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

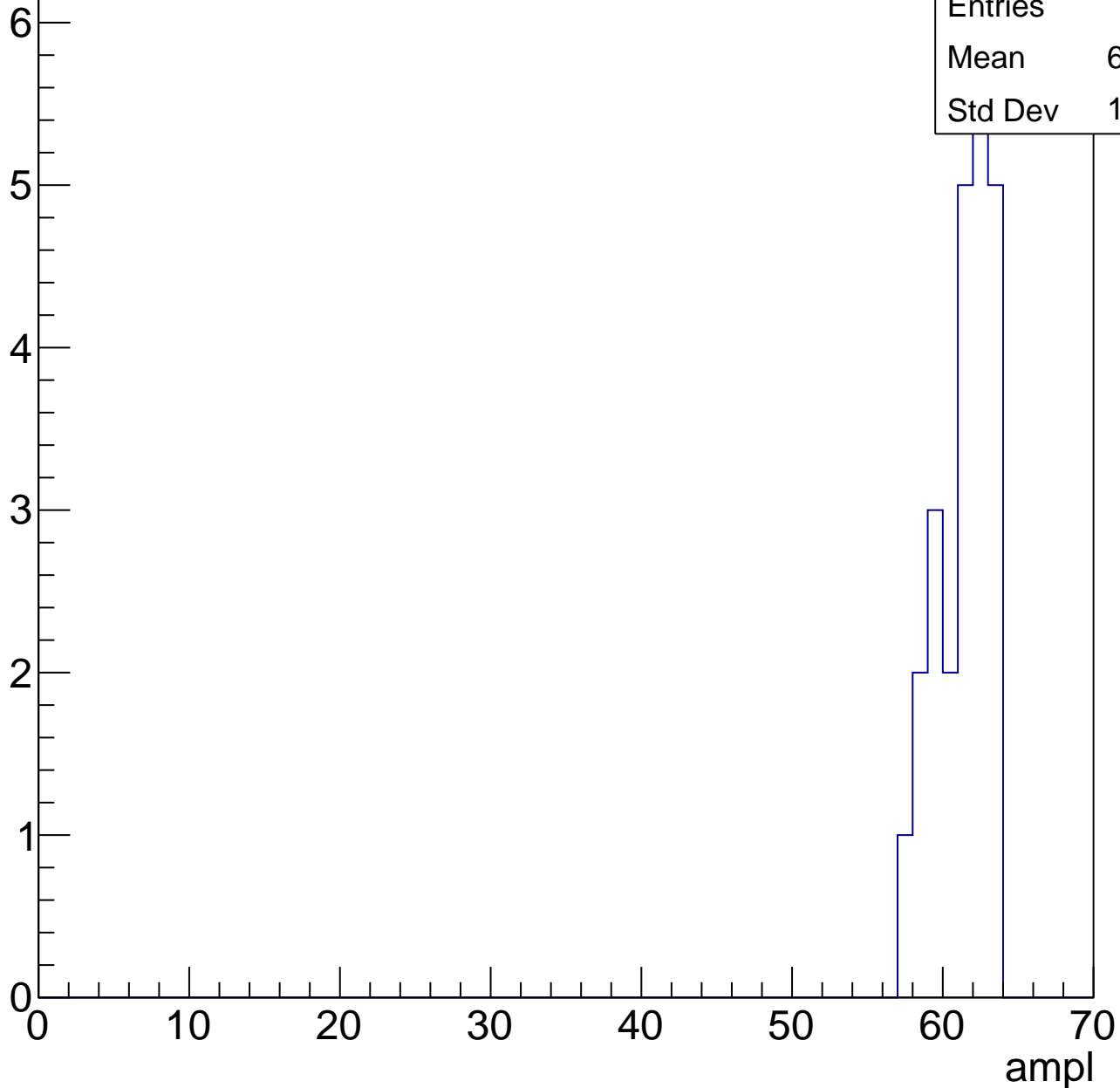
Entries	55
Mean	57
Std Dev	8.27



# B0L000S, U10-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	24
Mean	60.92
Std Dev	1.754

# B0L000S, U10-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

ampl



# B0L000S, U10-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	21
Std Dev	0

# B0L000S, U10-ch45, adc0

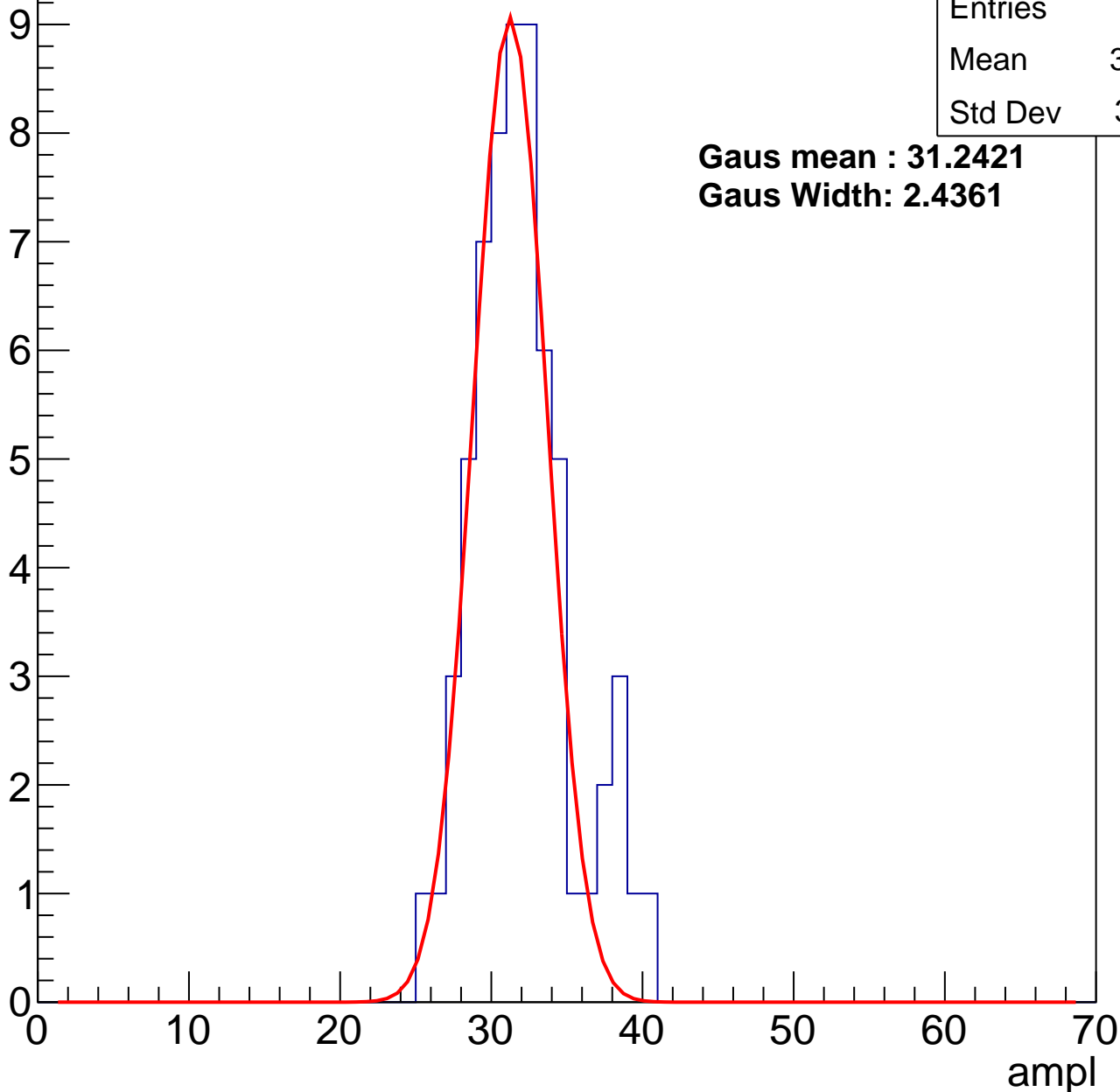
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	31.56
Std Dev	3.221

**Gaus mean : 31.2421**

**Gaus Width: 2.4361**



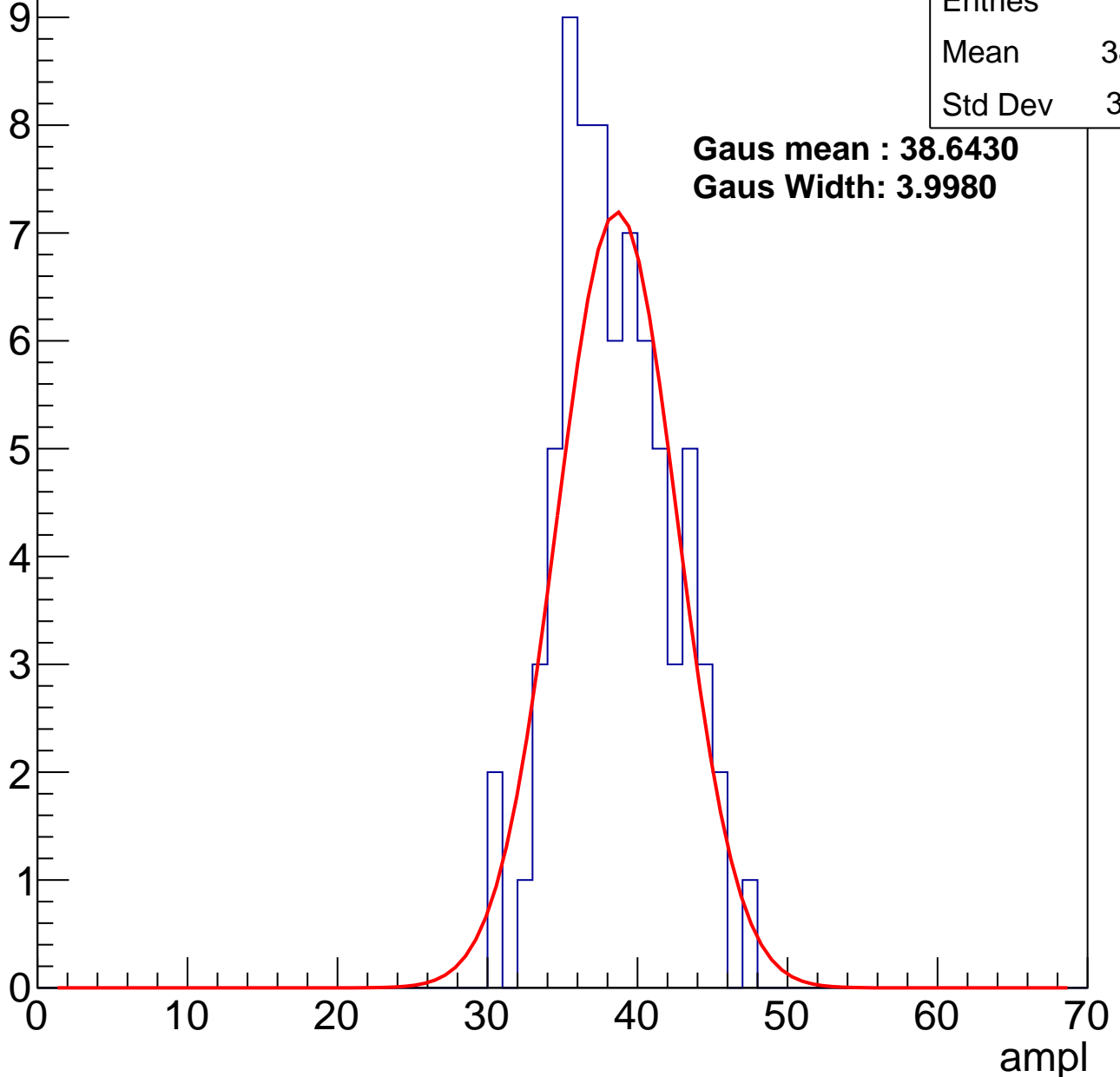
# B0L000S, U10-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	38.05
Std Dev	3.631

**Gaus mean : 38.6430**  
**Gaus Width: 3.9980**



# B0L000S, U10-ch45, adc2

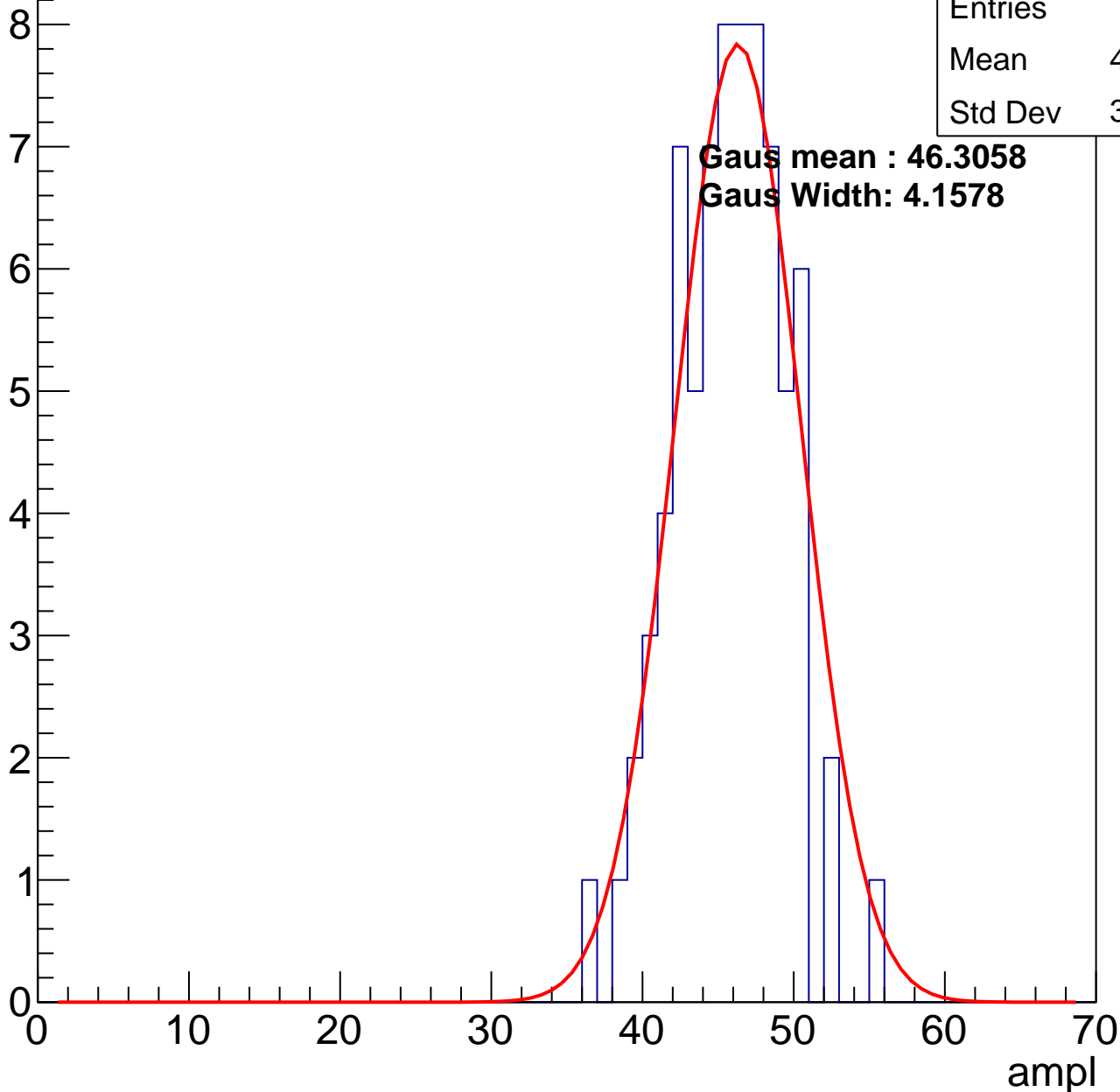
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	45.29
Std Dev	3.569

**Gaus mean : 46.3058**

**Gaus Width: 4.1578**

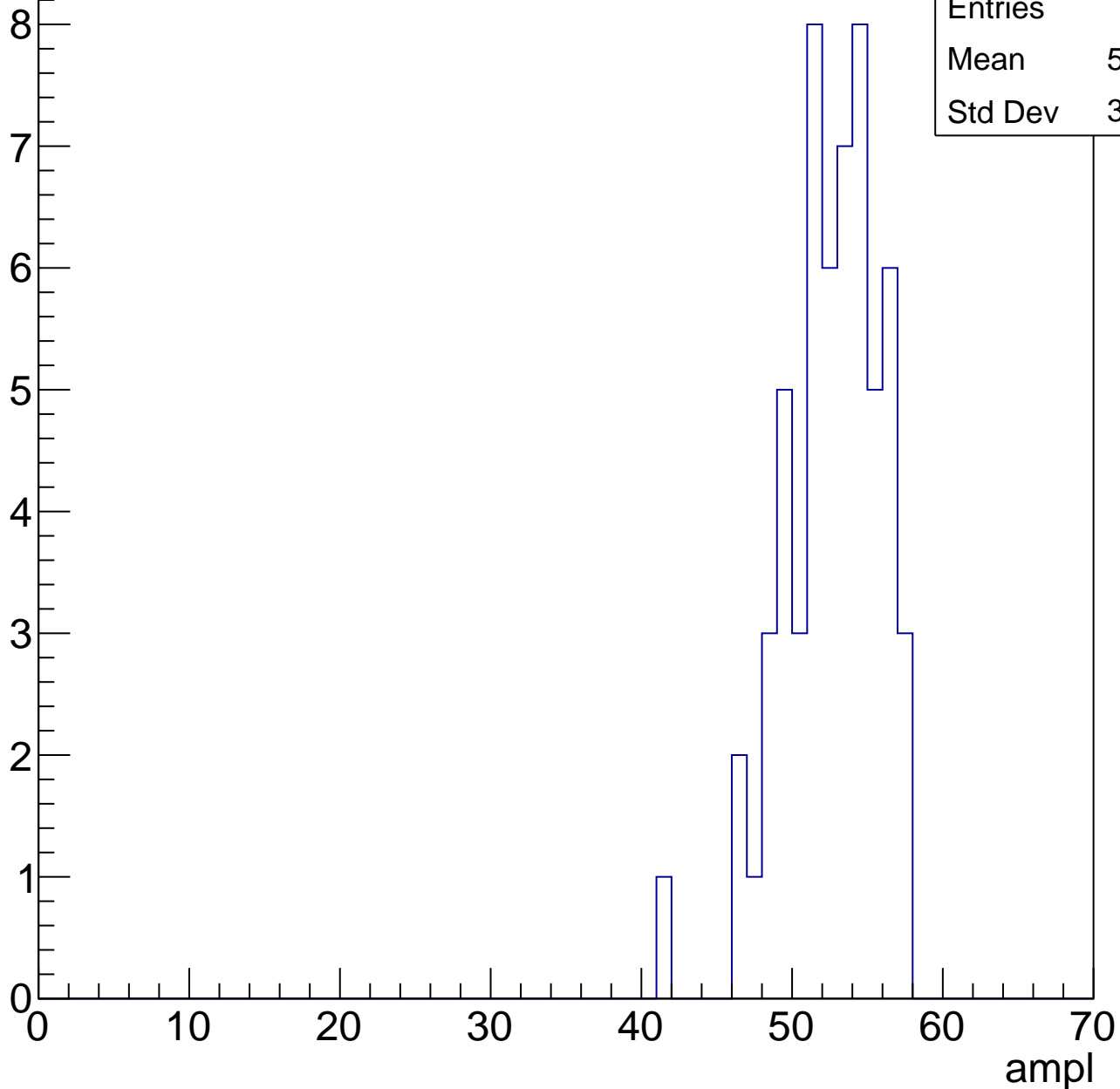


# B0L000S, U10-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

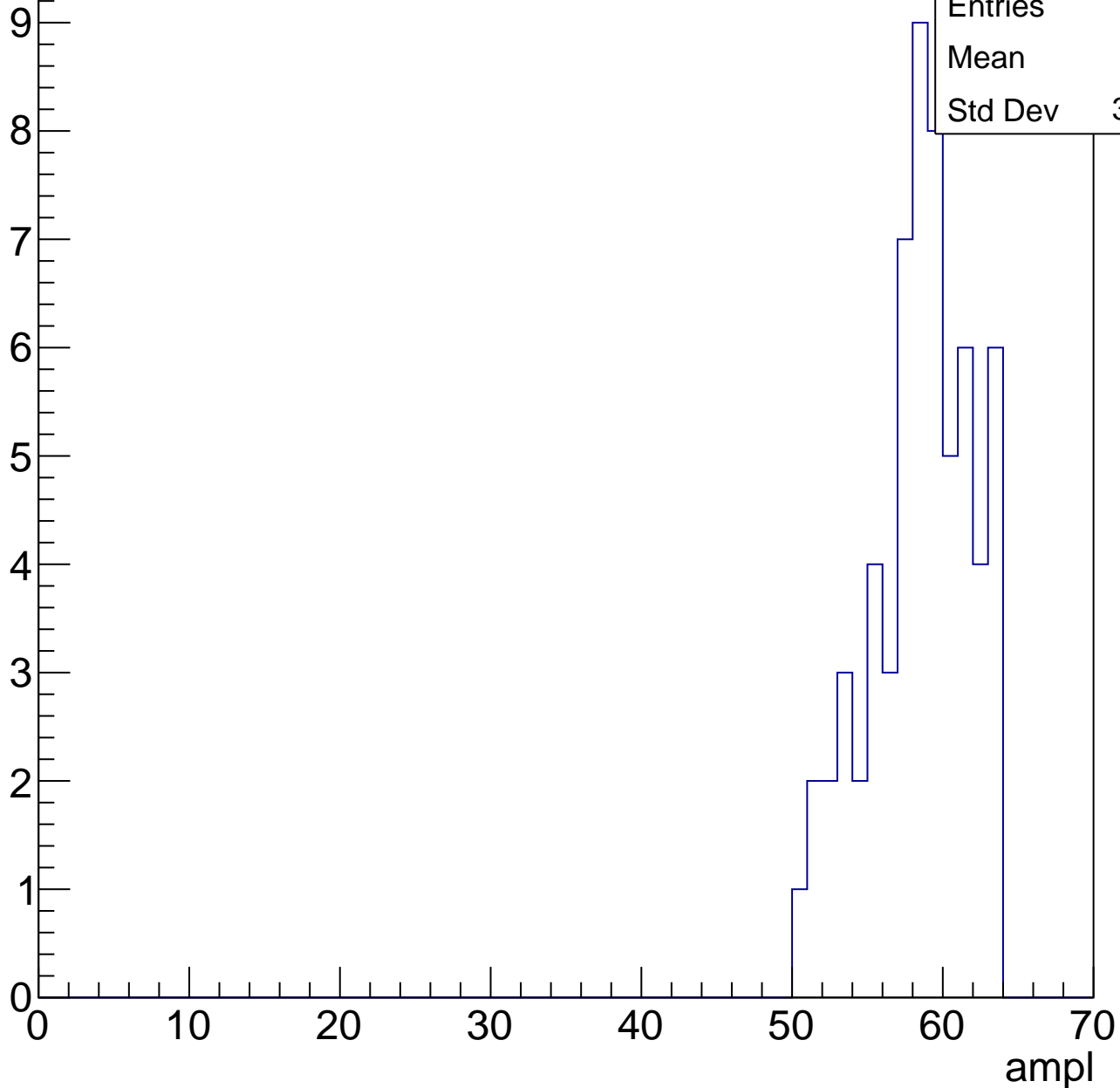
Entries	58
Mean	52.14
Std Dev	3.176



# B0L000S, U10-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



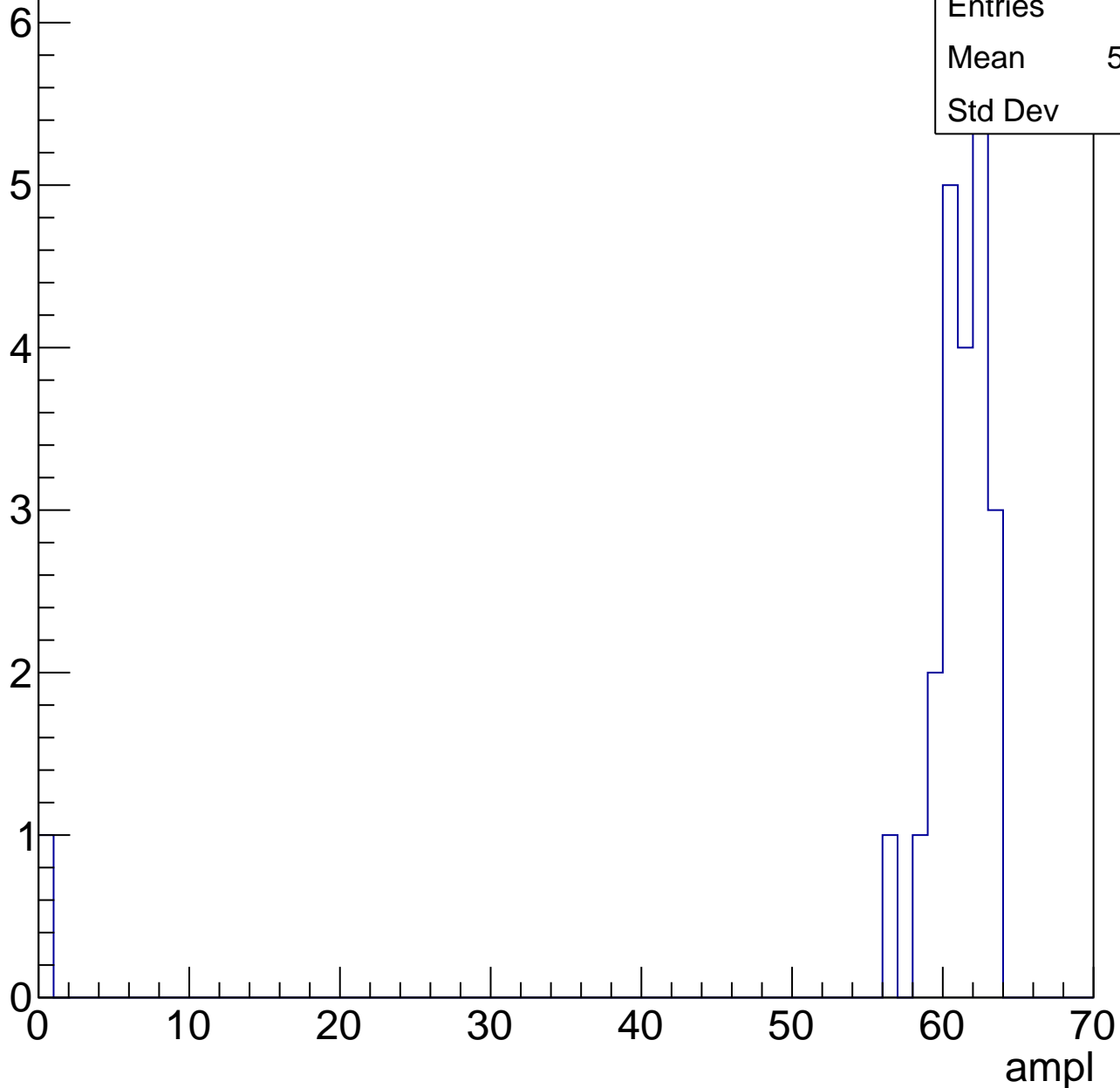
Entries	62
Mean	58
Std Dev	3.341

# B0L000S, U10-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.13
Std Dev	12.5



# B0L000S, U10-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch46, adc0

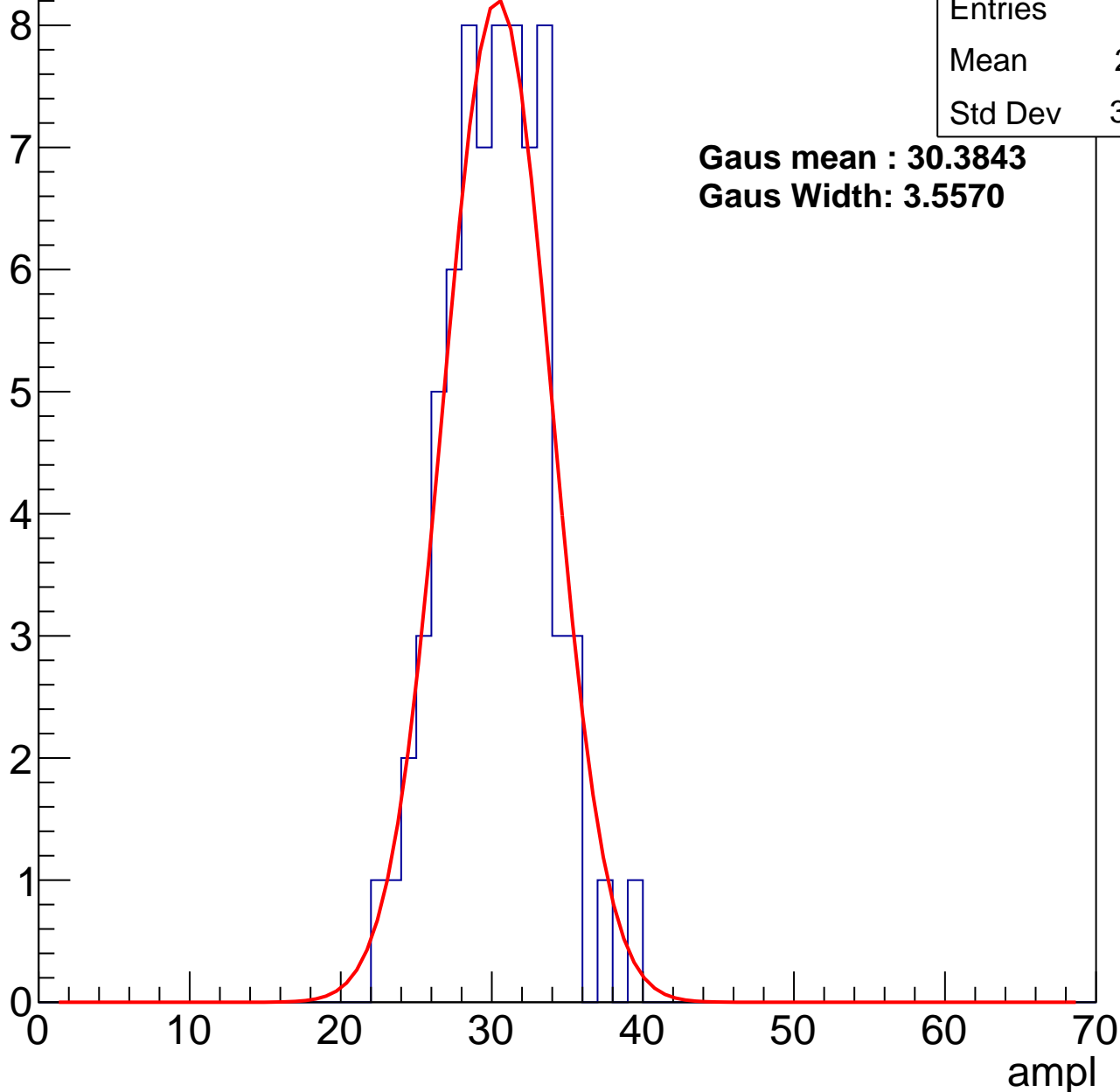
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.81
Std Dev	3.315

**Gaus mean : 30.3843**

**Gaus Width: 3.5570**



# B0L000S, U10-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	36.61
Std Dev	2.839

**Gaus mean : 37.1598**

**Gaus Width: 3.2182**

Entry

10

8

6

4

2

0

0

10

20

30

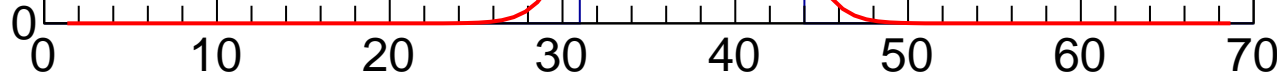
40

50

60

70

ampl



# B0L000S, U10-ch46, adc2

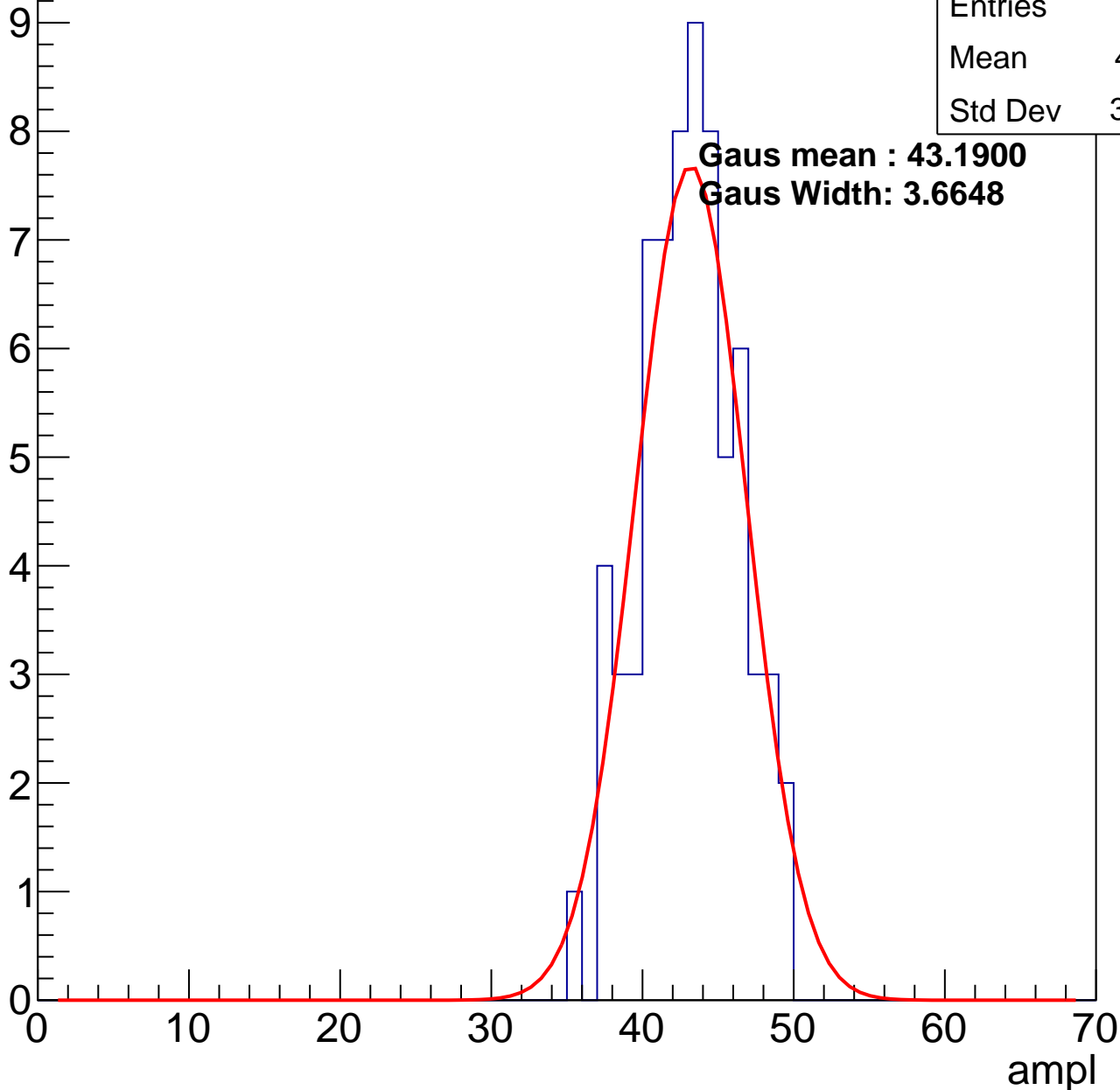
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.61
Std Dev	3.177

**Gaus mean : 43.1900**

**Gaus Width: 3.6648**

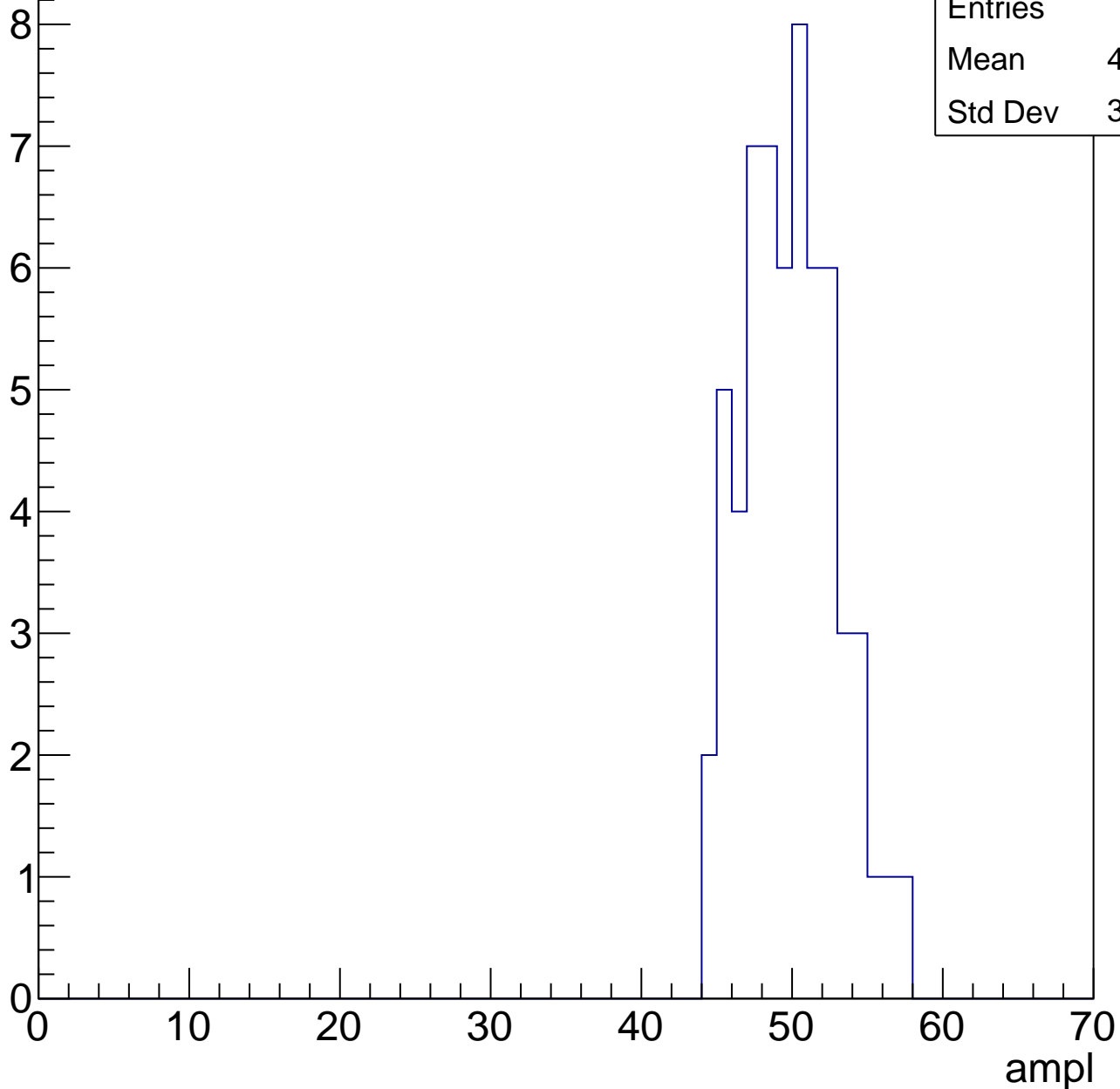


# B0L000S, U10-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

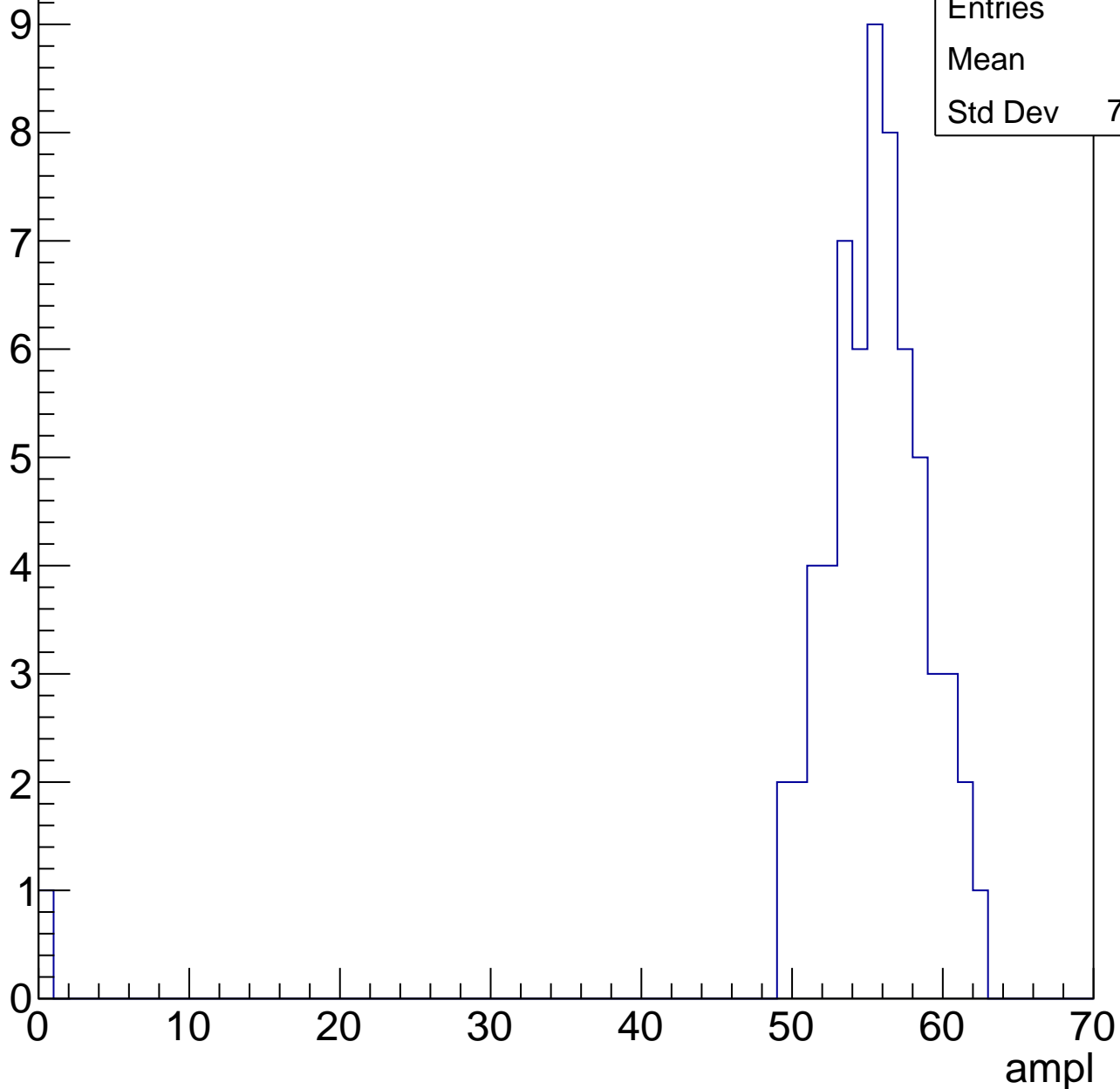
Entries	60
Mean	49.38
Std Dev	3.028



# B0L000S, U10-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

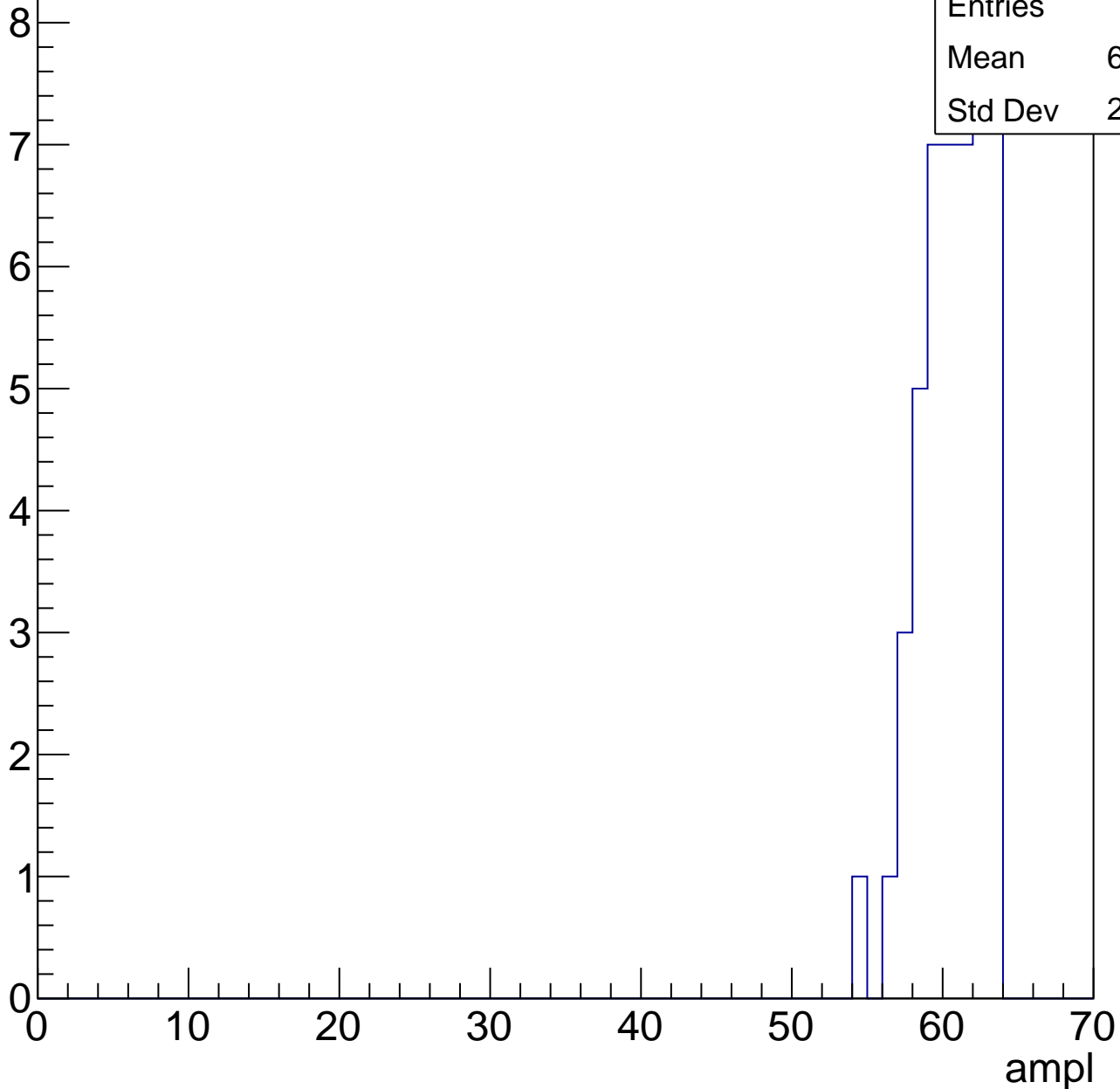


# B0L000S, U10-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	60.23
Std Dev	2.136



# B0L000S, U10-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch47, adc0

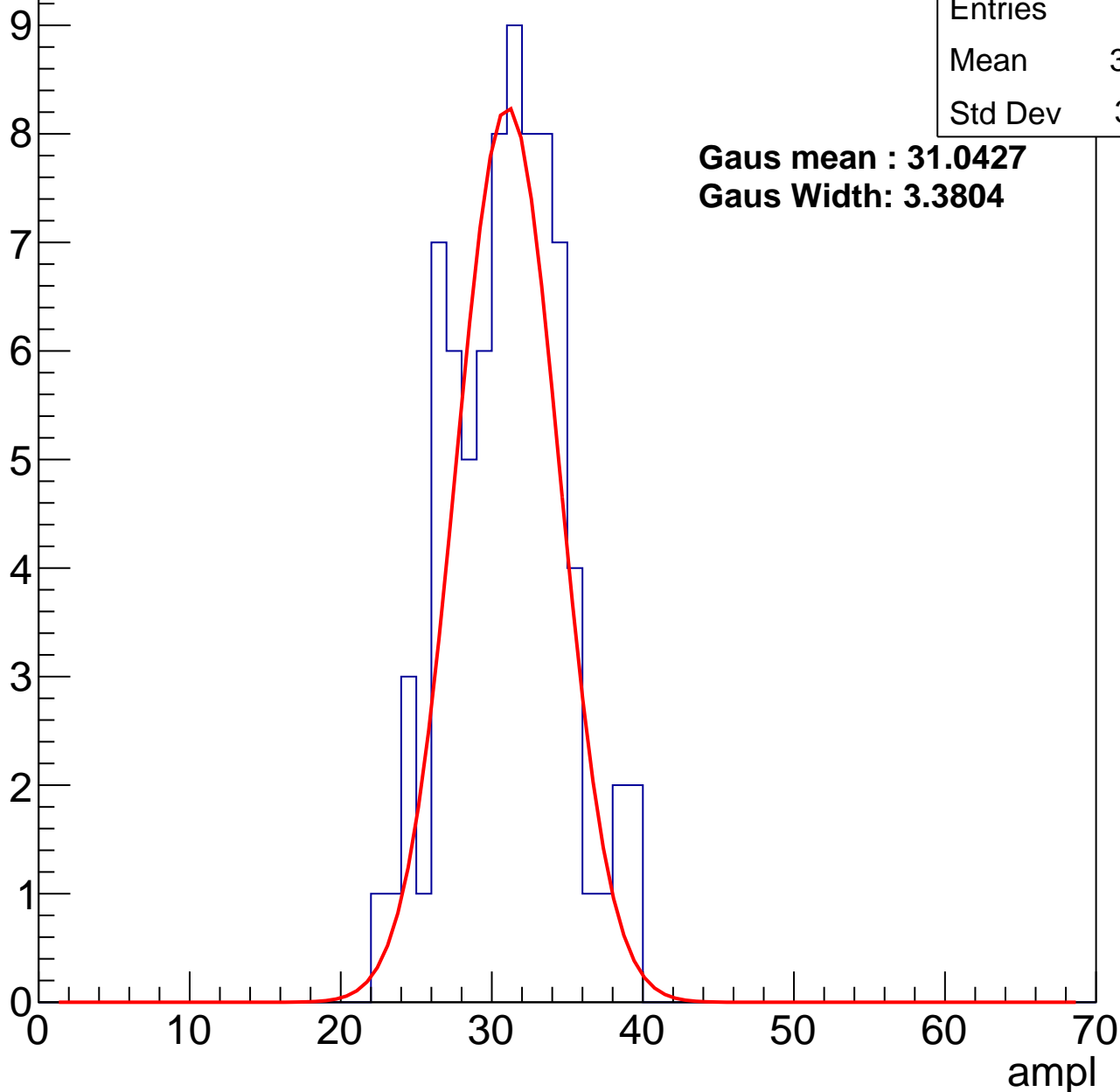
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	30.55
Std Dev	3.731

**Gaus mean : 31.0427**

**Gaus Width: 3.3804**



# B0L000S, U10-ch47, adc1

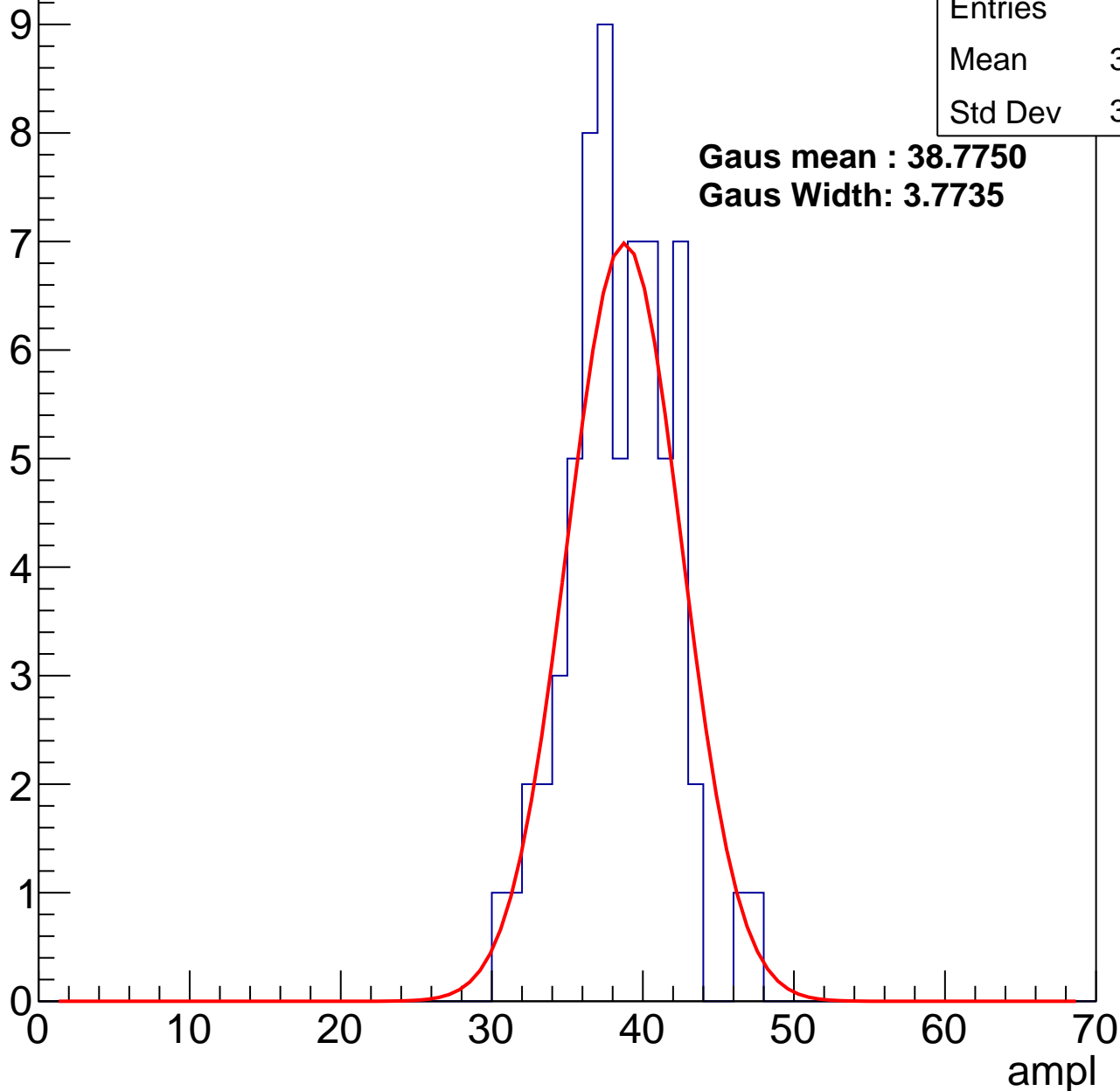
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	38.03
Std Dev	3.384

**Gaus mean : 38.7750**

**Gaus Width: 3.7735**



# B0L000S, U10-ch47, adc2

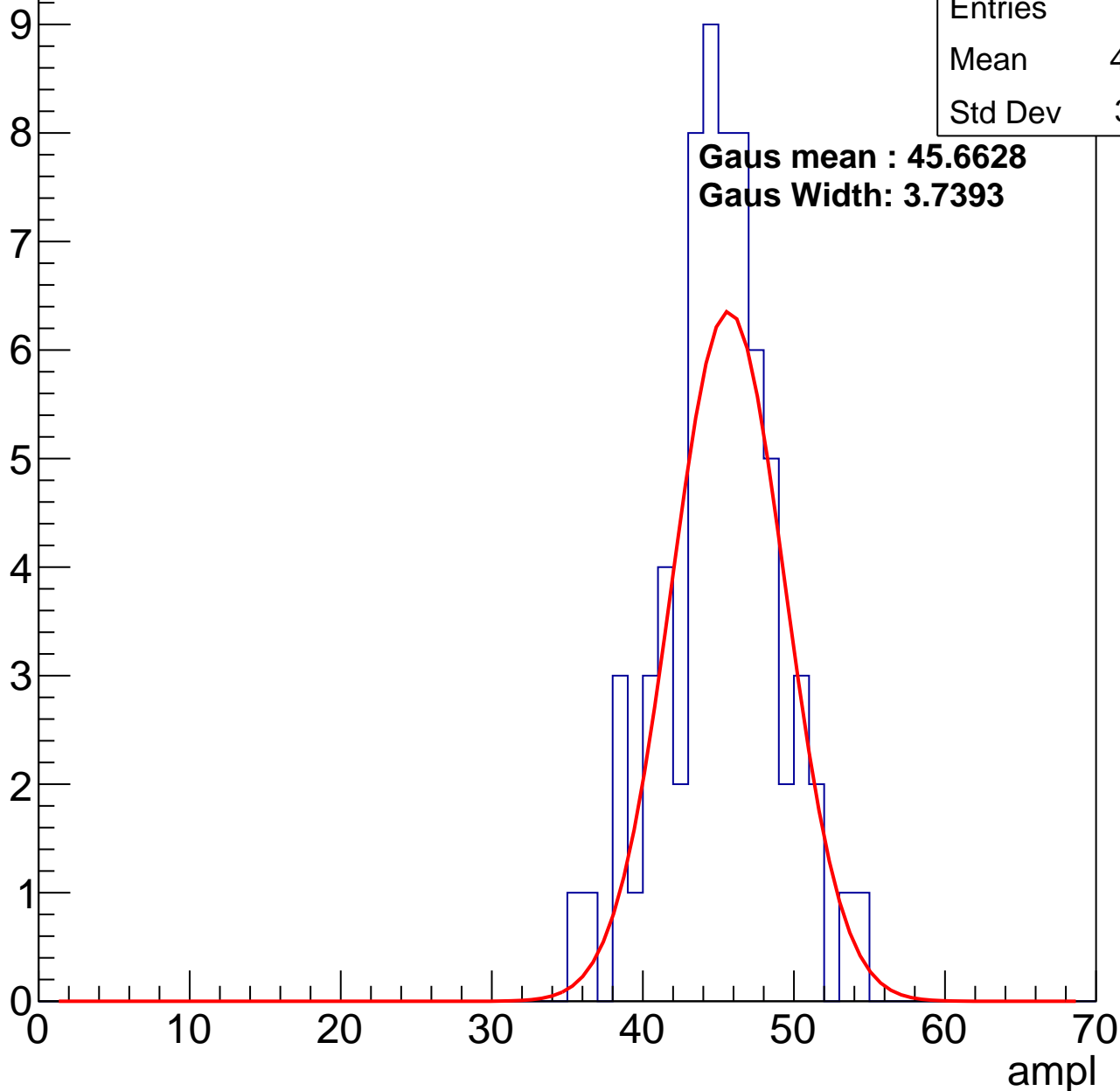
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	44.69
Std Dev	3.751

**Gaus mean : 45.6628**

**Gaus Width: 3.7393**

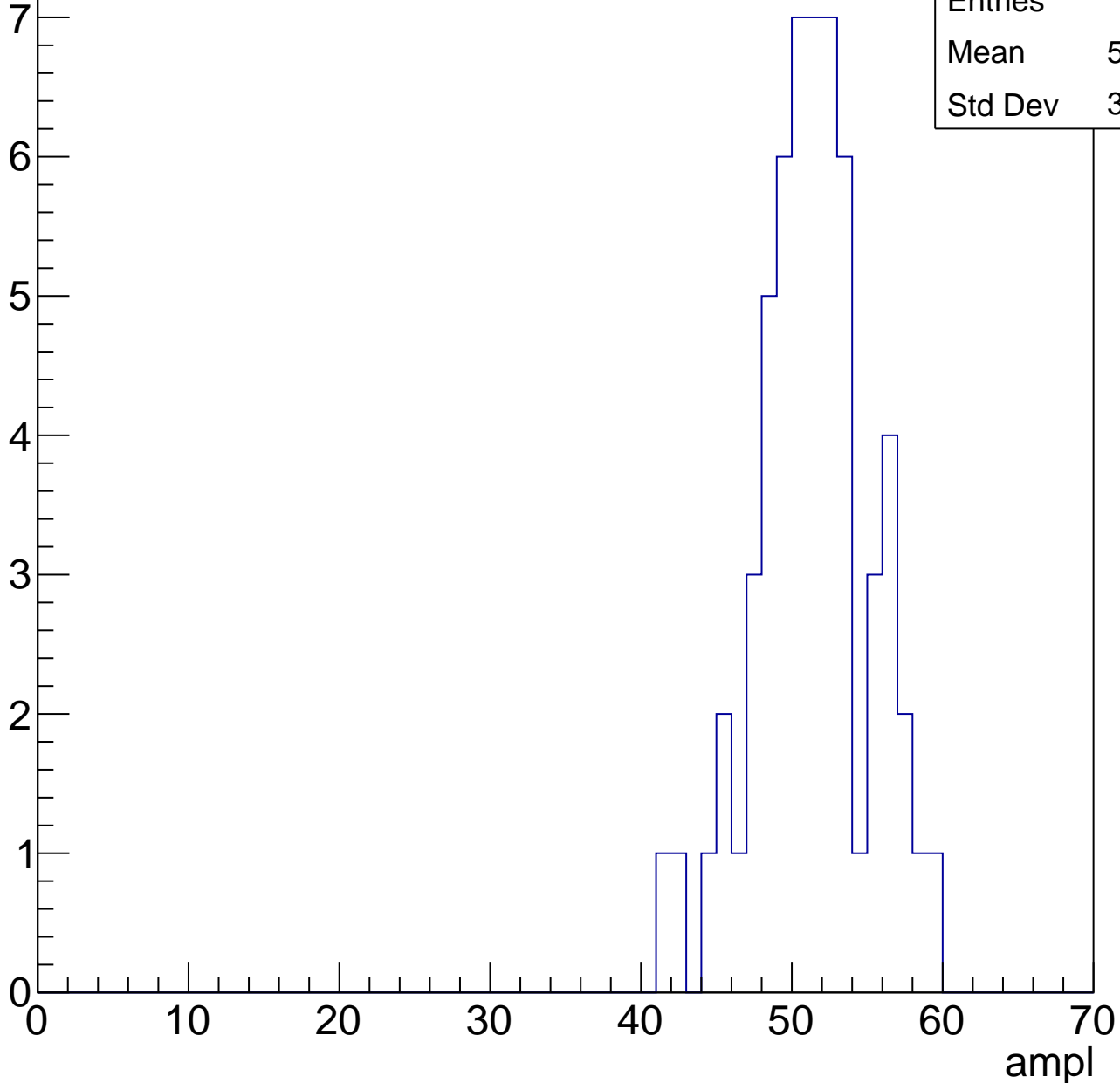


# B0L000S, U10-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.86
Std Dev	3.735

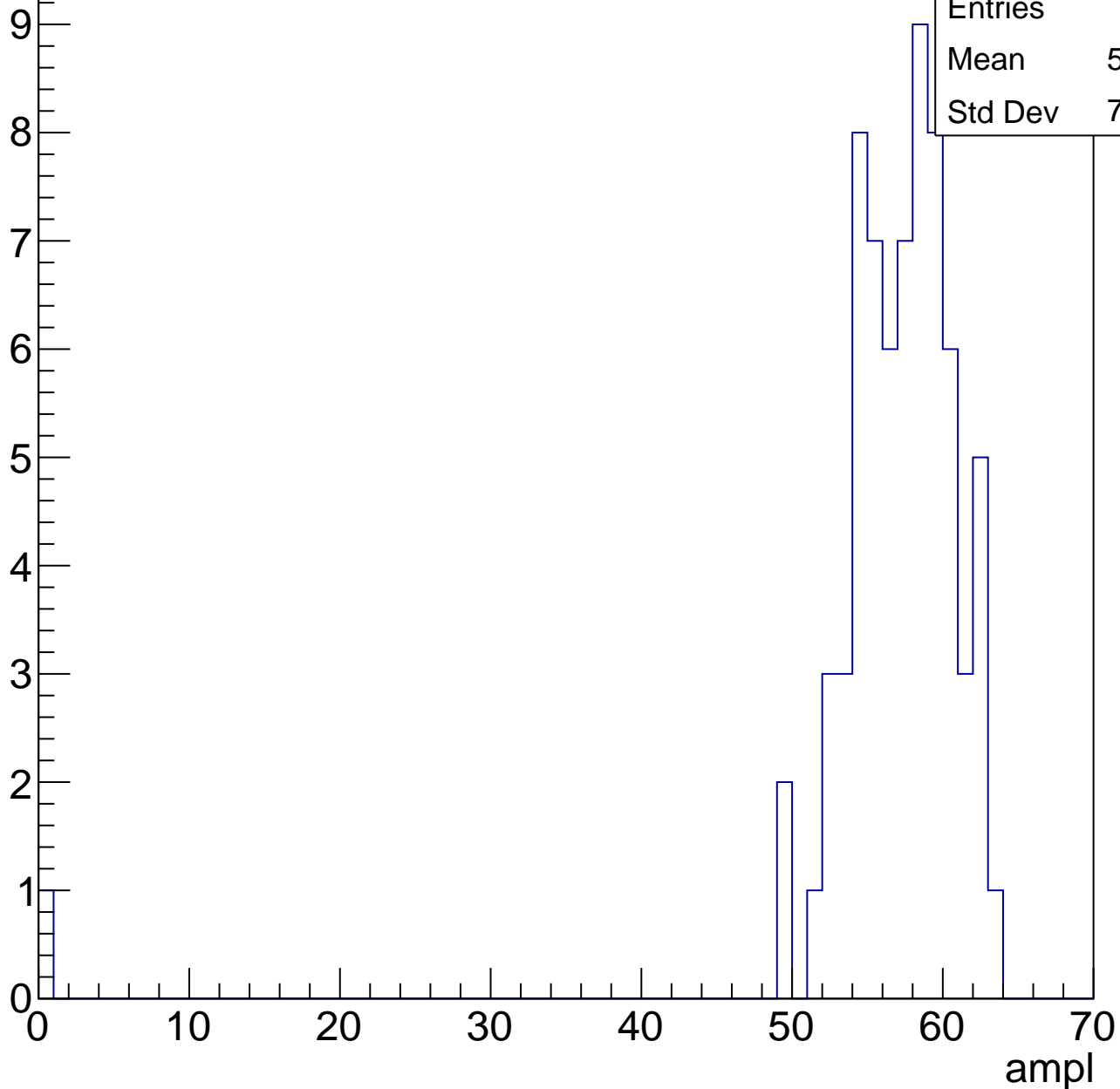


# B0L000S, U10-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	56.09
Std Dev	7.452

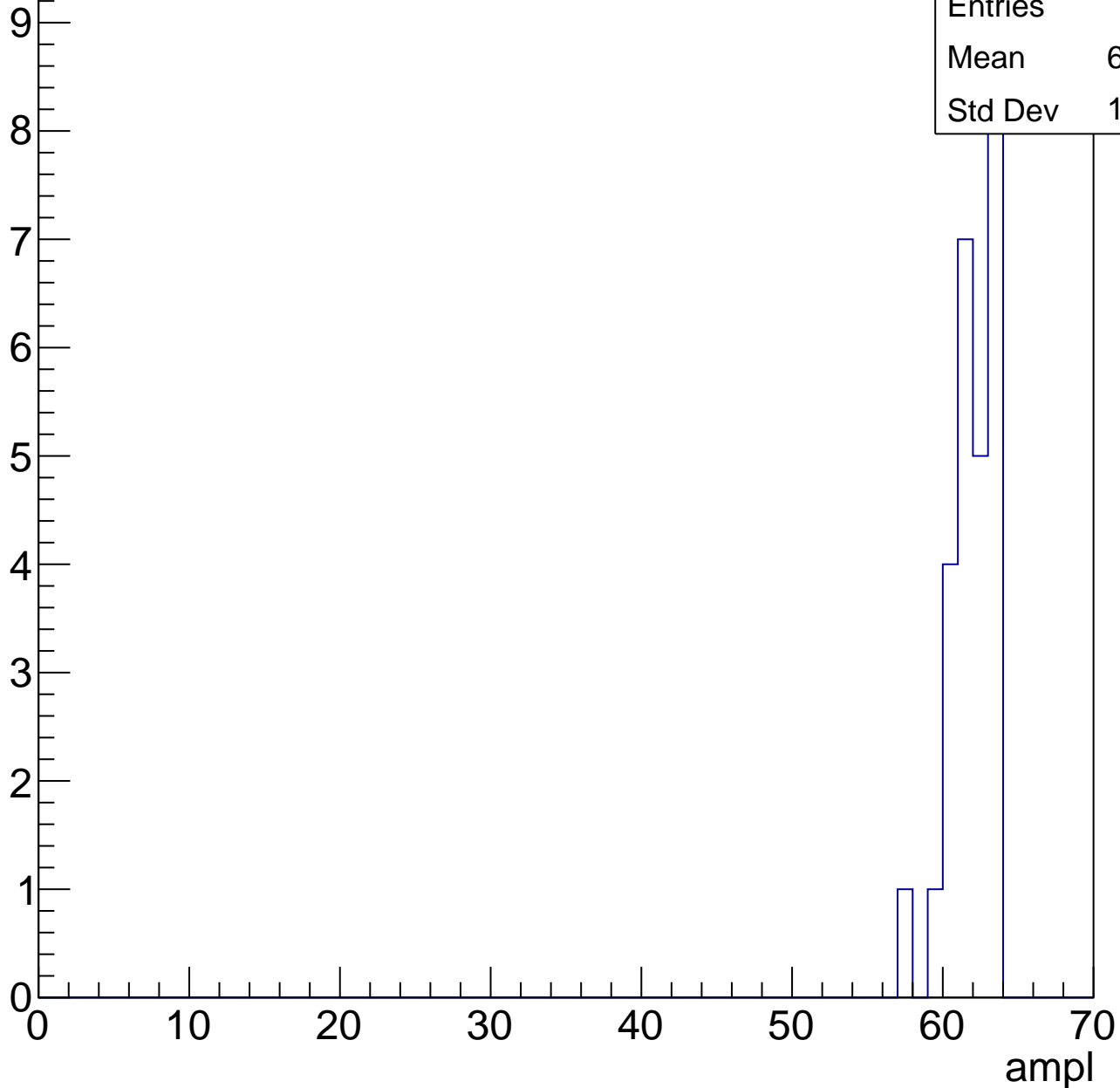


# B0L000S, U10-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	61.48
Std Dev	1.475



# B0L000S, U10-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch48, adc0

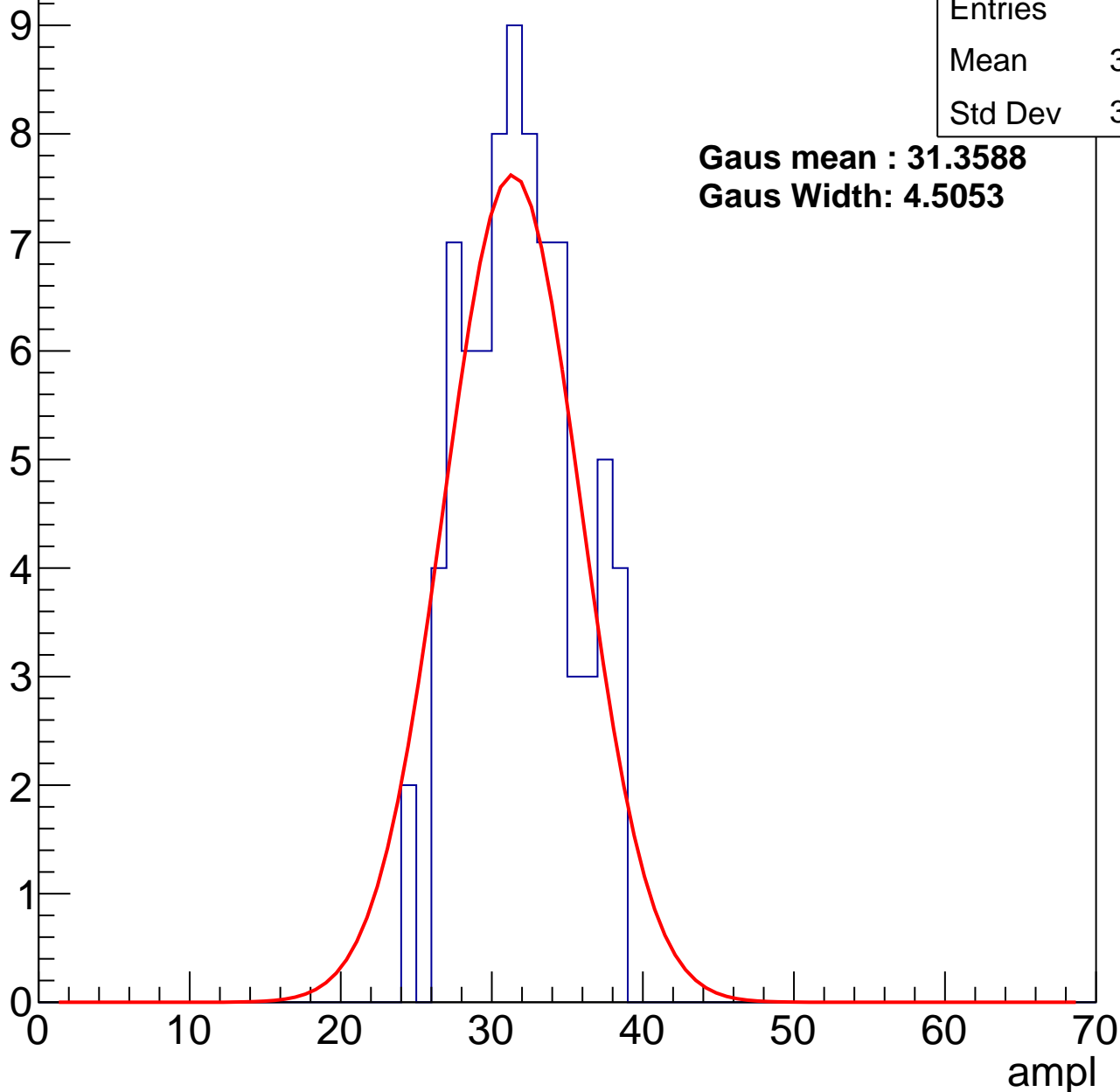
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	31.35
Std Dev	3.529

**Gaus mean : 31.3588**

**Gaus Width: 4.5053**



# B0L000S, U10-ch48, adc1

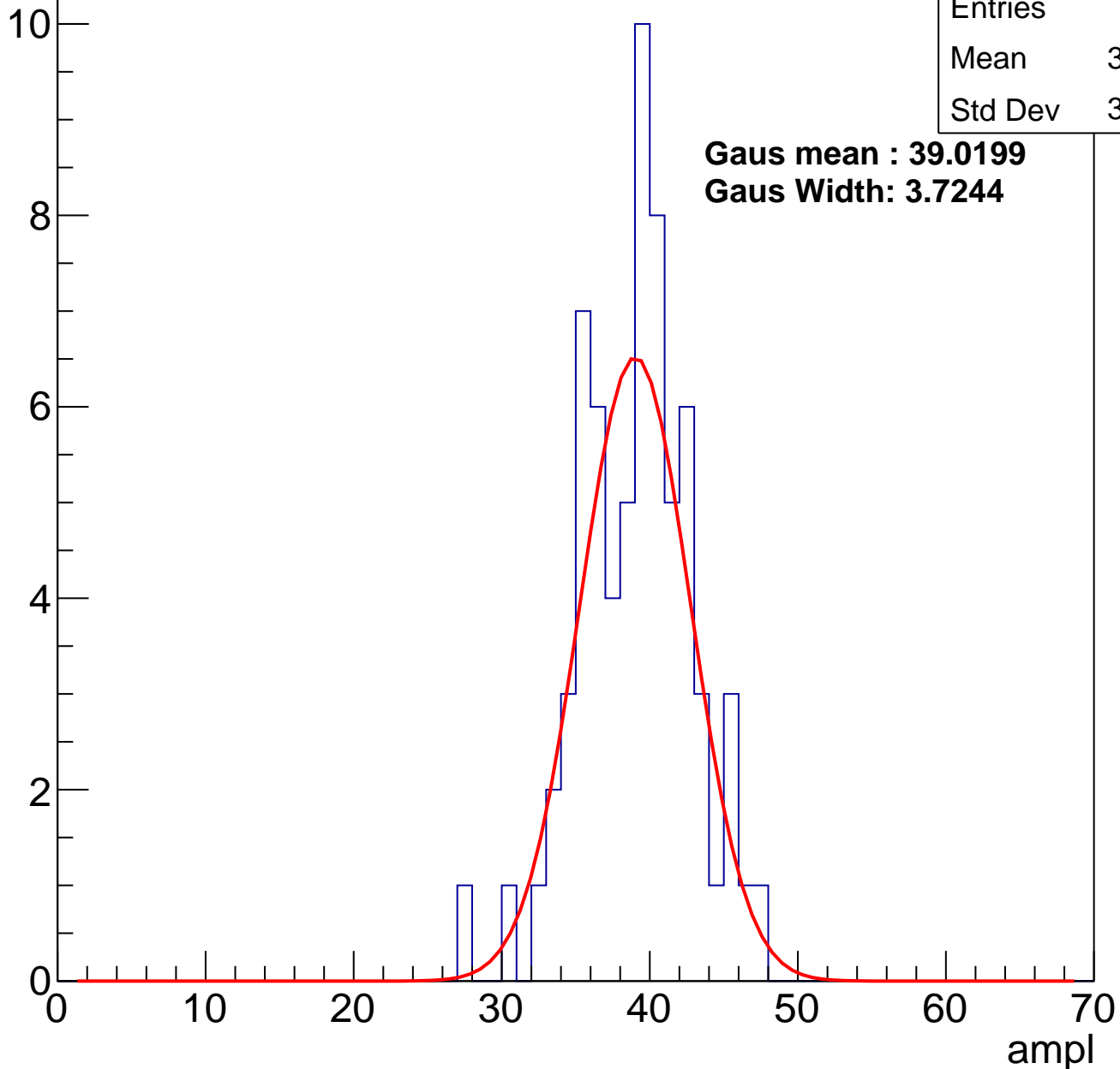
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	38.59
Std Dev	3.782

**Gaus mean : 39.0199**

**Gaus Width: 3.7244**

Entry



# B0L000S, U10-ch48, adc2

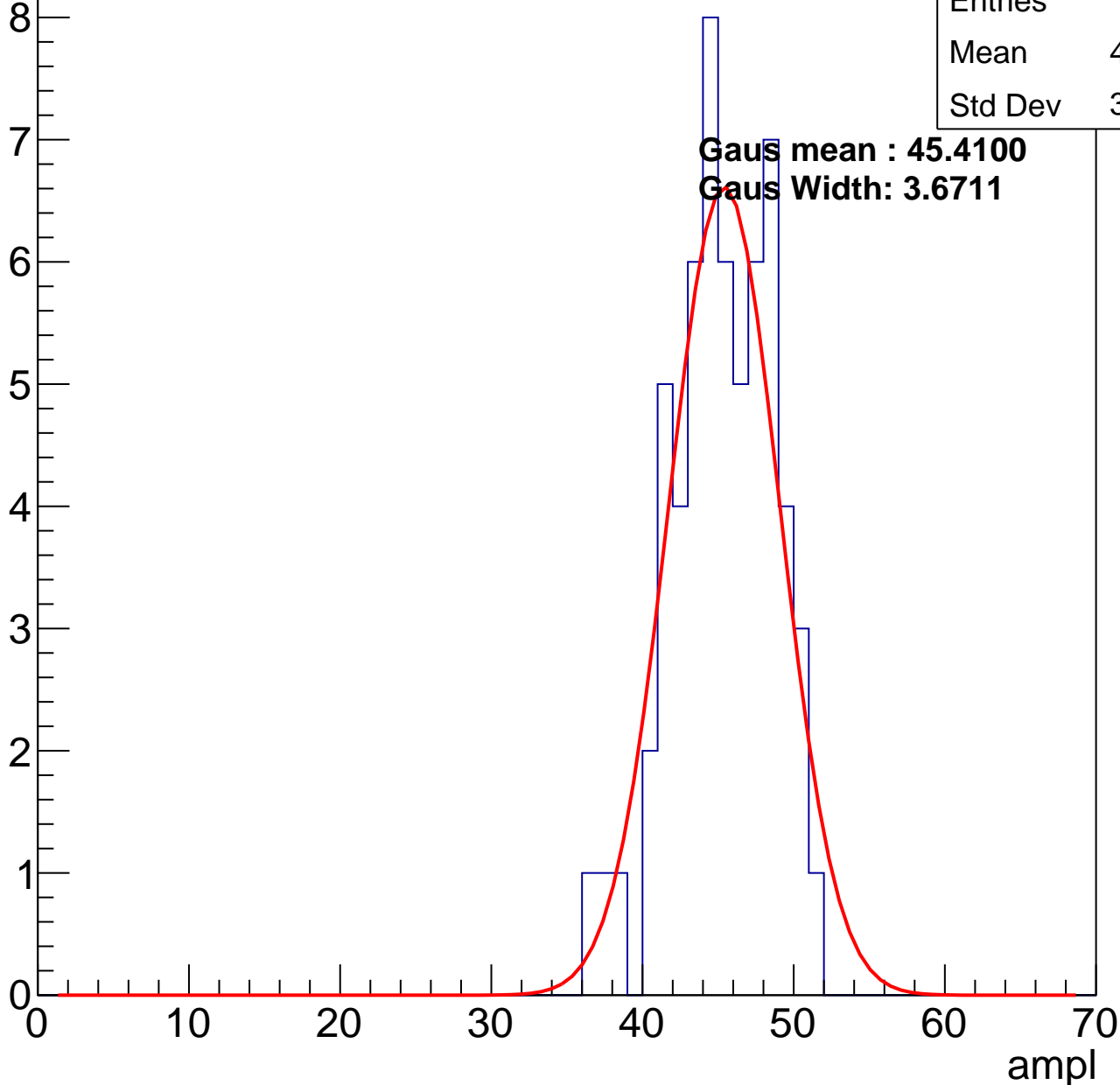
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.82
Std Dev	3.309

Gaus mean : 45.4100

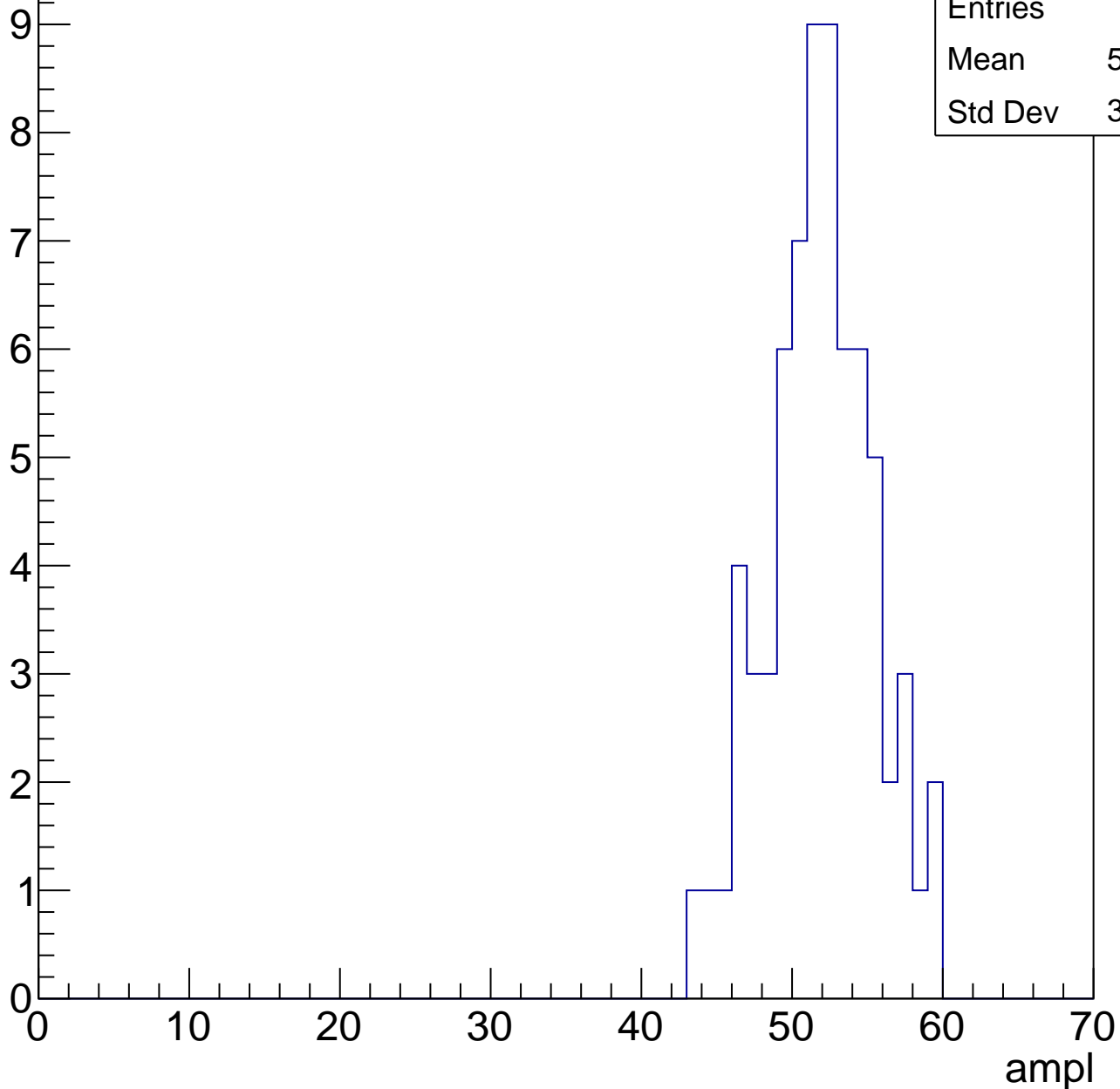
Gaus Width: 3.6711



# B0L000S, U10-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



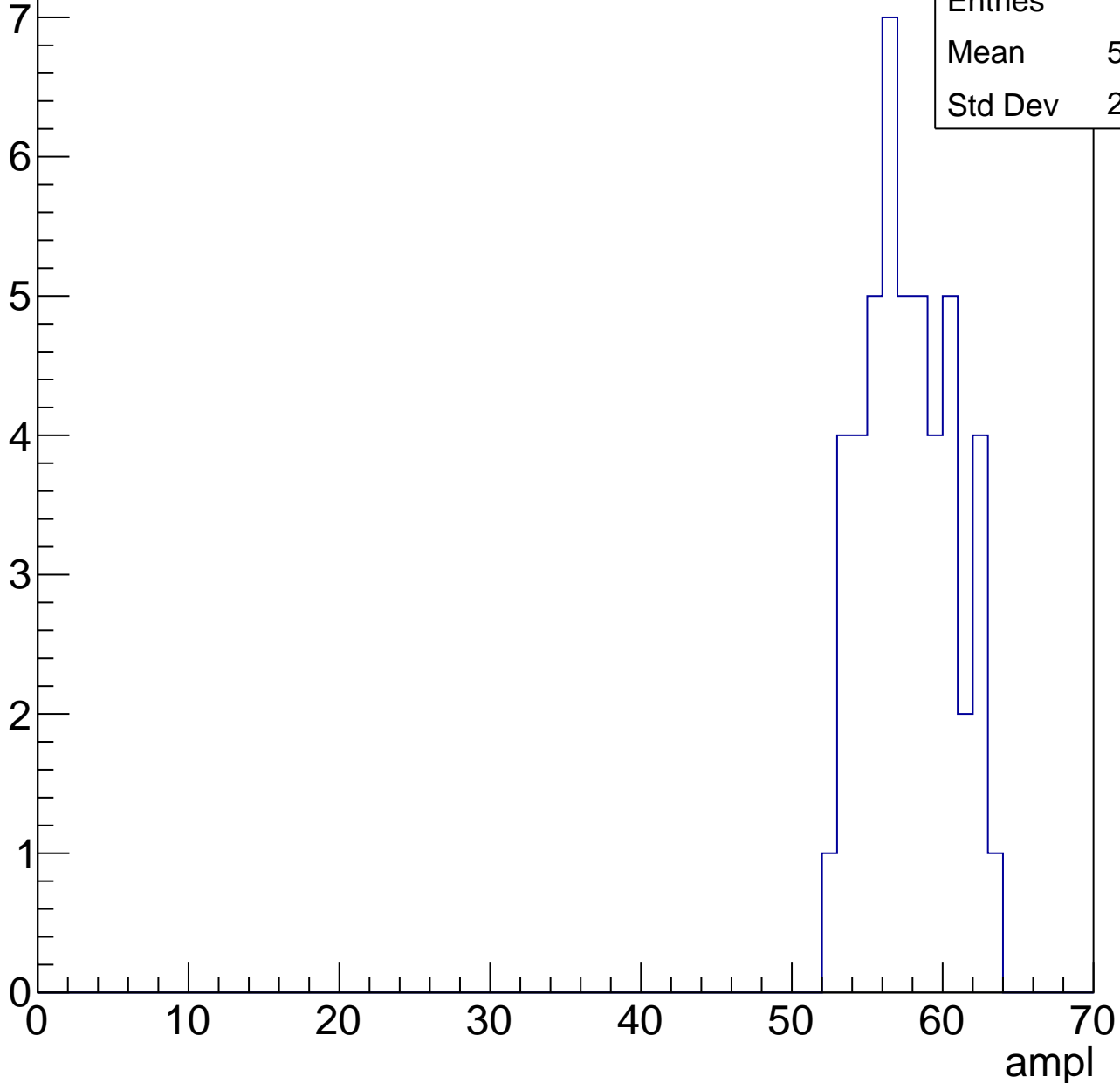
Entries	69
Mean	51.42
Std Dev	3.499

# B0L000S, U10-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	57.26
Std Dev	2.854

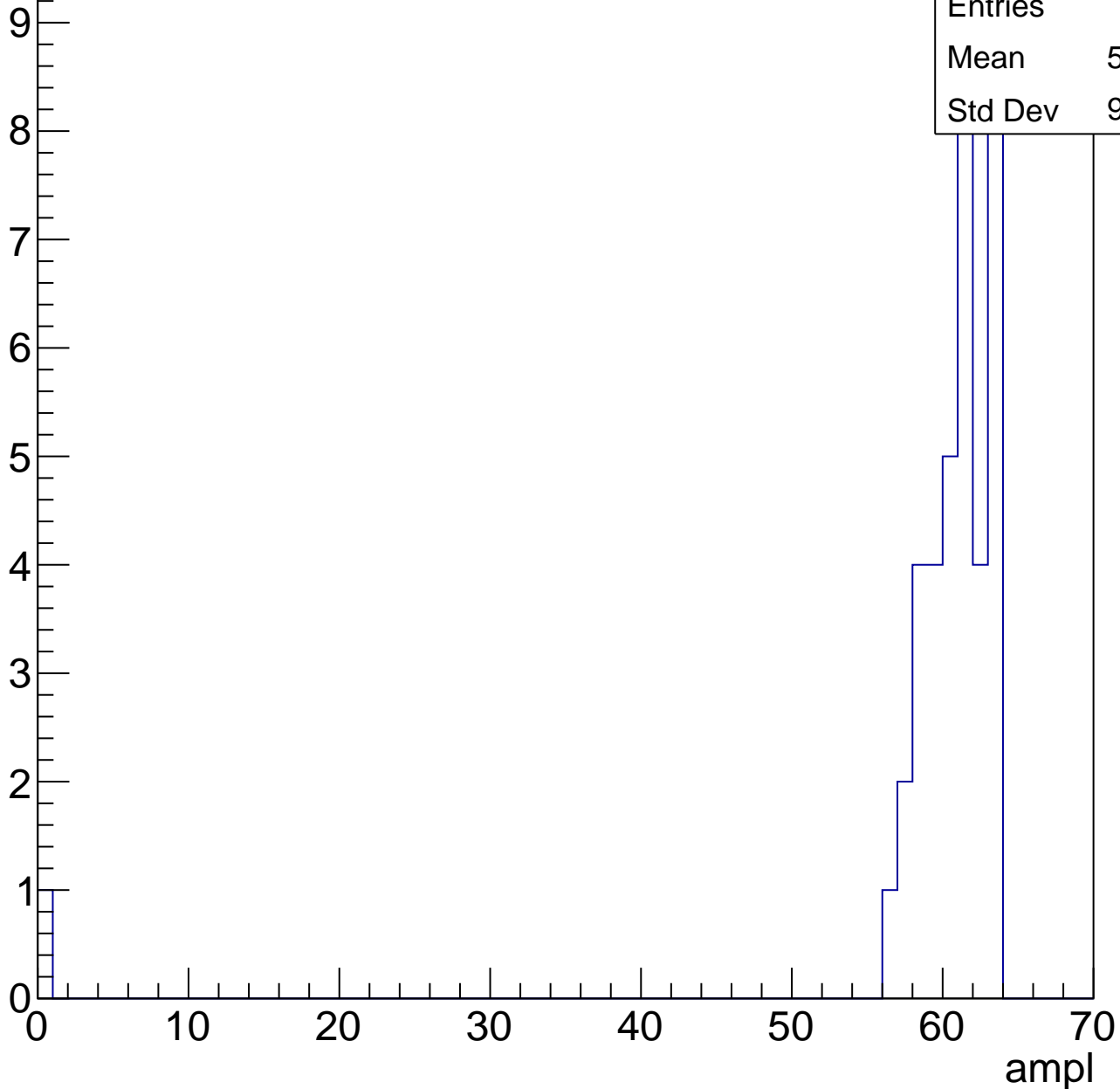


# B0L000S, U10-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.97
Std Dev	9.893



# B0L000S, U10-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch49, adc0

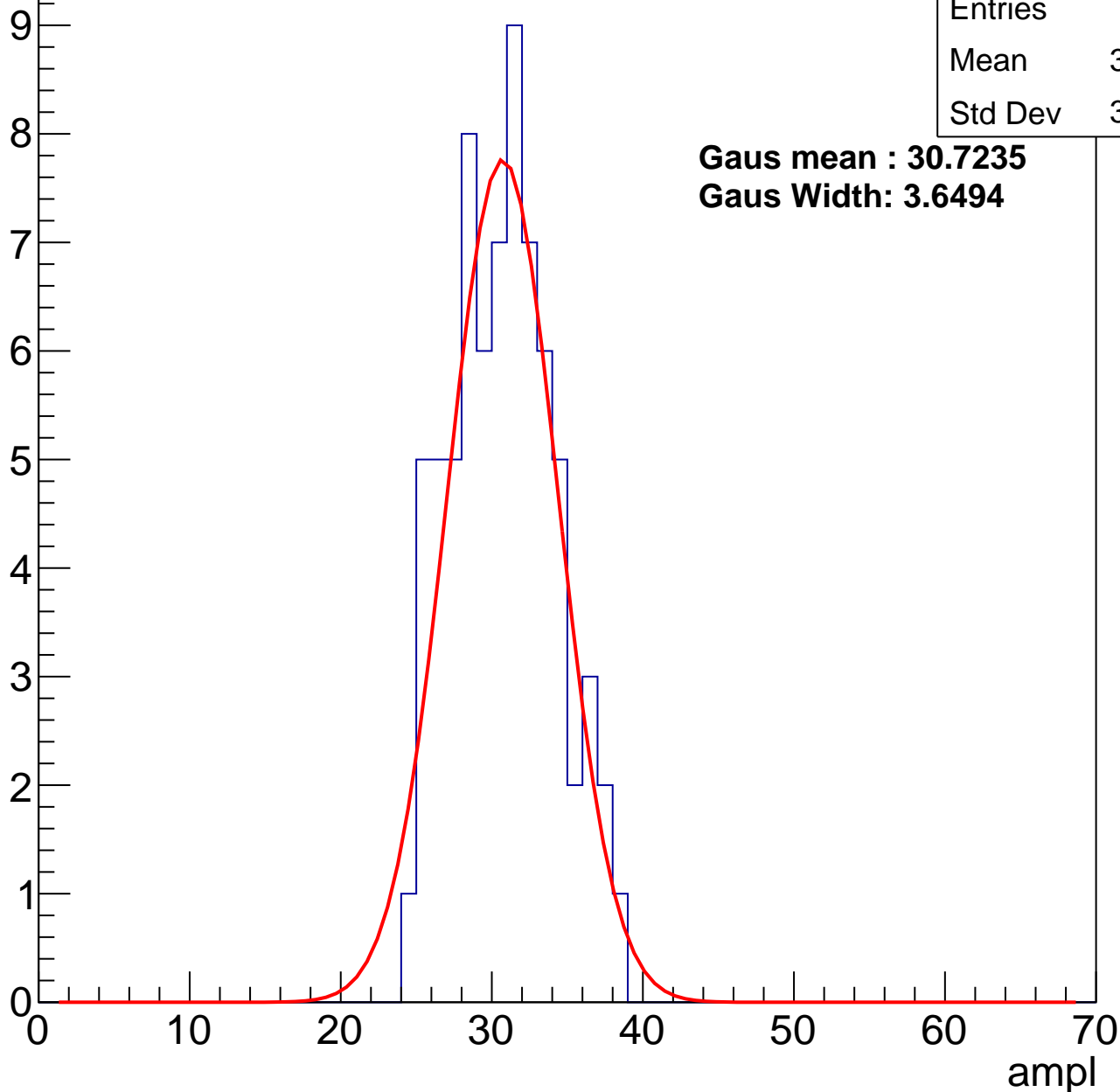
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.32
Std Dev	3.349

**Gaus mean : 30.7235**

**Gaus Width: 3.6494**



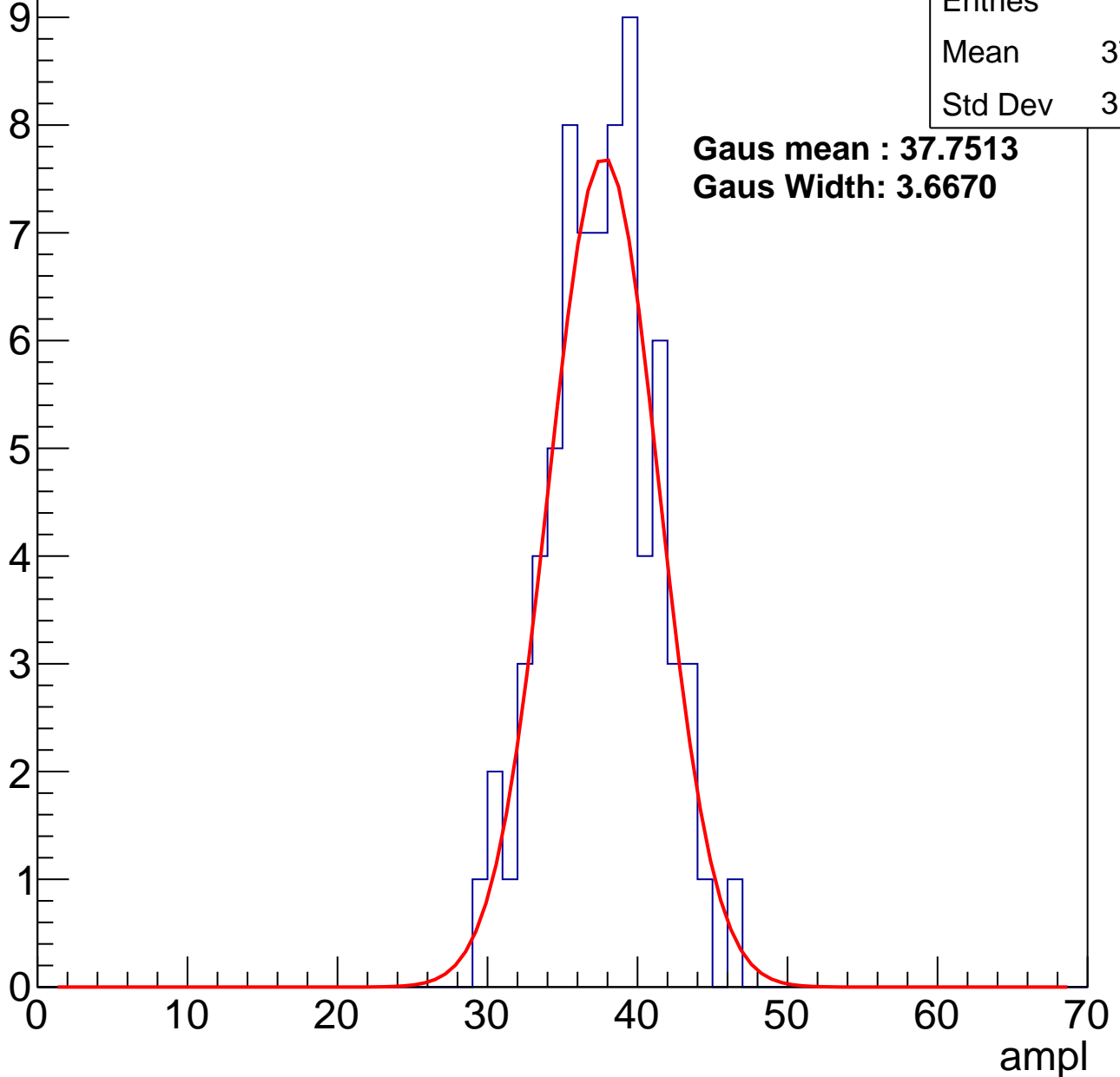
# B0L000S, U10-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	37.19
Std Dev	3.525

**Gaus mean : 37.7513**  
**Gaus Width: 3.6670**

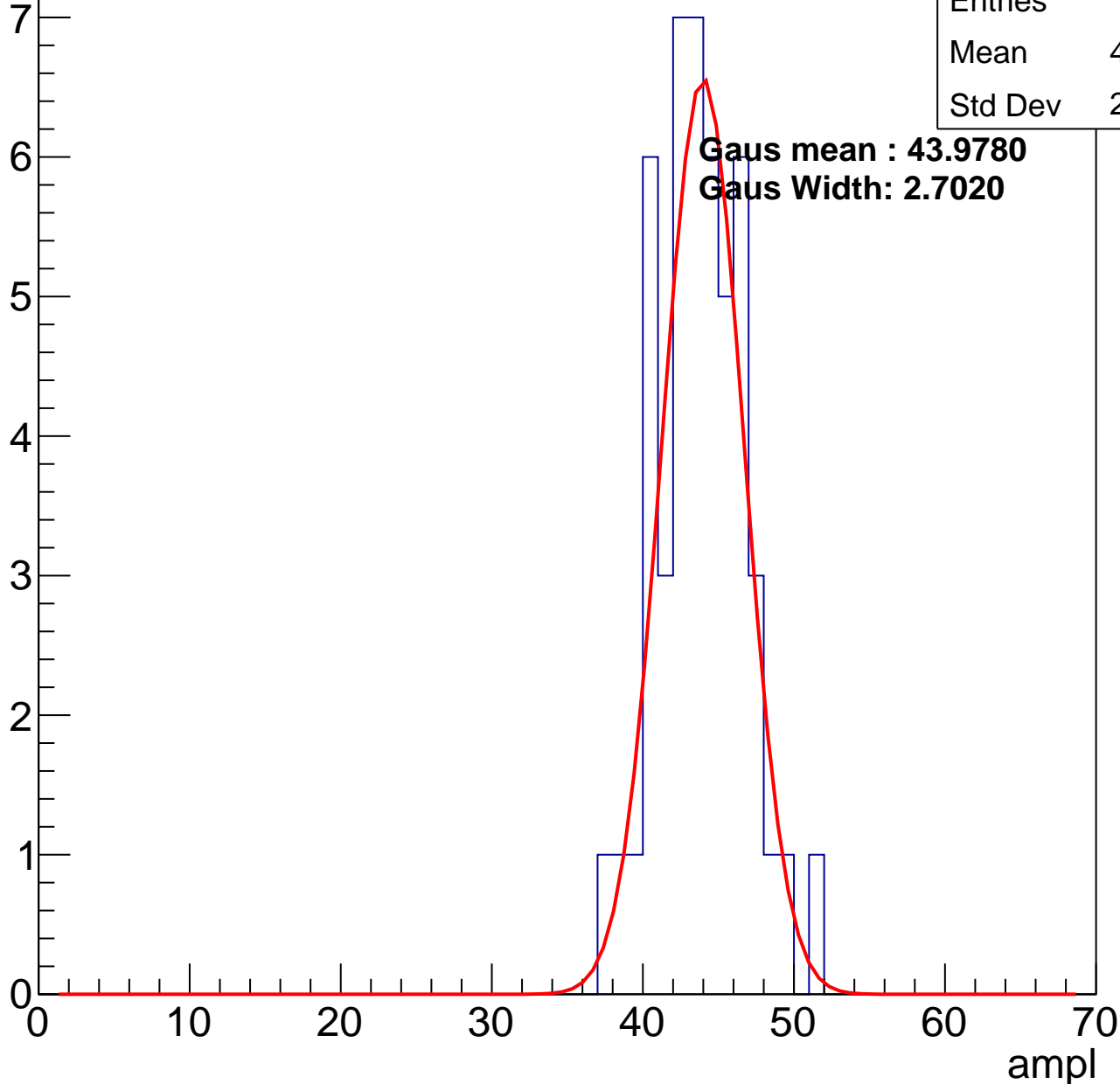


# B0L000S, U10-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	43.39
Std Dev	2.842

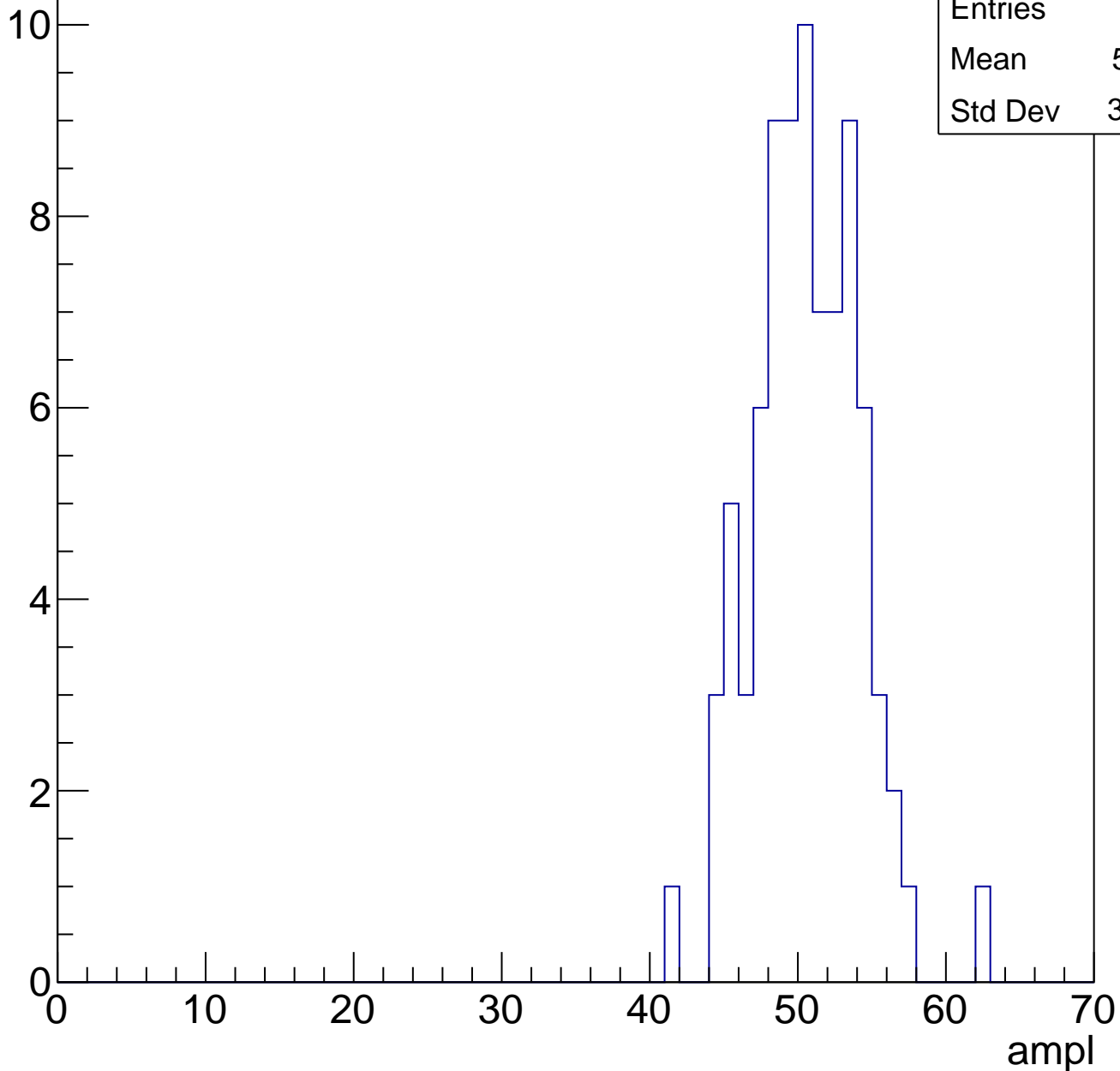


# B0L000S, U10-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	50.11
Std Dev	3.513

Entry

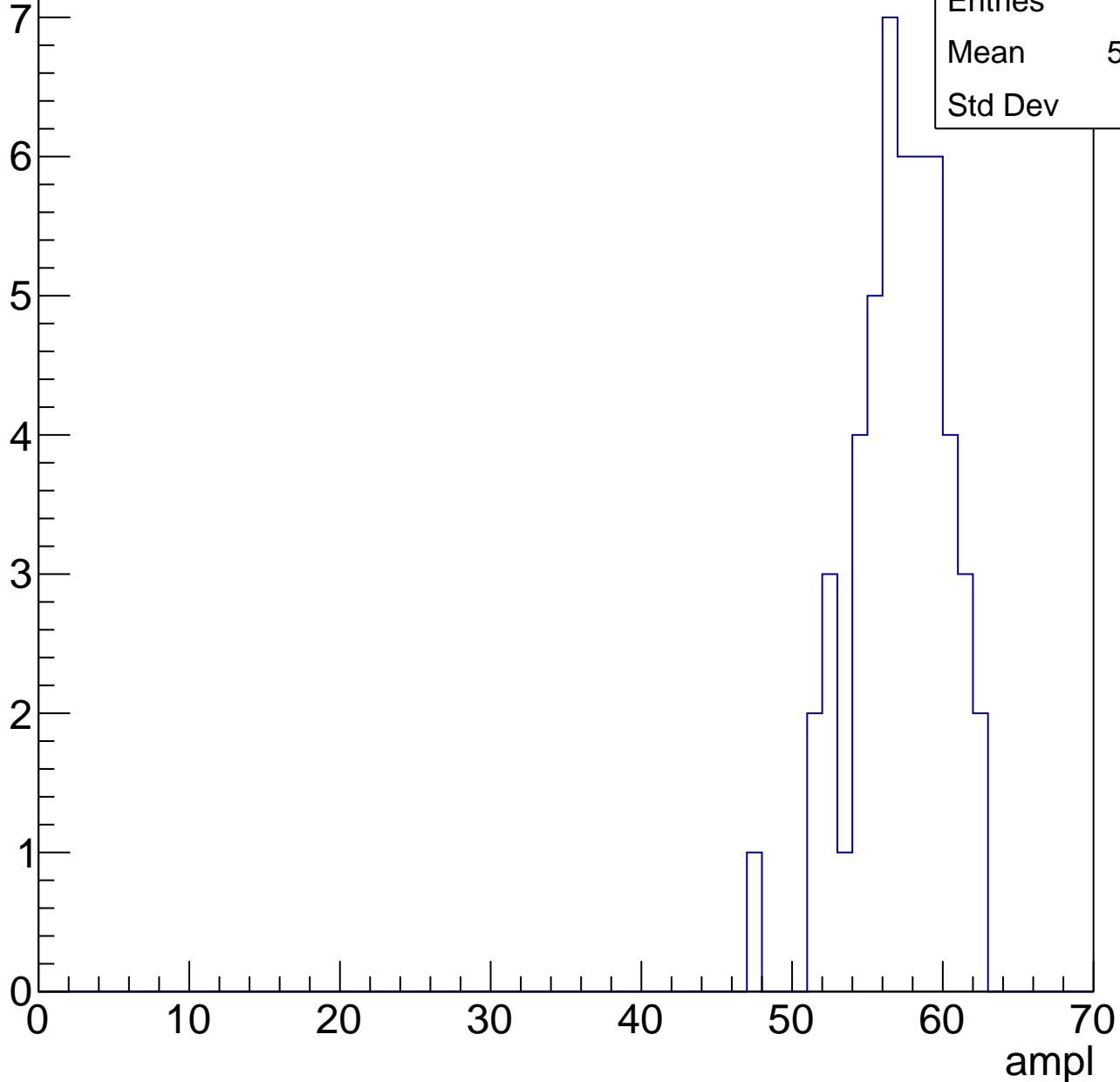


# B0L000S, U10-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.64
Std Dev	3.11

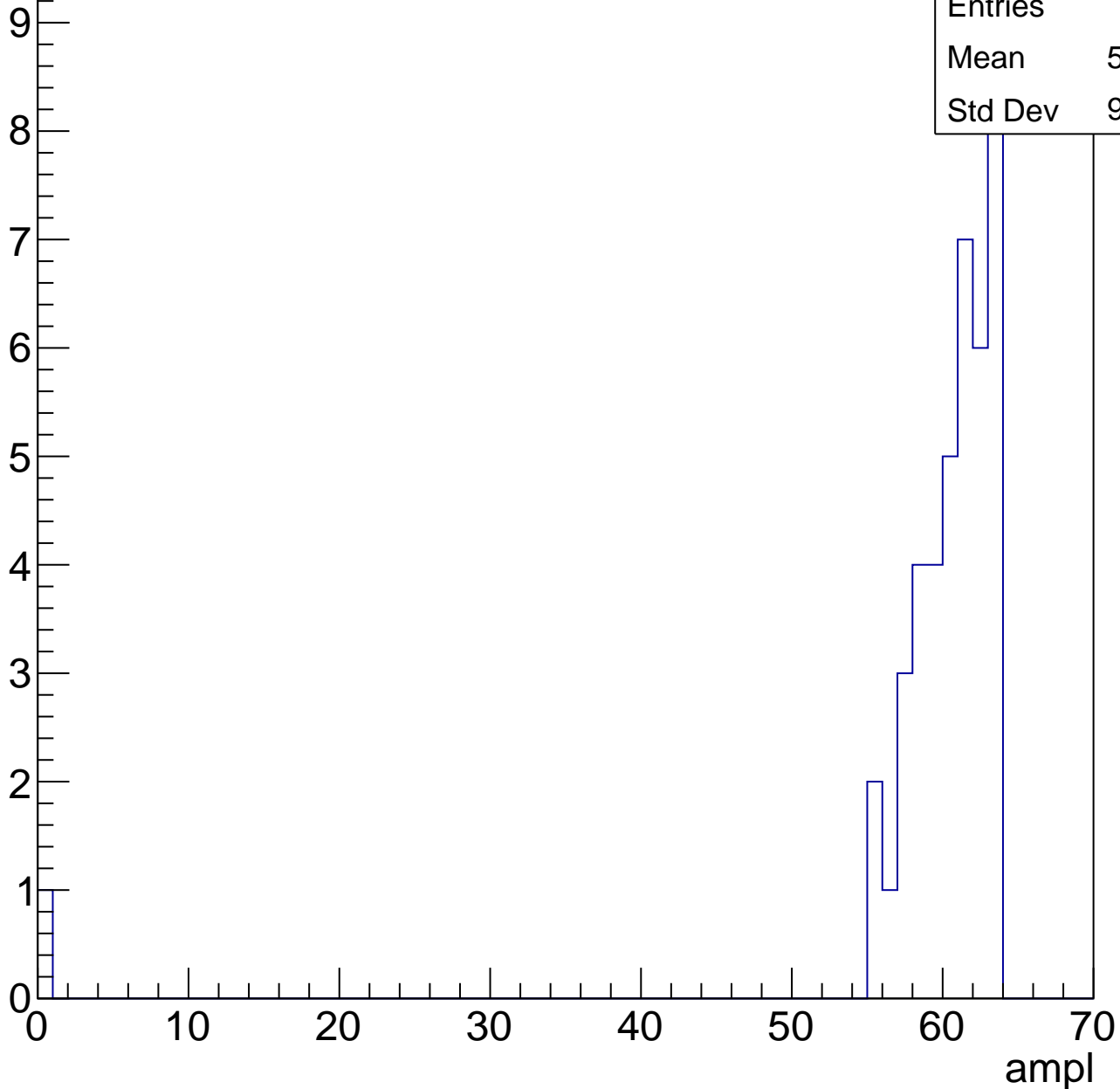


# B0L000S, U10-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.83
Std Dev	9.472



# B0L000S, U10-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch50, adc0

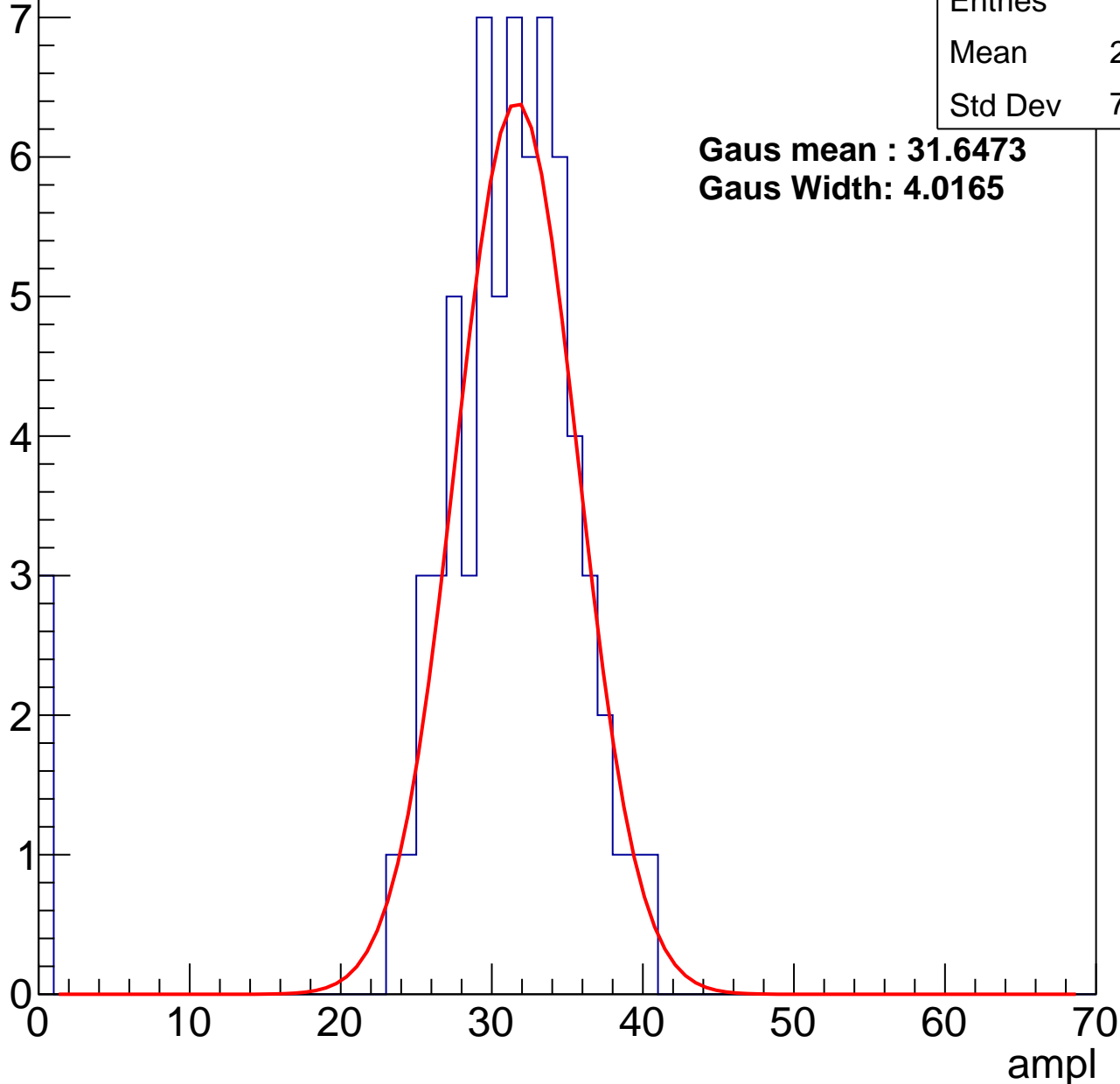
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.78
Std Dev	7.332

**Gaus mean : 31.6473**

**Gaus Width: 4.0165**



# B0L000S, U10-ch50, adc1

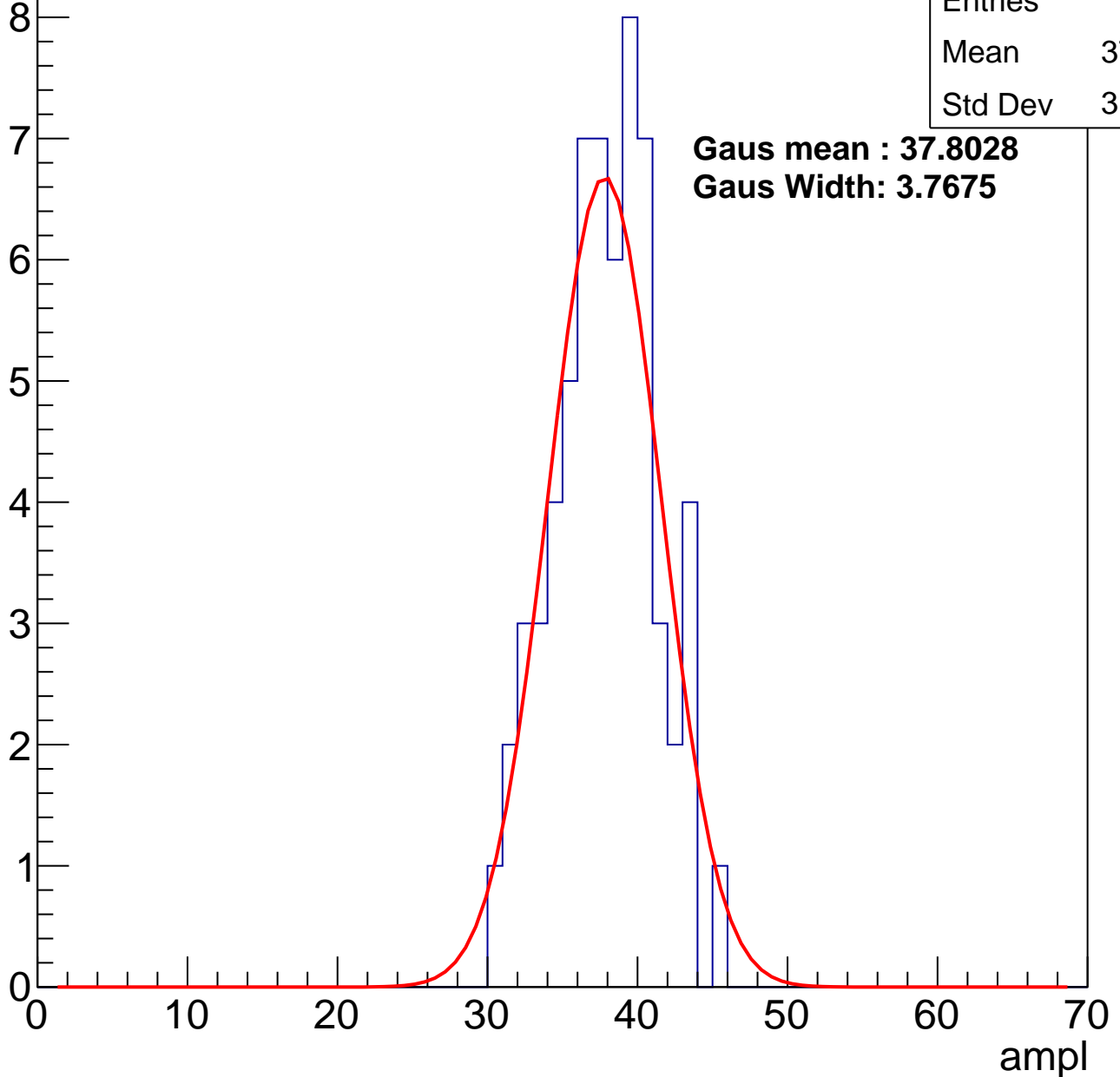
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.35
Std Dev	3.348

**Gaus mean : 37.8028**

**Gaus Width: 3.7675**



# B0L000S, U10-ch50, adc2

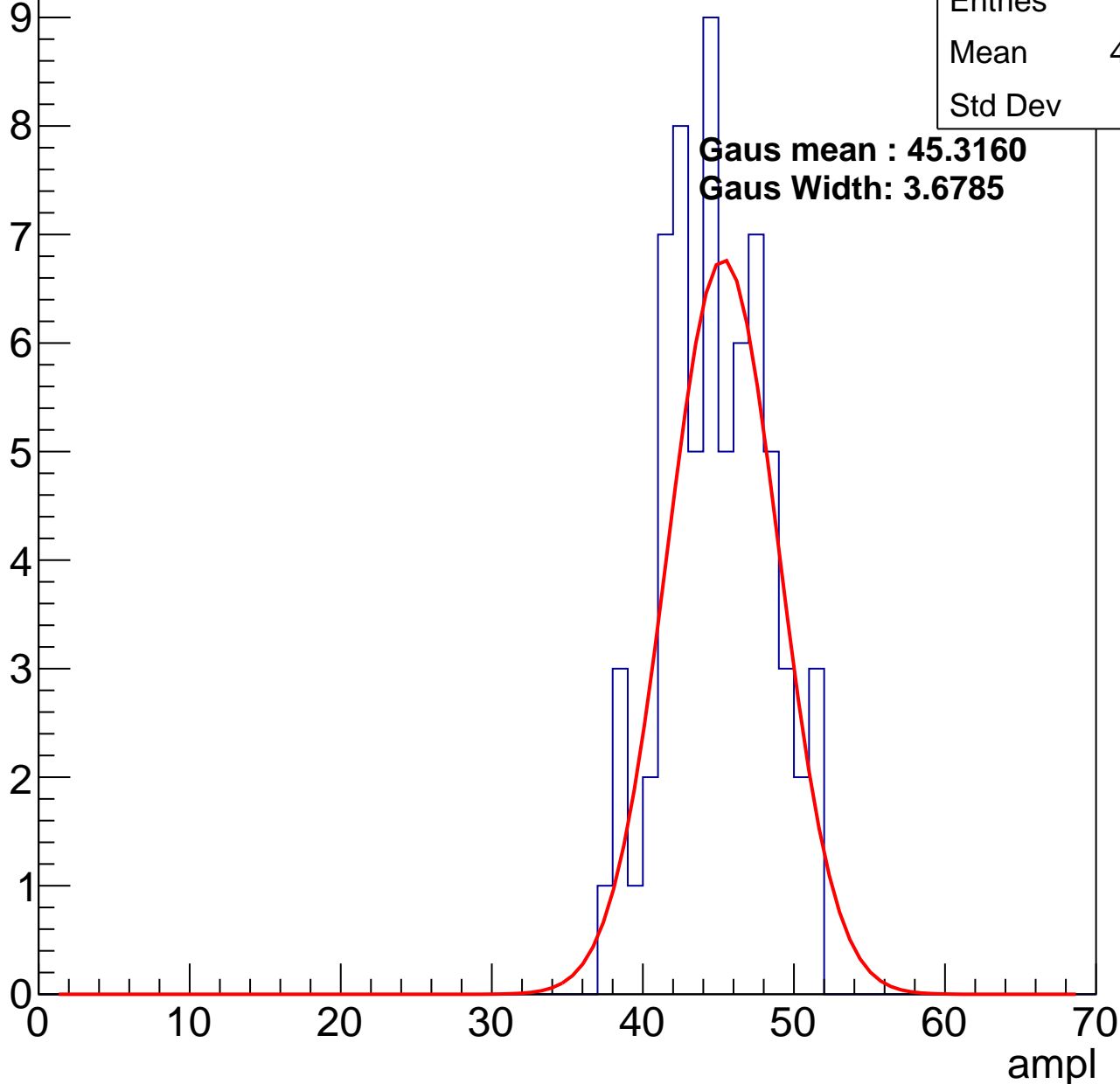
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.39
Std Dev	3.39

**Gaus mean : 45.3160**

**Gaus Width: 3.6785**

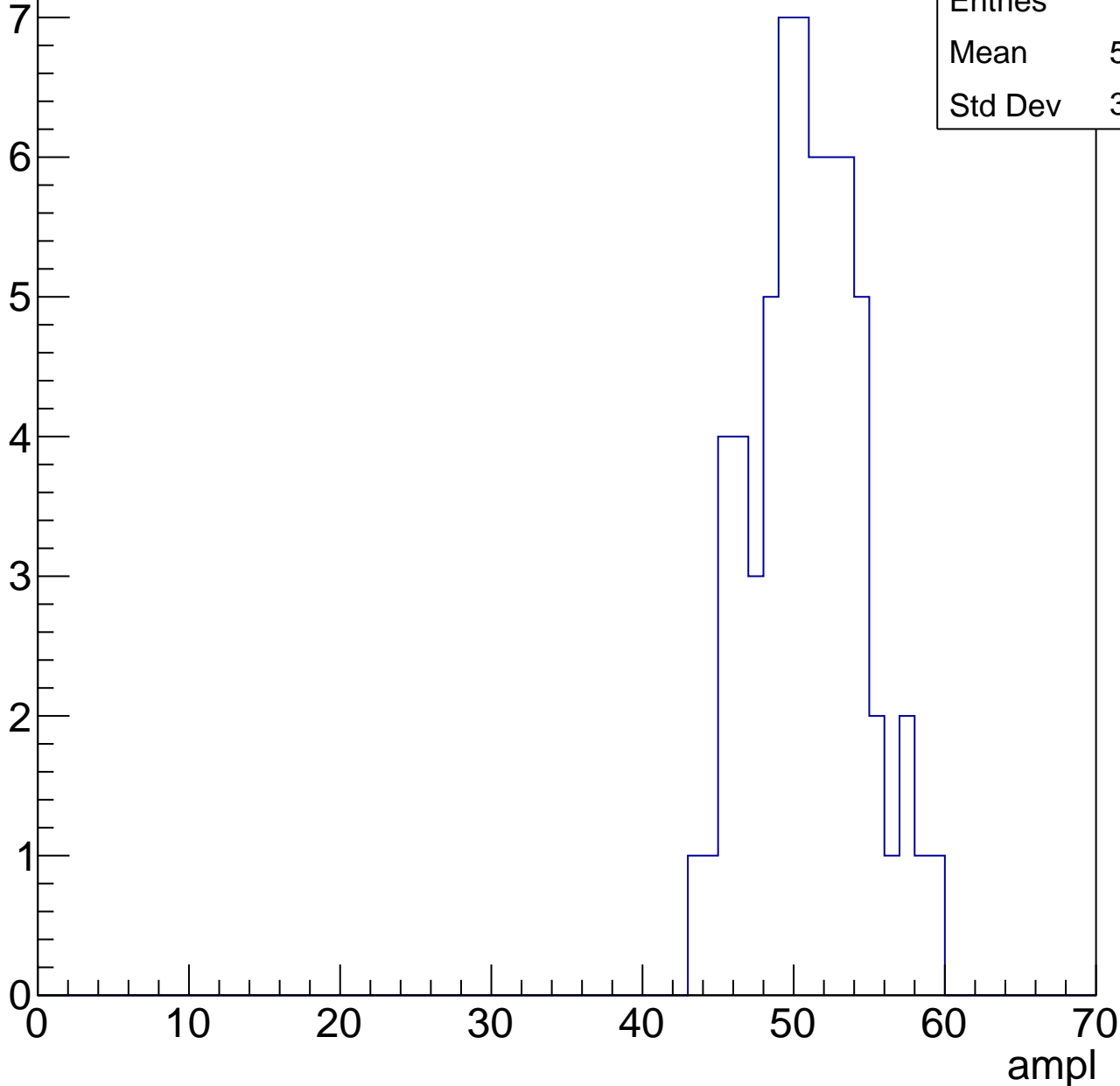


# B0L000S, U10-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.45
Std Dev	3.555

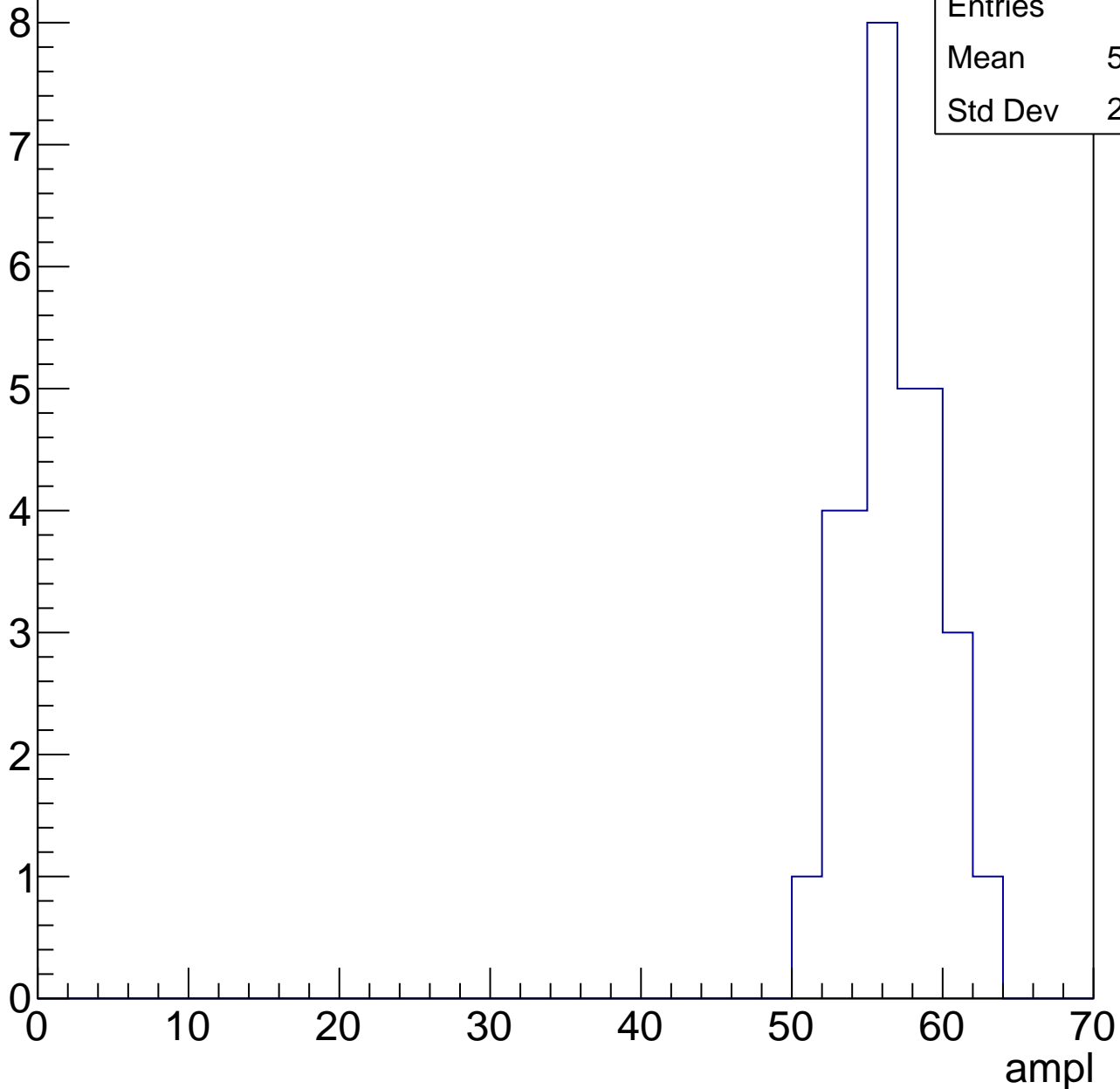


# B0L000S, U10-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.28
Std Dev	2.942

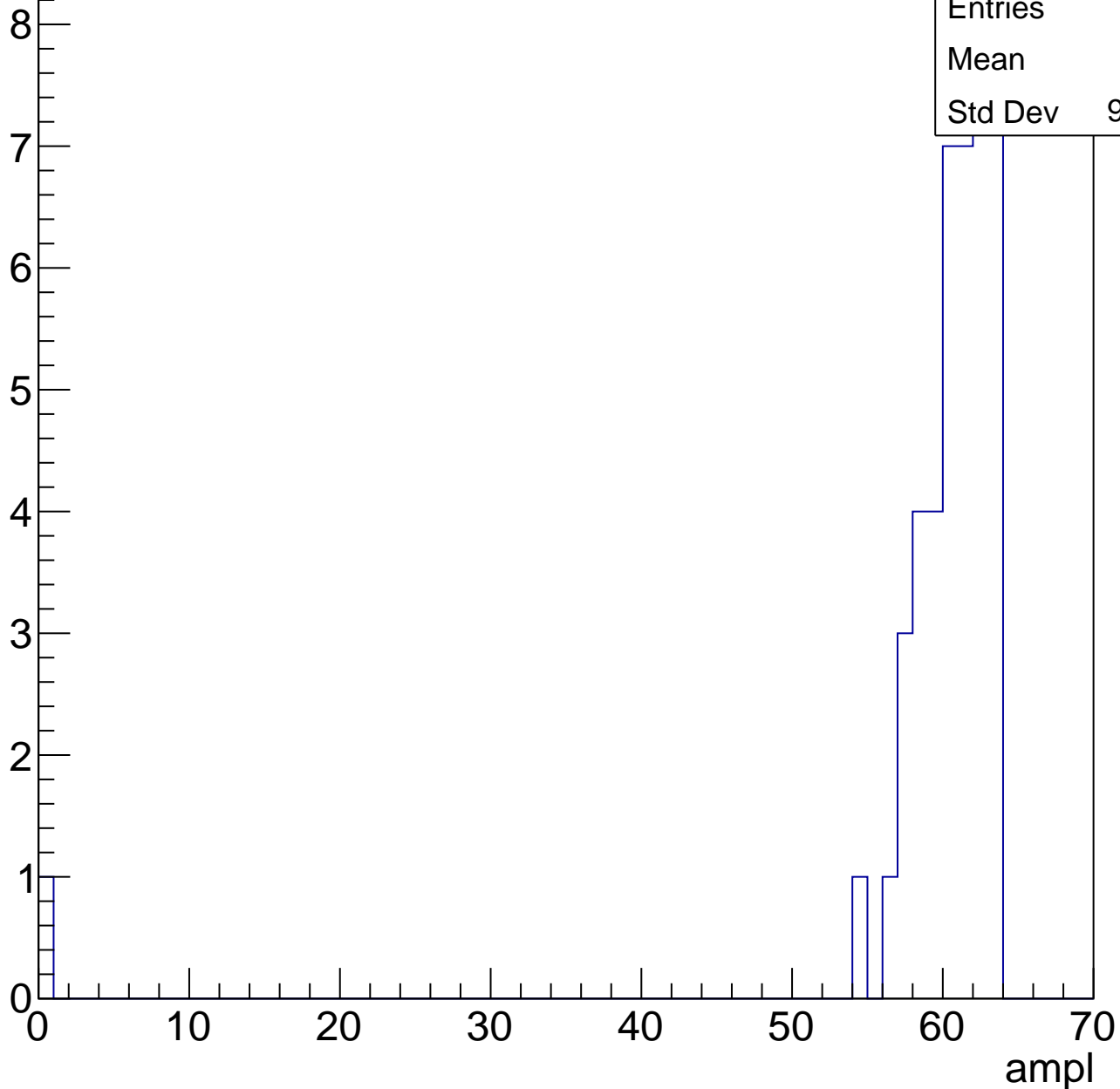


# B0L000S, U10-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	59
Std Dev	9.252



# B0L000S, U10-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U10-ch51, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	30.1
Std Dev	4.731

**Gaus mean : 31.2824**

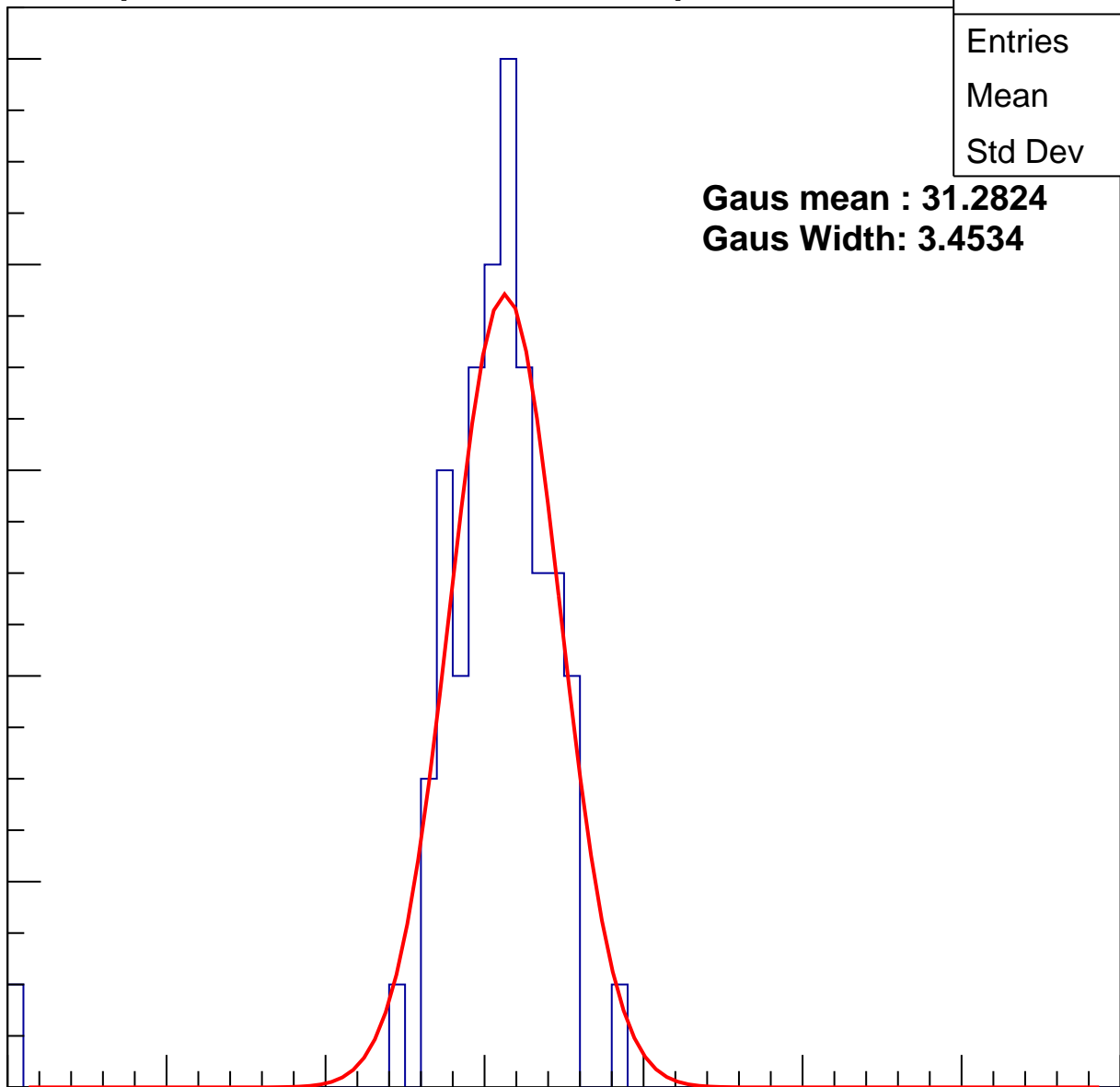
**Gaus Width: 3.4534**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	37.08
Std Dev	3.459

**Gaus mean : 37.7768**

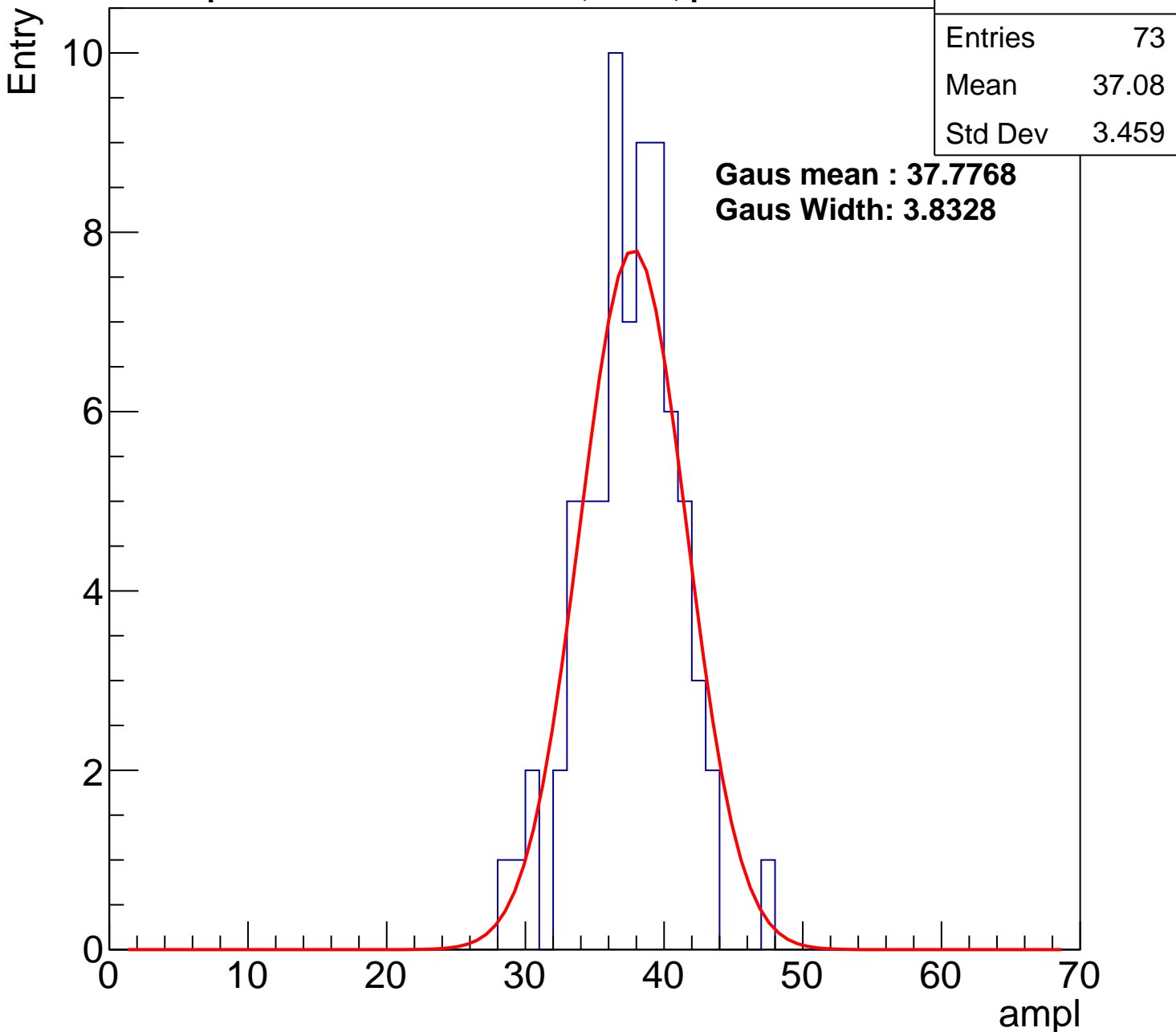
**Gaus Width: 3.8328**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch51, adc2

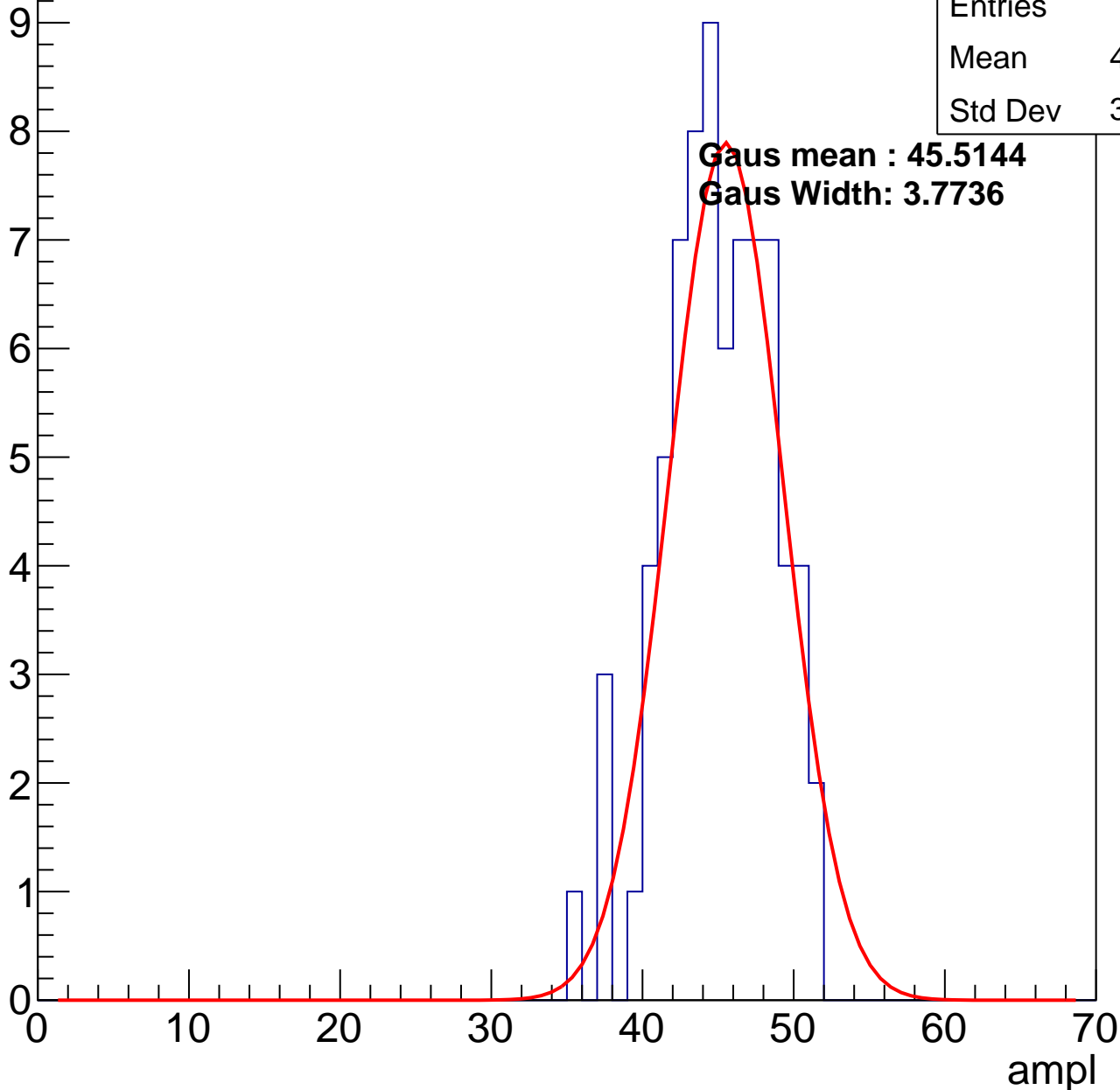
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.52
Std Dev	3.519

**Gaus mean : 45.5144**

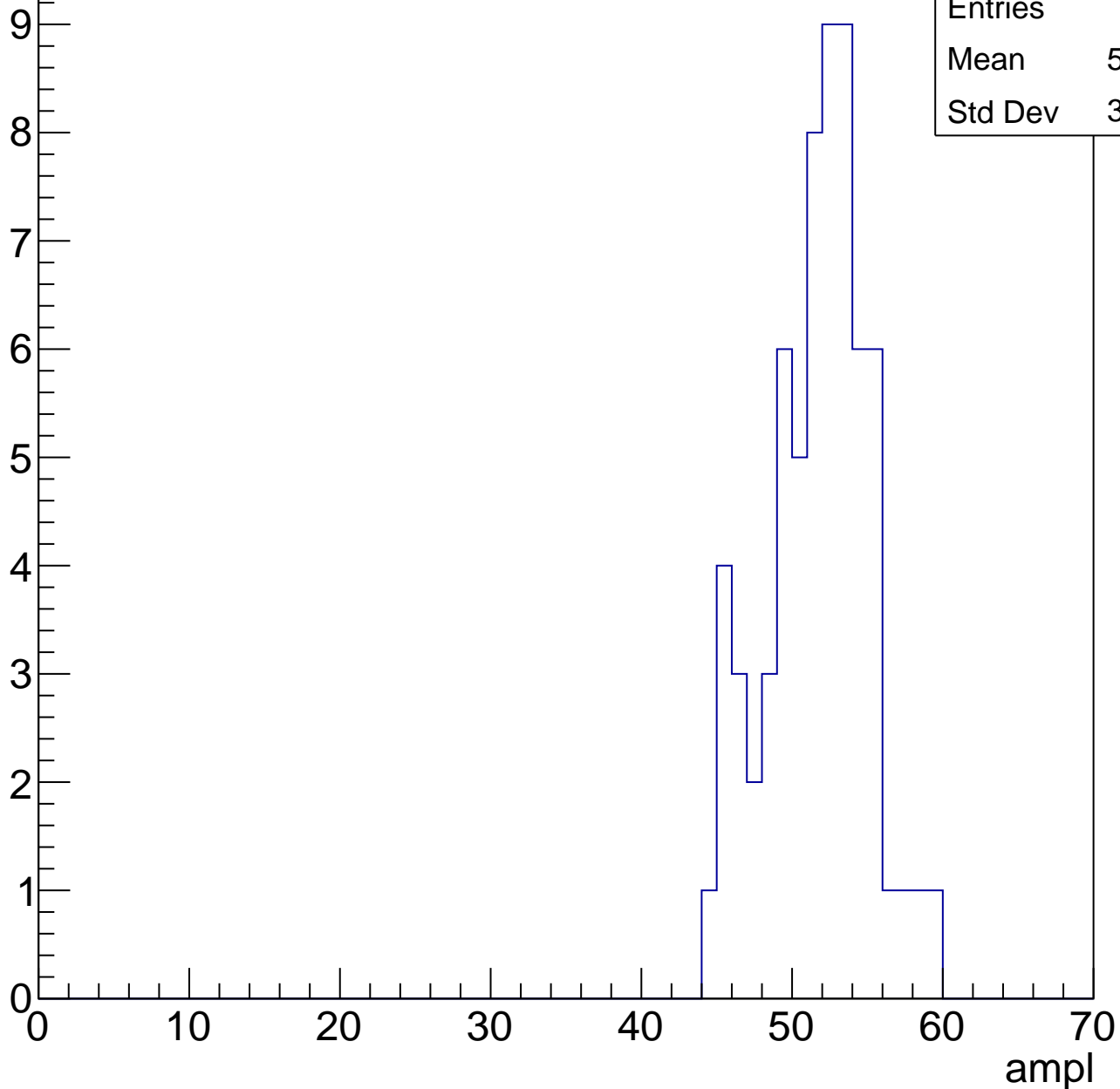
**Gaus Width: 3.7736**



# B0L000S, U10-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

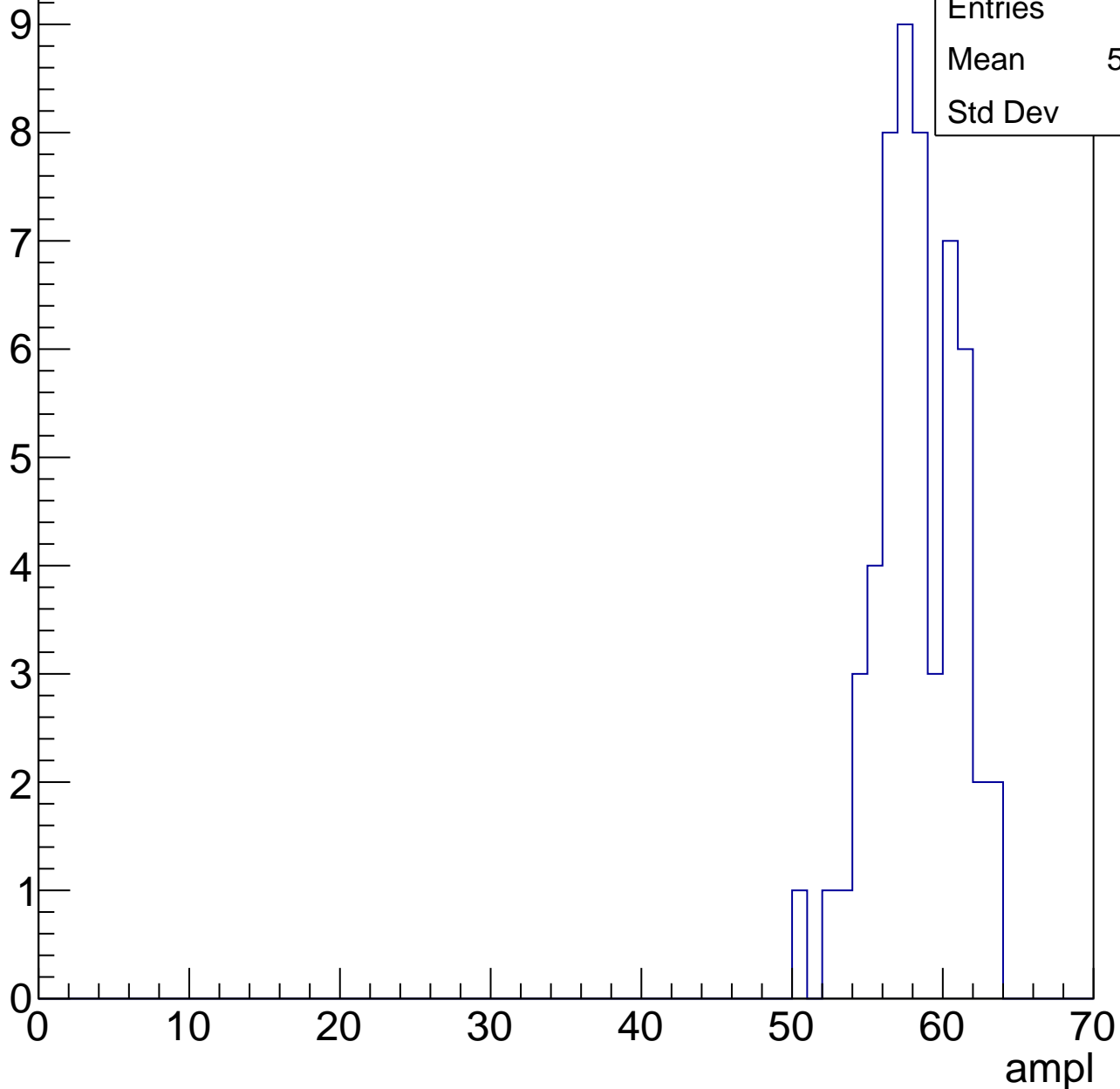
Entry



# B0L000S, U10-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



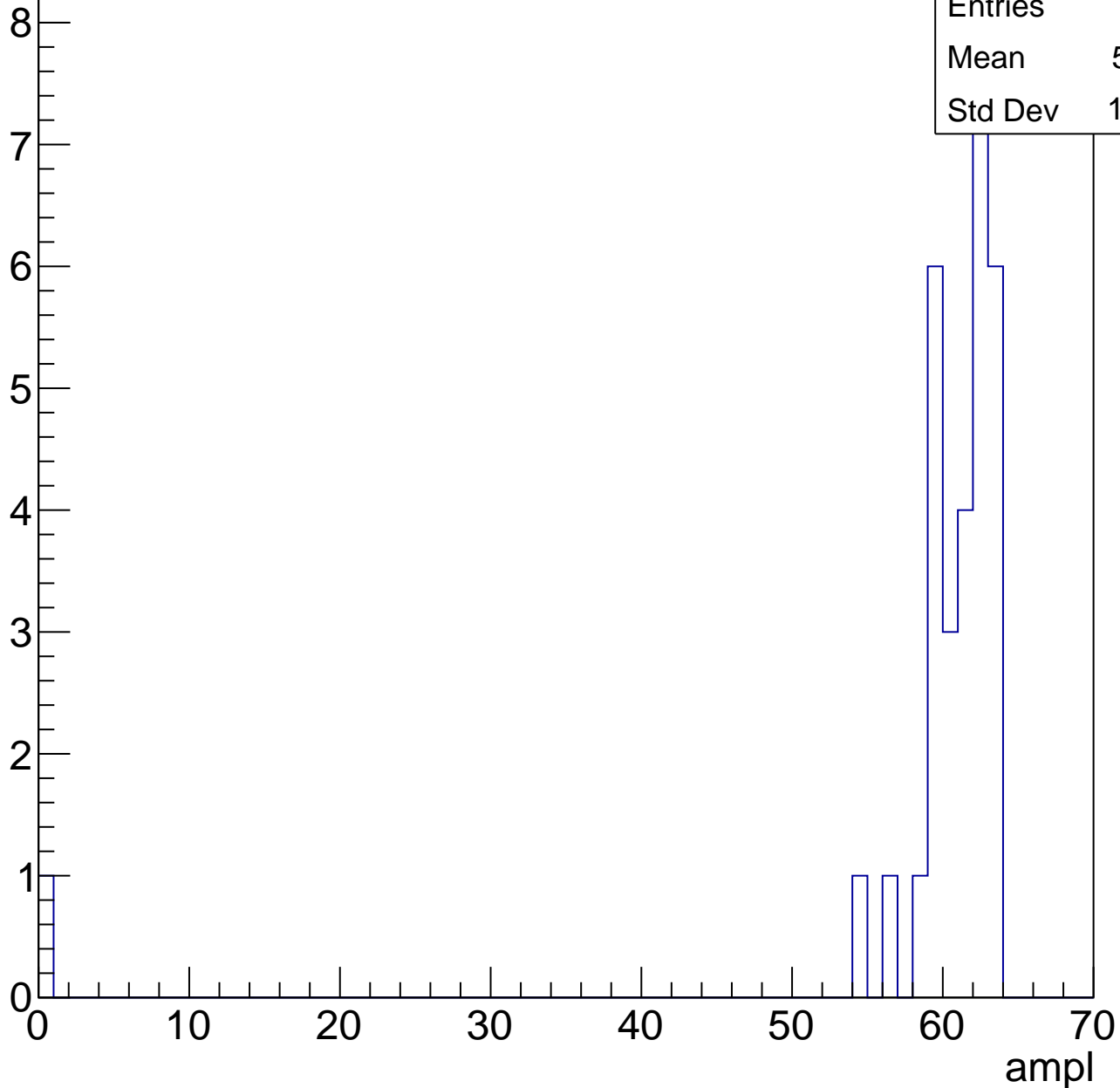
Entries	55
Mean	57.73
Std Dev	2.74

# B0L000S, U10-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	58.71
Std Dev	10.93



# B0L000S, U10-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch52, adc0

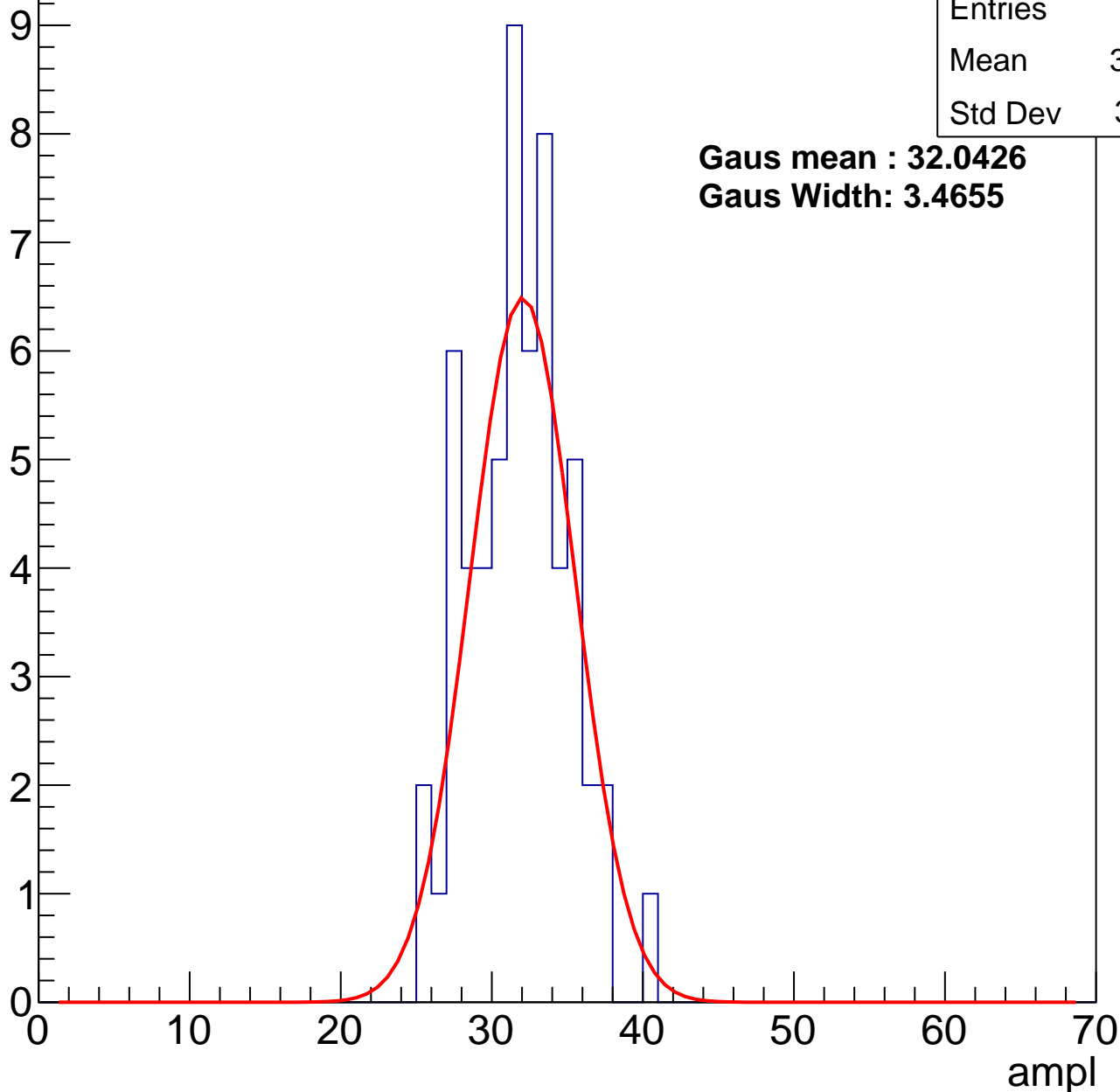
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	31.32
Std Dev	3.191

**Gaus mean : 32.0426**

**Gaus Width: 3.4655**



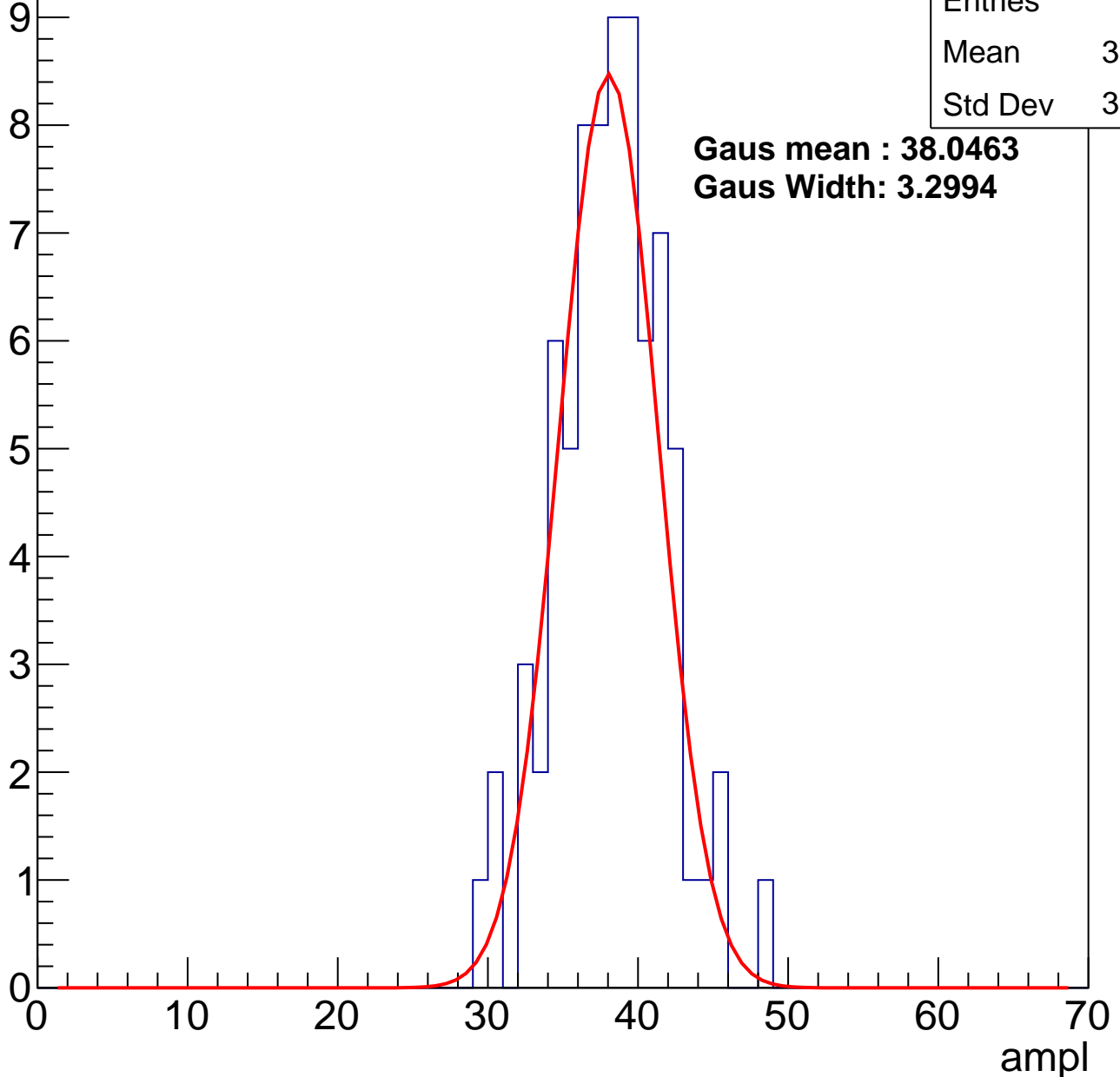
# B0L000S, U10-ch52, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	37.75
Std Dev	3.588

**Gaus mean : 38.0463**  
**Gaus Width: 3.2994**



# B0L000S, U10-ch52, adc2

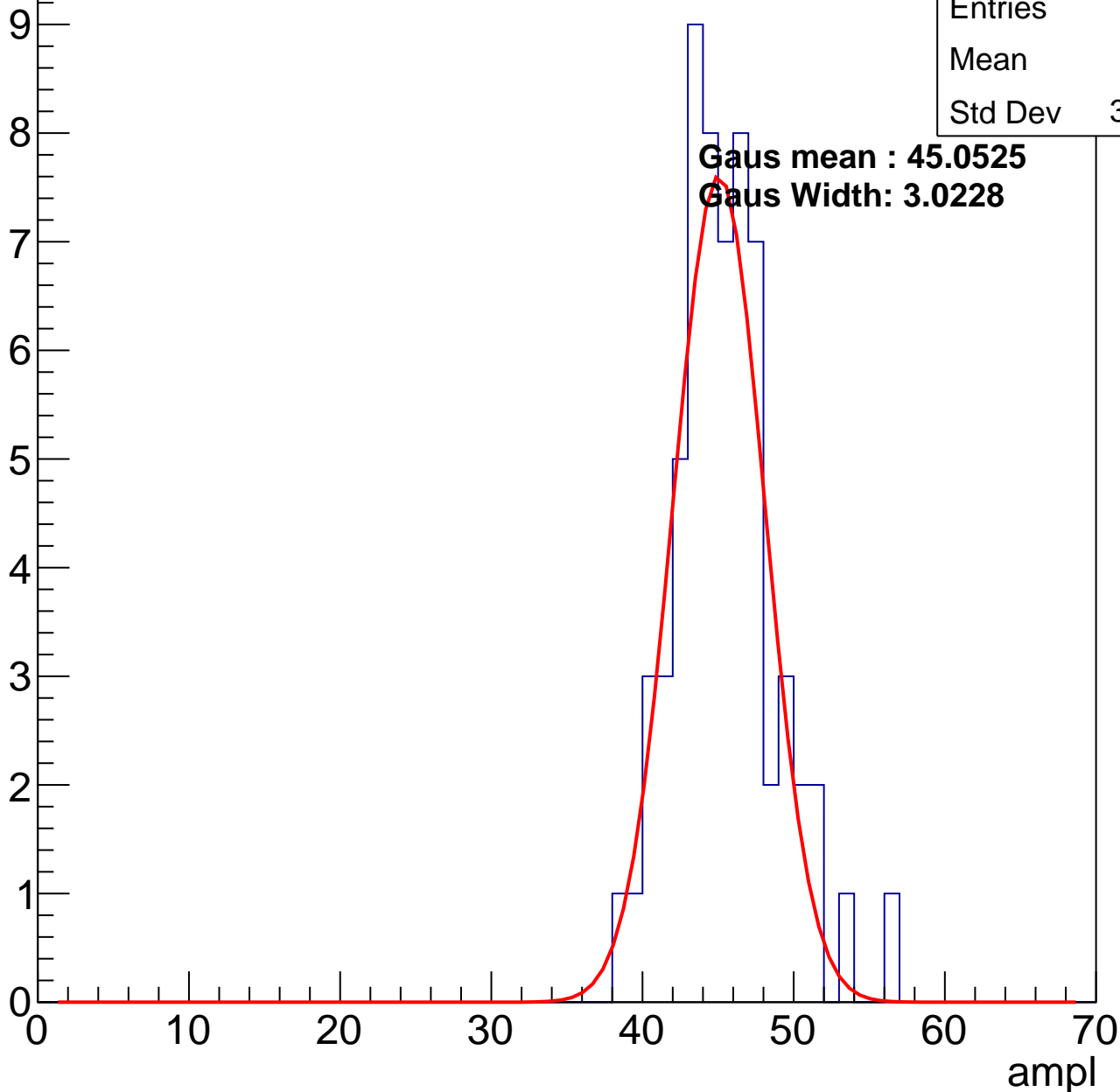
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	45
Std Dev	3.348

**Gaus mean : 45.0525**

**Gaus Width: 3.0228**

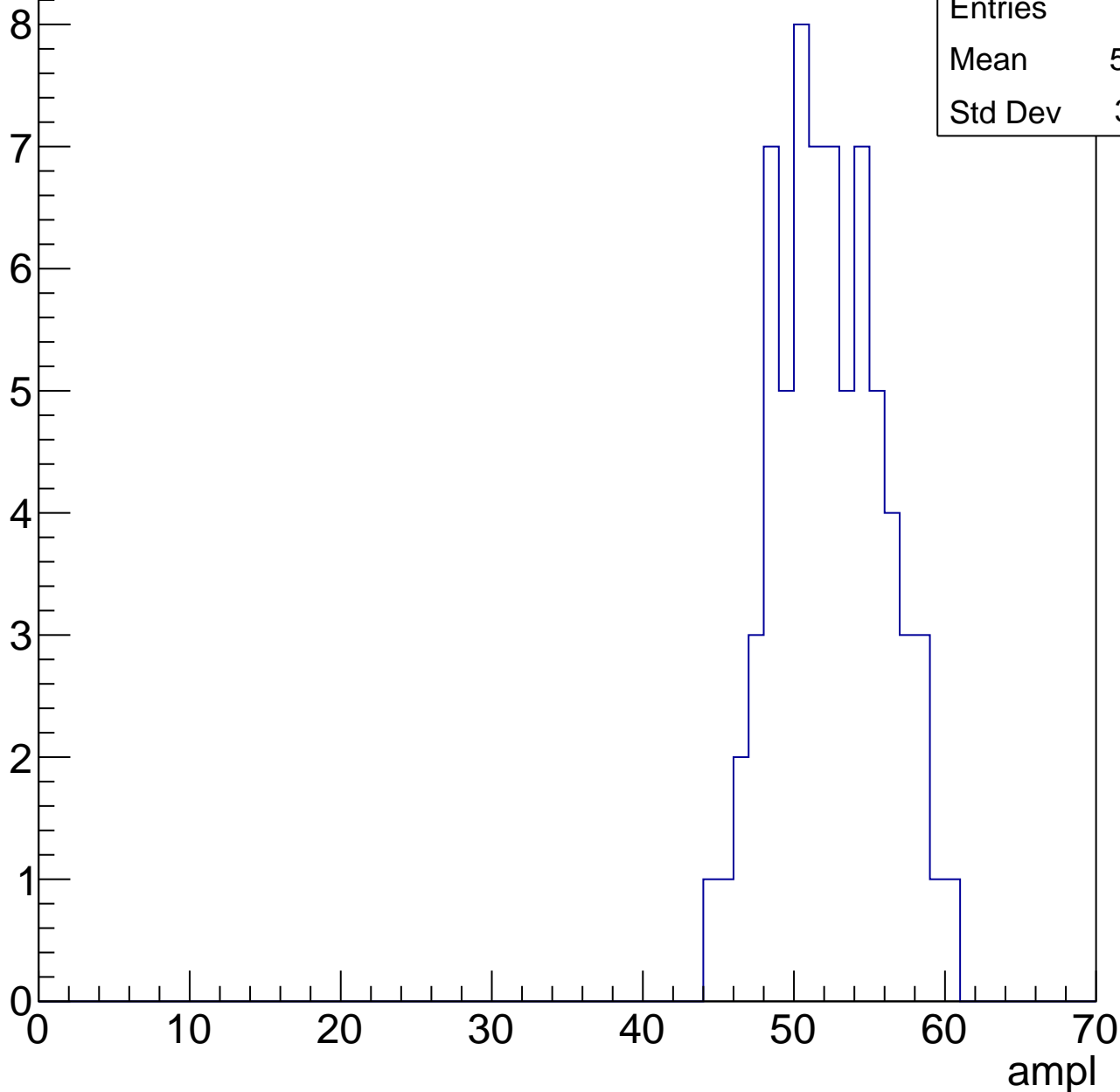


# B0L000S, U10-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

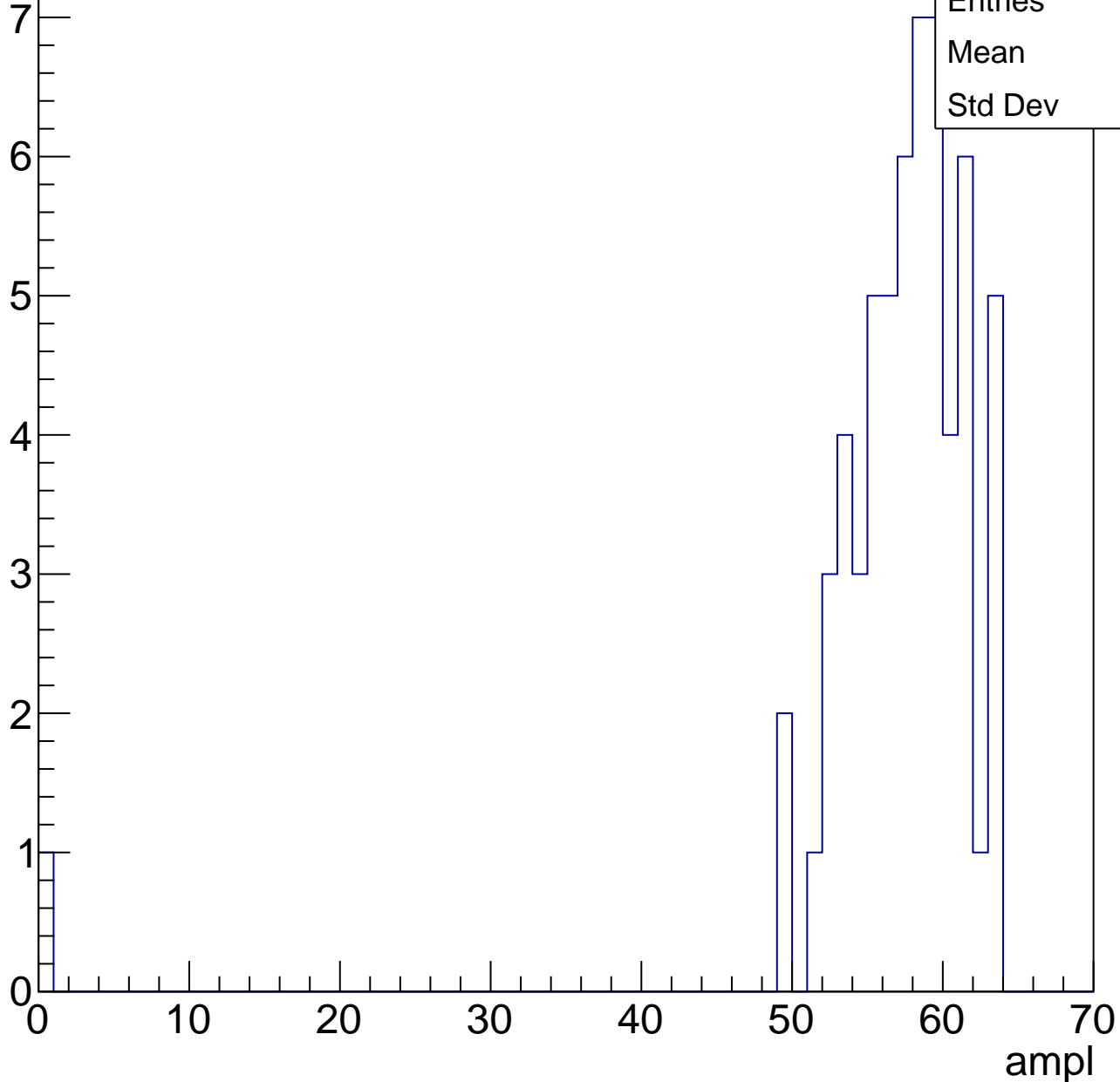
Entries	70
Mean	51.86
Std Dev	3.571



# B0L000S, U10-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

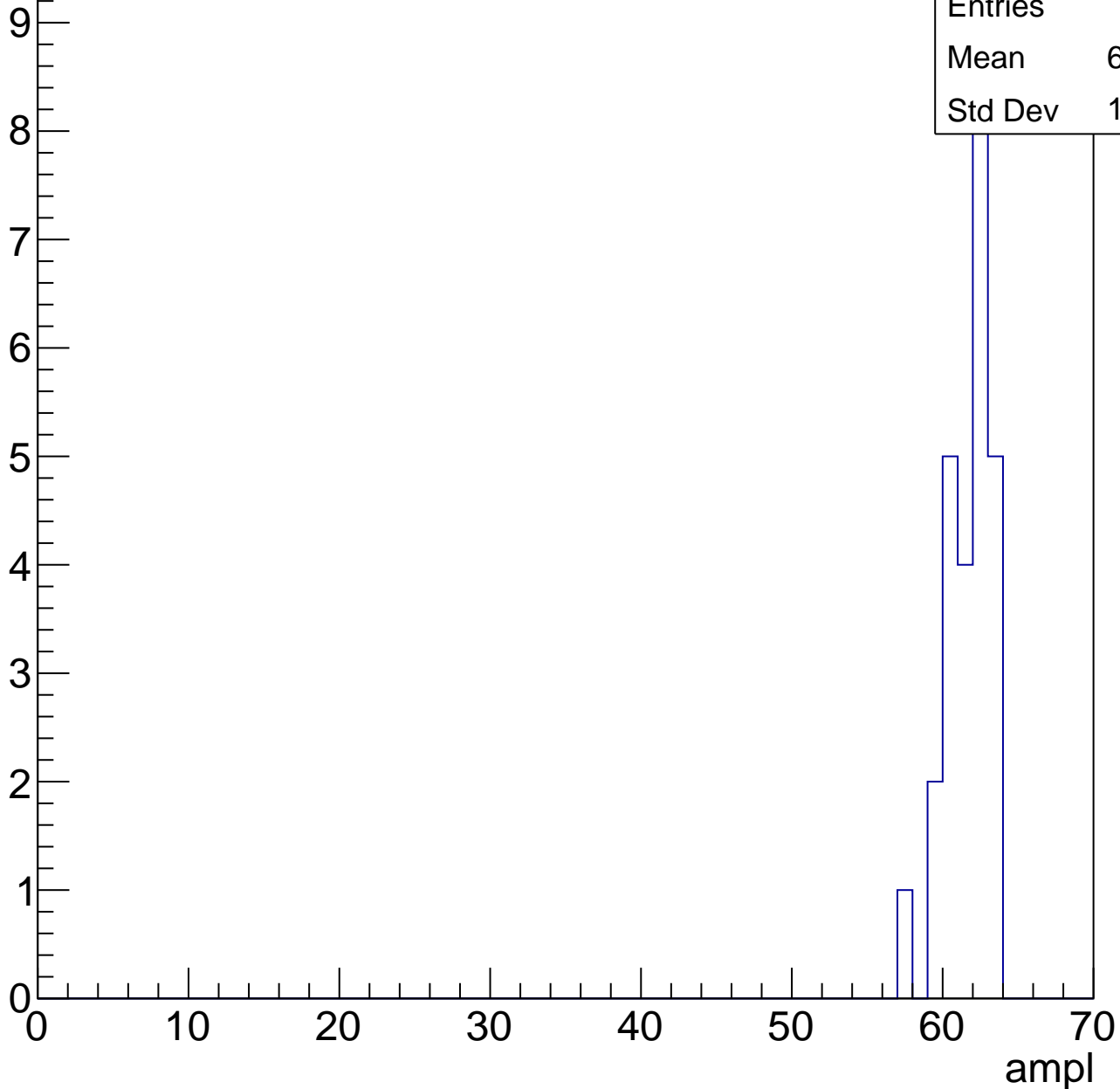


# B0L000S, U10-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	61.23
Std Dev	1.476



# B0L000S, U10-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



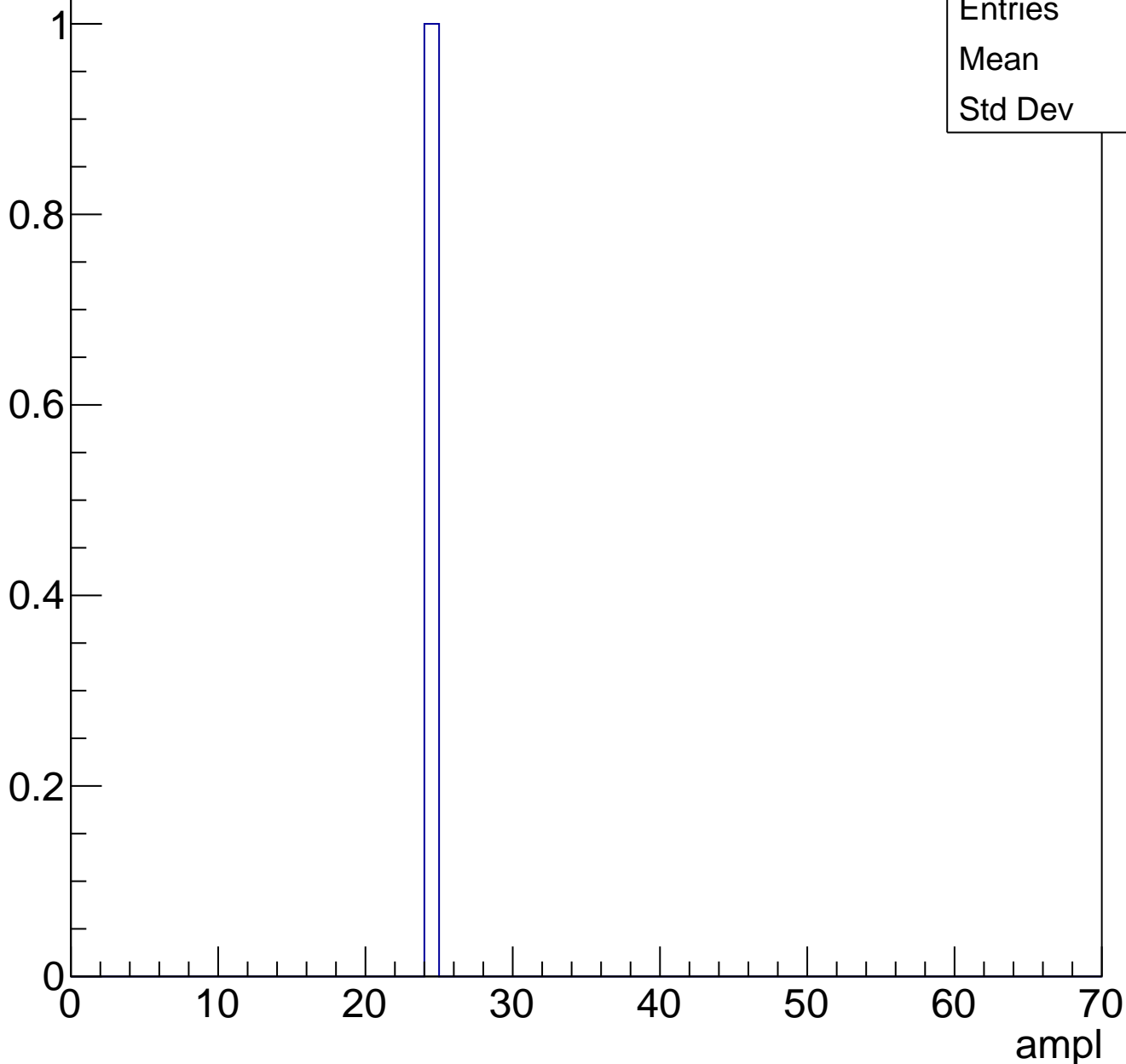
Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U10-ch53, adc0

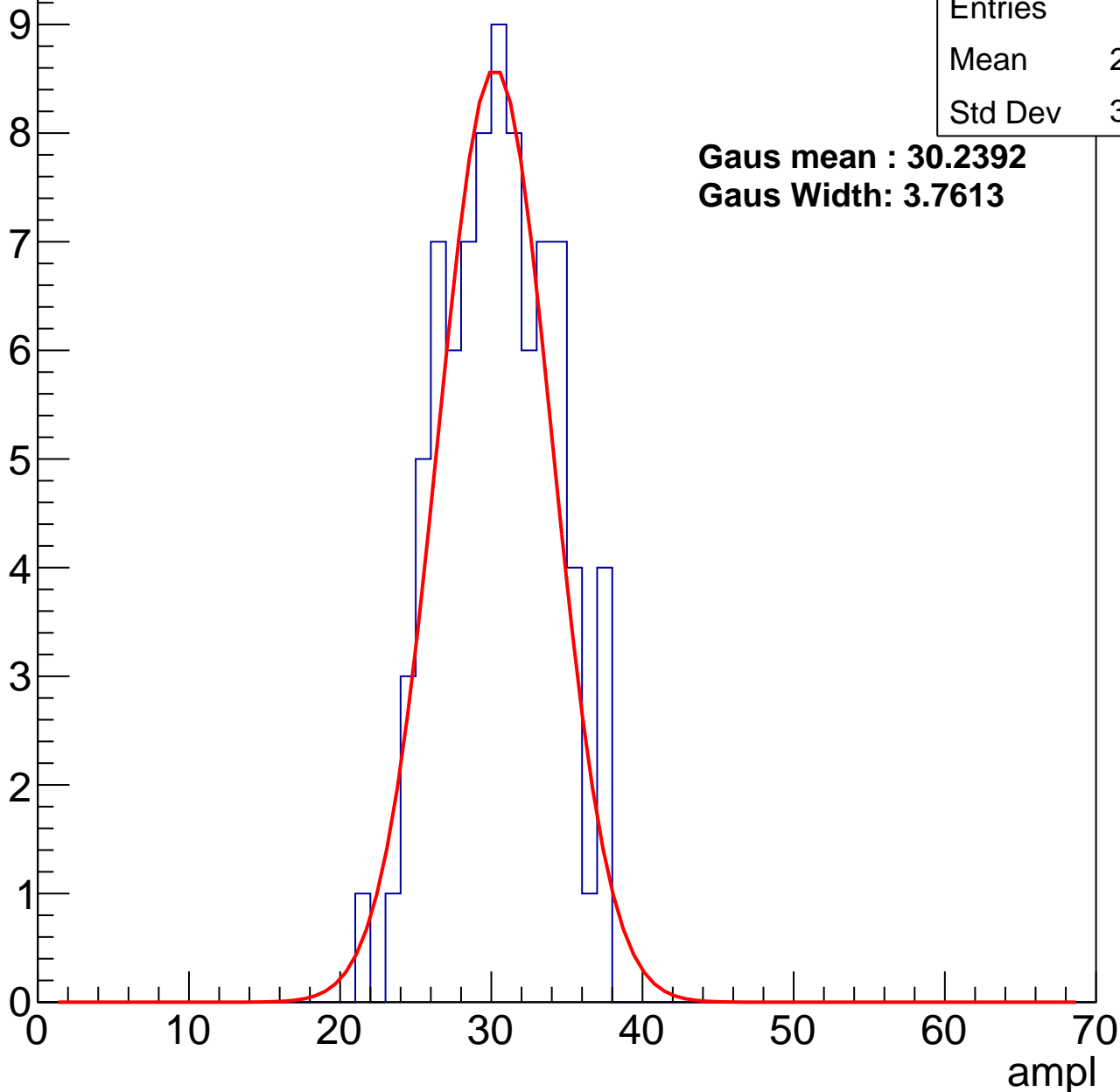
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	29.95
Std Dev	3.625

**Gaus mean : 30.2392**

**Gaus Width: 3.7613**



# B0L000S, U10-ch53, adc1

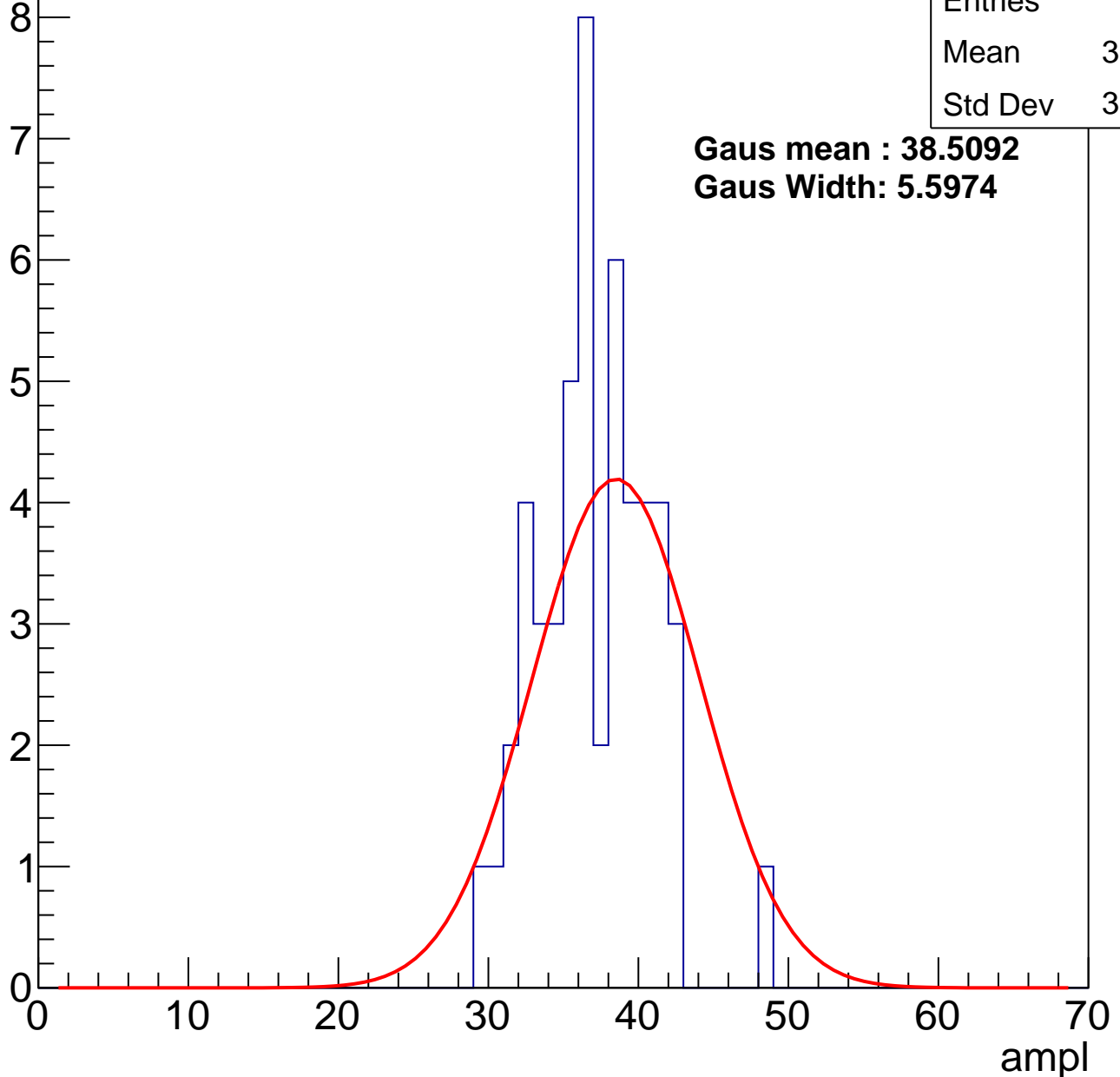
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	36.65
Std Dev	3.704

**Gaus mean : 38.5092**

**Gaus Width: 5.5974**



# B0L000S, U10-ch53, adc2

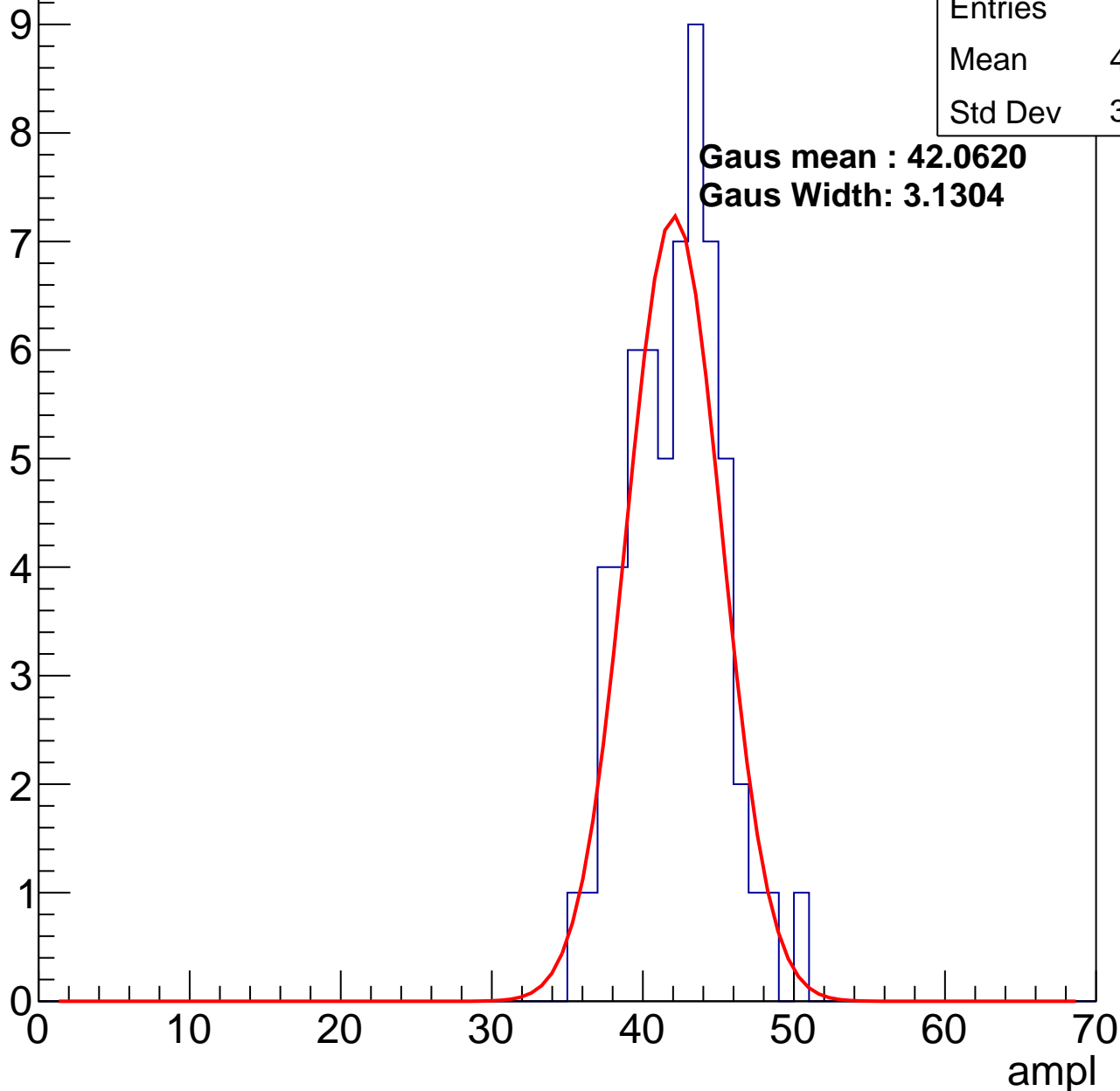
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.68
Std Dev	3.069

**Gaus mean : 42.0620**

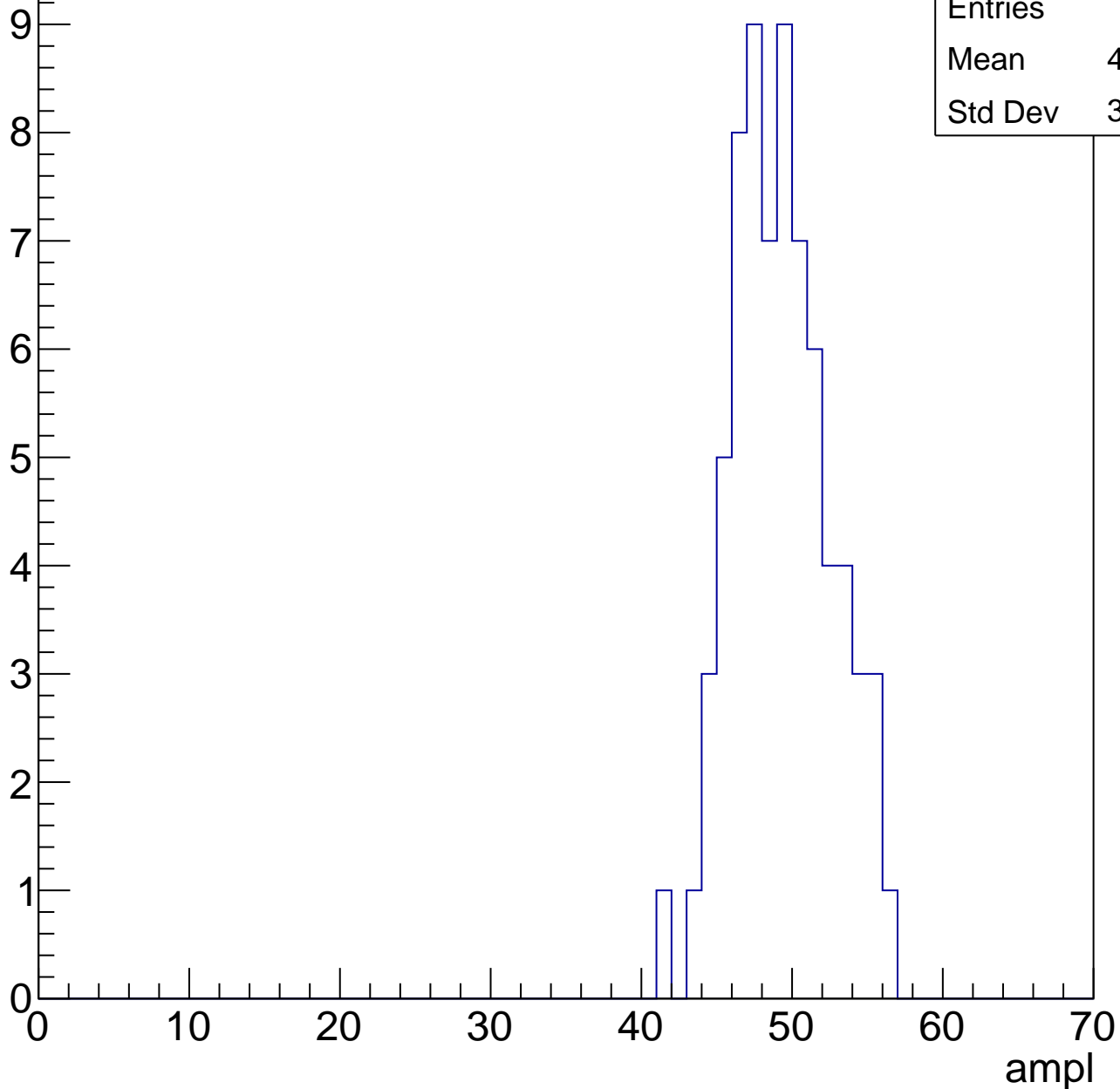
**Gaus Width: 3.1304**



# B0L000S, U10-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



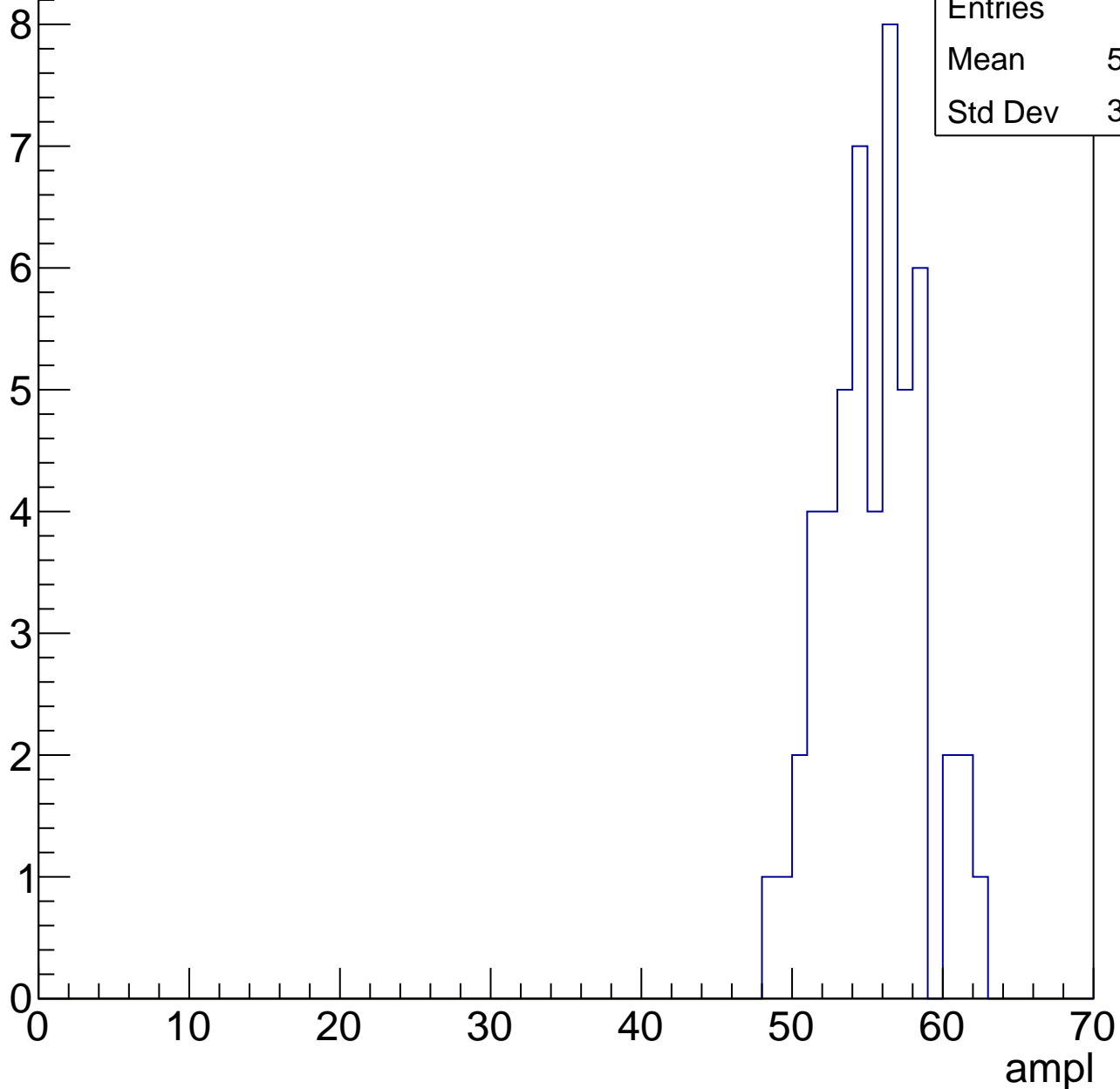
Entries	71
Mean	48.85
Std Dev	3.209

# B0L000S, U10-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	54.94
Std Dev	3.147

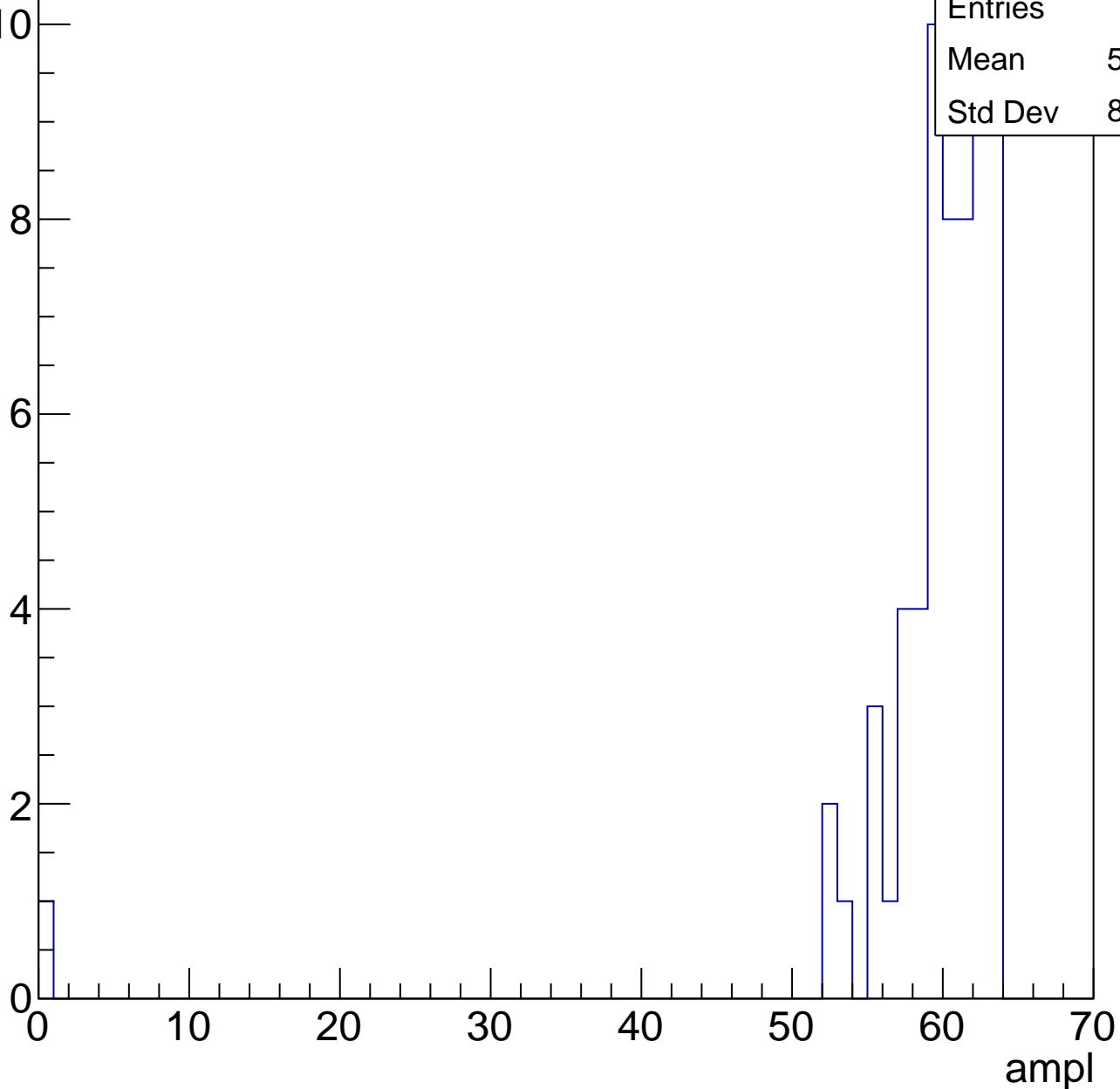


# B0L000S, U10-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

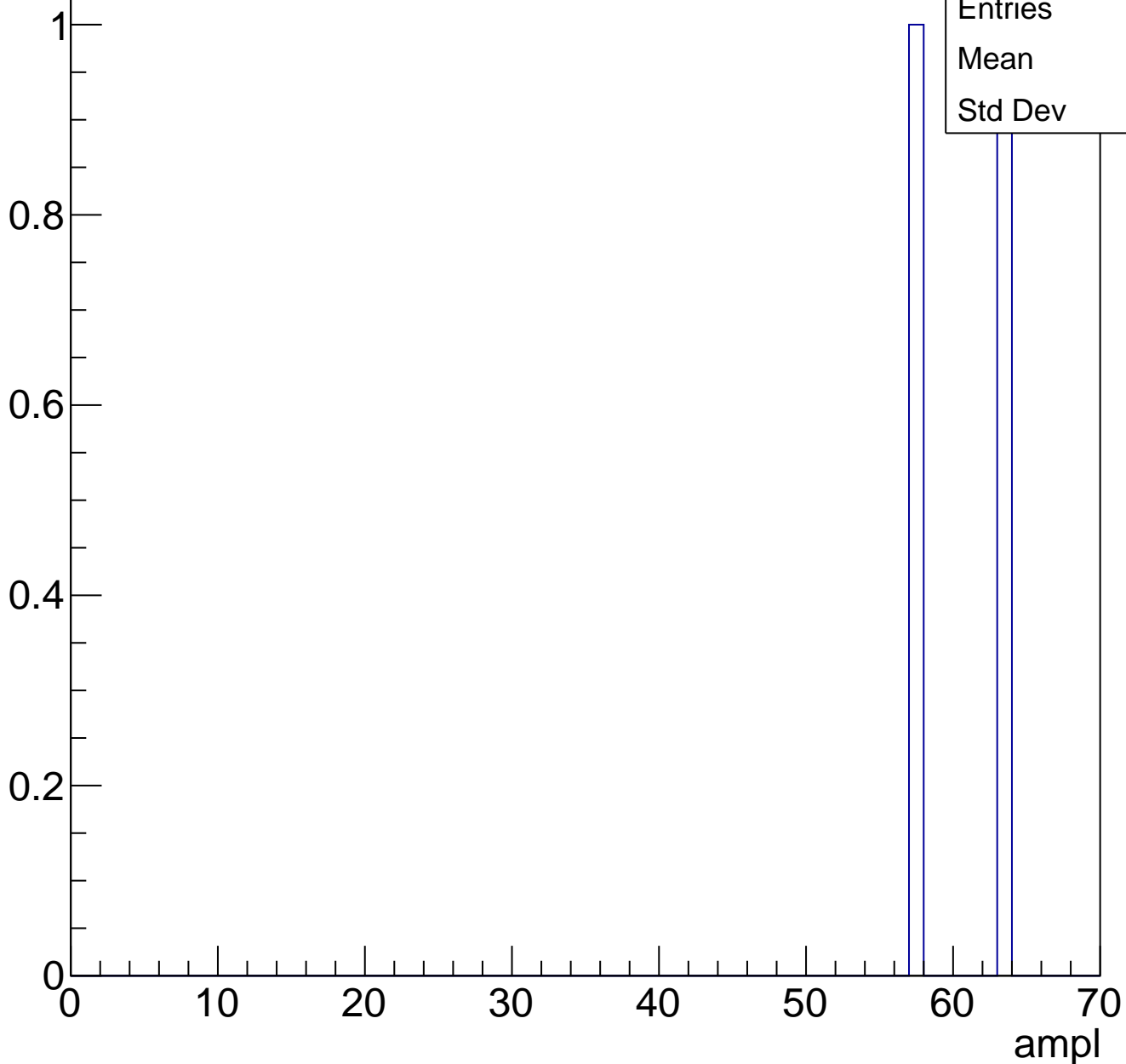
Entries	60
Mean	58.68
Std Dev	8.115



# B0L000S, U10-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch54, adc0

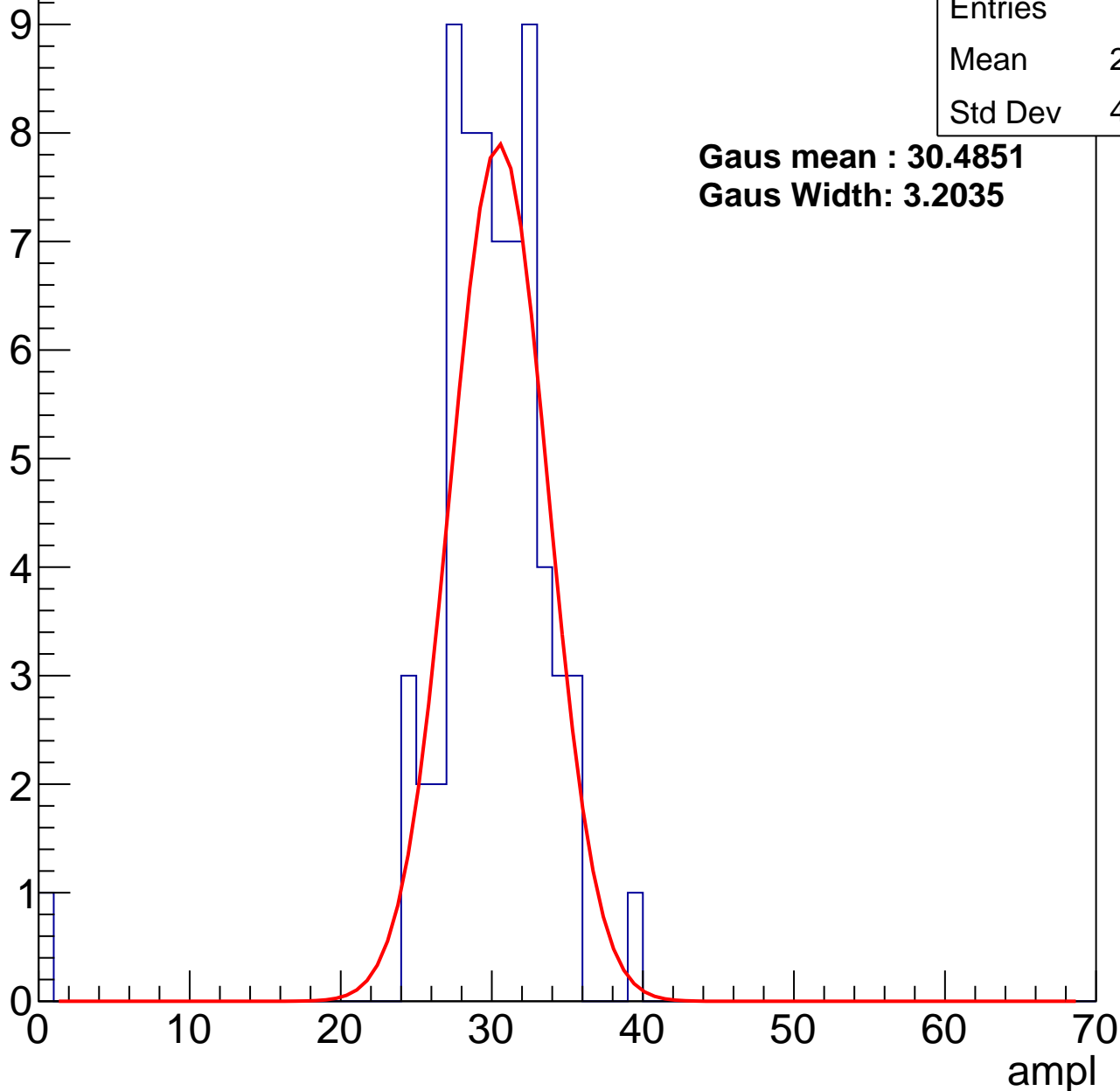
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.34
Std Dev	4.676

**Gaus mean : 30.4851**

**Gaus Width: 3.2035**



# B0L000S, U10-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.7
Std Dev	3.11

**Gaus mean : 36.4907**

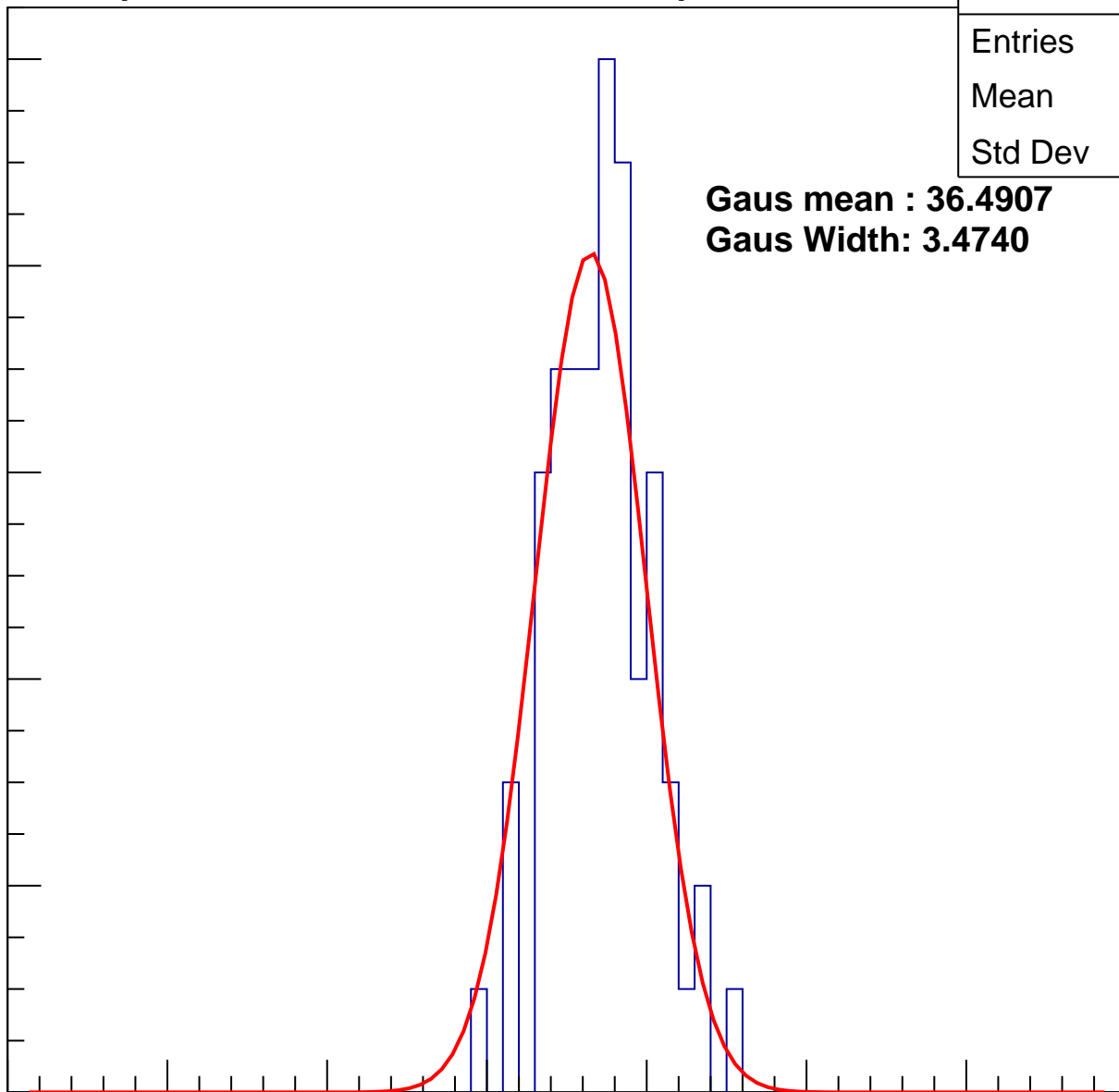
**Gaus Width: 3.4740**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch54, adc2

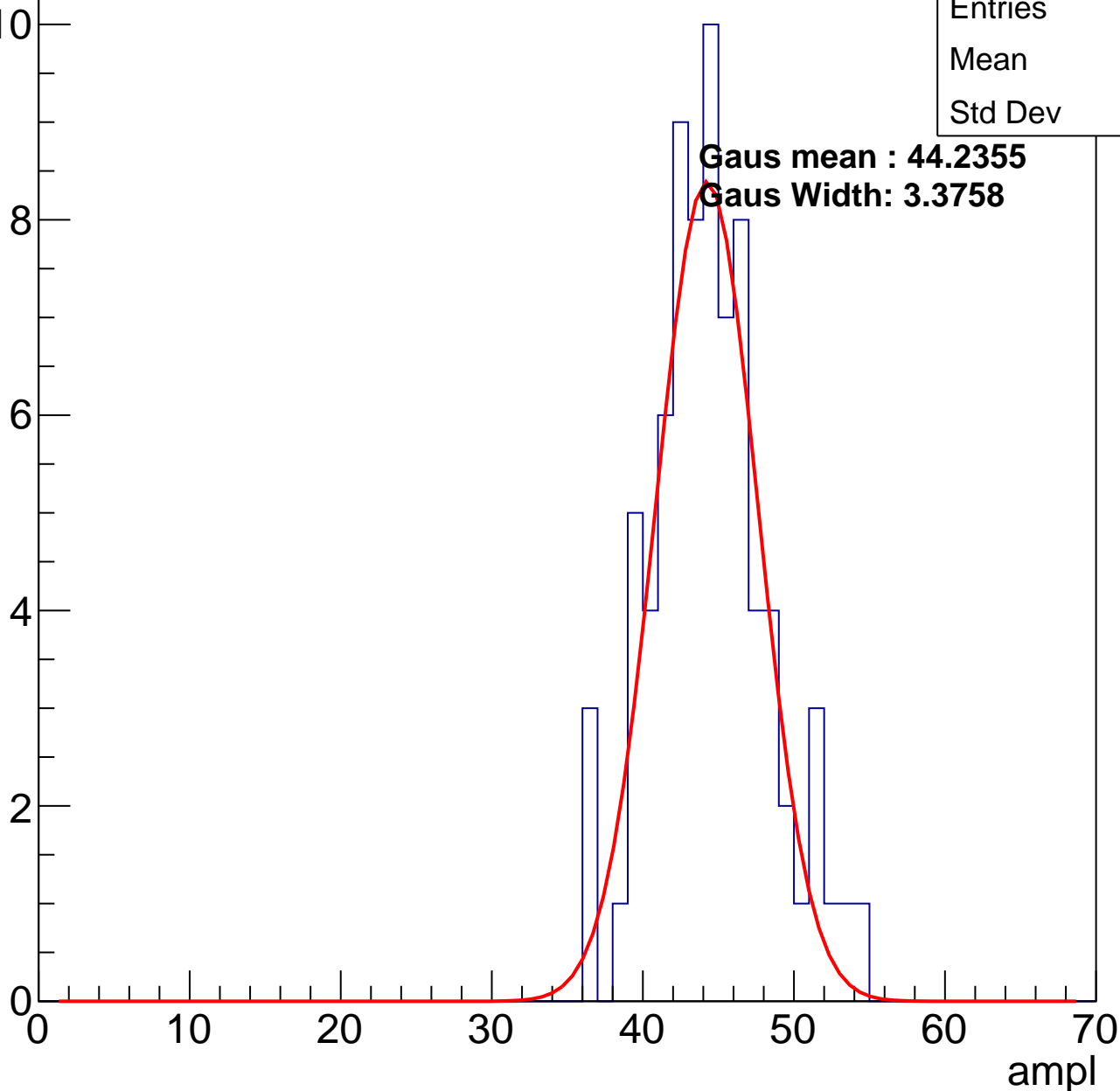
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	44
Std Dev	3.81

**Gaus mean : 44.2355**

**Gaus Width: 3.3758**

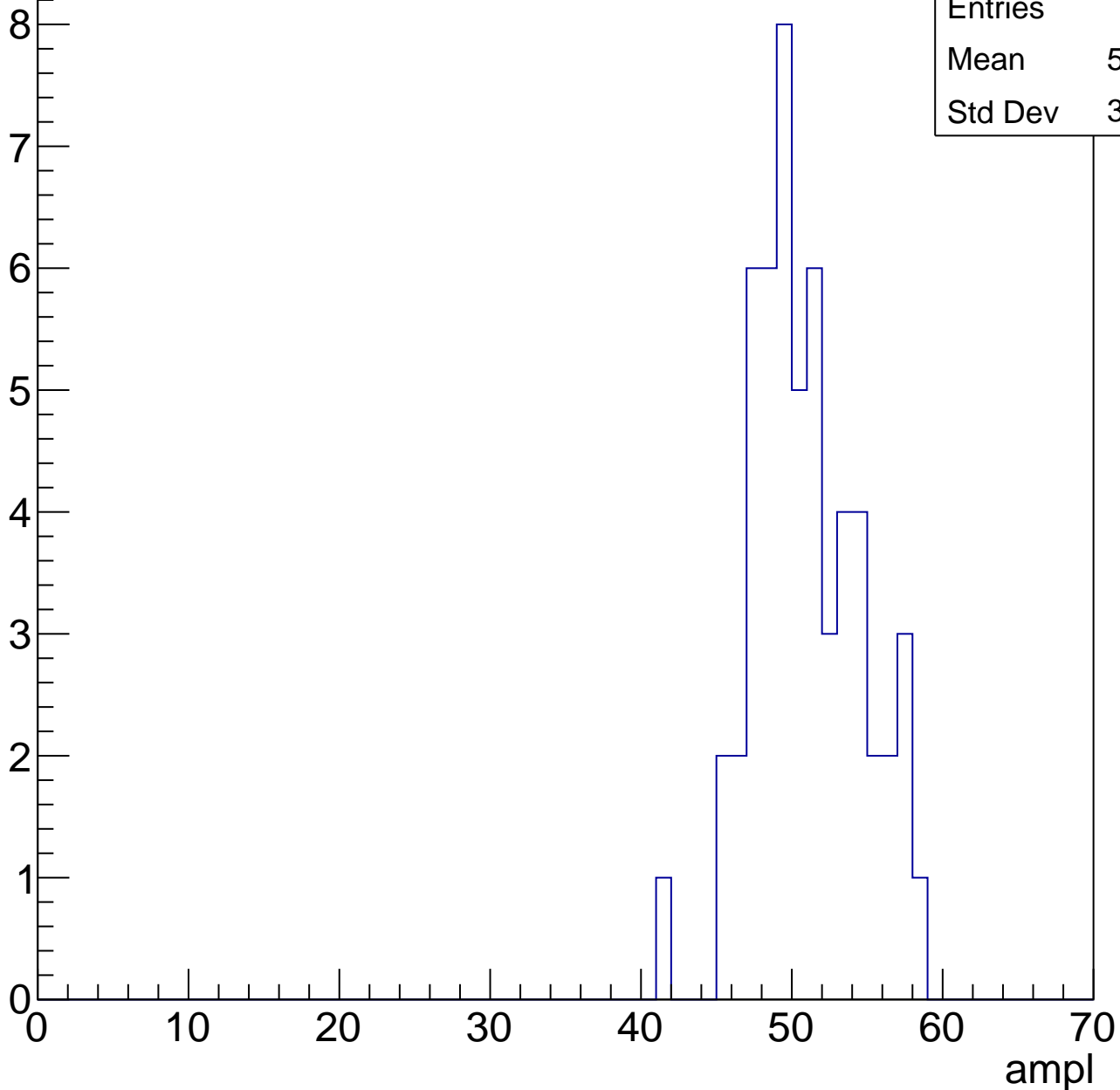


# B0L000S, U10-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	50.47
Std Dev	3.536

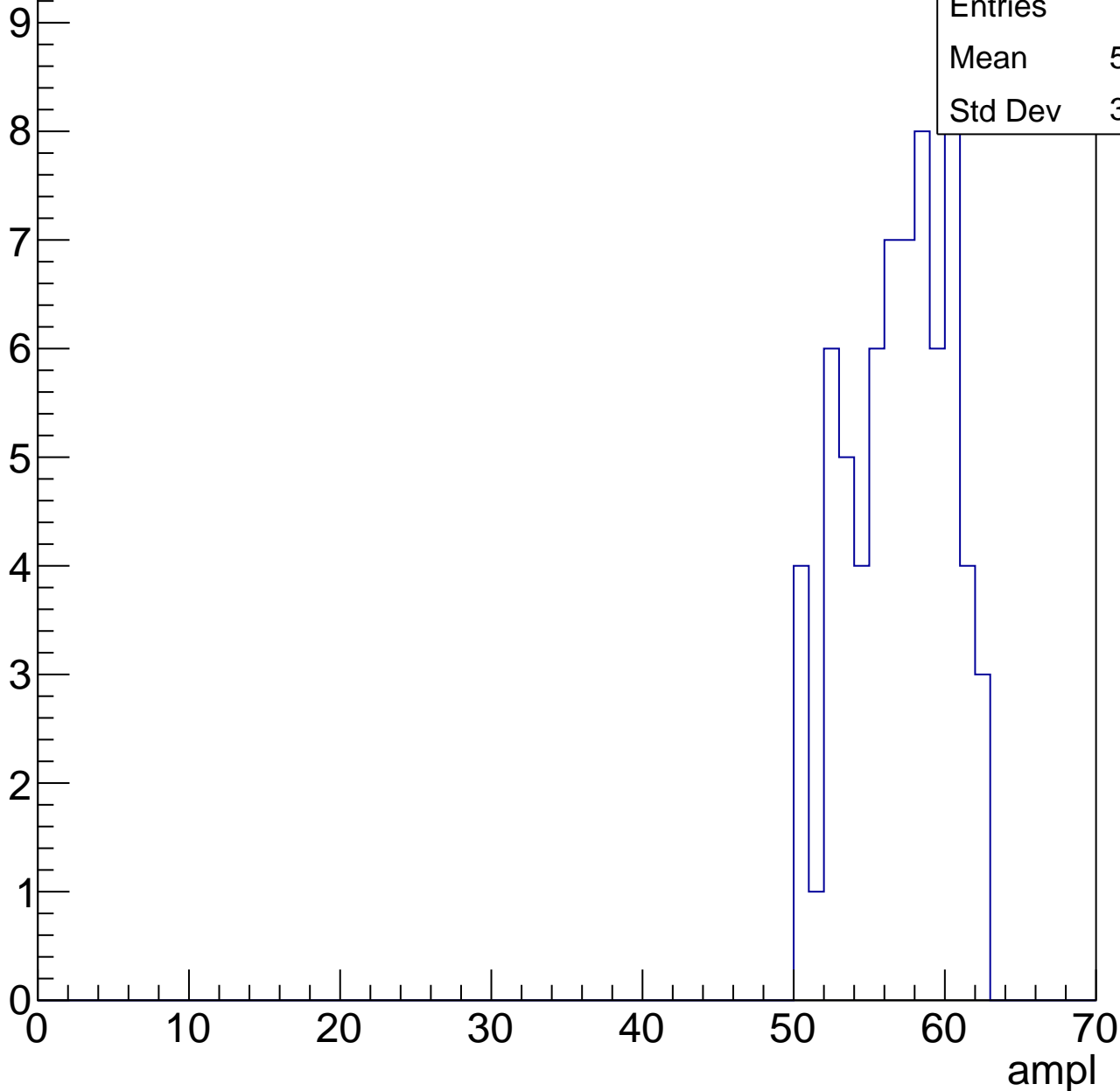


# B0L000S, U10-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	56.47
Std Dev	3.298

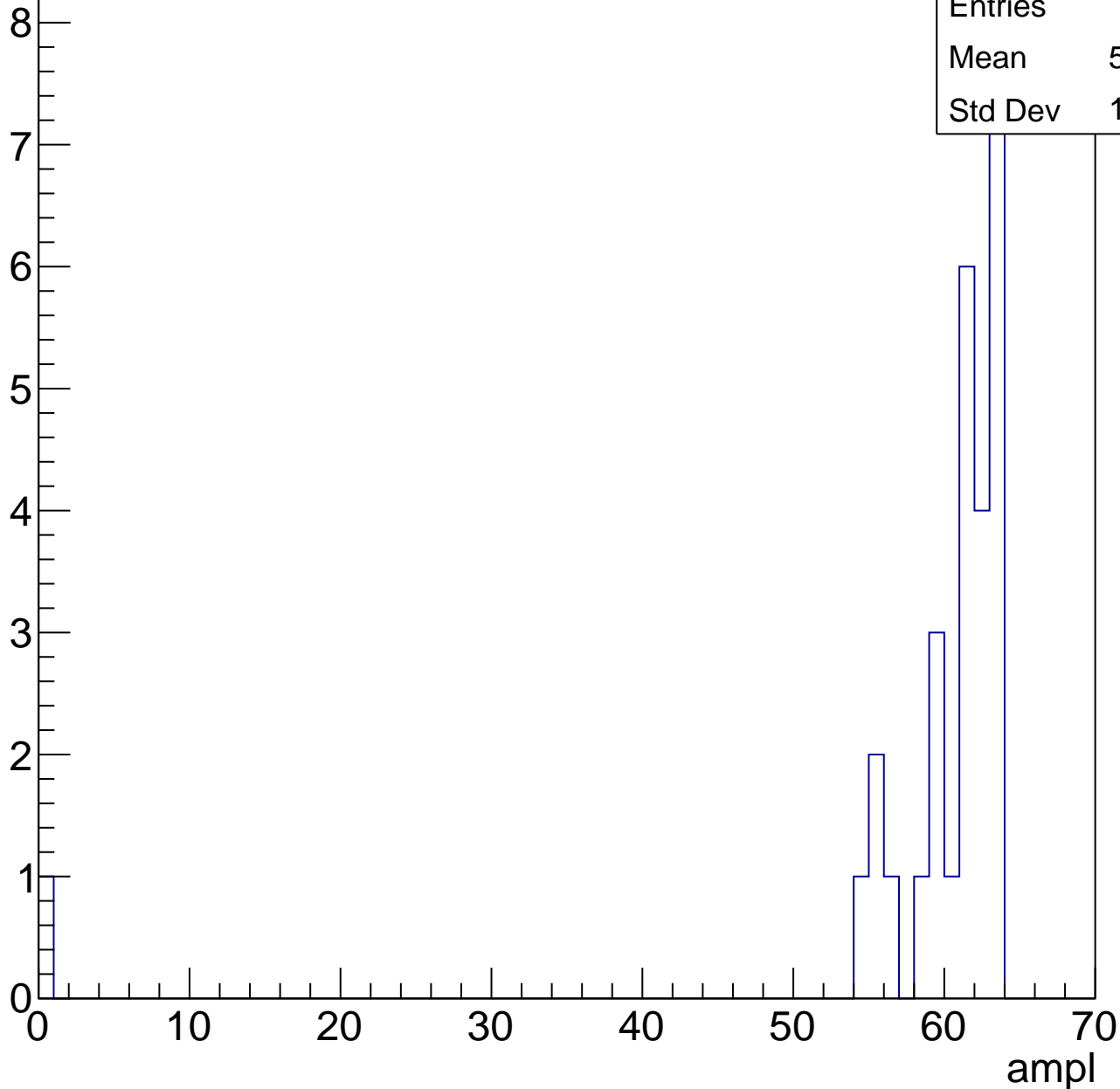


# B0L000S, U10-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

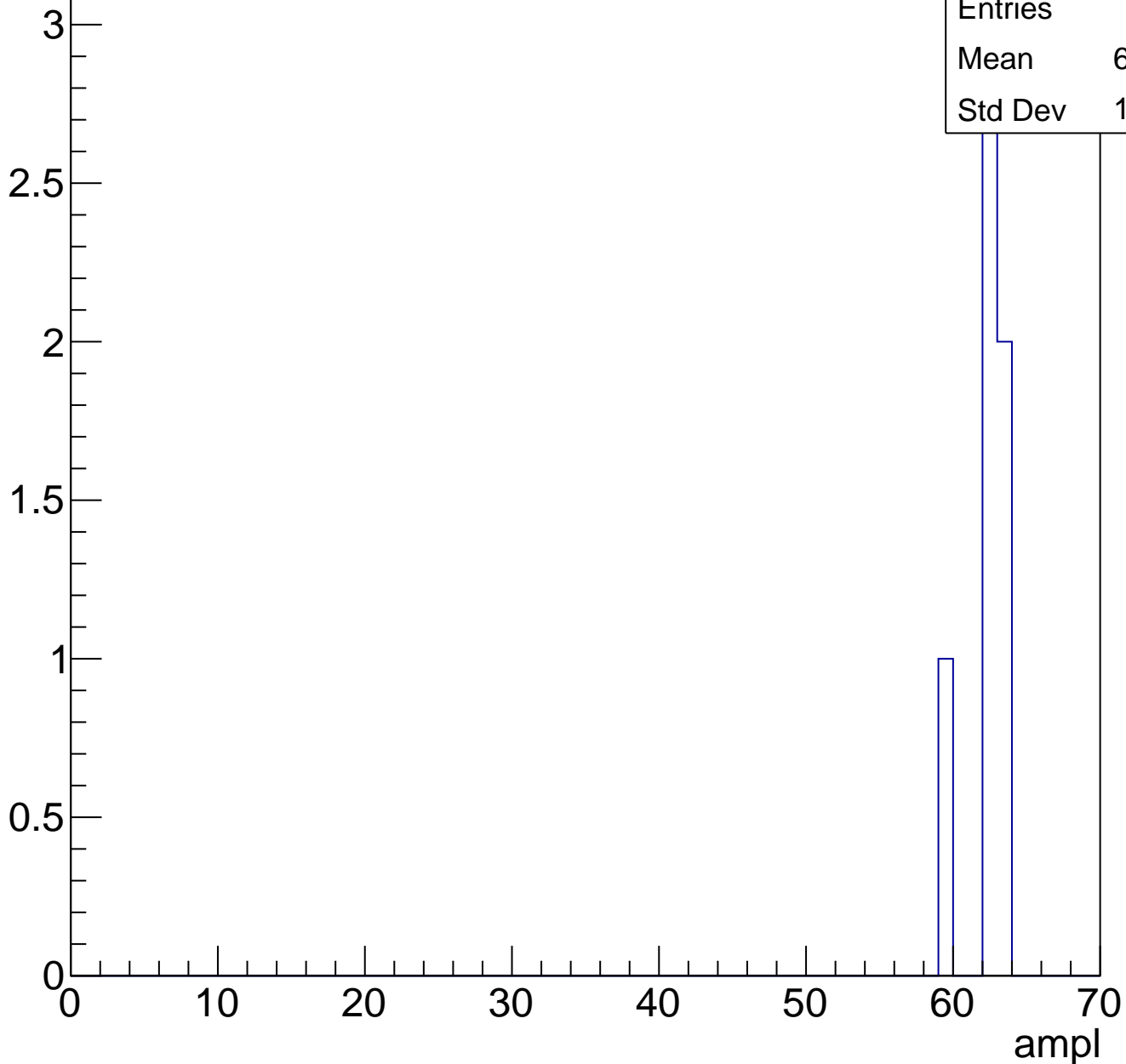
Entries	28
Mean	58.32
Std Dev	11.53



# B0L000S, U10-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

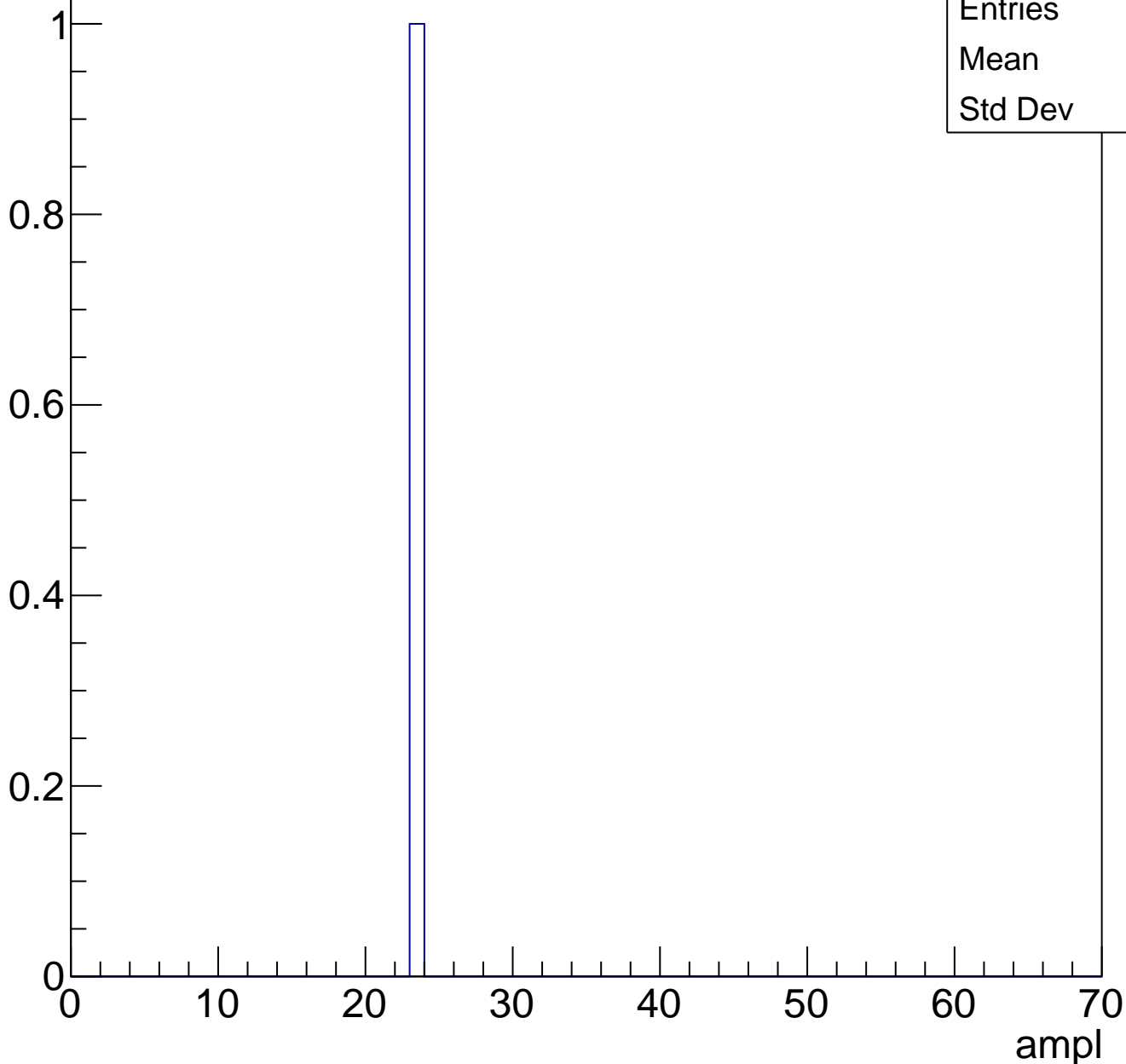




# B0L000S, U10-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U10-ch55, adc0

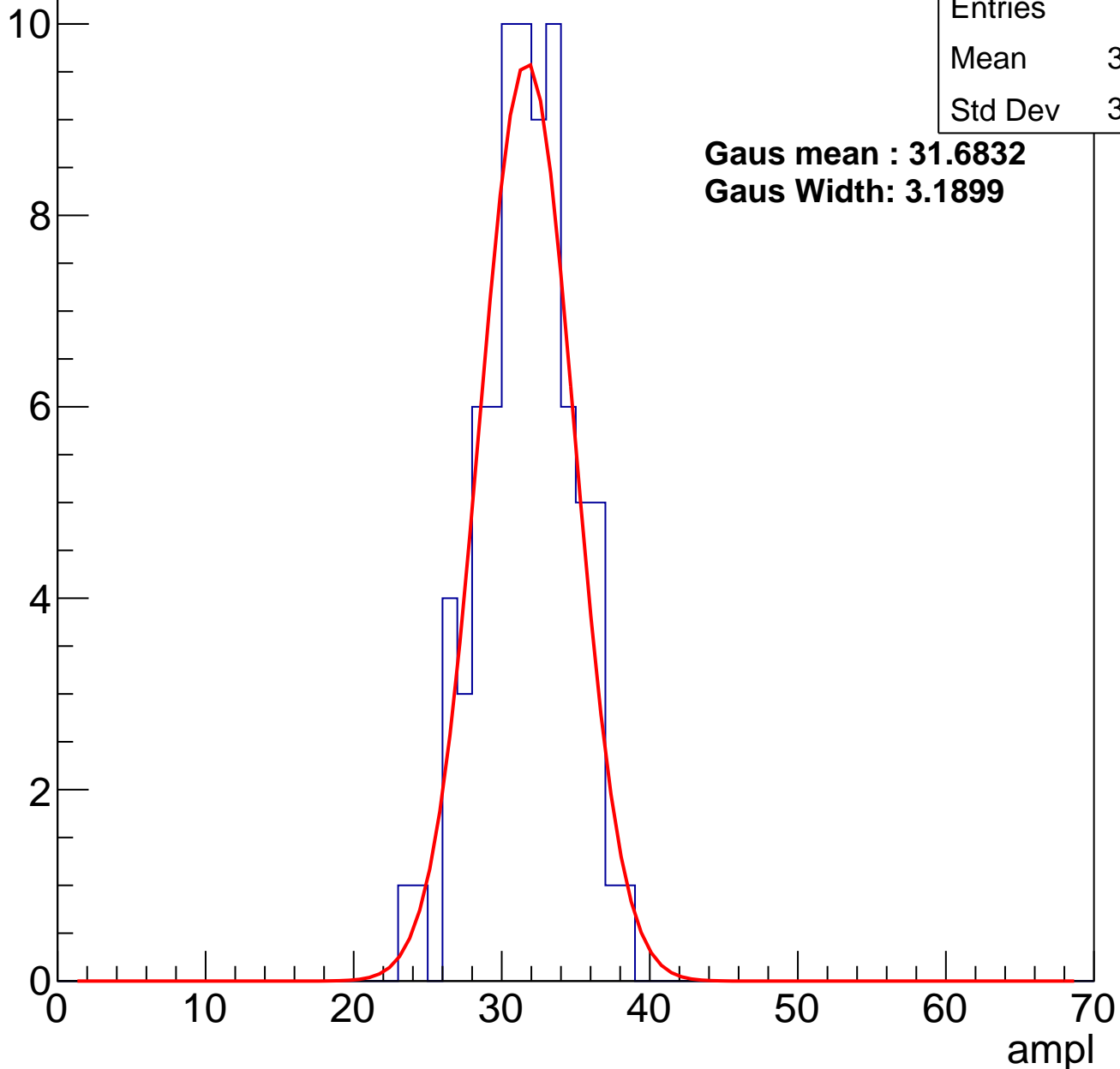
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	31.23
Std Dev	3.076

**Gaus mean : 31.6832**

**Gaus Width: 3.1899**

Entry



# B0L000S, U10-ch55, adc1

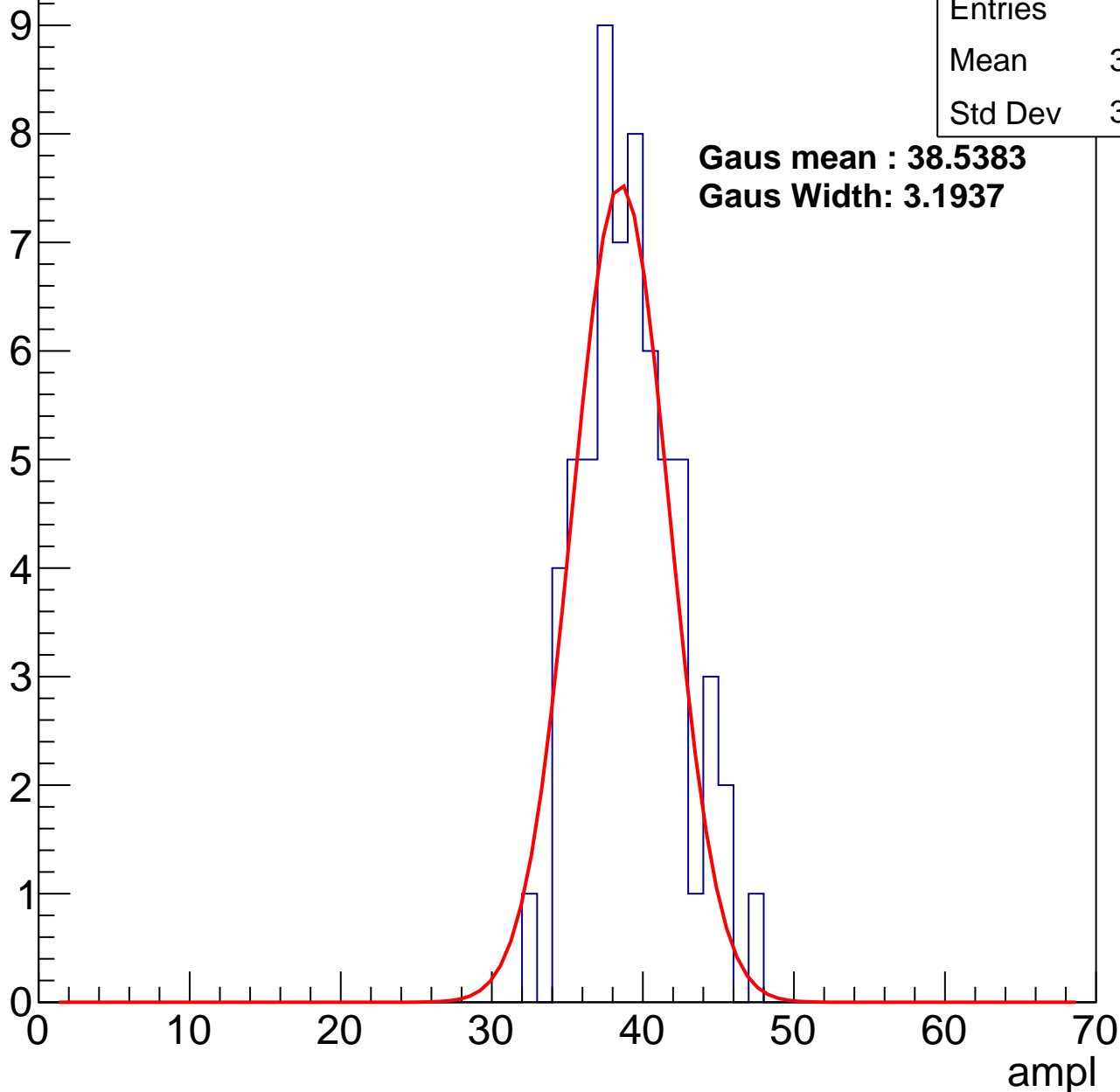
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.73
Std Dev	3.138

**Gaus mean : 38.5383**

**Gaus Width: 3.1937**



# B0L000S, U10-ch55, adc2

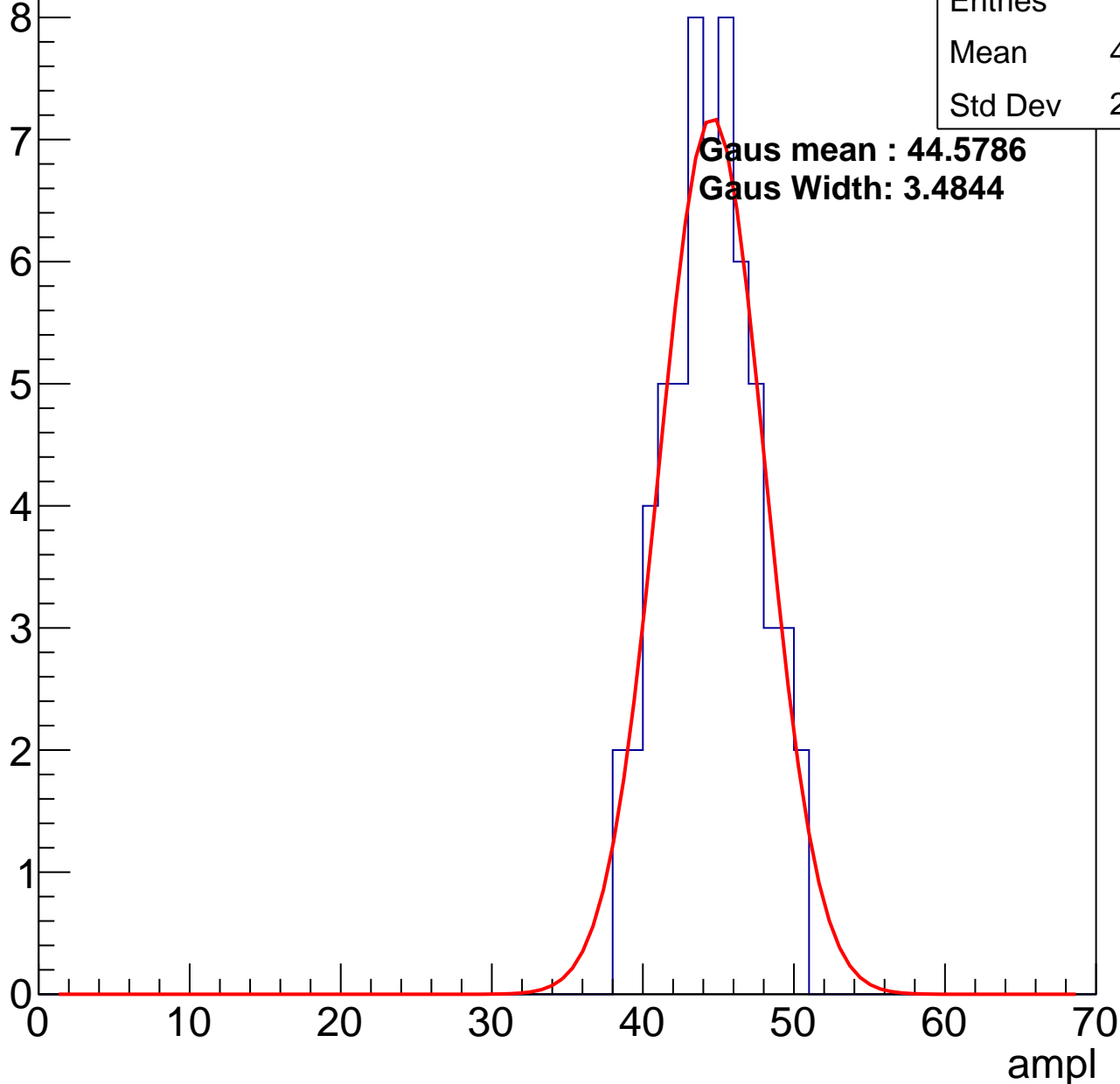
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.05
Std Dev	2.974

**Gaus mean : 44.5786**

**Gaus Width: 3.4844**

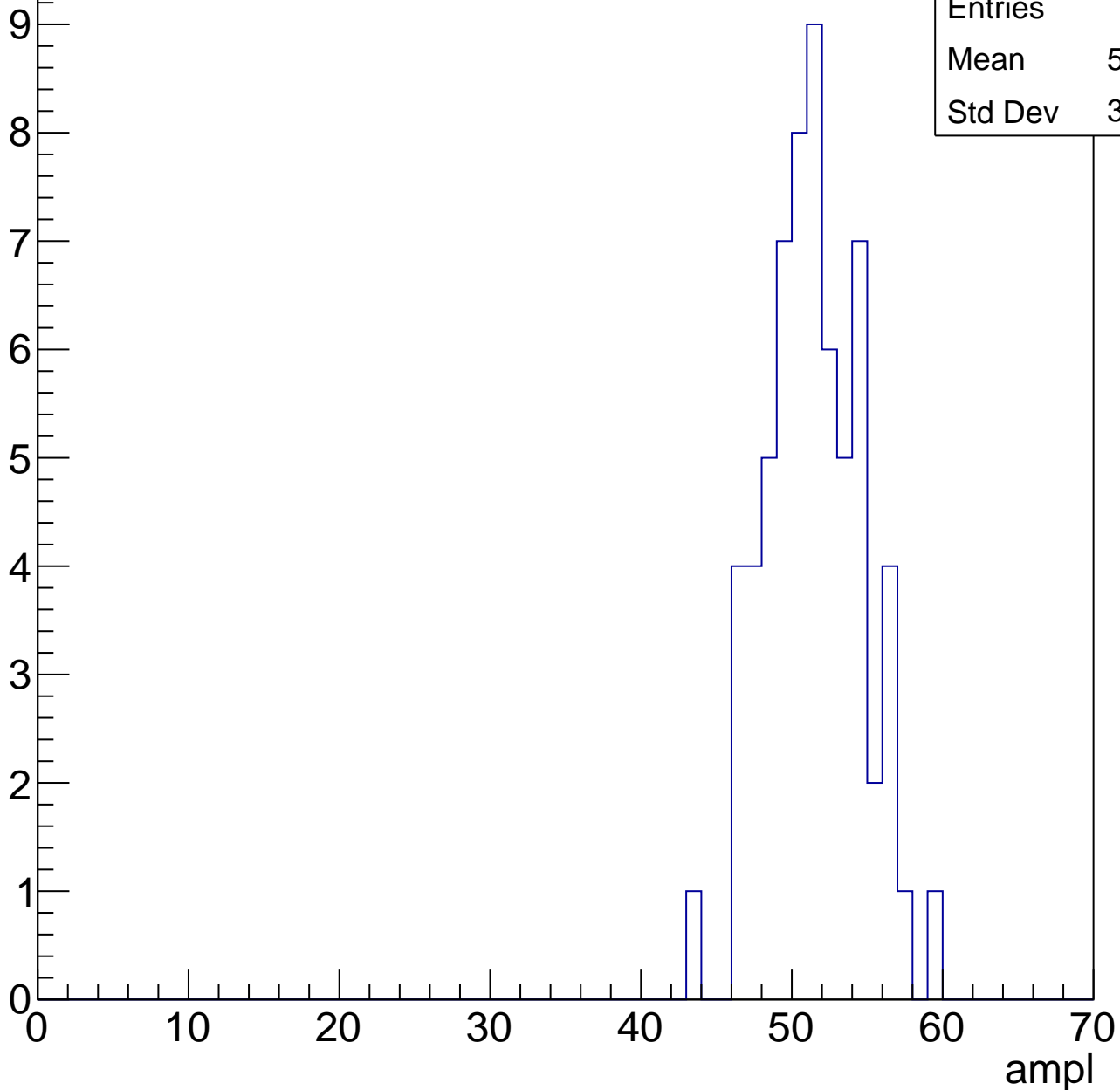


# B0L000S, U10-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

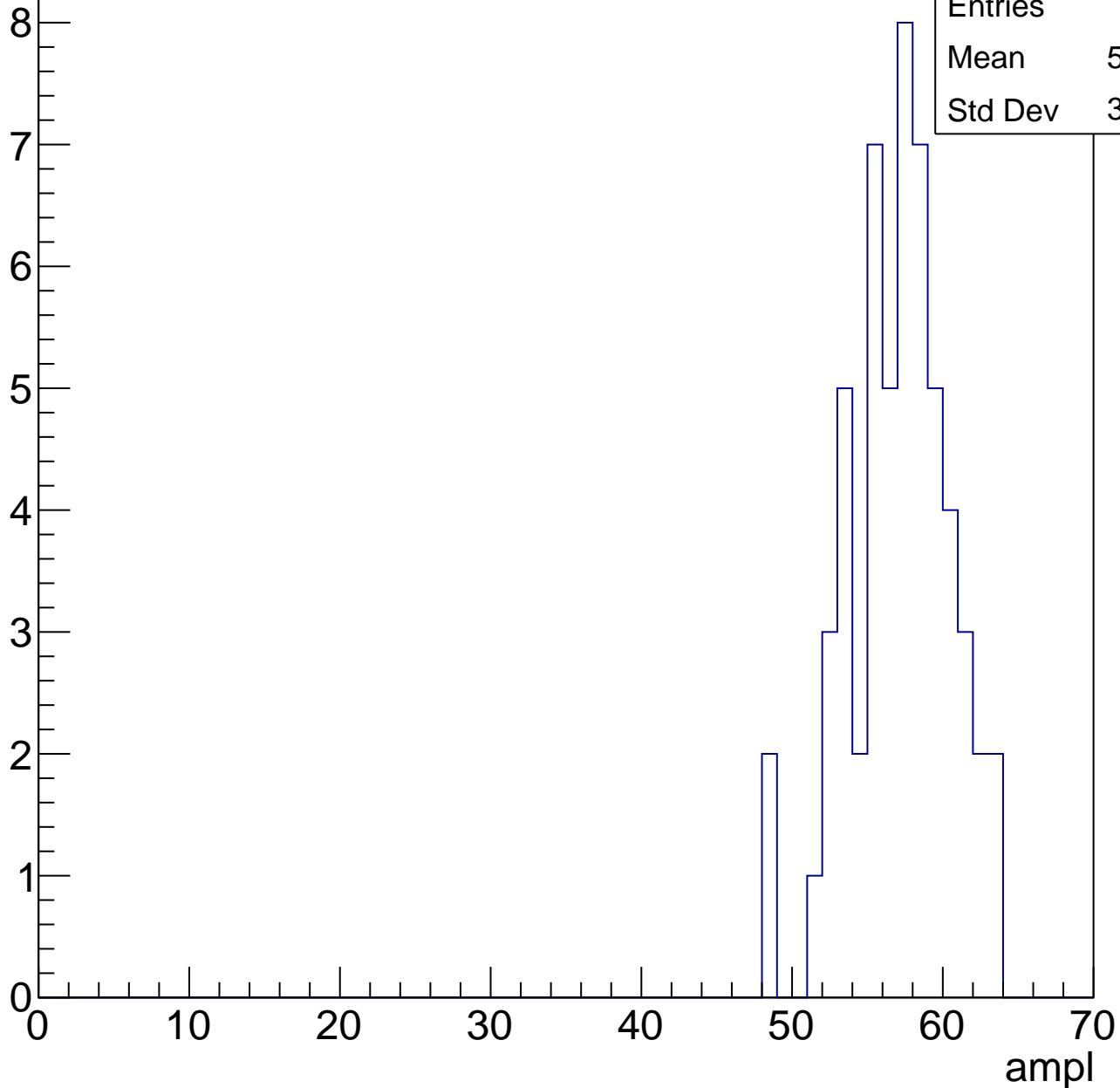
Entries	64
Mean	50.97
Std Dev	3.137



# B0L000S, U10-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



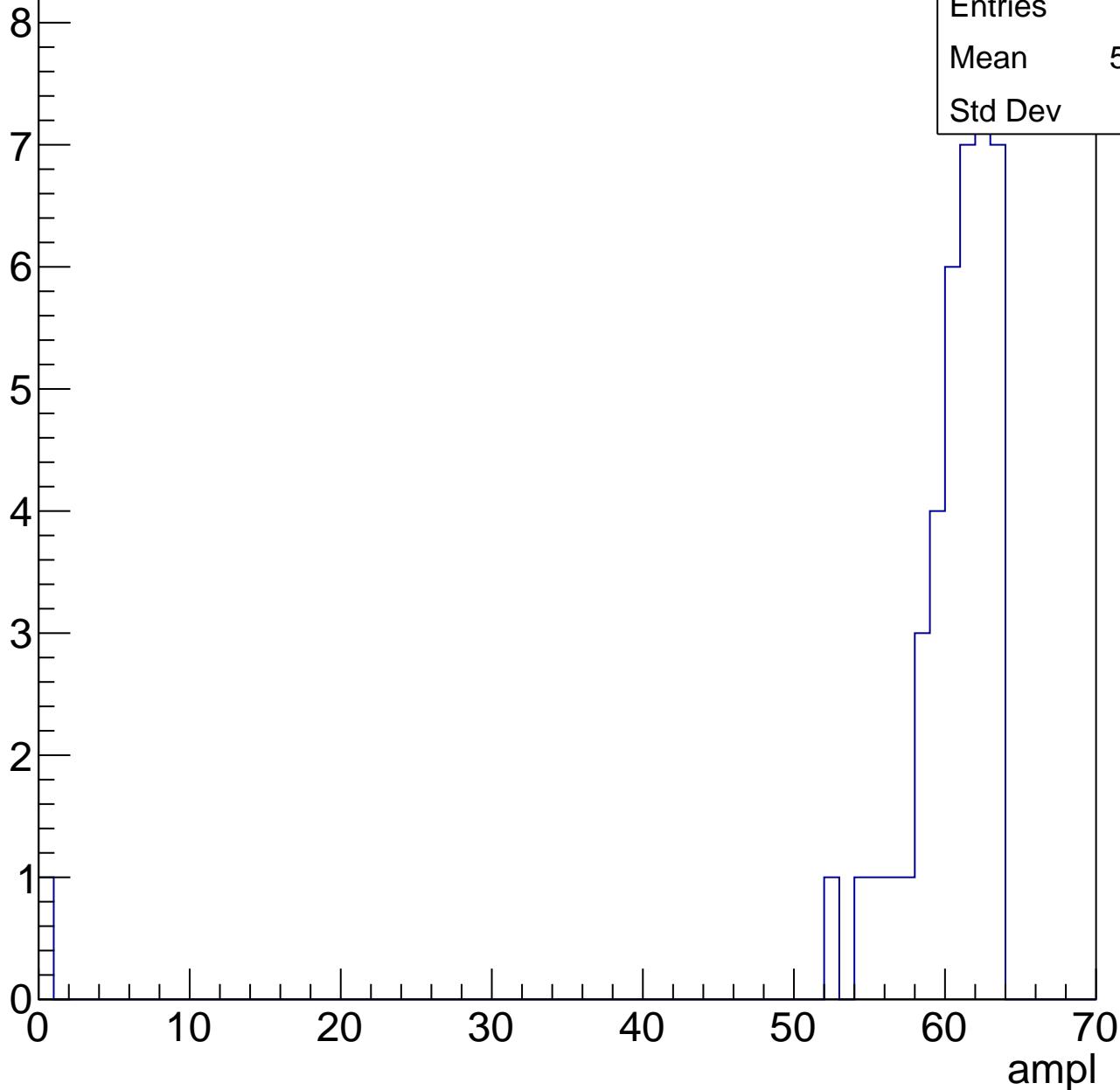
Entries	56
Mean	56.62
Std Dev	3.352

# B0L000S, U10-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.73
Std Dev	9.63



# B0L000S, U10-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U10-ch56, adc0

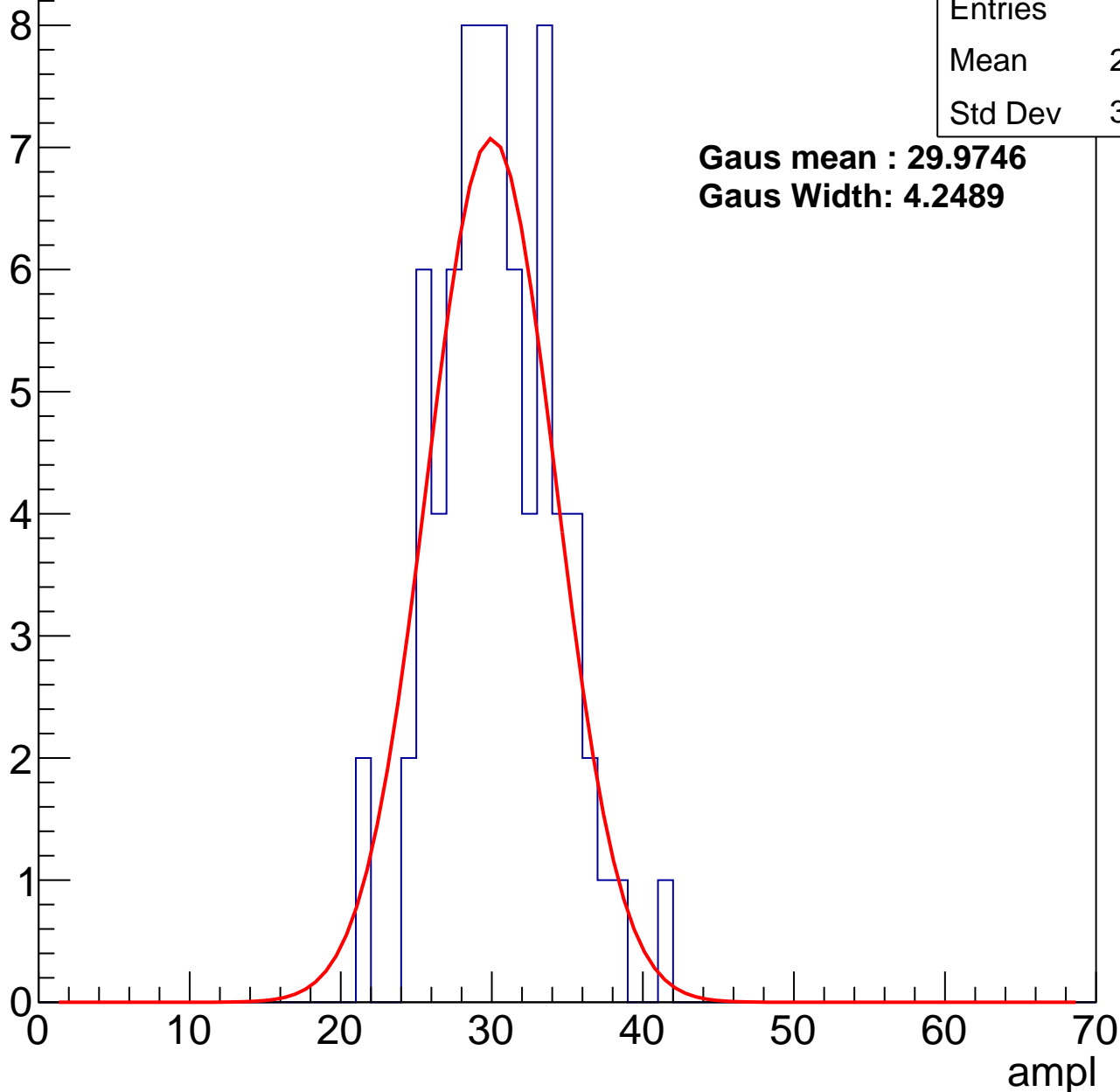
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.92
Std Dev	3.839

**Gaus mean : 29.9746**

**Gaus Width: 4.2489**



# B0L000S, U10-ch56, adc1

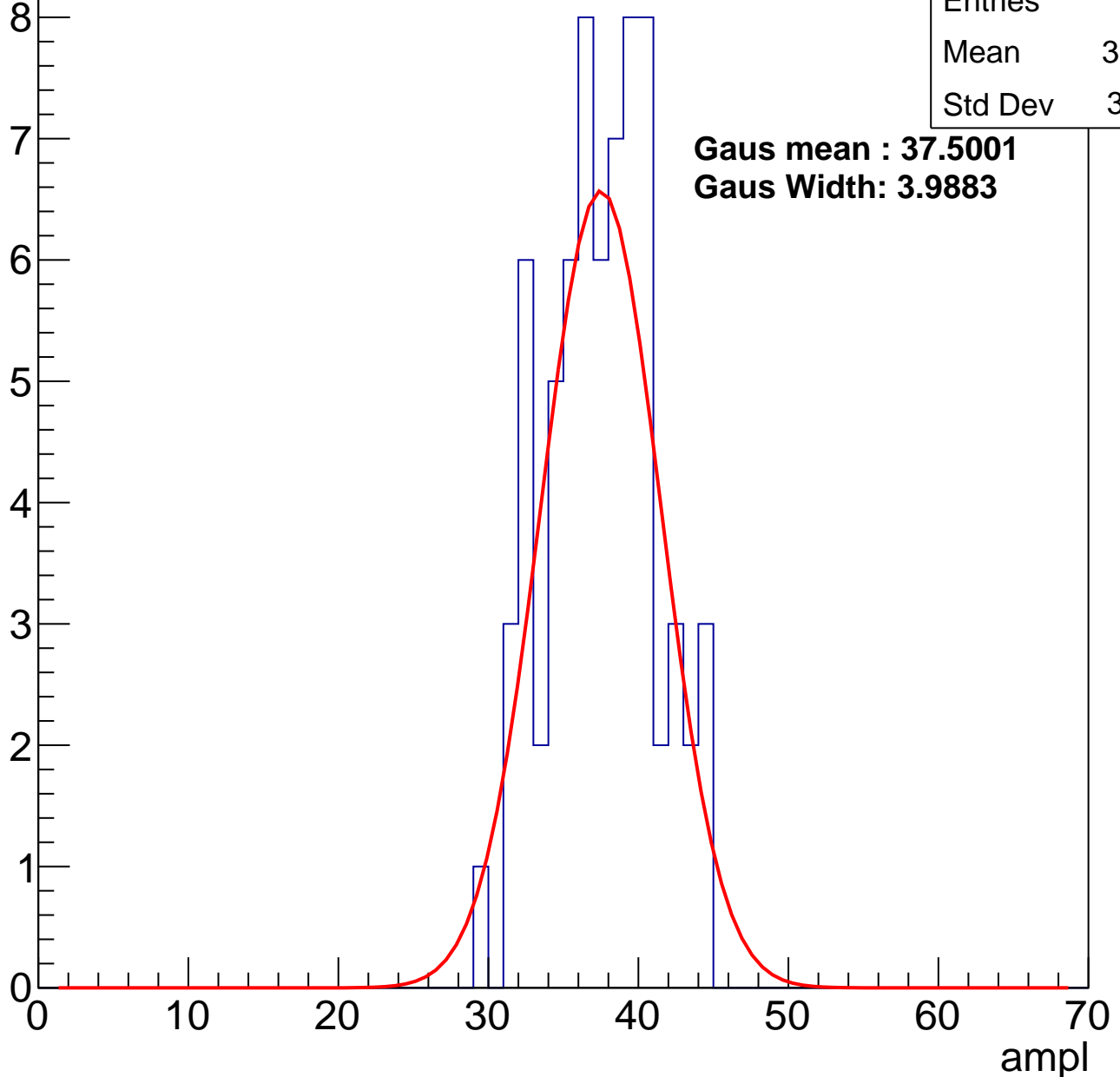
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.06
Std Dev	3.521

**Gaus mean : 37.5001**

**Gaus Width: 3.9883**



# B0L000S, U10-ch56, adc2

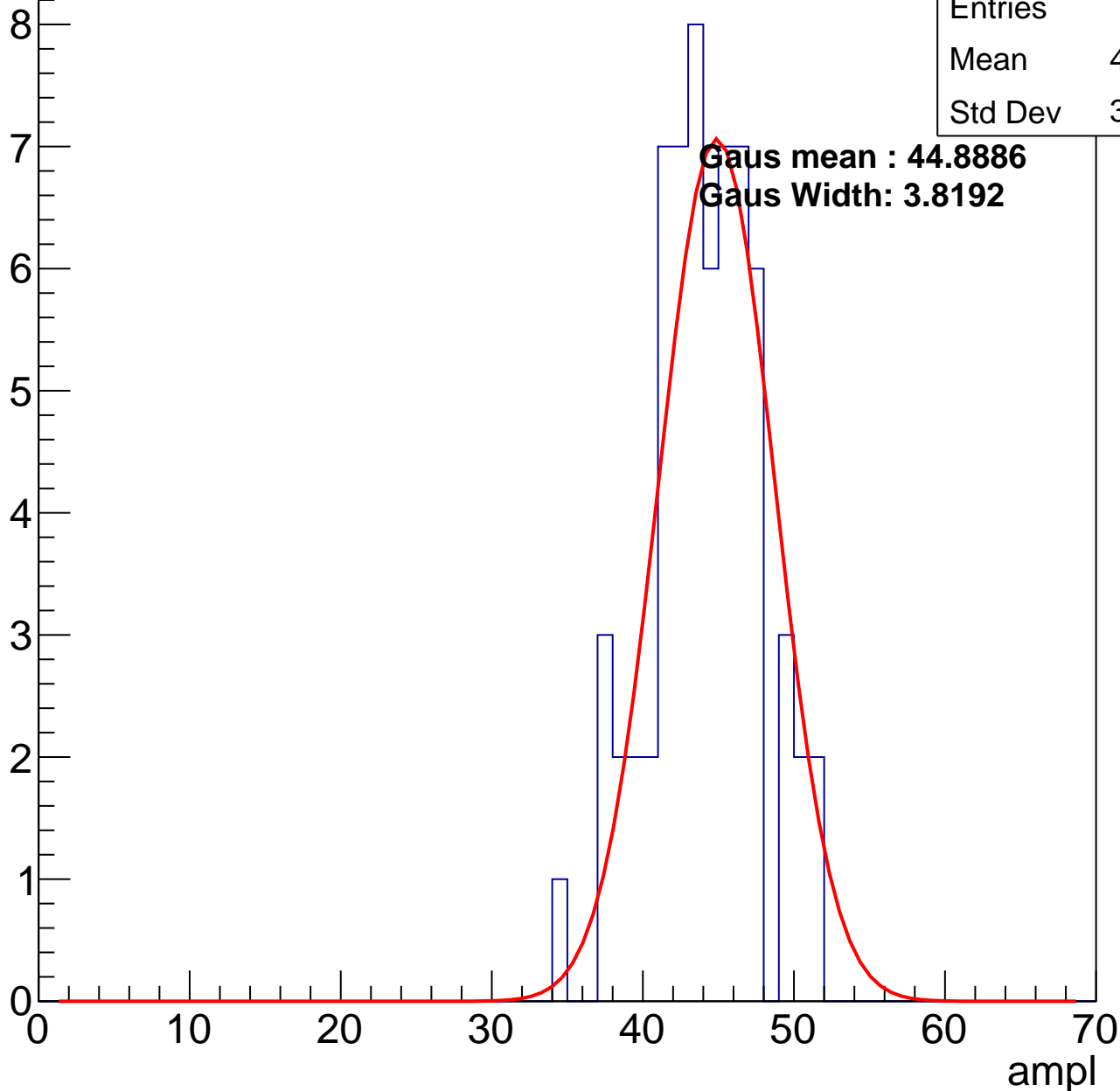
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.63
Std Dev	3.563

**Gaus mean : 44.8886**

**Gaus Width: 3.8192**

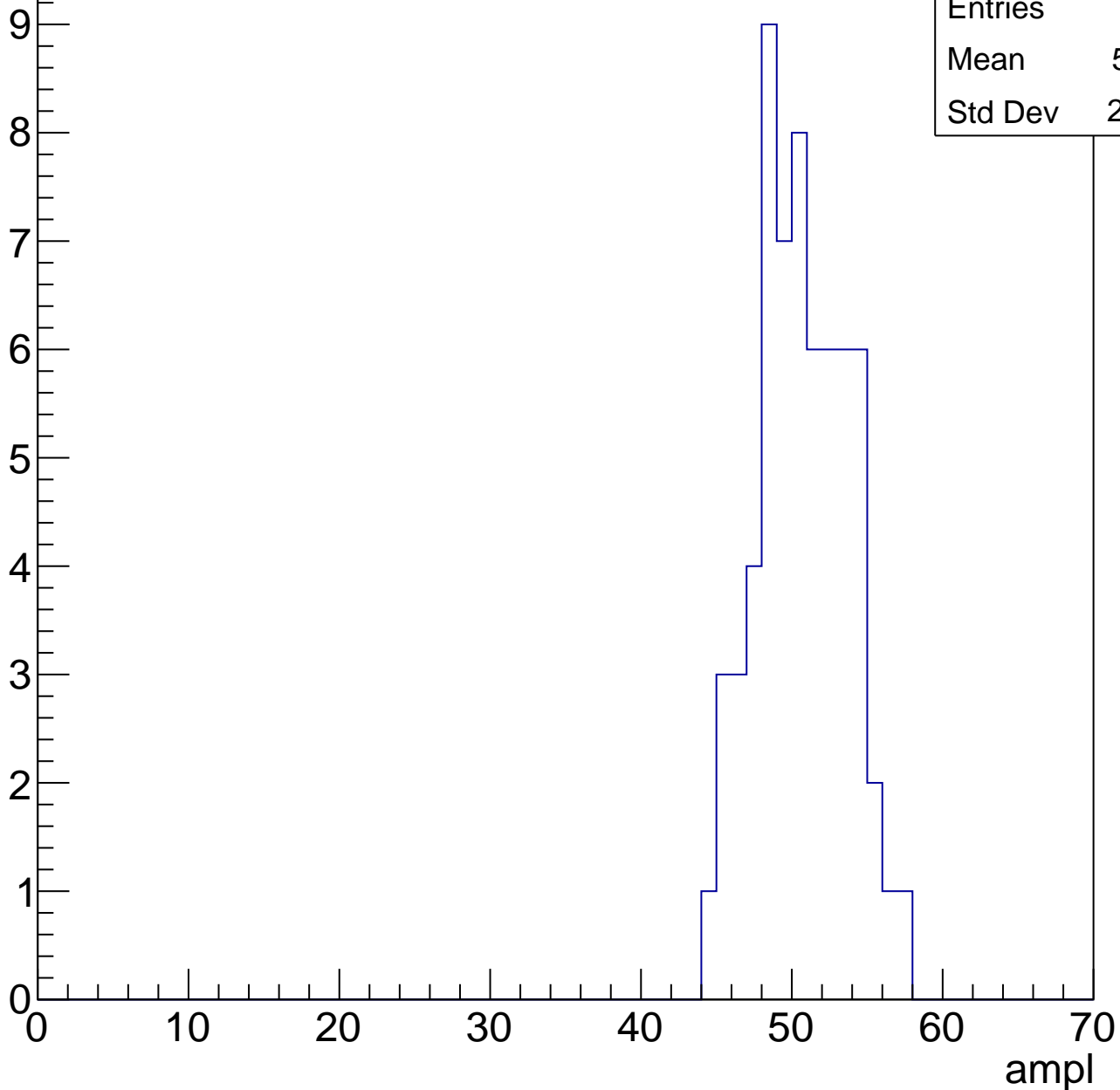


# B0L000S, U10-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

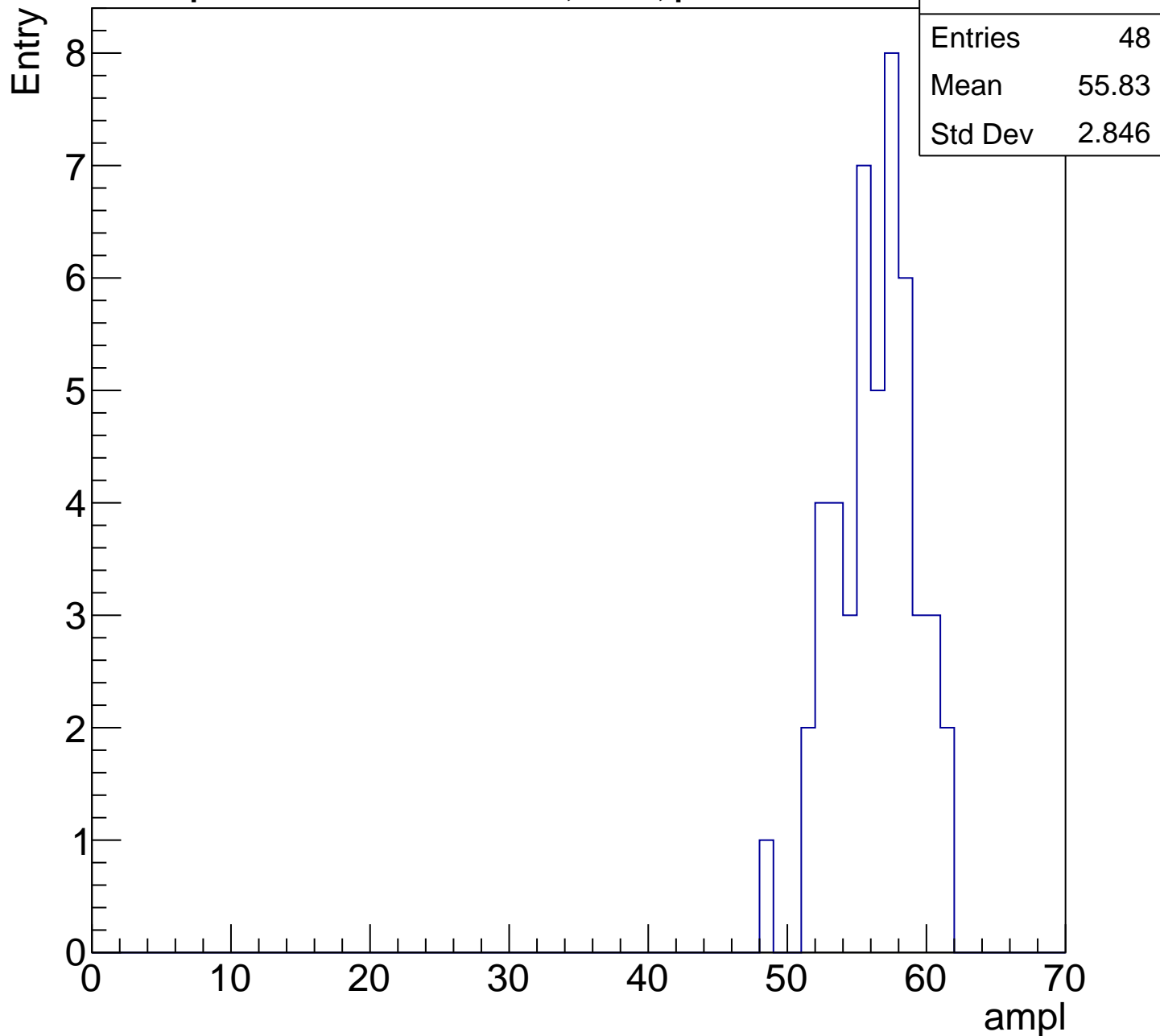
Entry

Entries	63
Mean	50.21
Std Dev	2.956



# B0L000S, U10-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

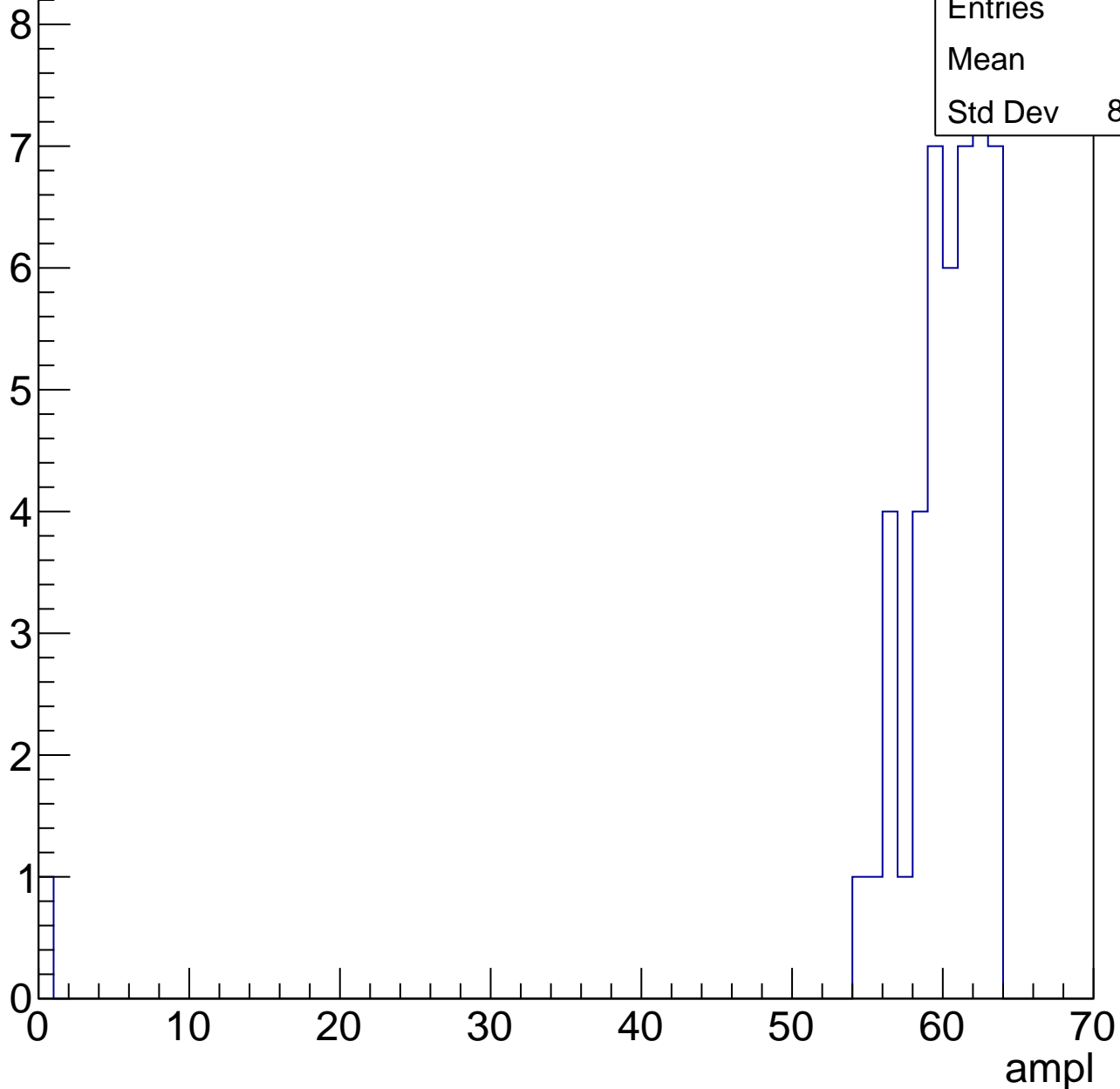


# B0L000S, U10-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	58.7
Std Dev	8.968



# B0L000S, U10-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch57, adc0

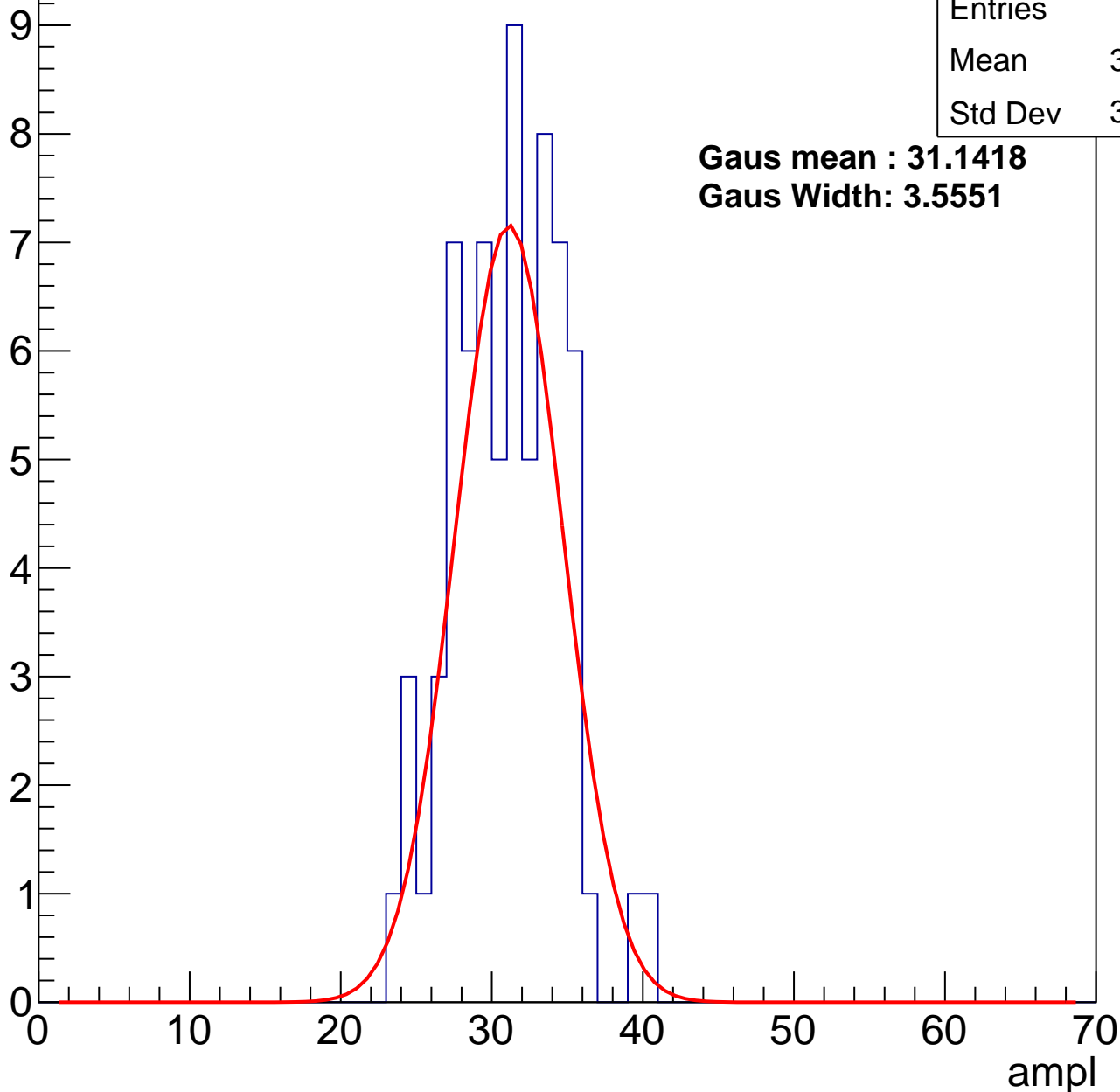
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.62
Std Dev	3.514

**Gaus mean : 31.1418**

**Gaus Width: 3.5551**



# B0L000S, U10-ch57, adc1

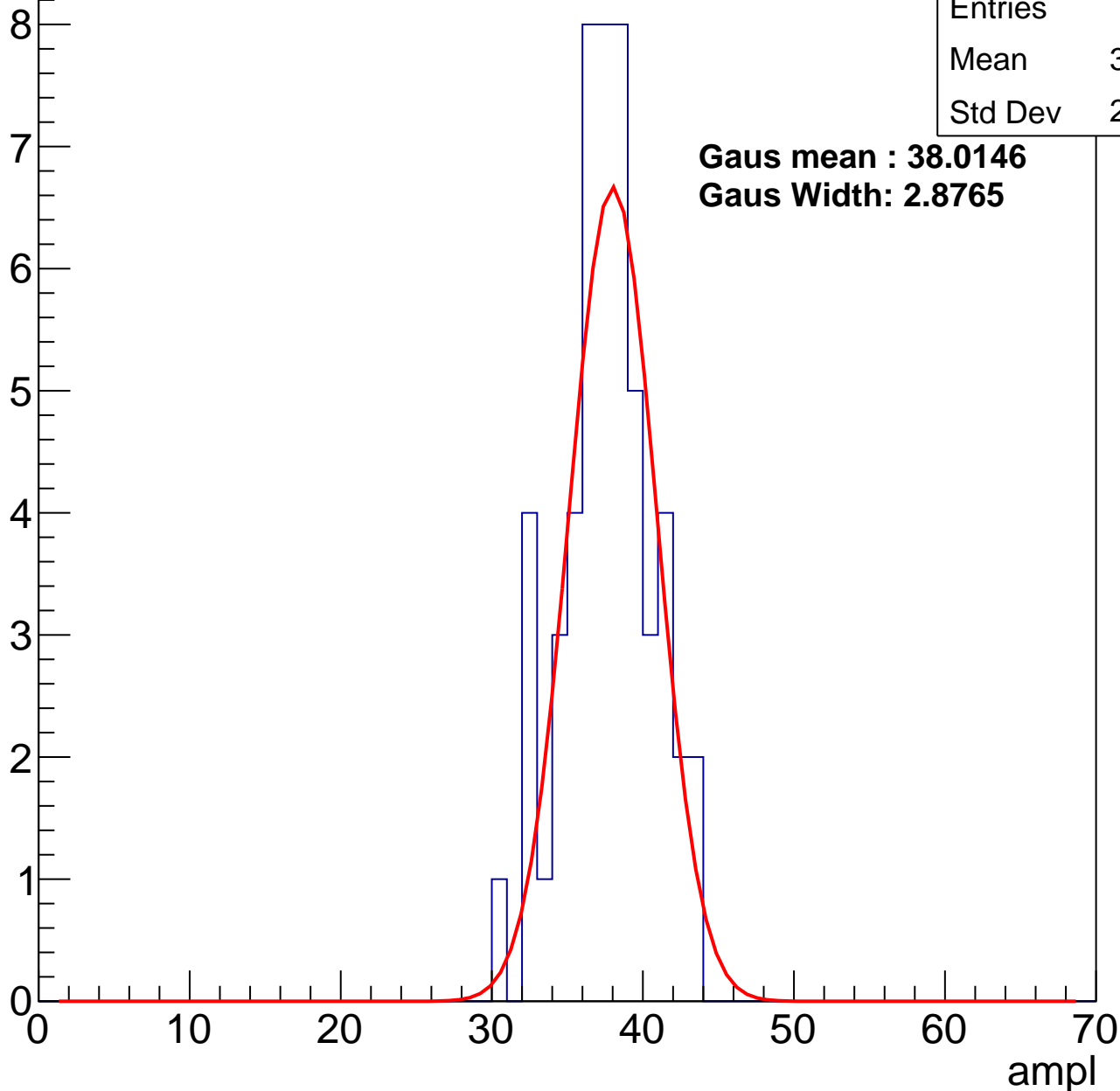
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	37.17
Std Dev	2.932

**Gaus mean : 38.0146**

**Gaus Width: 2.8765**



# B0L000S, U10-ch57, adc2

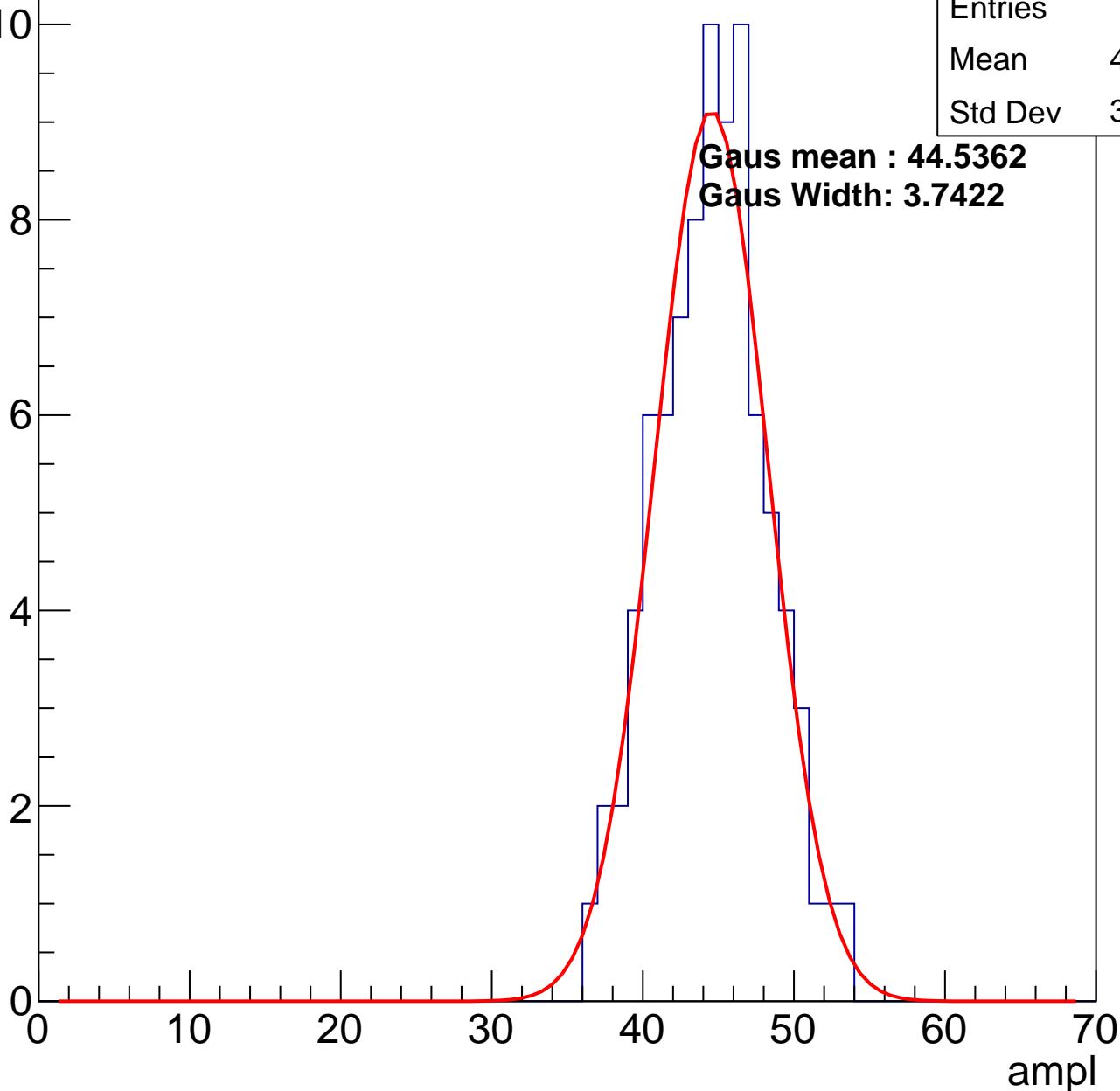
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	44.13
Std Dev	3.582

**Gaus mean : 44.5362**

**Gaus Width: 3.7422**

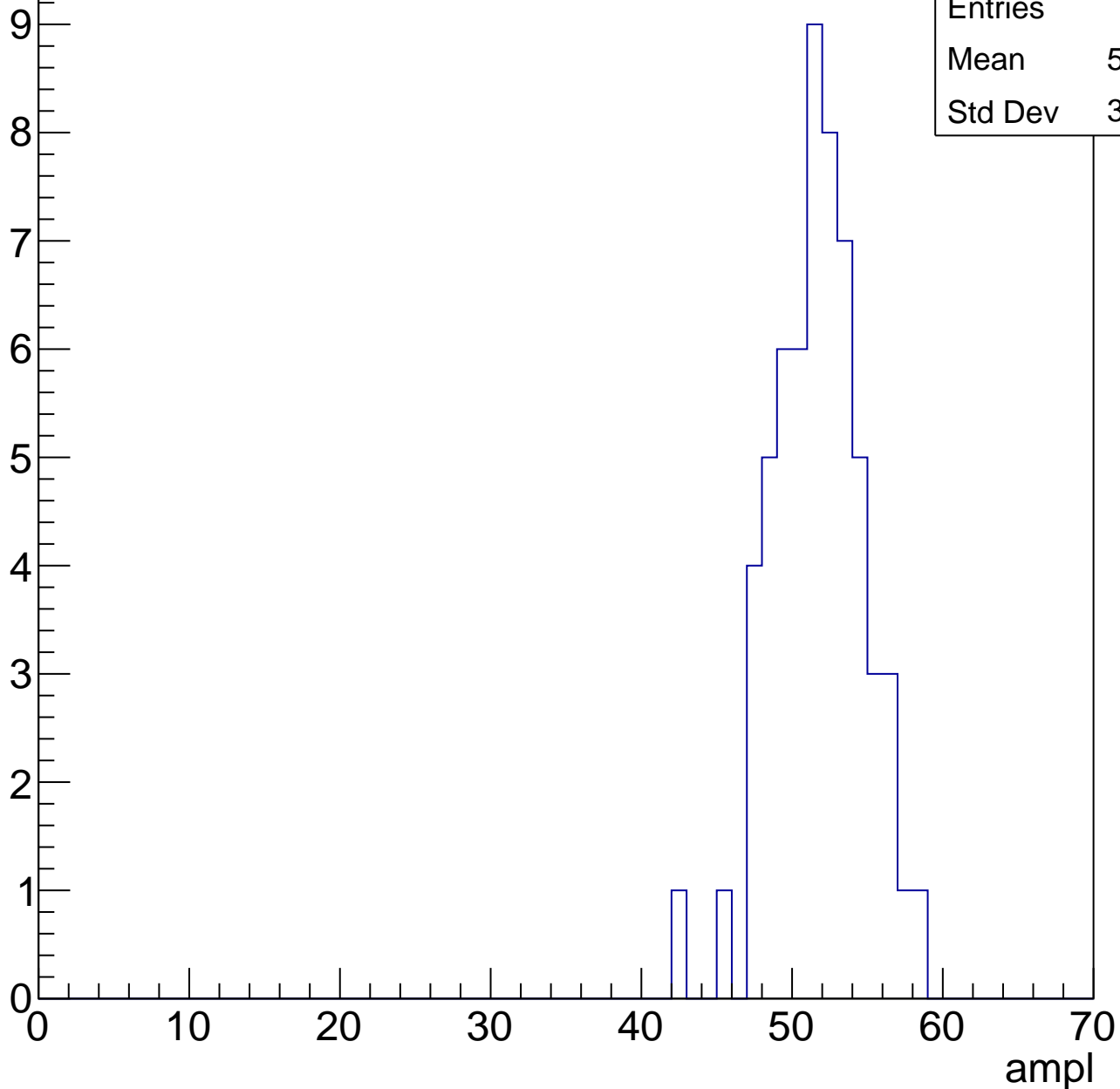


# B0L000S, U10-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.22
Std Dev	3.006

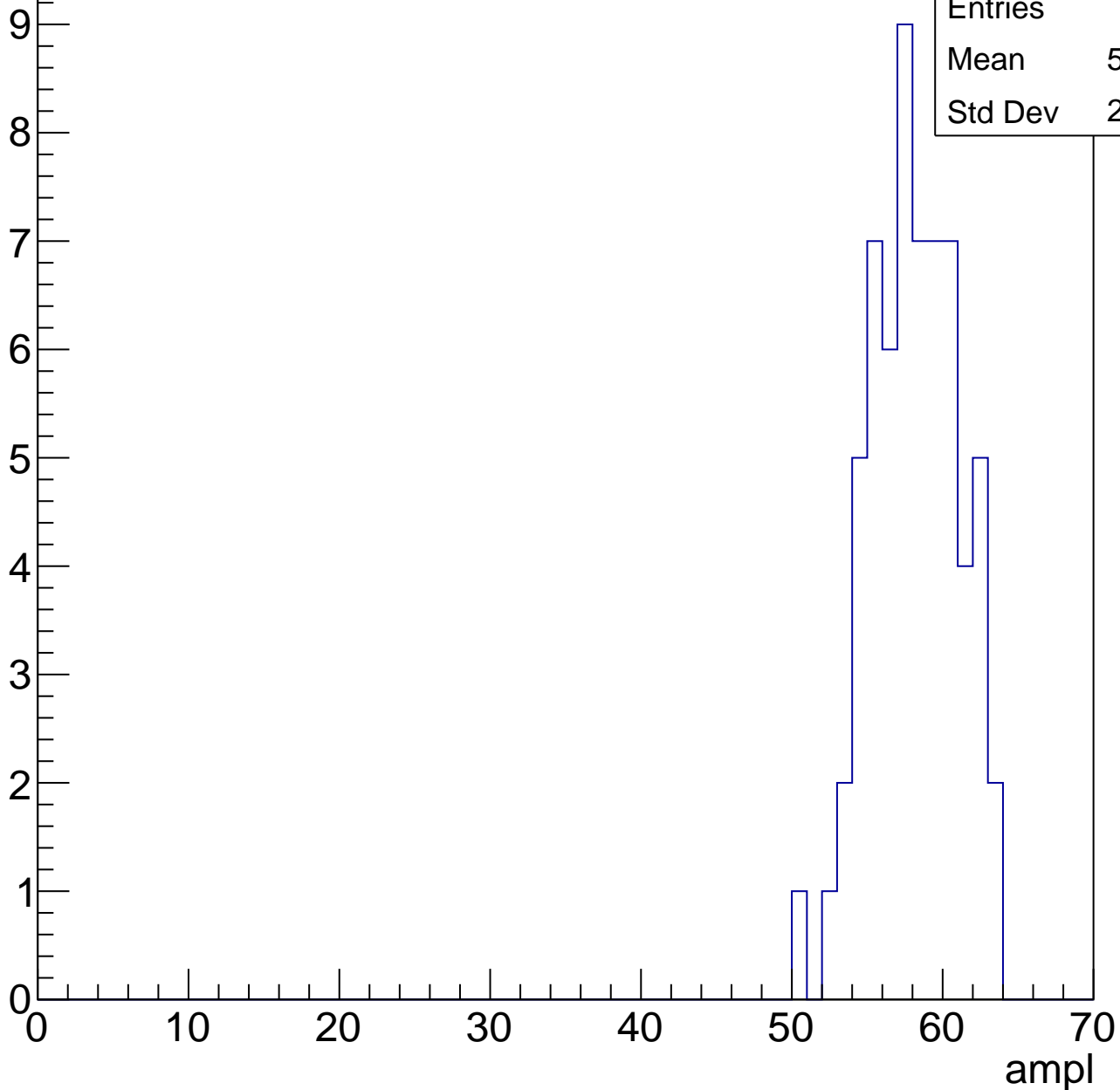


# B0L000S, U10-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	57.63
Std Dev	2.869

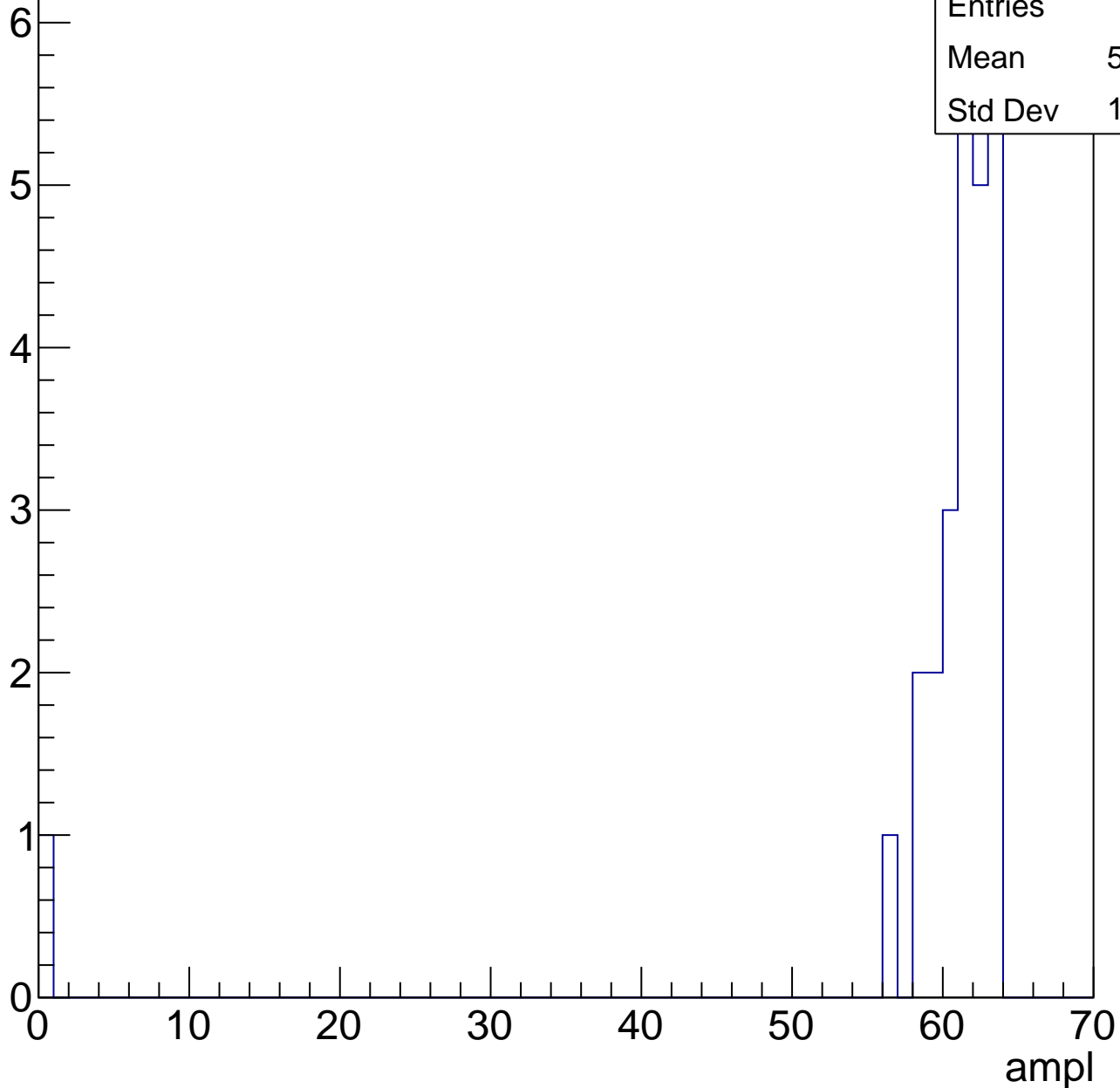


# B0L000S, U10-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.62
Std Dev	11.86



# B0L000S, U10-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch58, adc0

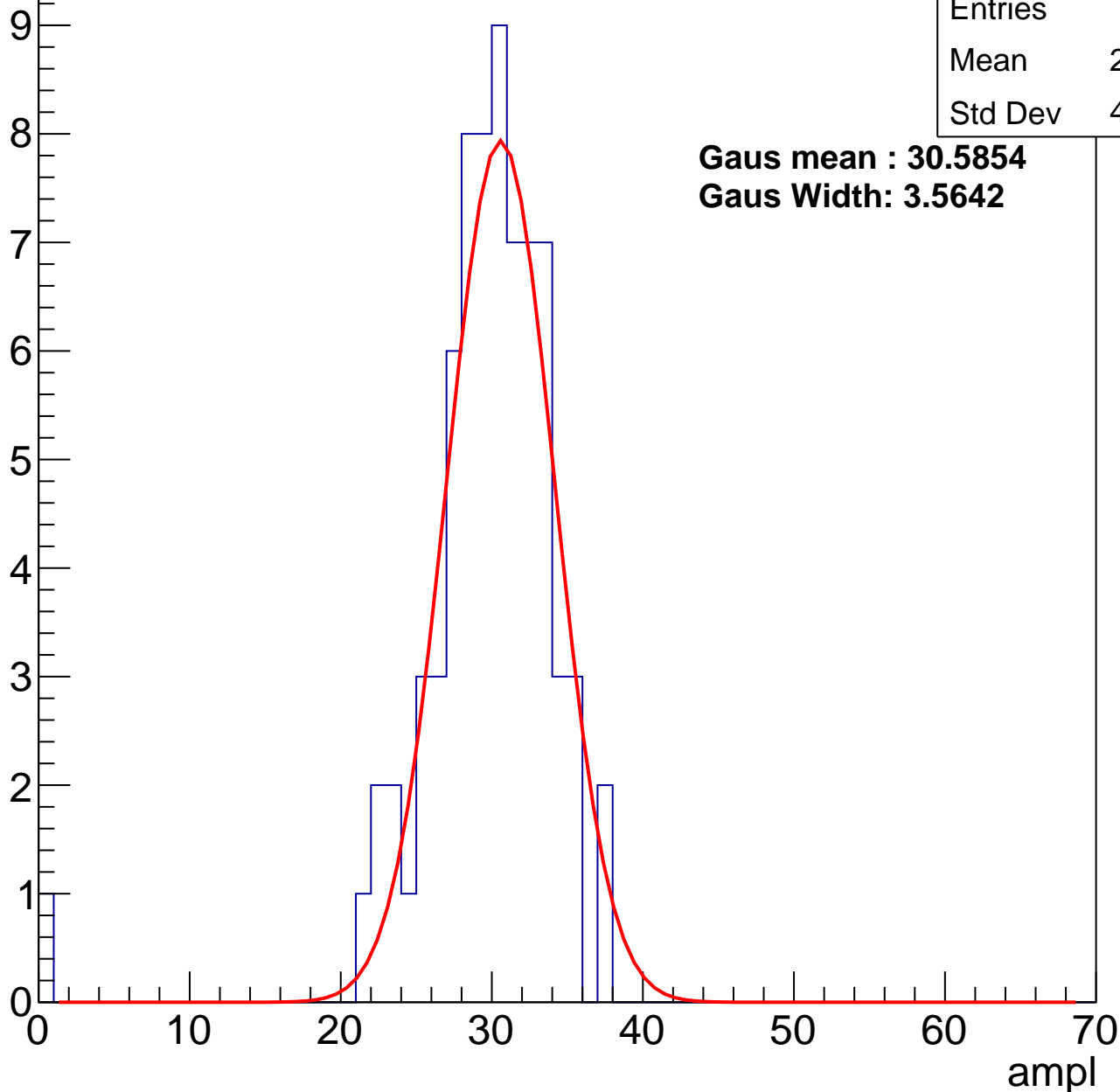
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.16
Std Dev	4.865

**Gaus mean : 30.5854**

**Gaus Width: 3.5642**



# B0L000S, U10-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.87
Std Dev	3.886

**Gaus mean : 36.7414**

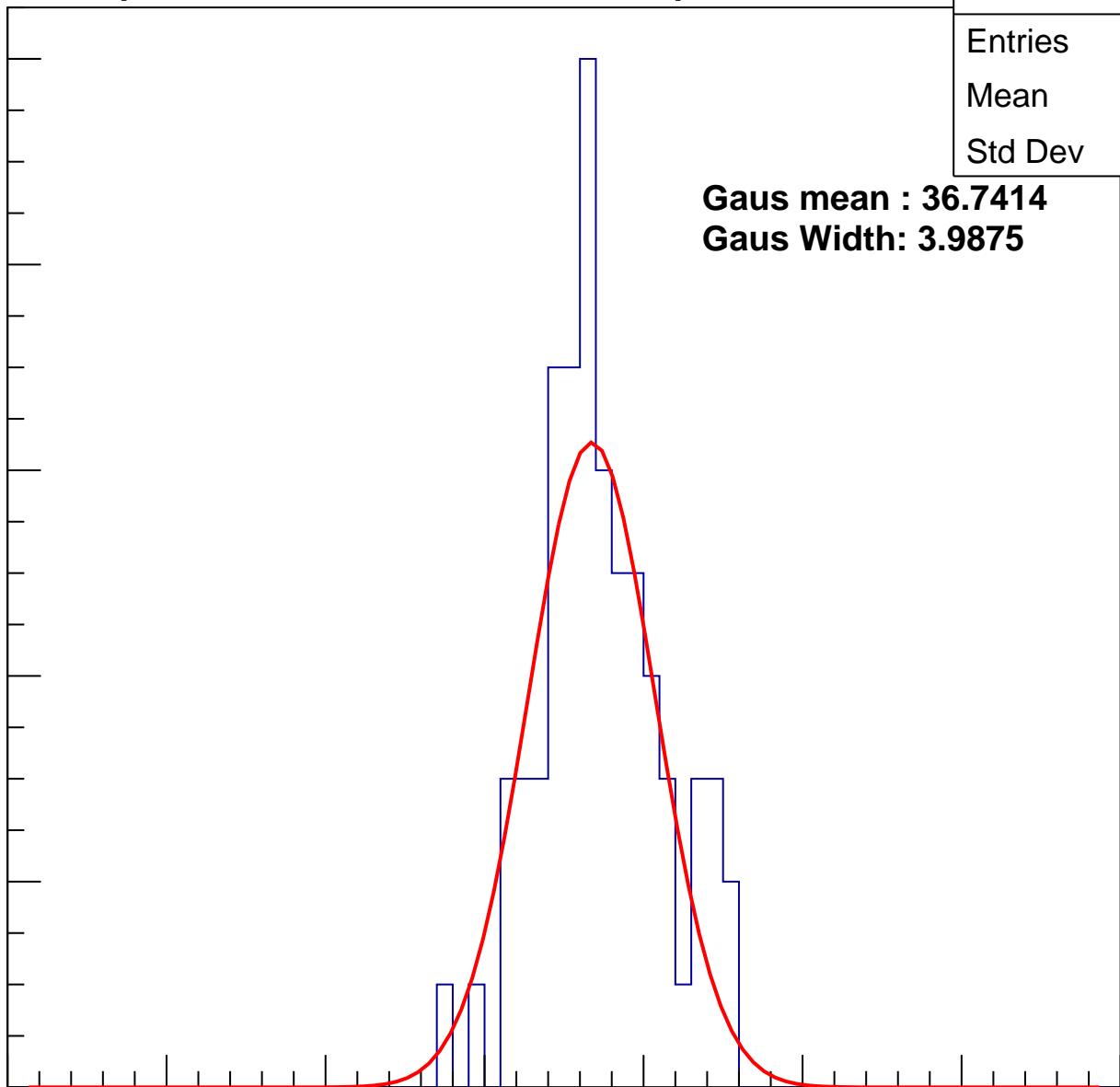
**Gaus Width: 3.9875**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch58, adc2

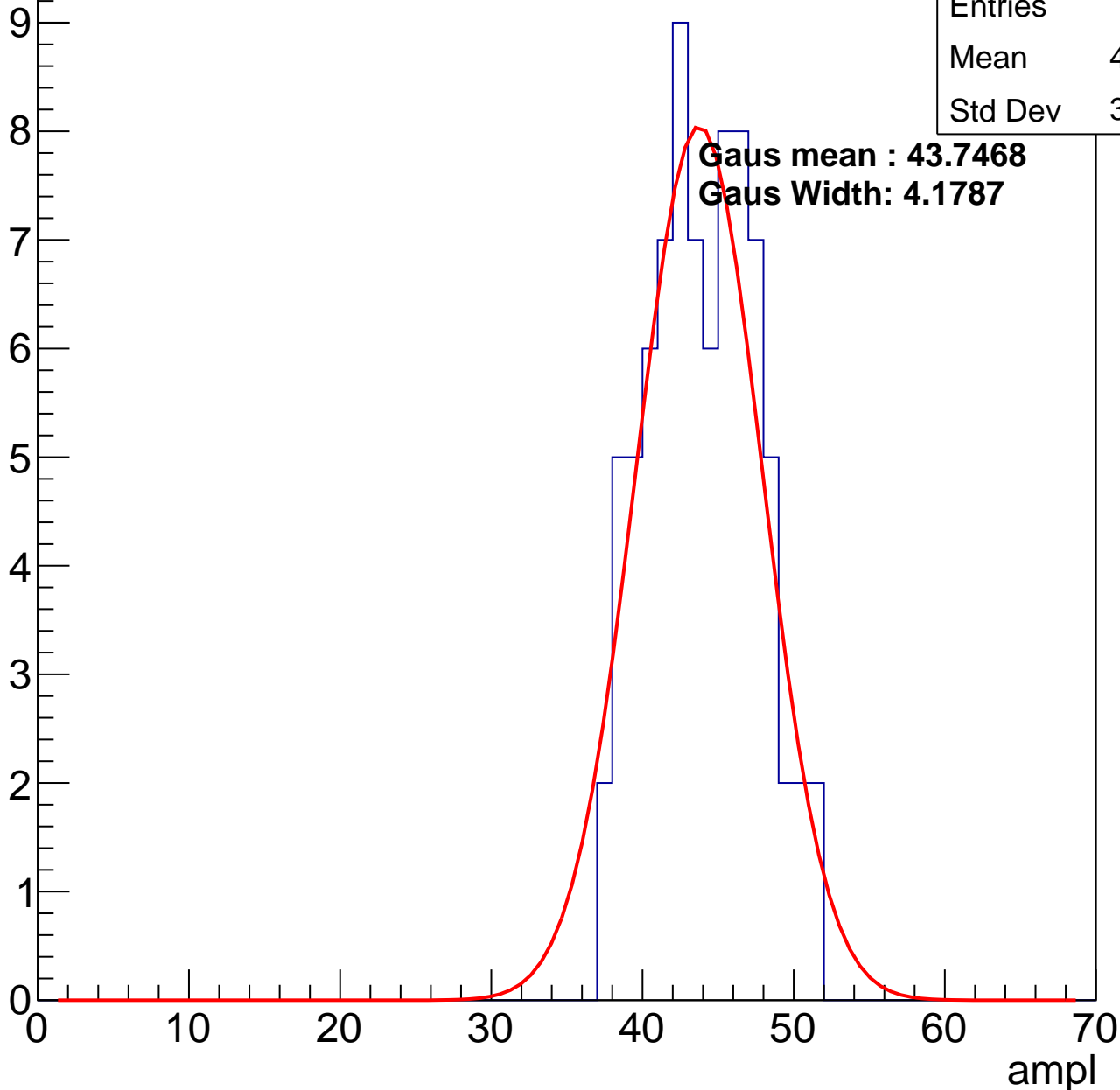
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	43.53
Std Dev	3.496

**Gaus mean : 43.7468**

**Gaus Width: 4.1787**

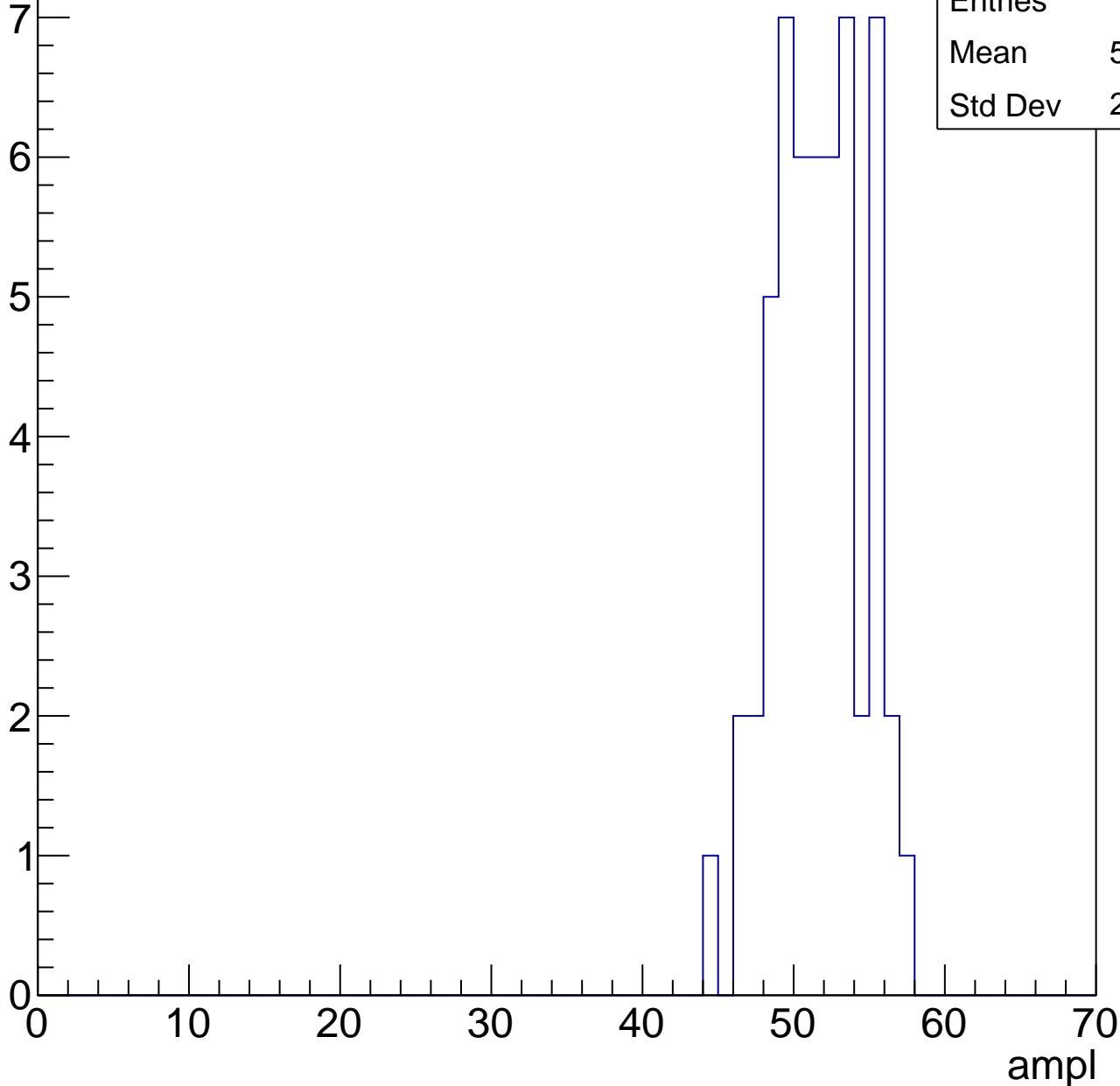


# B0L000S, U10-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	51.19
Std Dev	2.913

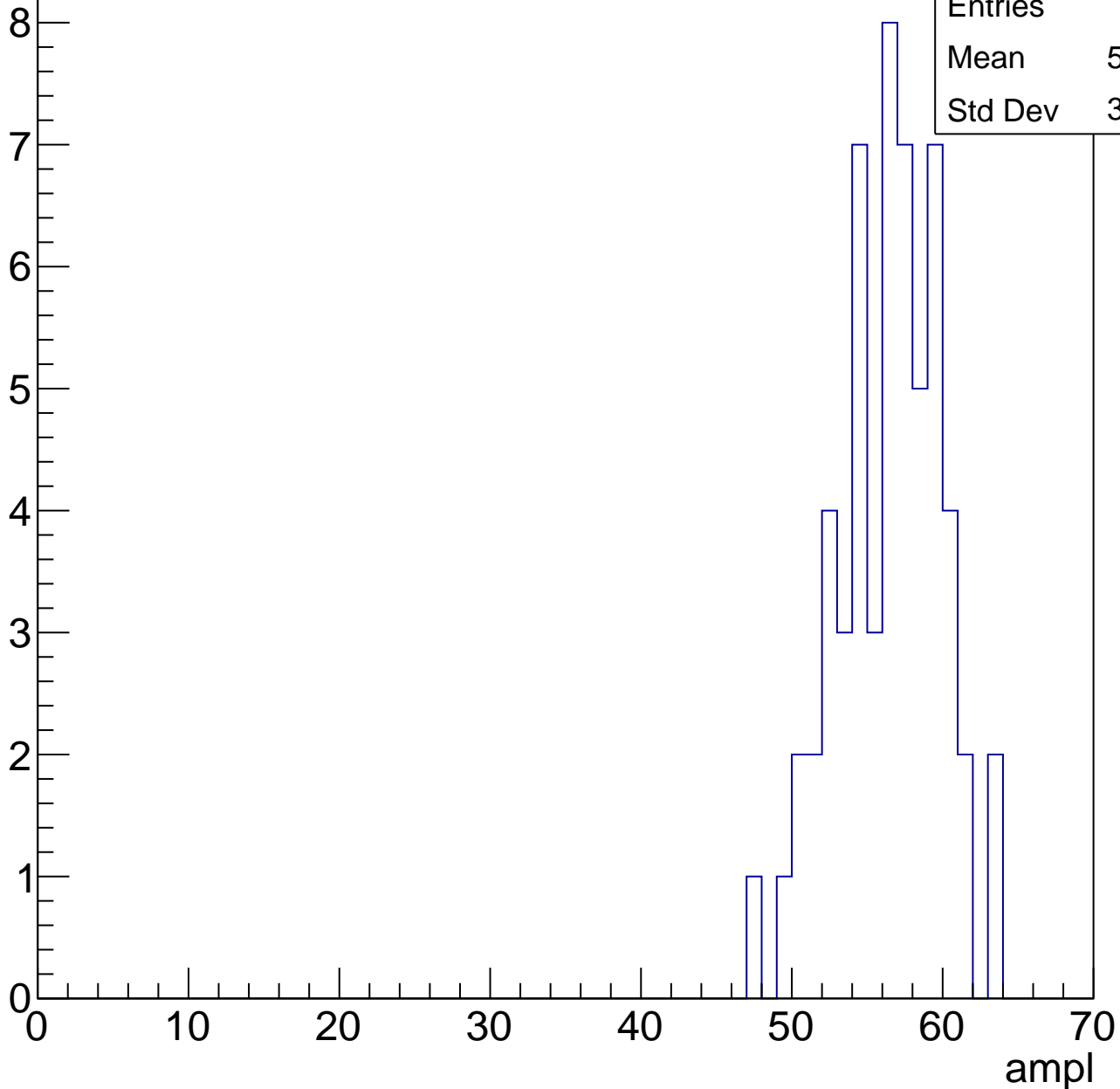


# B0L000S, U10-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.97
Std Dev	3.414

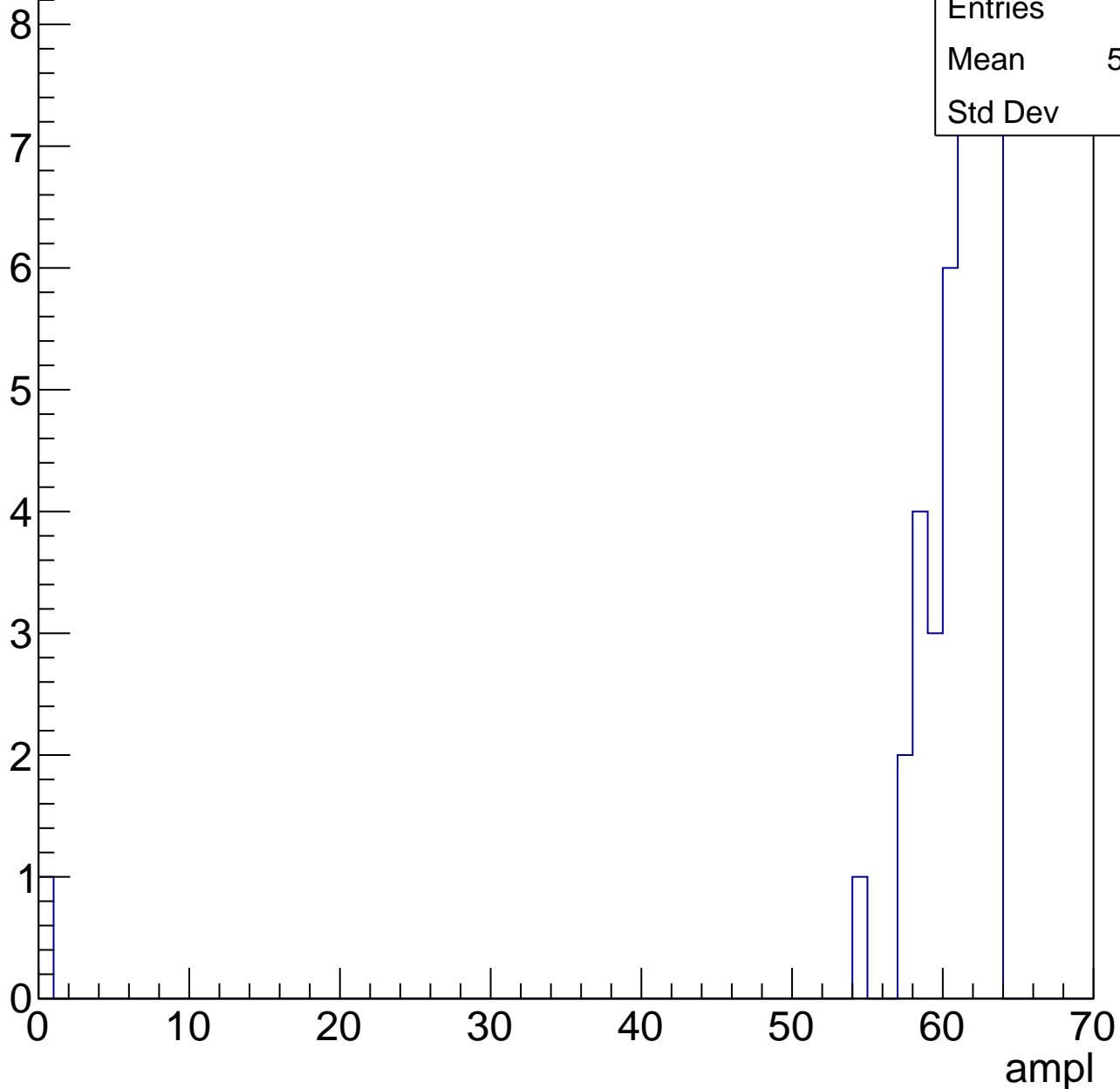


# B0L000S, U10-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

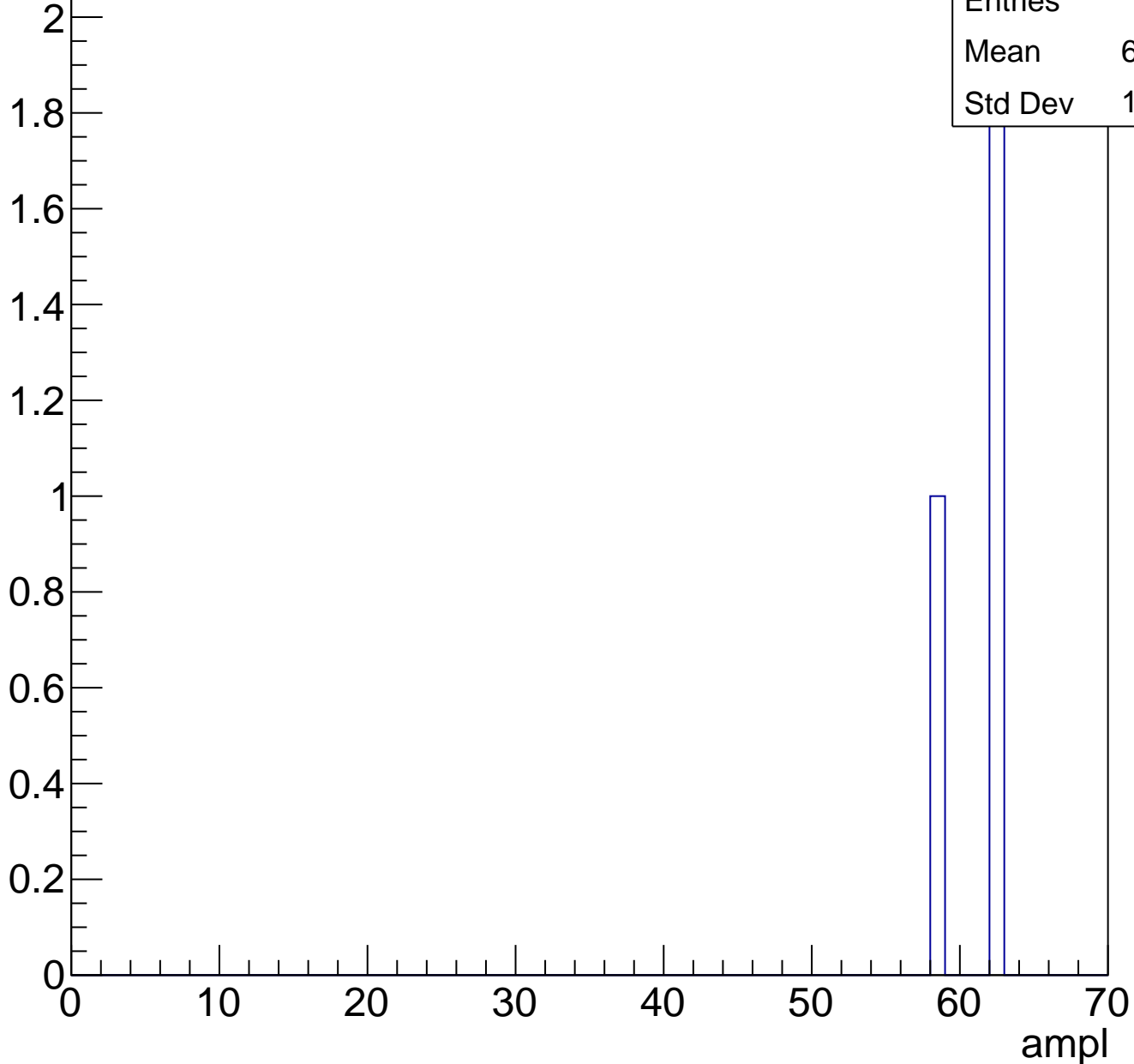
Entries	41
Mean	59.15
Std Dev	9.57



# B0L000S, U10-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch59, adc0

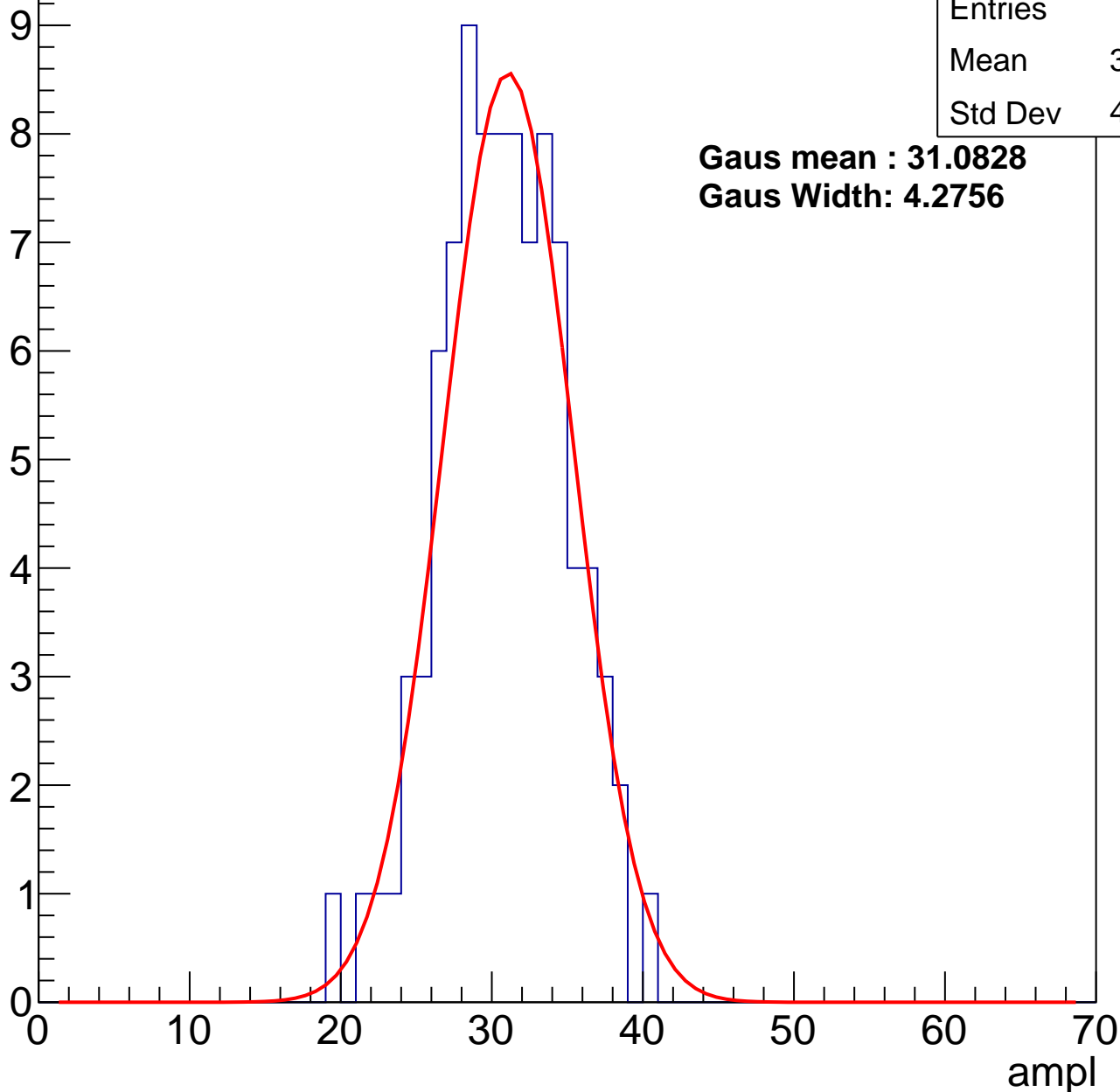
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	92
Mean	30.28
Std Dev	4.082

**Gaus mean : 31.0828**

**Gaus Width: 4.2756**



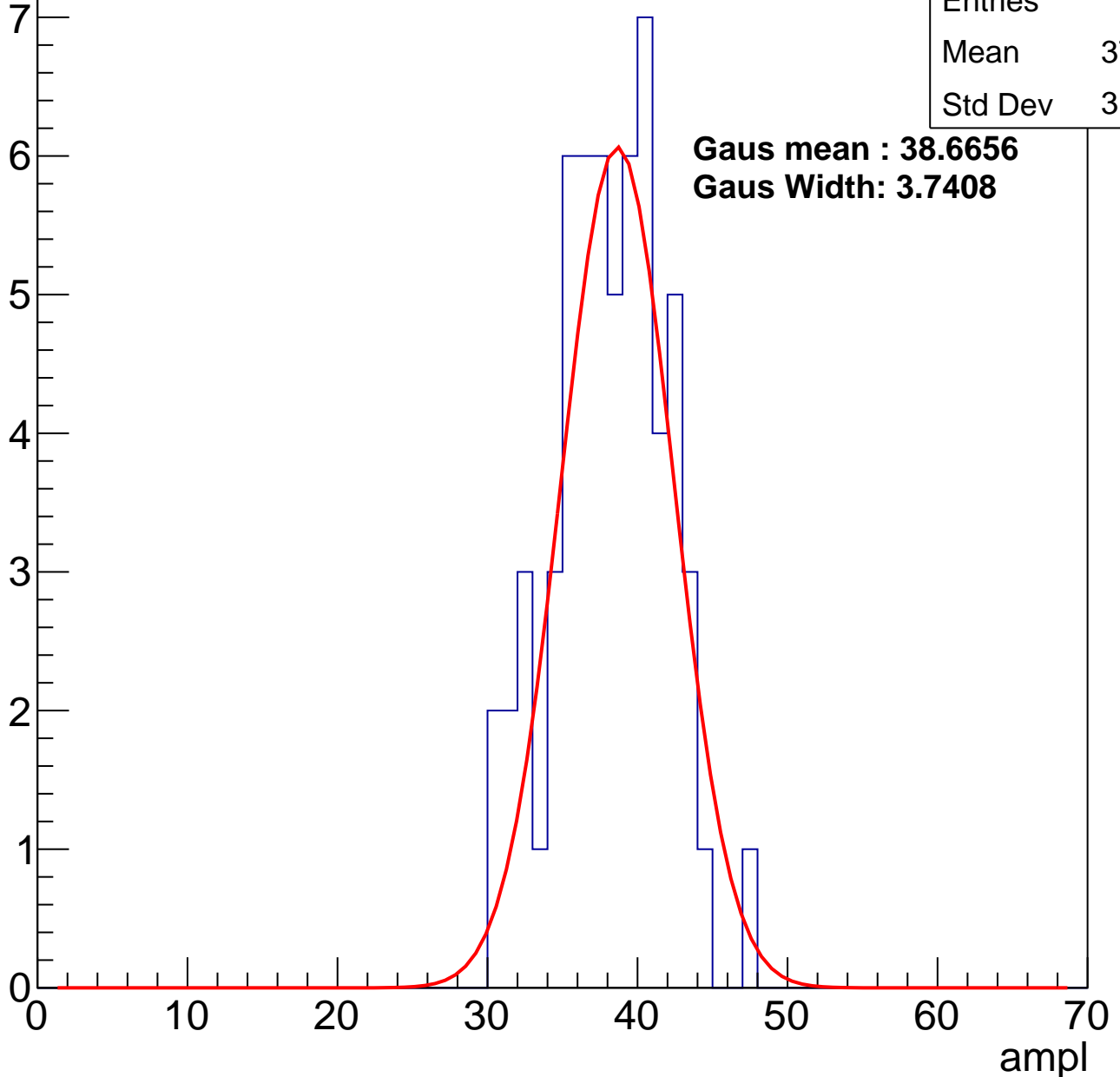
# B0L000S, U10-ch59, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.69
Std Dev	3.682

**Gaus mean : 38.6656**  
**Gaus Width: 3.7408**



# B0L000S, U10-ch59, adc2

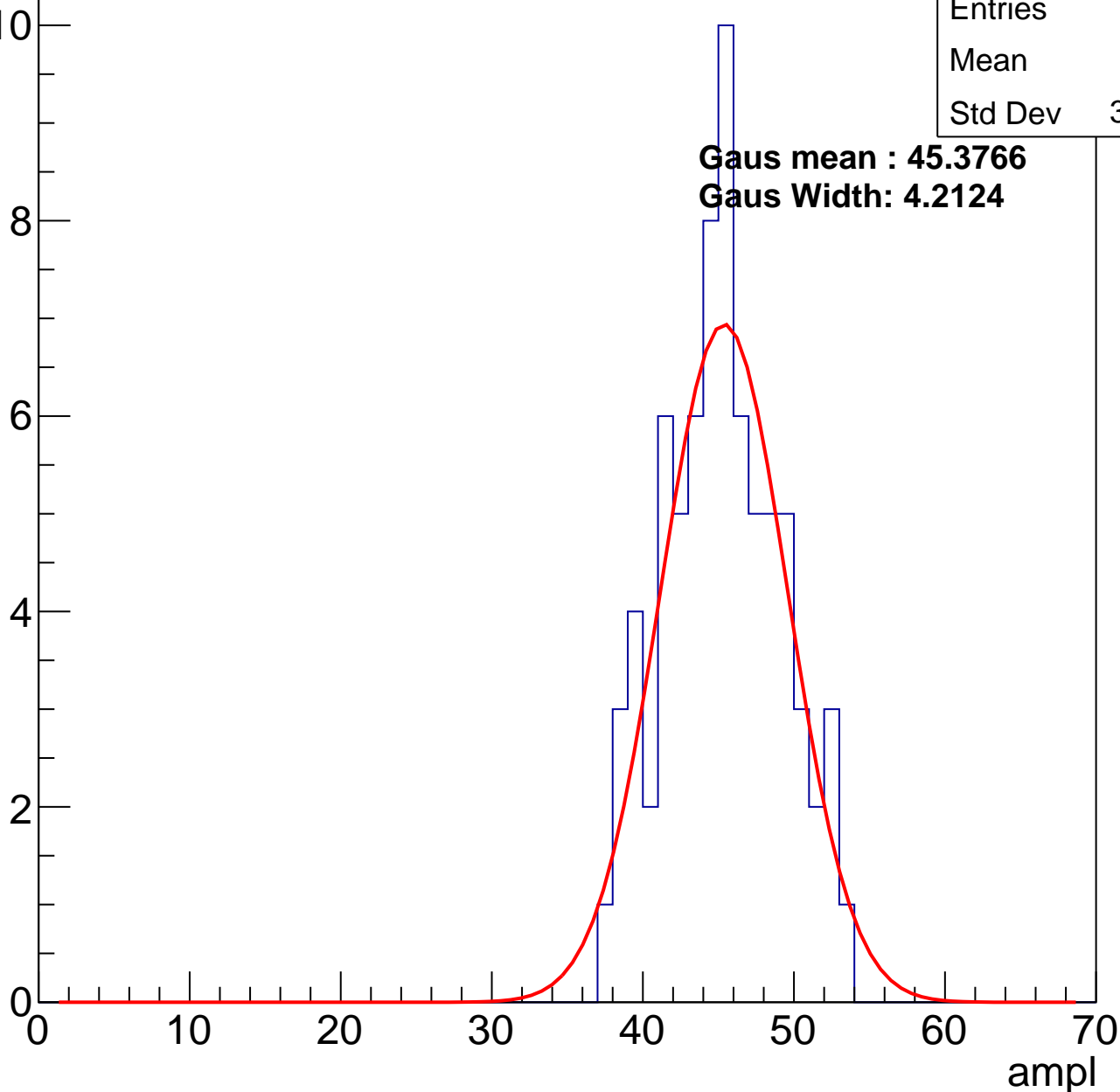
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.8
Std Dev	3.802

**Gaus mean : 45.3766**

**Gaus Width: 4.2124**

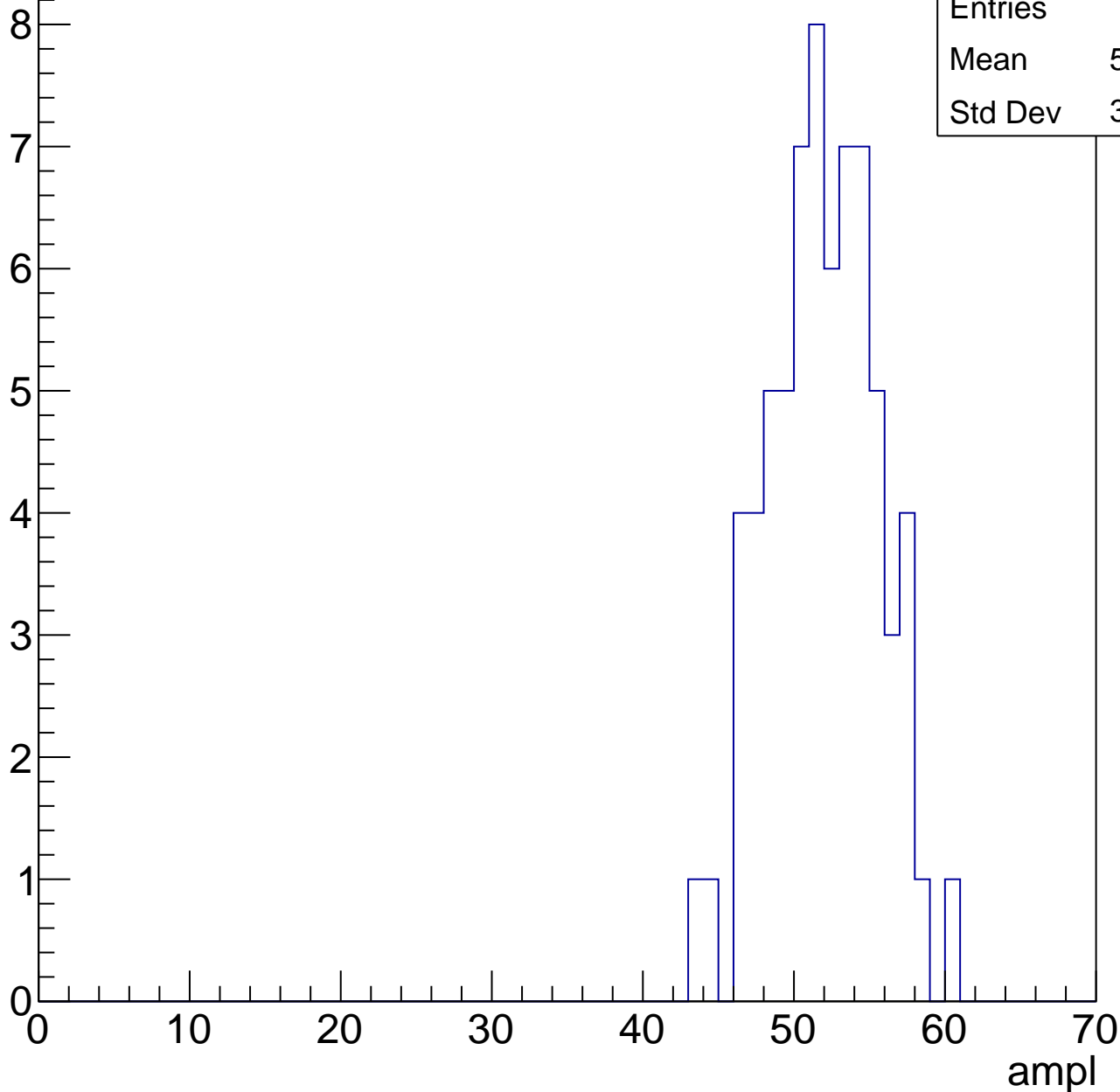


# B0L000S, U10-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

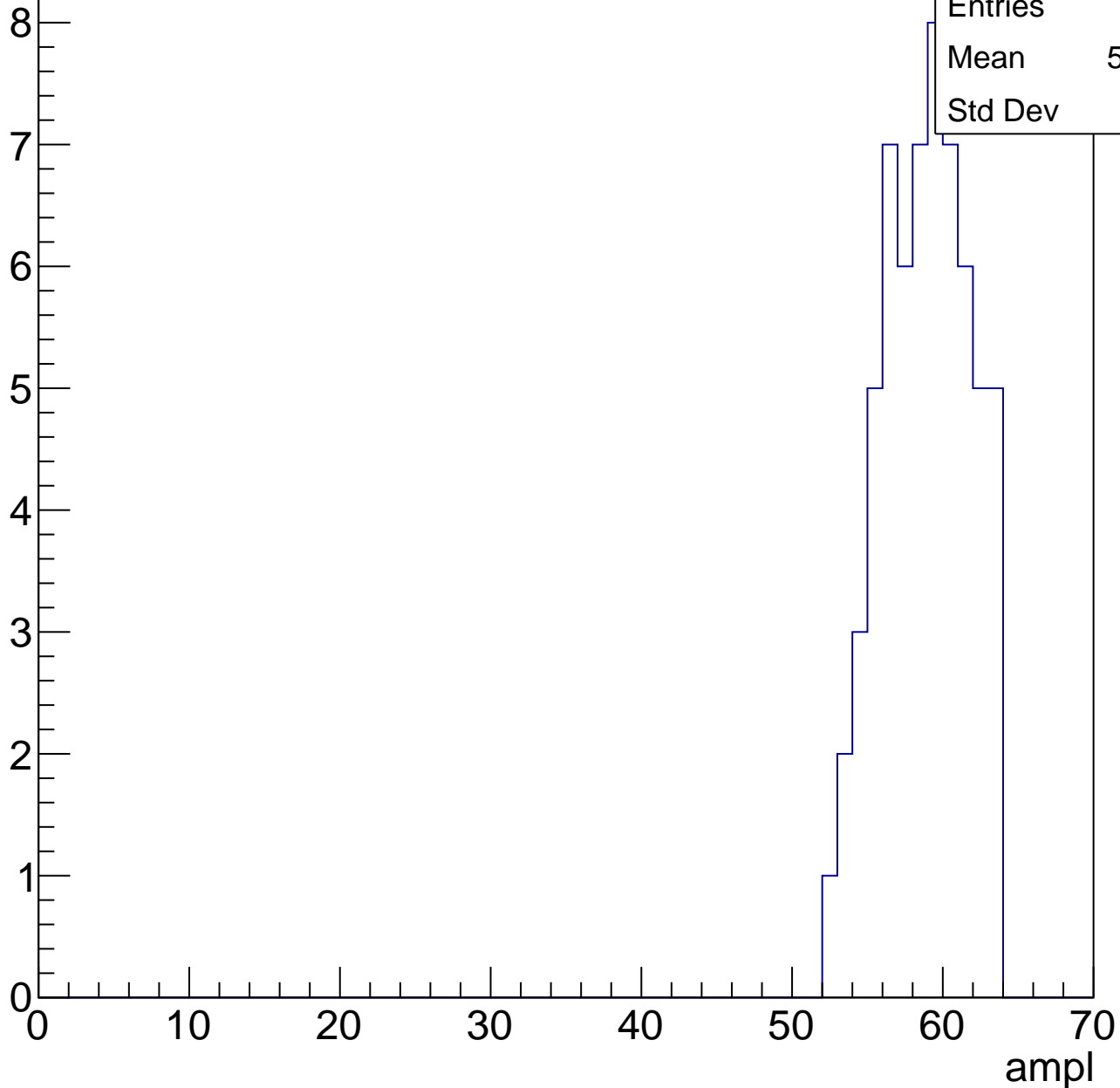
Entries	69
Mean	51.48
Std Dev	3.529



# B0L000S, U10-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

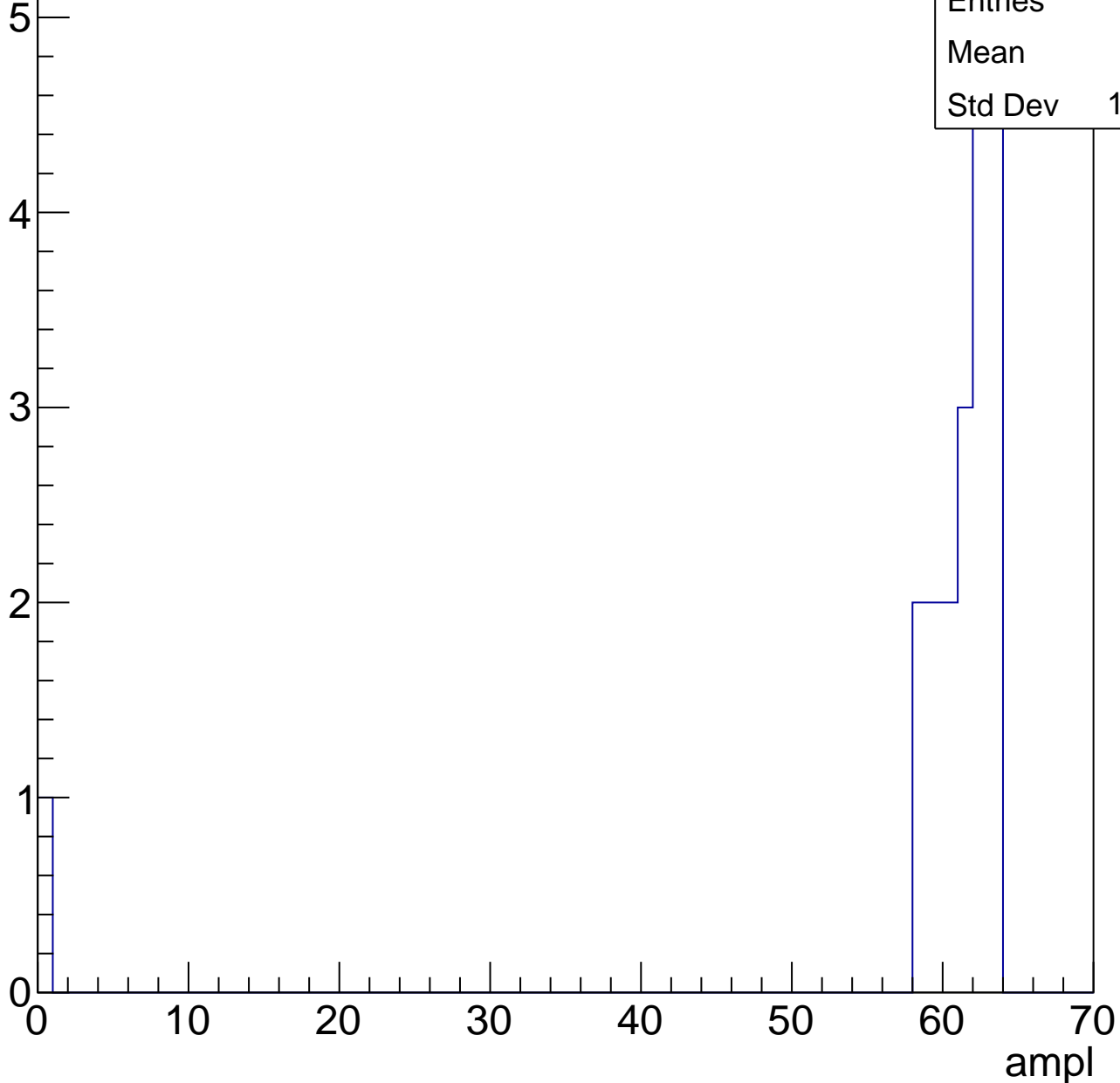


# B0L000S, U10-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	58.1
Std Dev	13.43



# B0L000S, U10-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch60, adc0

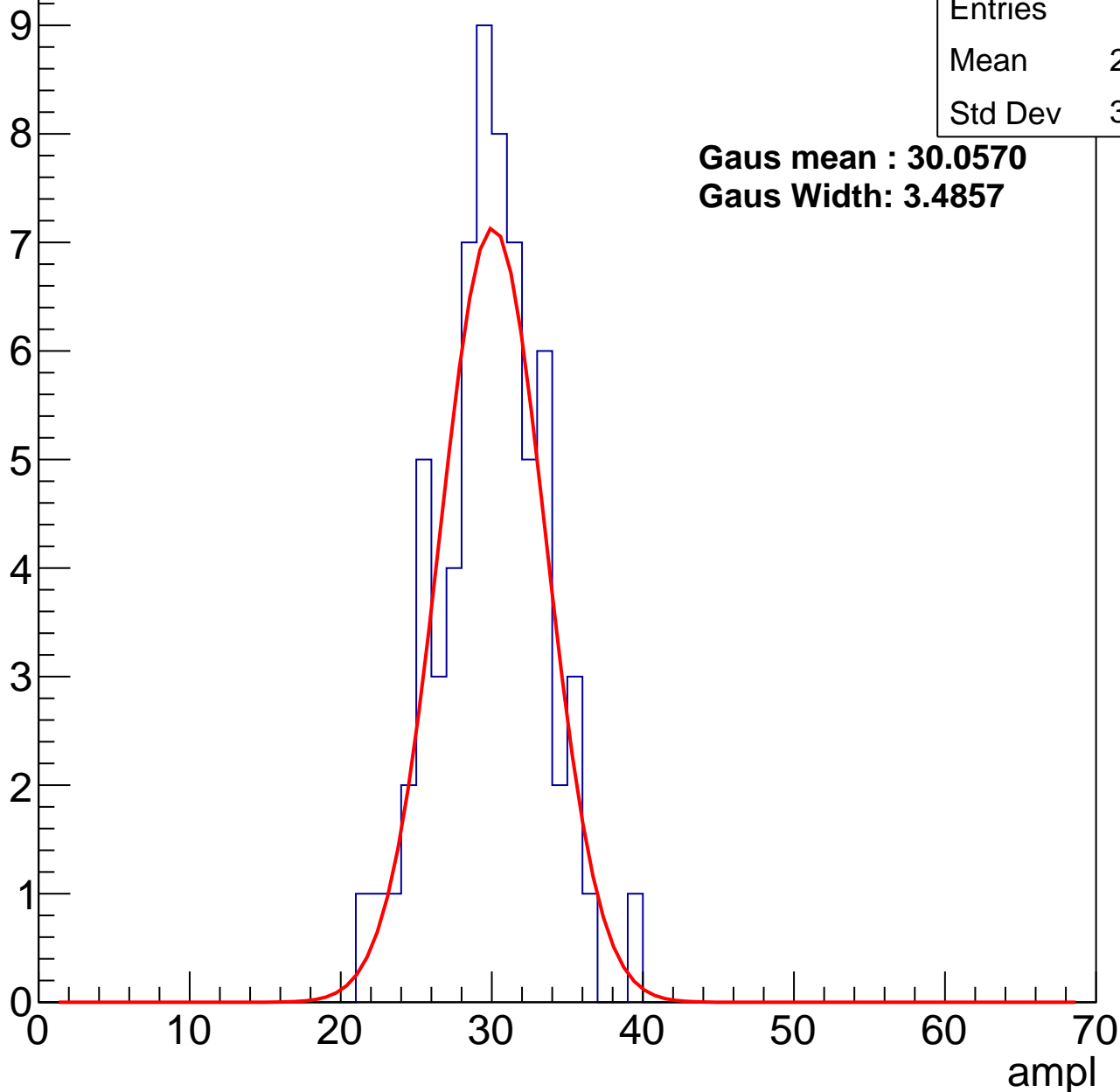
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.47
Std Dev	3.478

**Gaus mean : 30.0570**

**Gaus Width: 3.4857**



# B0L000S, U10-ch60, adc1

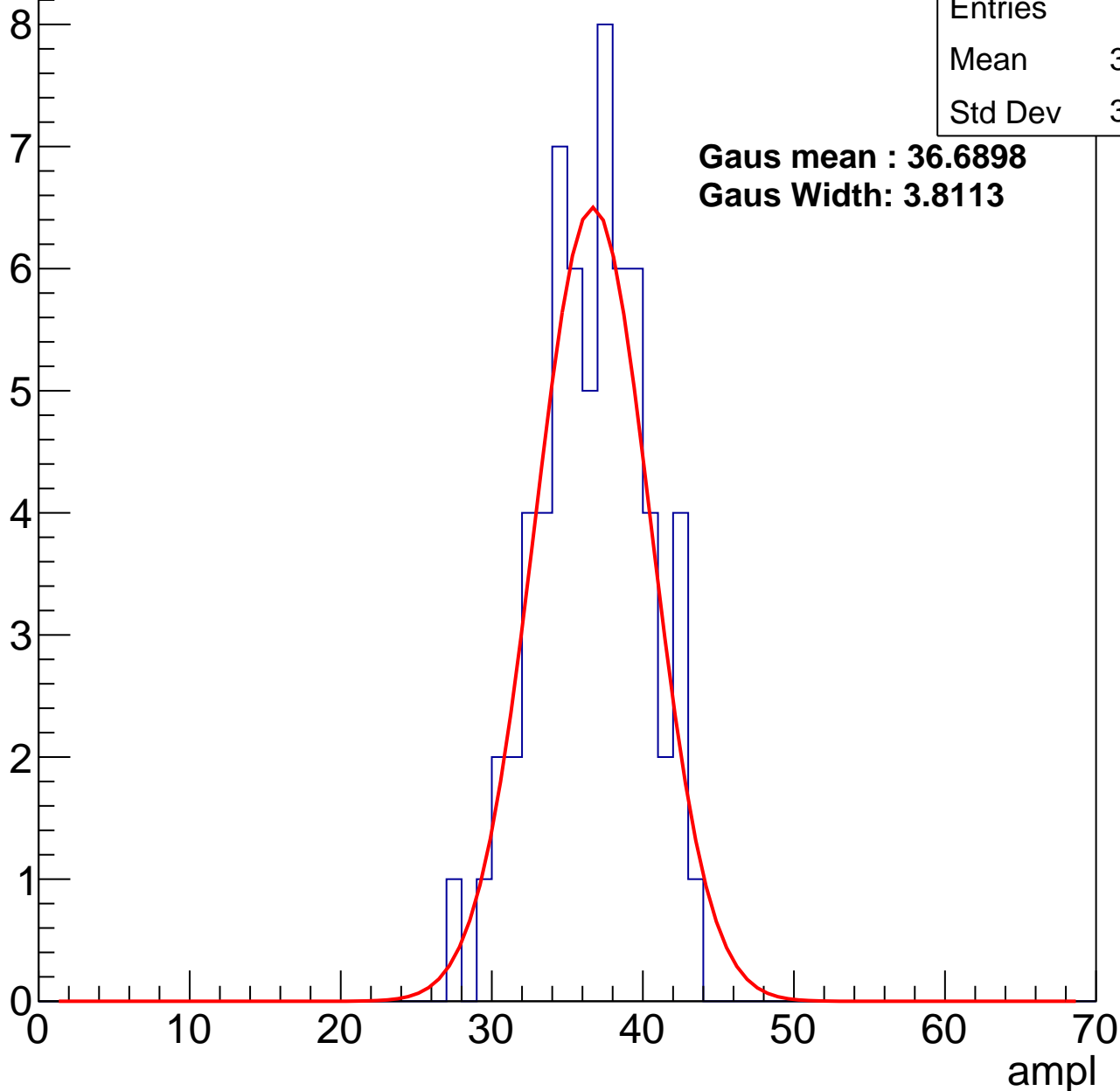
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.14
Std Dev	3.513

**Gaus mean : 36.6898**

**Gaus Width: 3.8113**

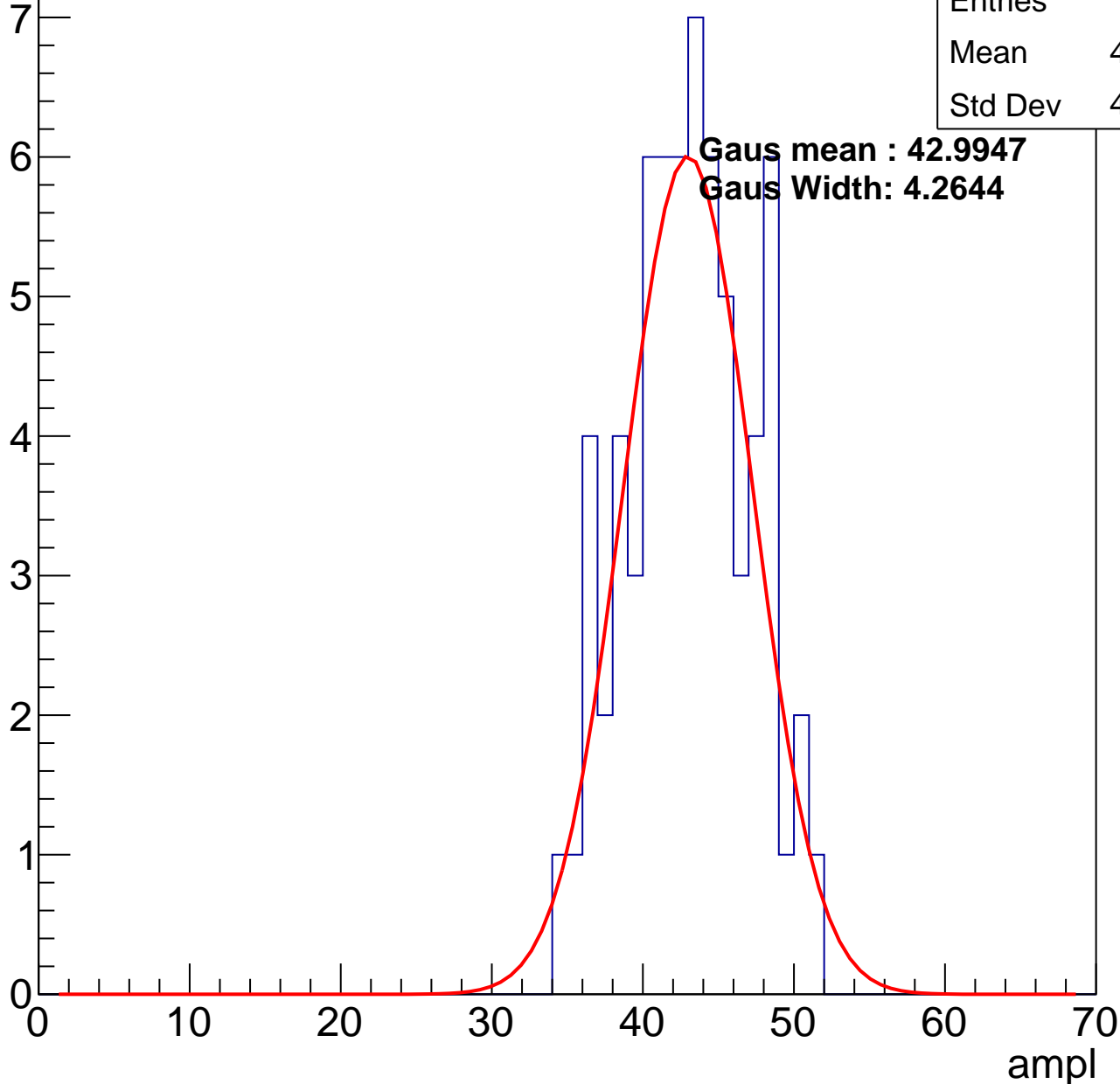


# B0L000S, U10-ch60, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.62
Std Dev	4.022

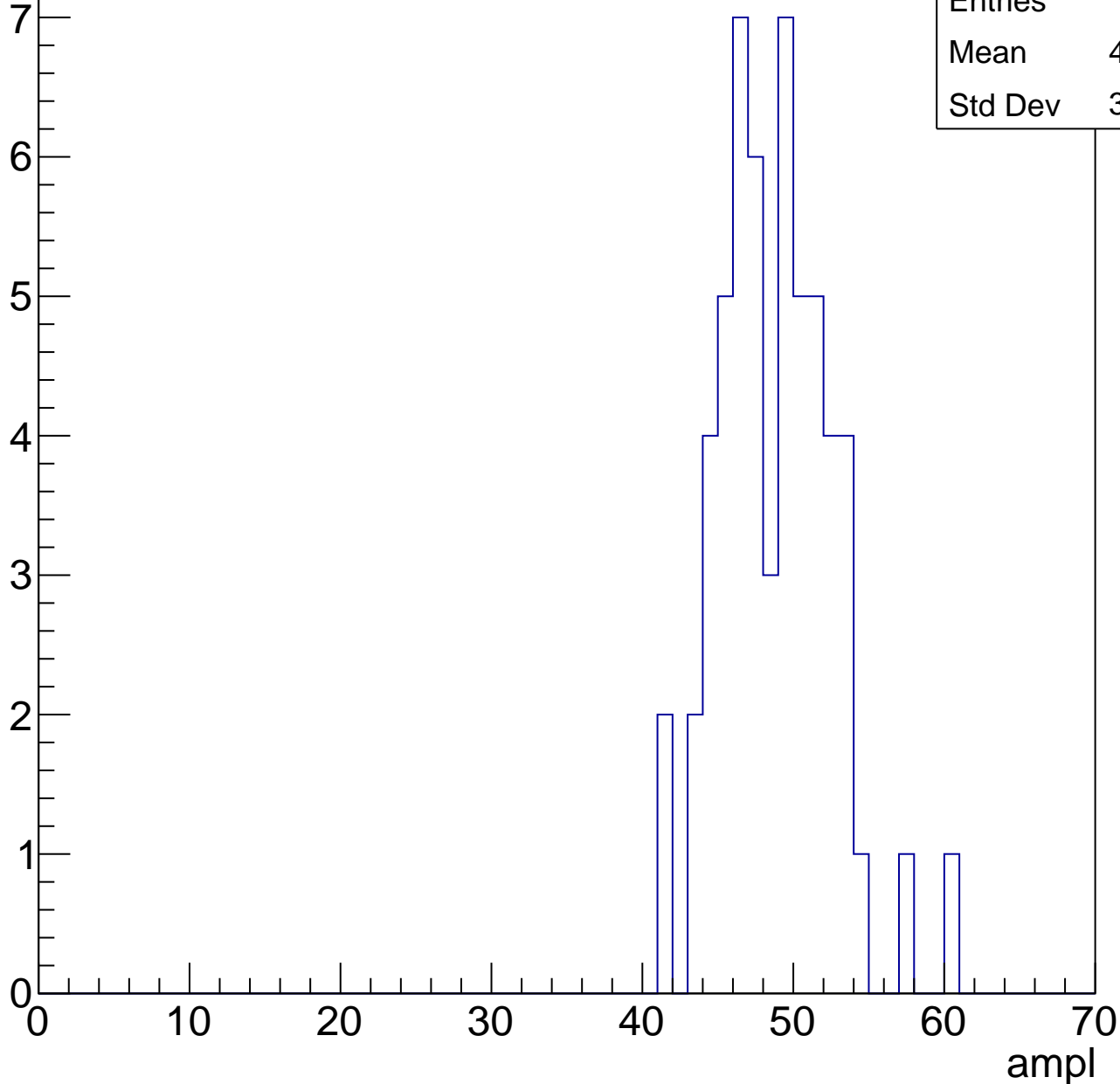


# B0L000S, U10-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	48.35
Std Dev	3.702

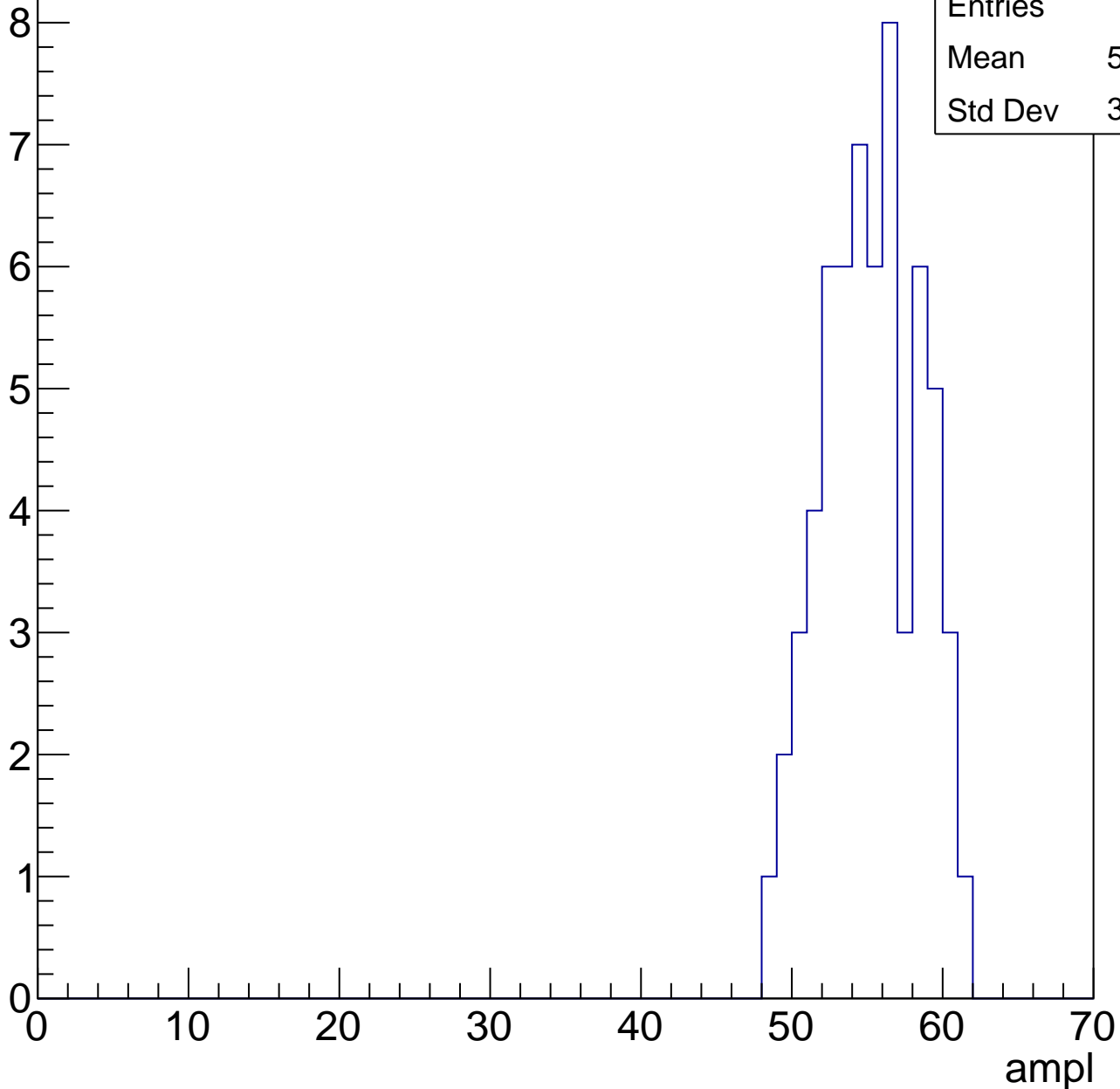


# B0L000S, U10-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	54.77
Std Dev	3.154

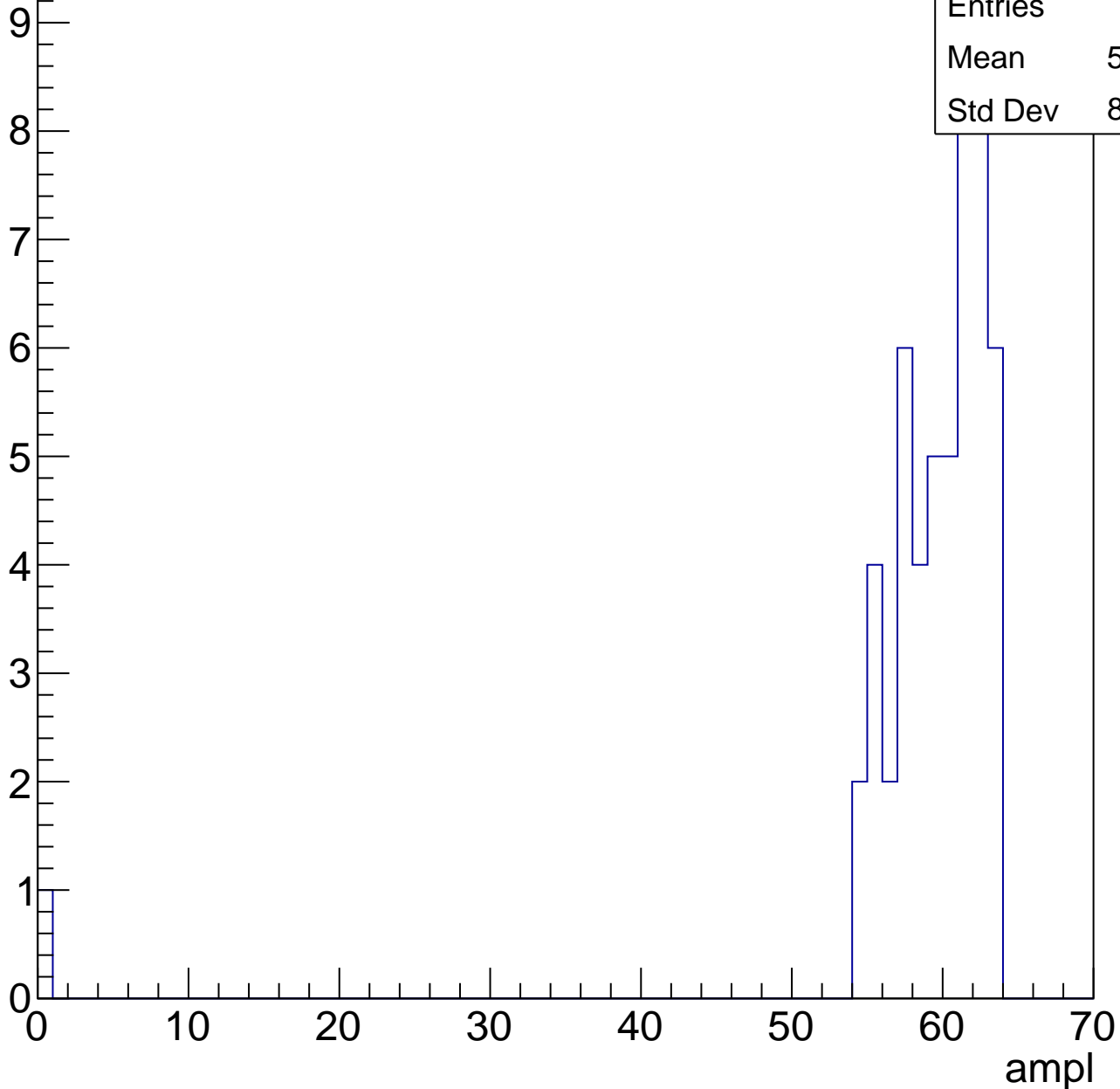


# B0L000S, U10-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.33
Std Dev	8.584



# B0L000S, U10-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch61, adc0

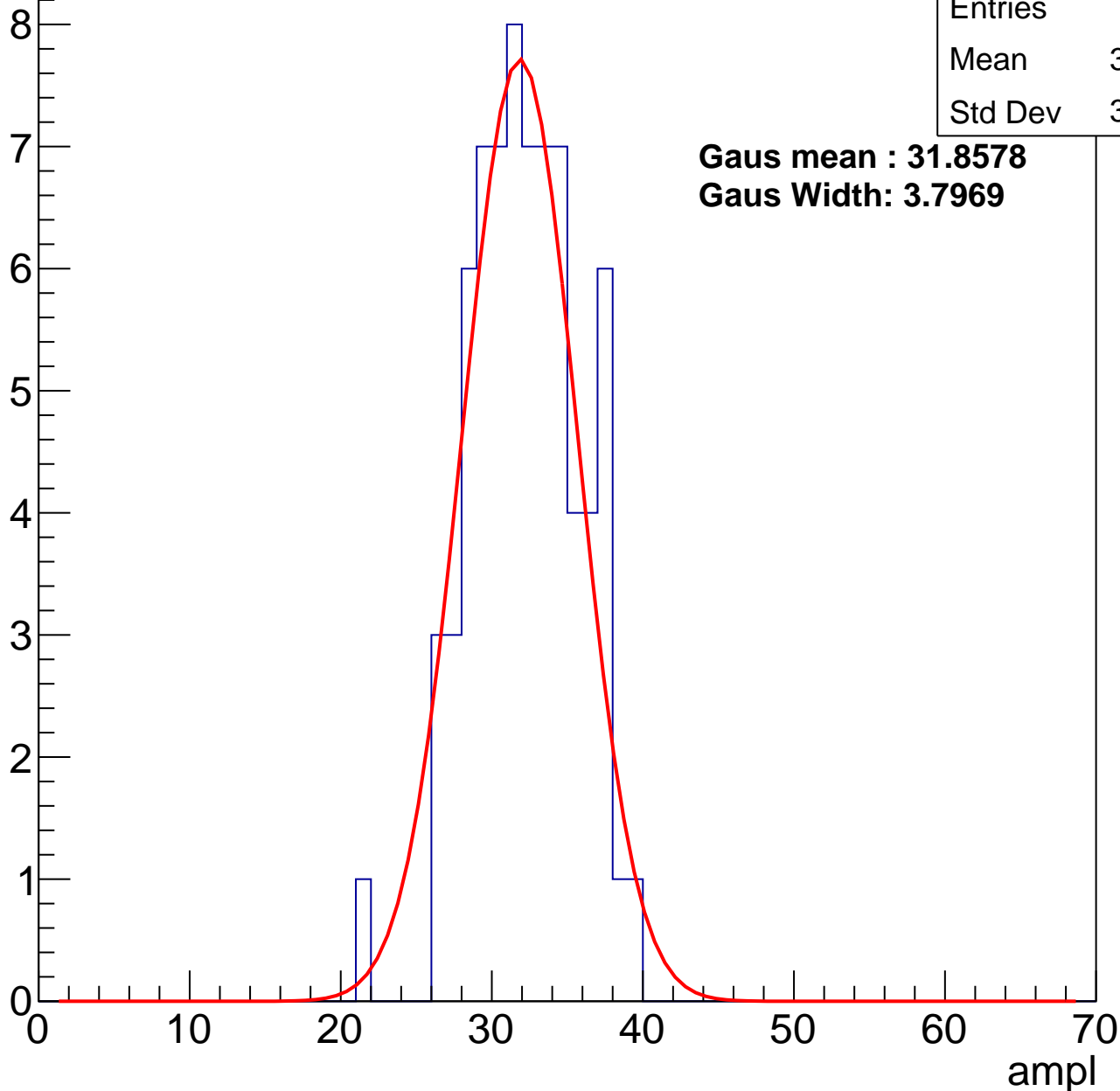
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	31.74
Std Dev	3.464

**Gaus mean : 31.8578**

**Gaus Width: 3.7969**



# B0L000S, U10-ch61, adc1

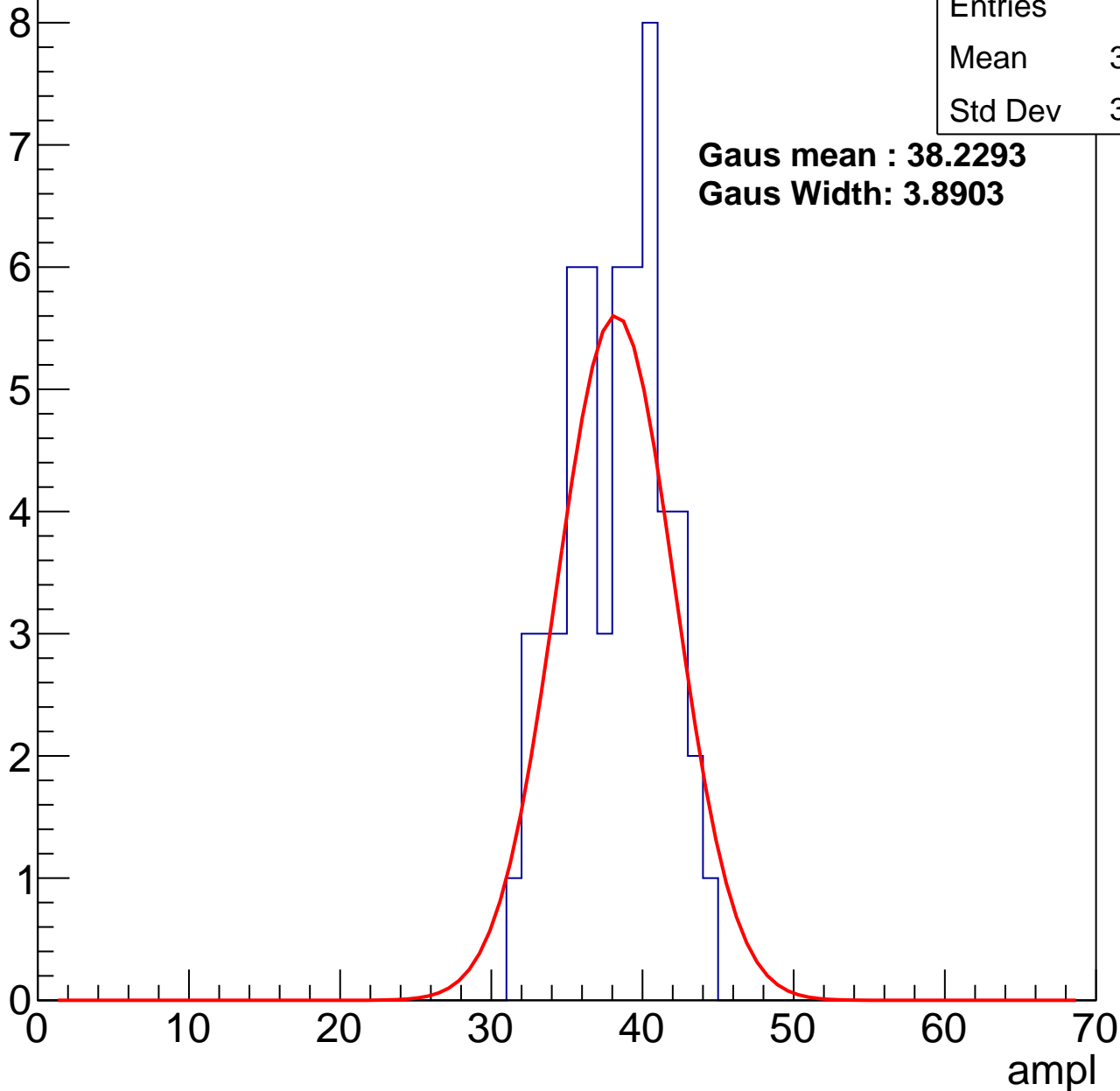
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	37.66
Std Dev	3.214

**Gaus mean : 38.2293**

**Gaus Width: 3.8903**



# B0L000S, U10-ch61, adc2

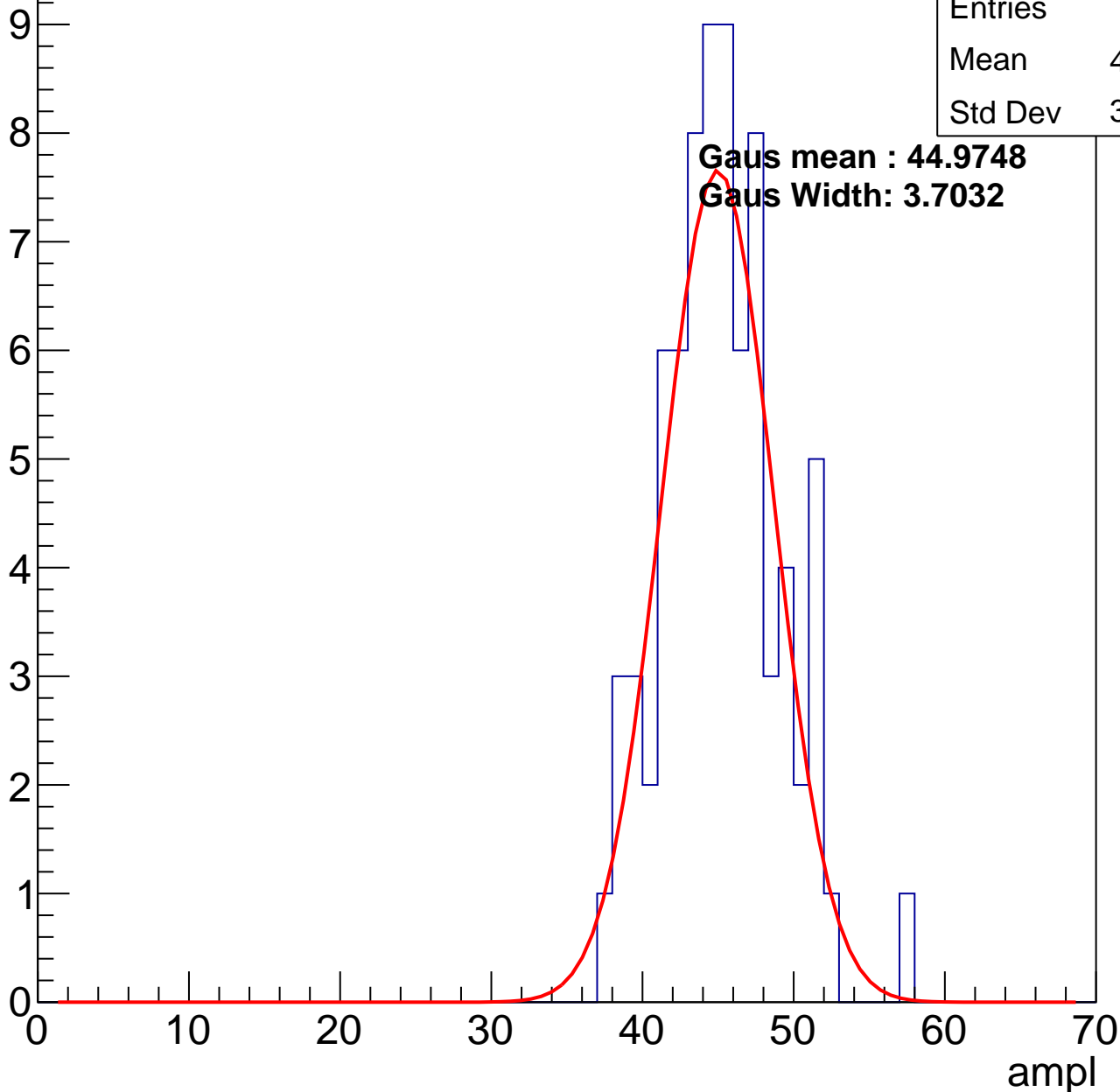
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	44.77
Std Dev	3.793

**Gaus mean : 44.9748**

**Gaus Width: 3.7032**

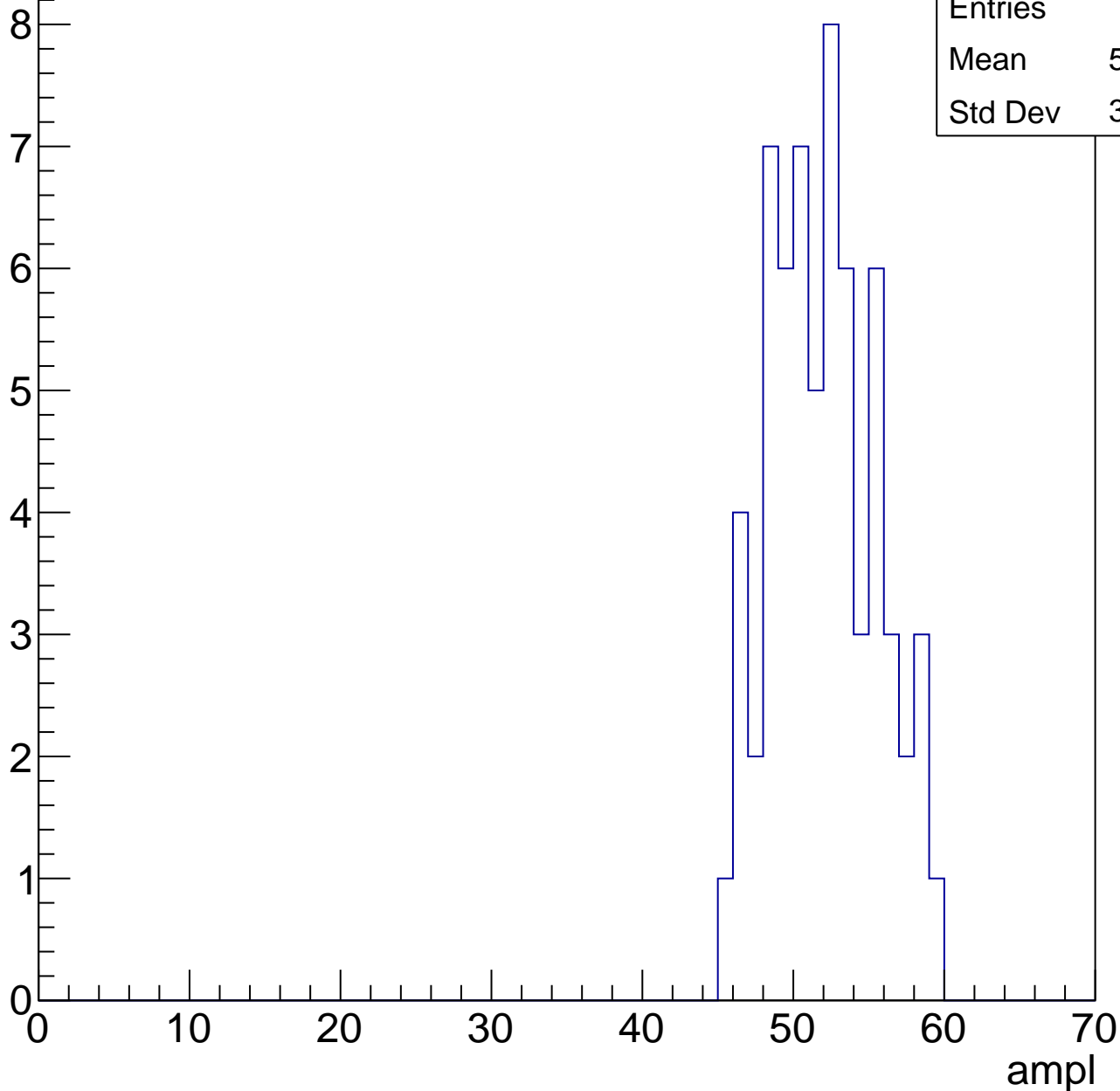


# B0L000S, U10-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.55
Std Dev	3.437

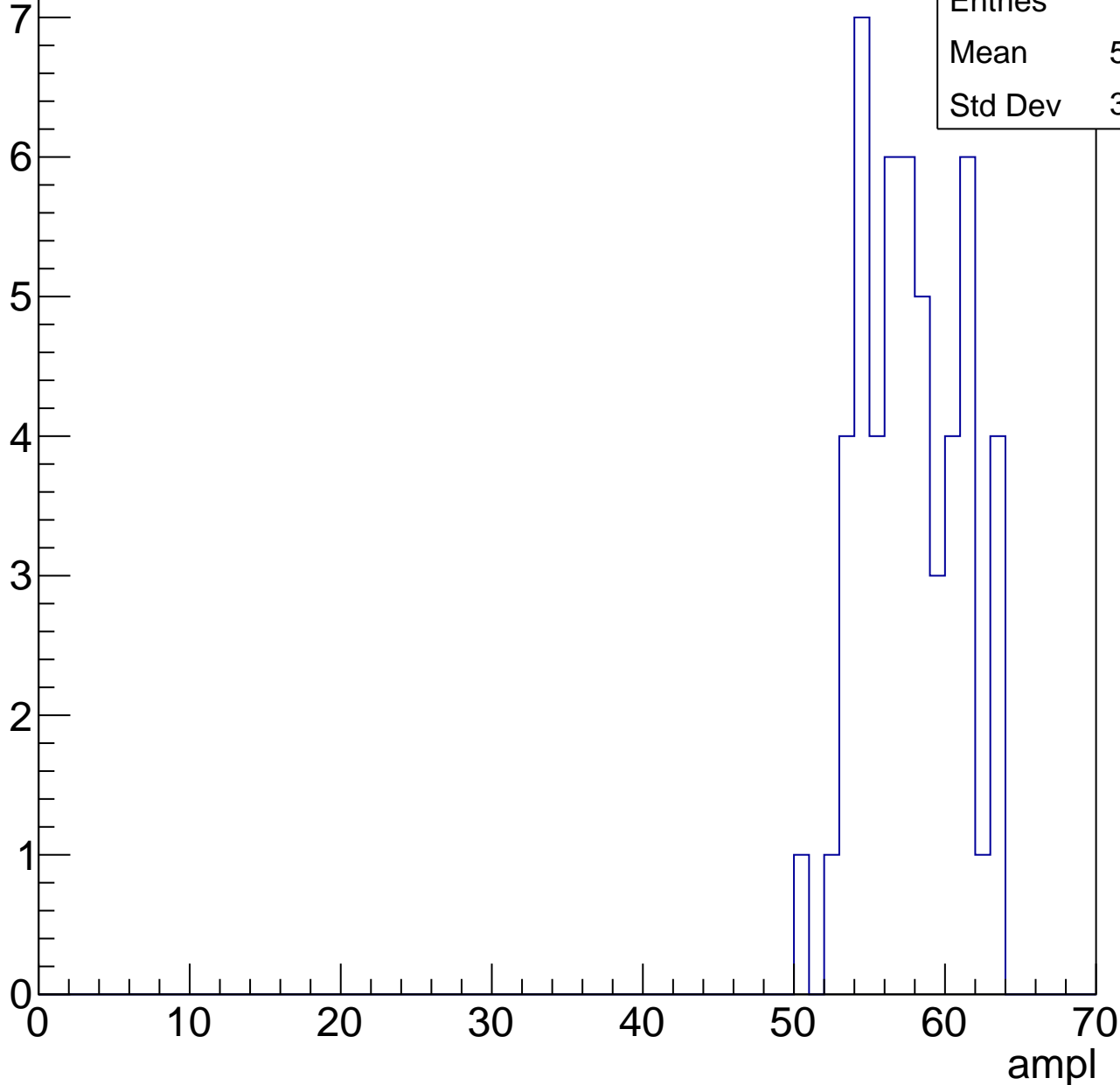


# B0L000S, U10-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	57.25
Std Dev	3.216

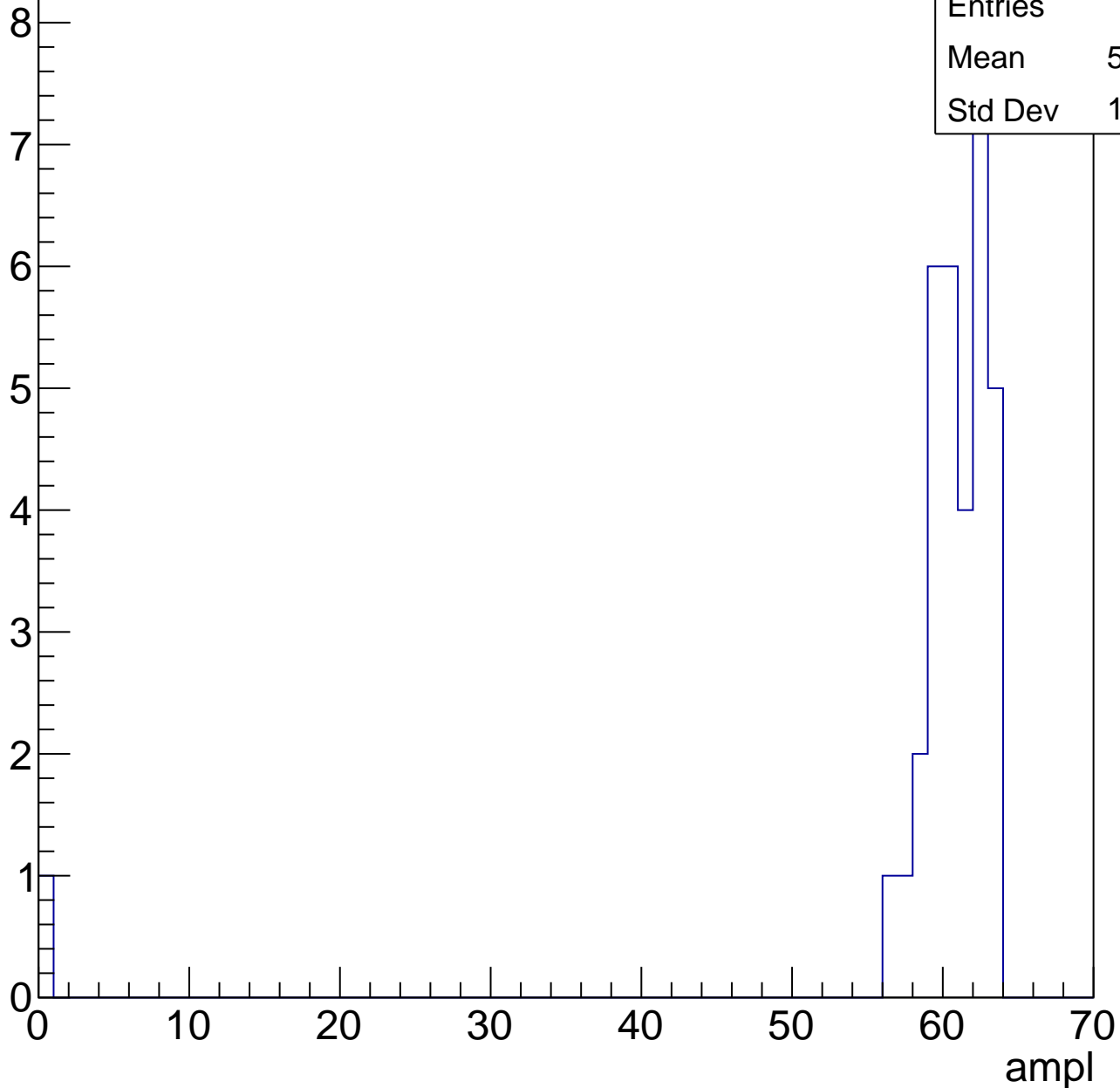


# B0L000S, U10-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.76
Std Dev	10.39



# B0L000S, U10-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U10-ch62, adc0

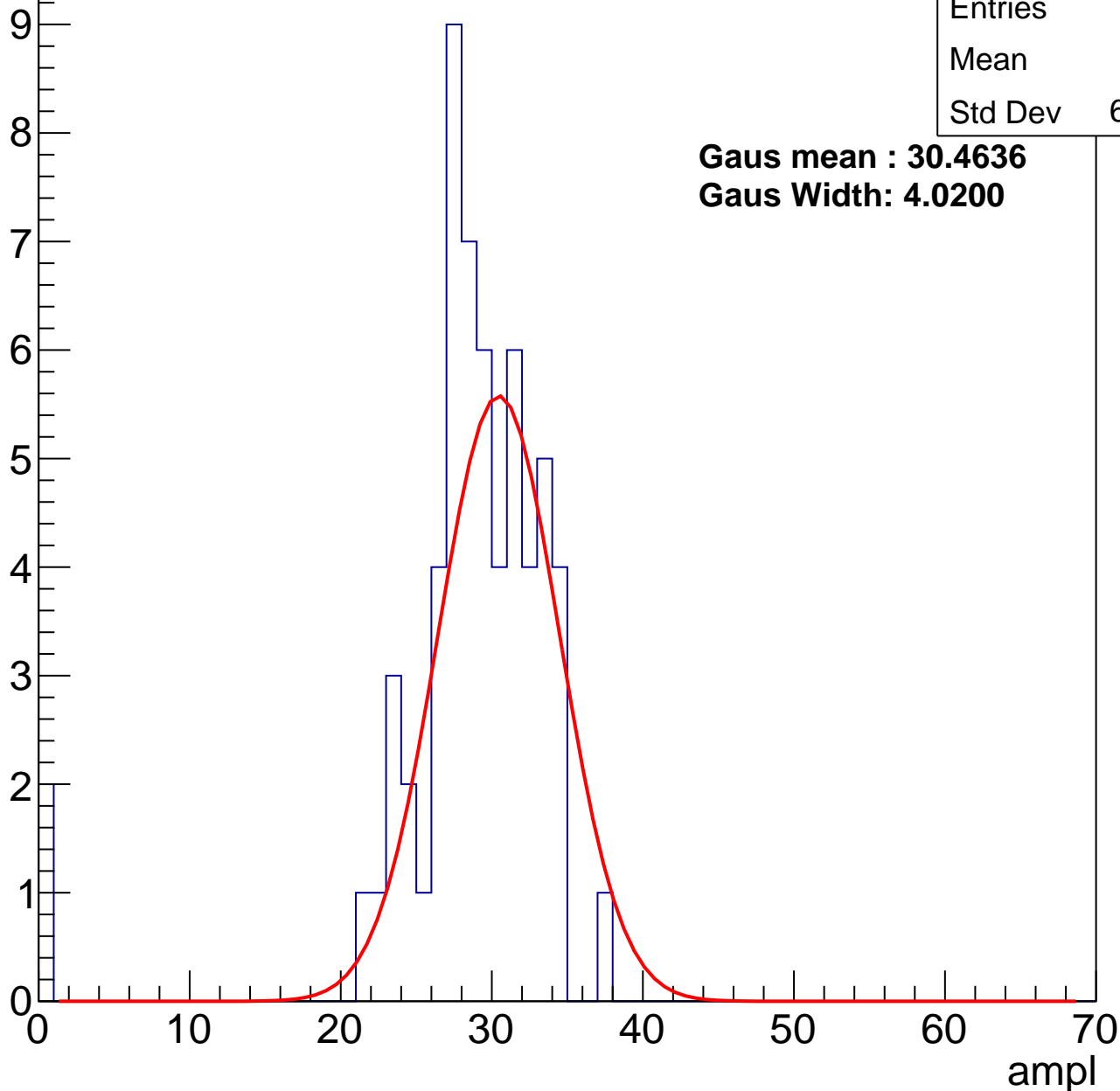
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	27.9
Std Dev	6.169

**Gaus mean : 30.4636**

**Gaus Width: 4.0200**



# B0L000S, U10-ch62, adc1

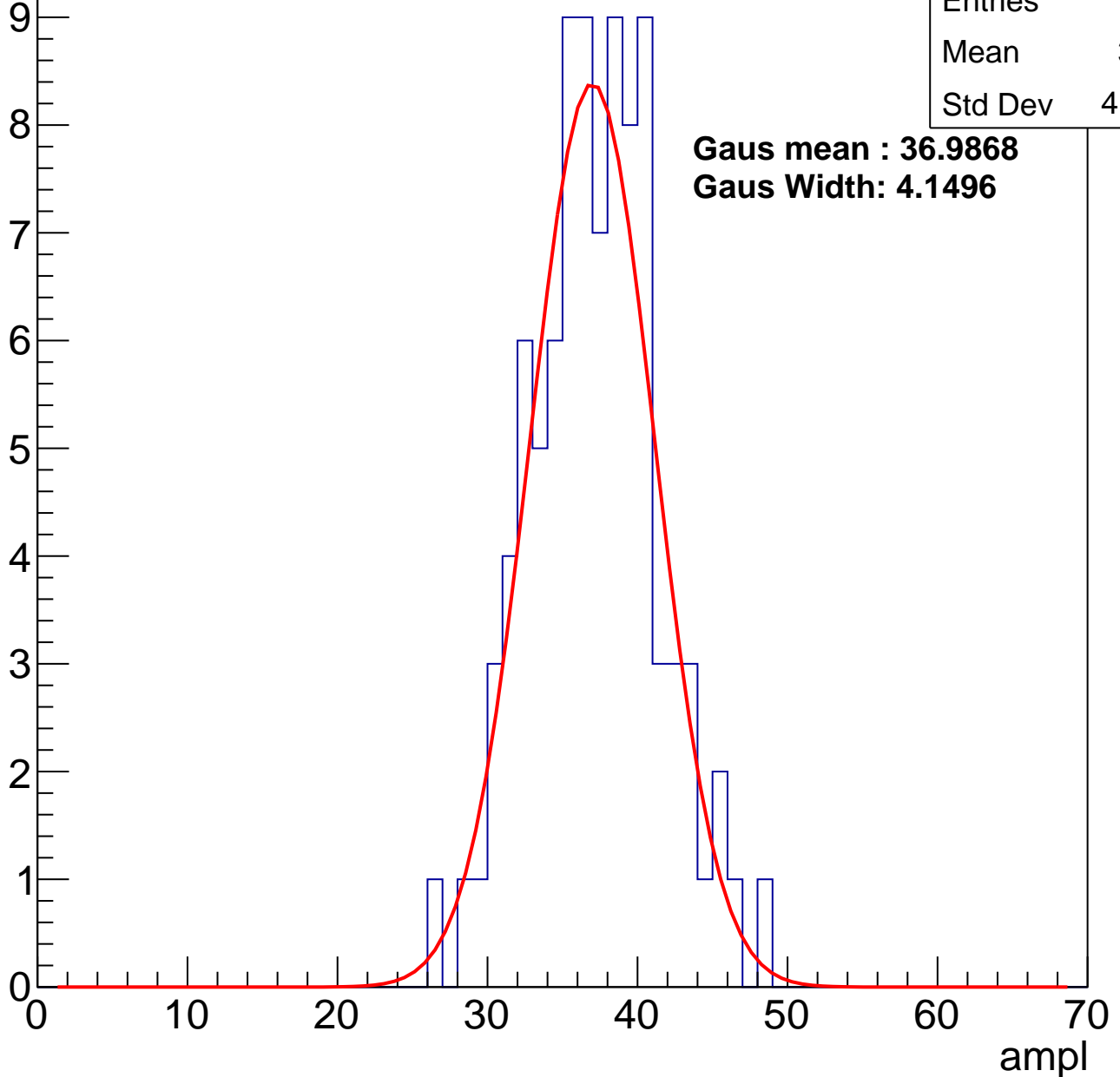
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	92
Mean	36.7
Std Dev	4.183

**Gaus mean : 36.9868**

**Gaus Width: 4.1496**



# B0L000S, U10-ch62, adc2

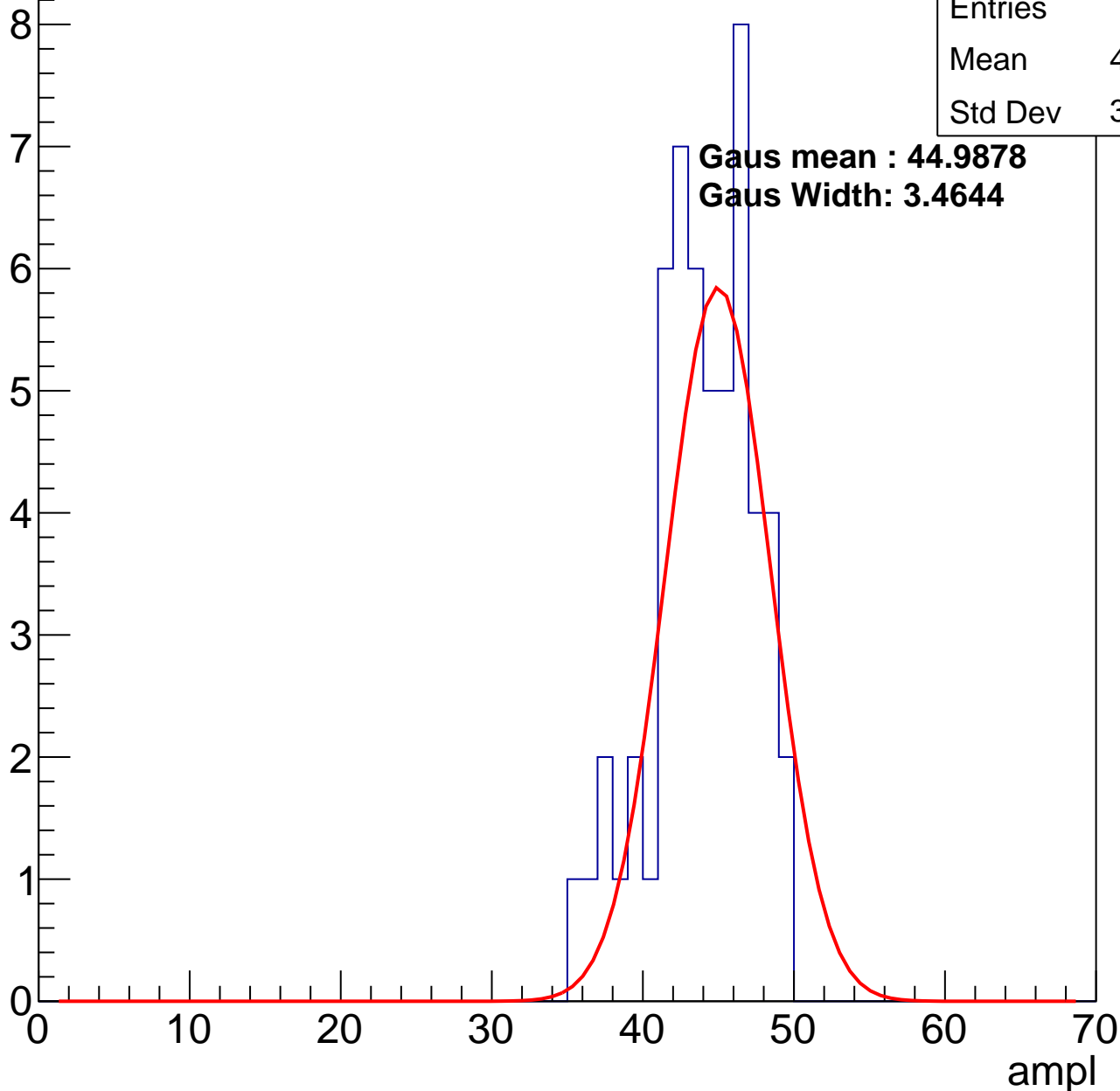
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	43.45
Std Dev	3.313

**Gaus mean : 44.9878**

**Gaus Width: 3.4644**

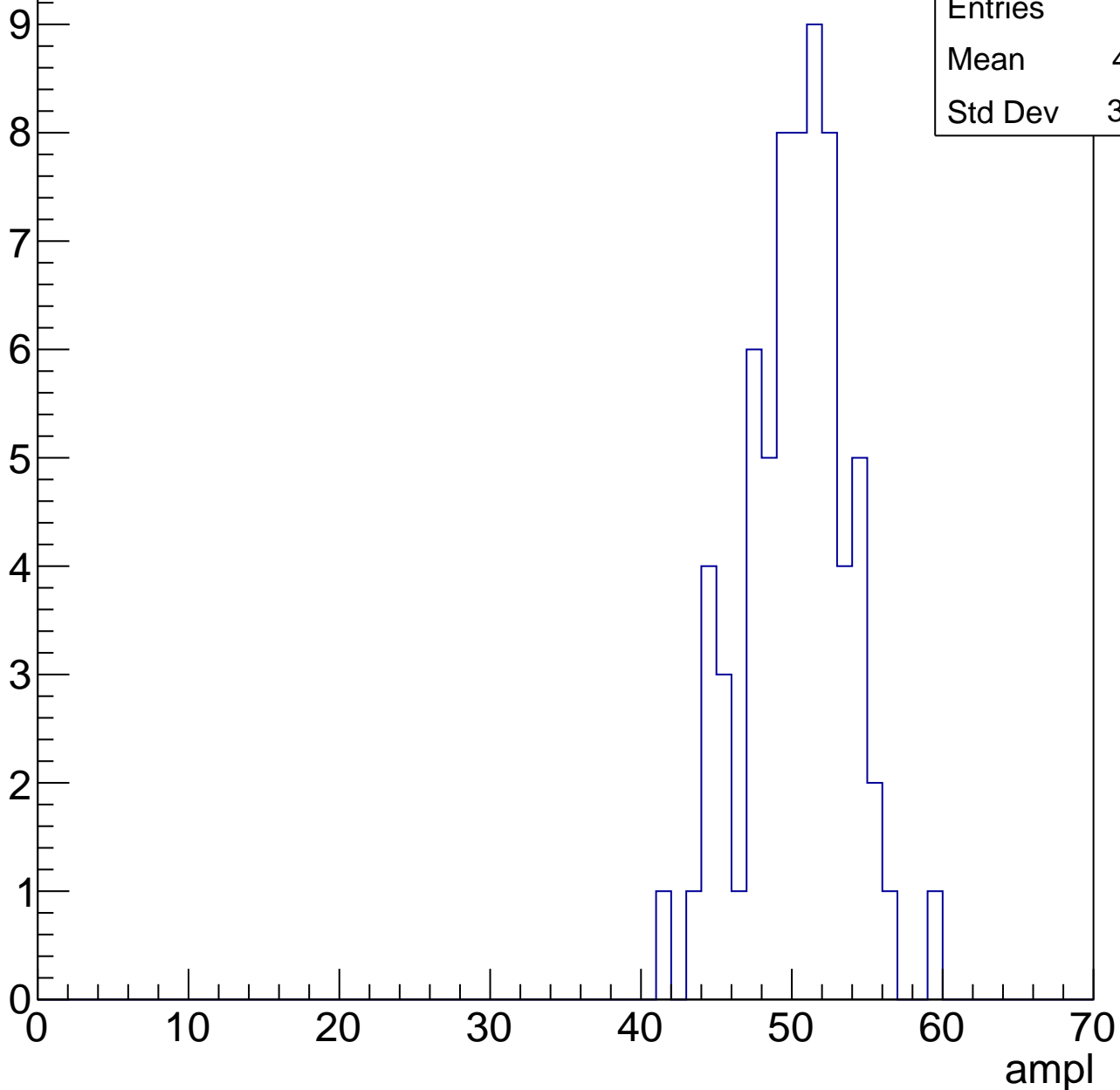


# B0L000S, U10-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.81
Std Dev	3.387

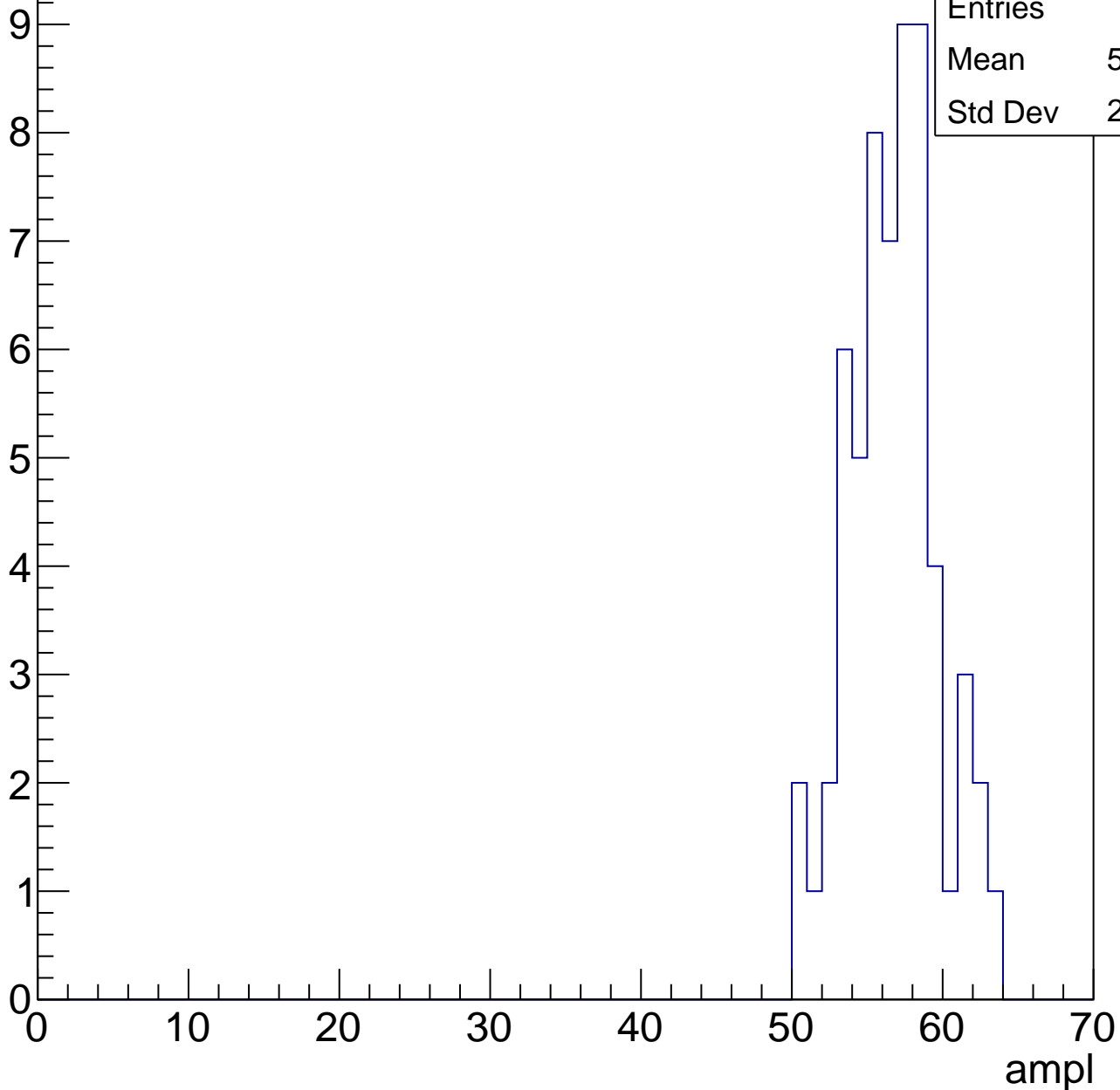


# B0L000S, U10-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

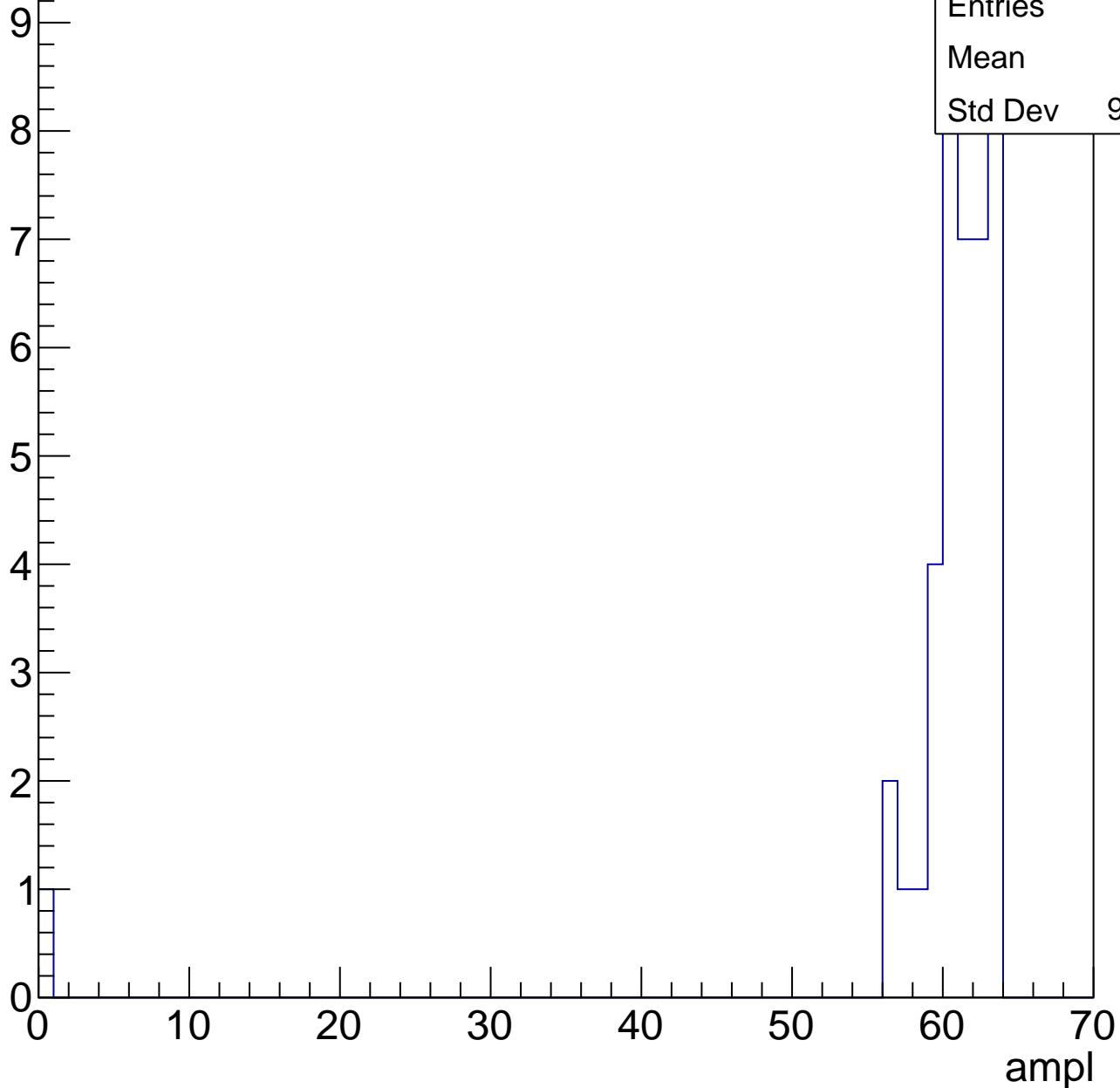
Entries	60
Mean	56.27
Std Dev	2.886



# B0L000S, U10-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

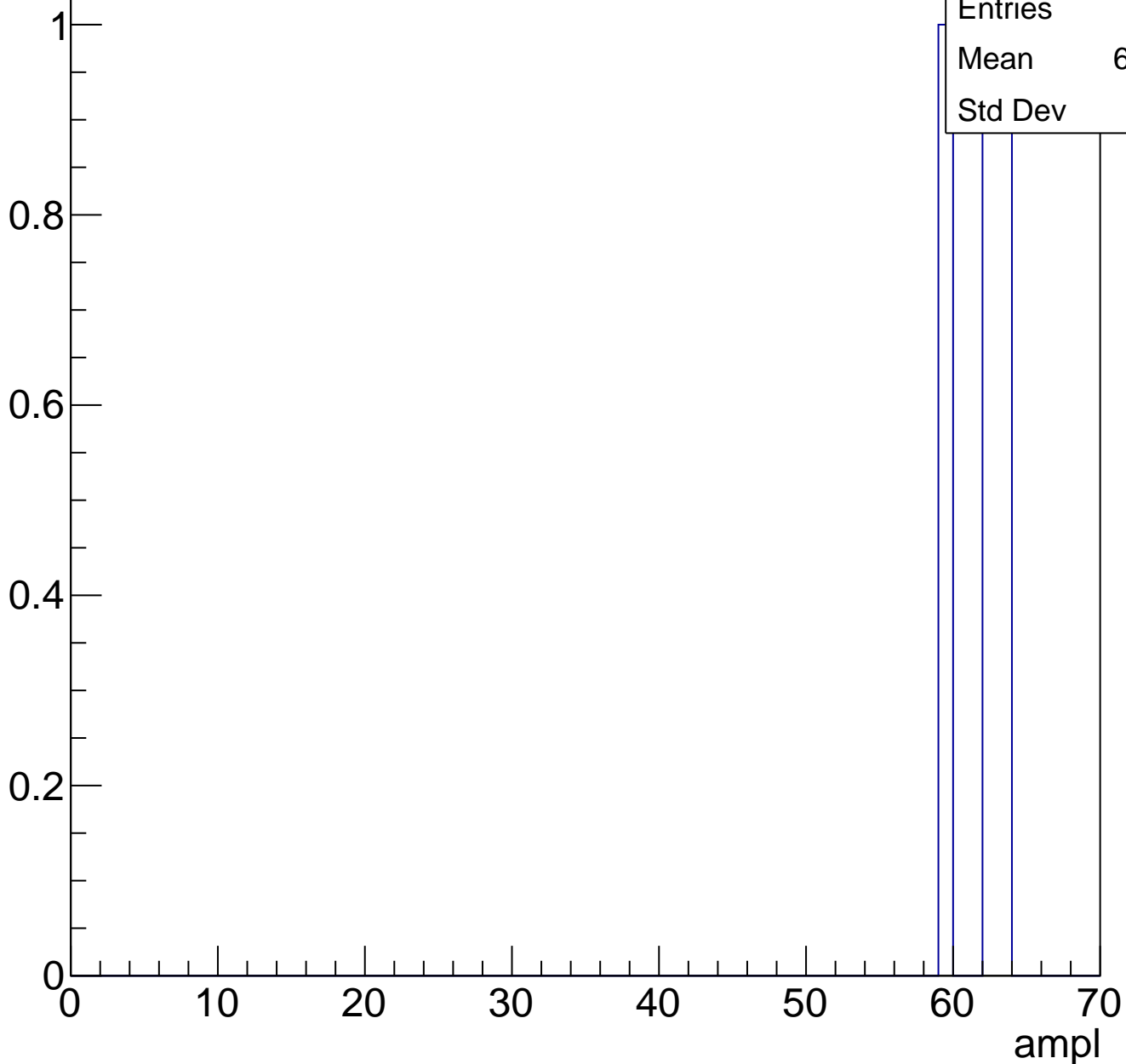
Entry



# B0L000S, U10-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch63, adc0

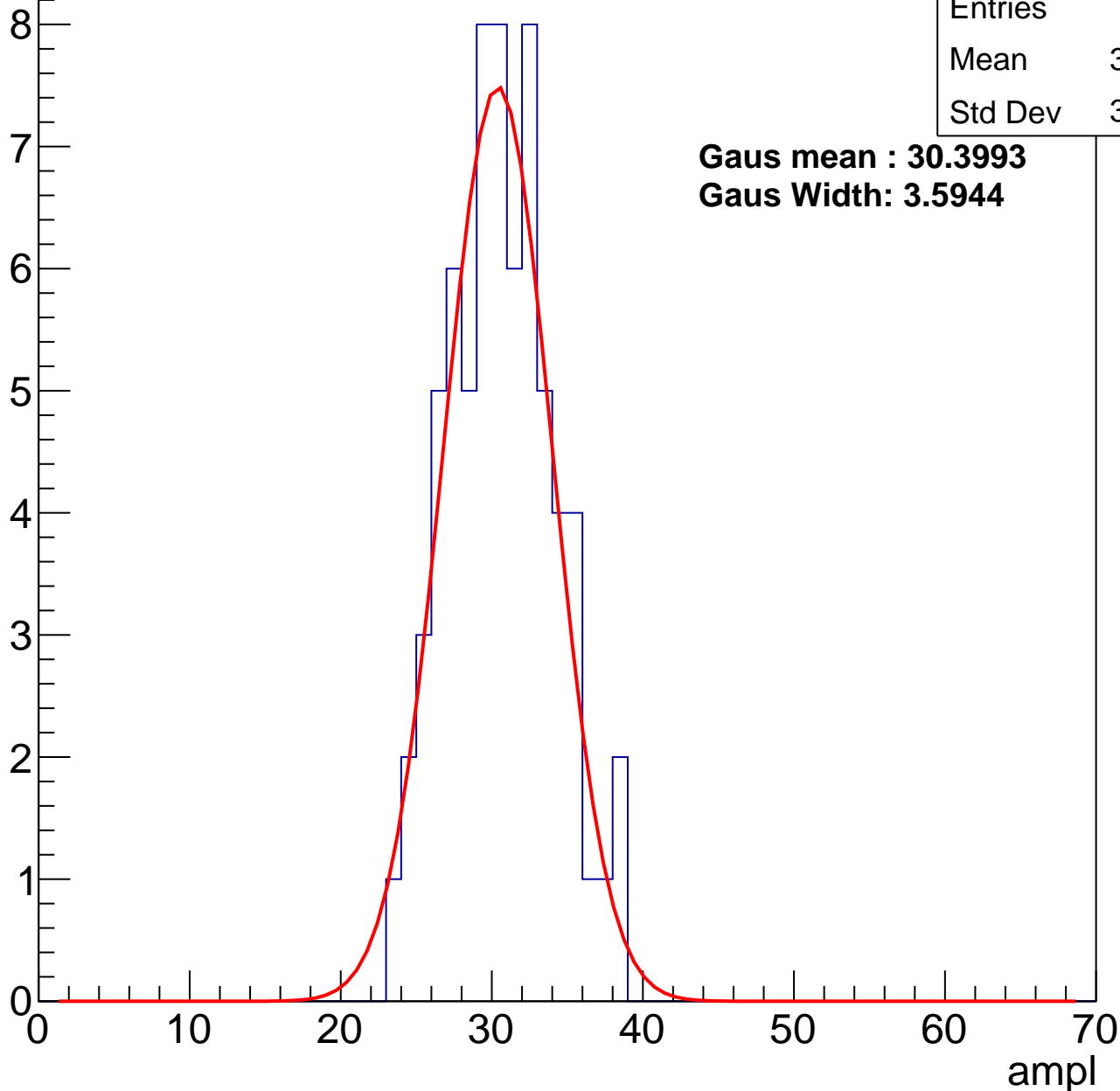
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.17
Std Dev	3.439

**Gaus mean : 30.3993**

**Gaus Width: 3.5944**



# B0L000S, U10-ch63, adc1

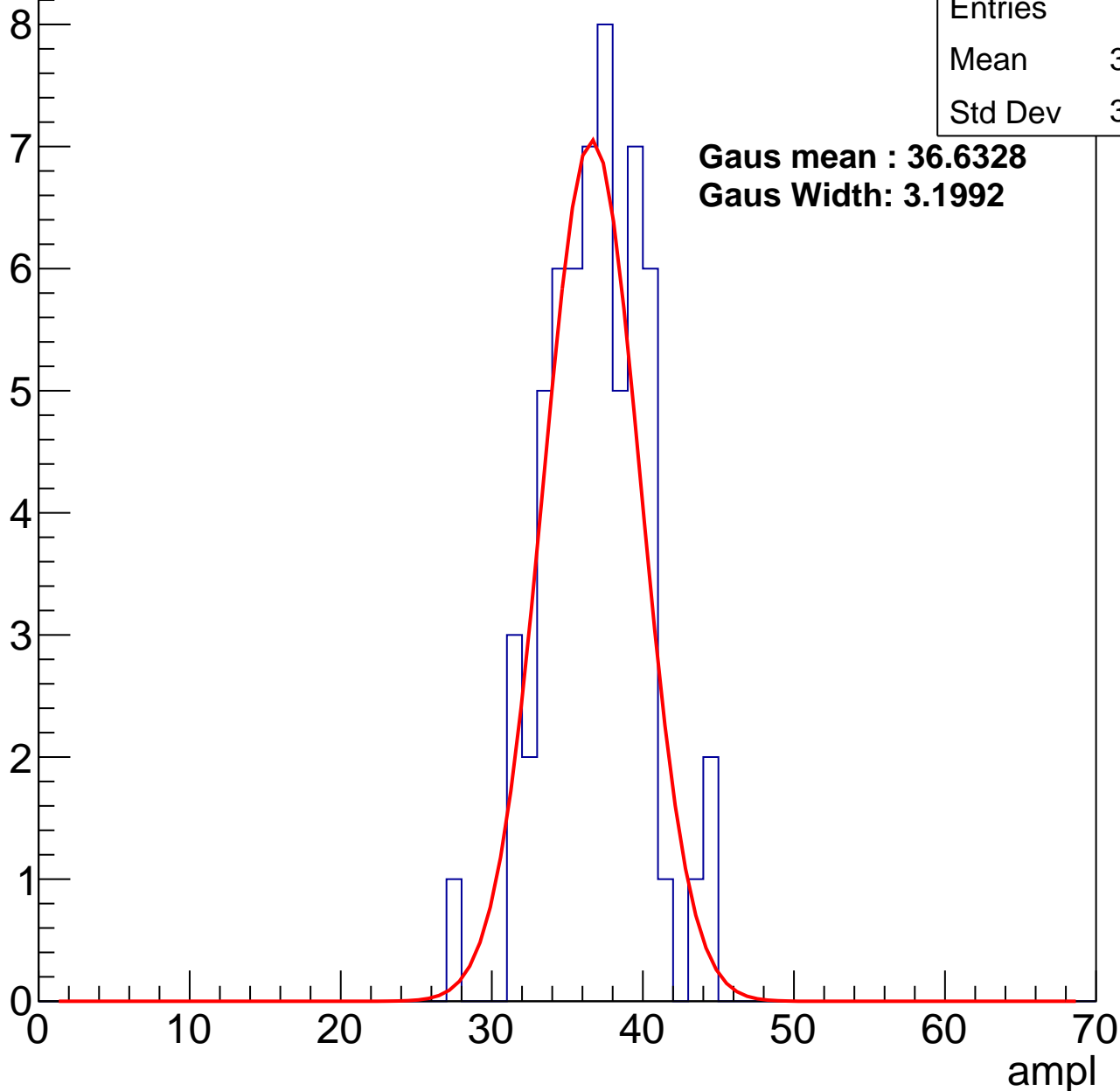
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.43
Std Dev	3.268

**Gaus mean : 36.6328**

**Gaus Width: 3.1992**



# B0L000S, U10-ch63, adc2

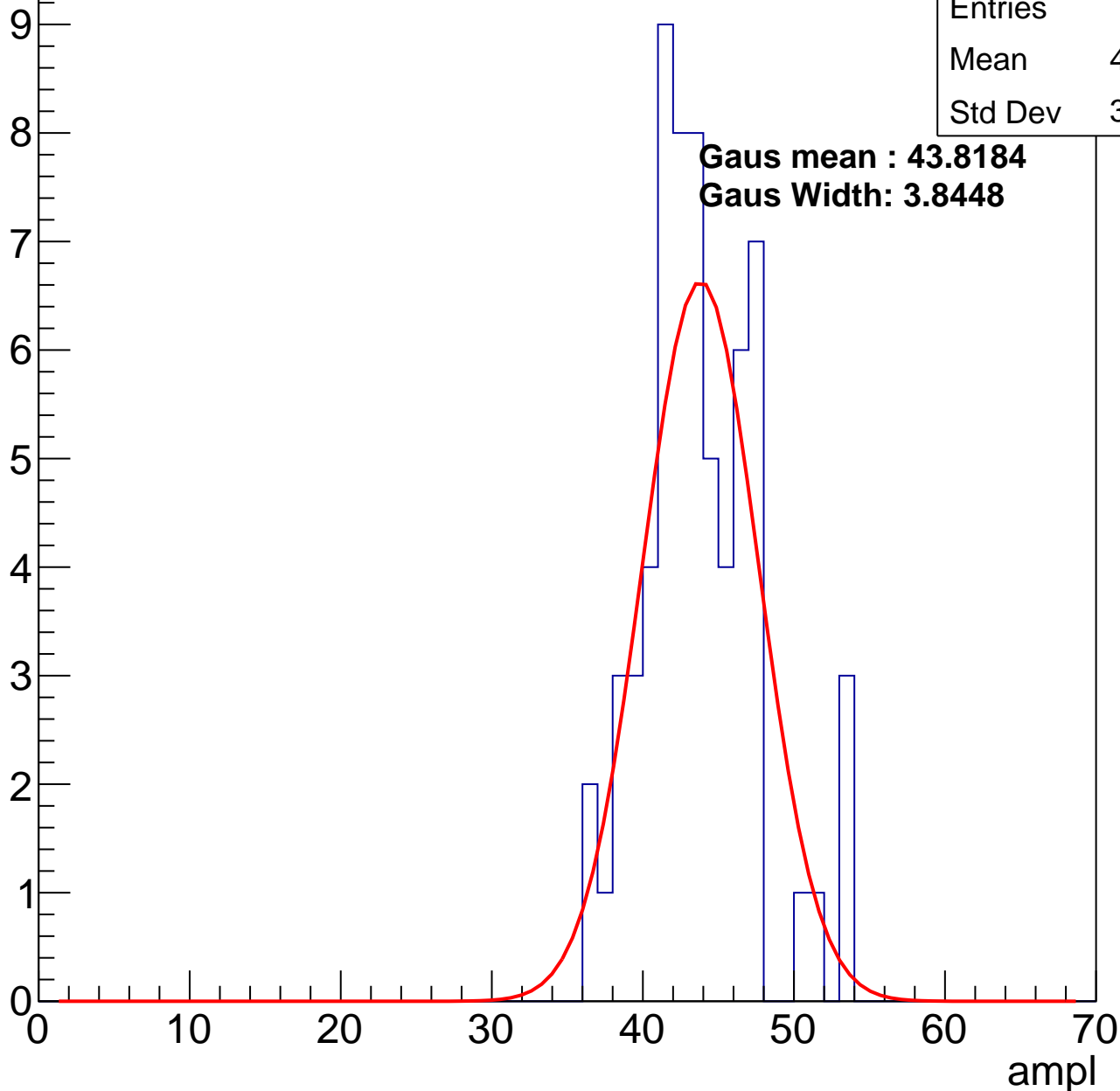
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.29
Std Dev	3.785

**Gaus mean : 43.8184**

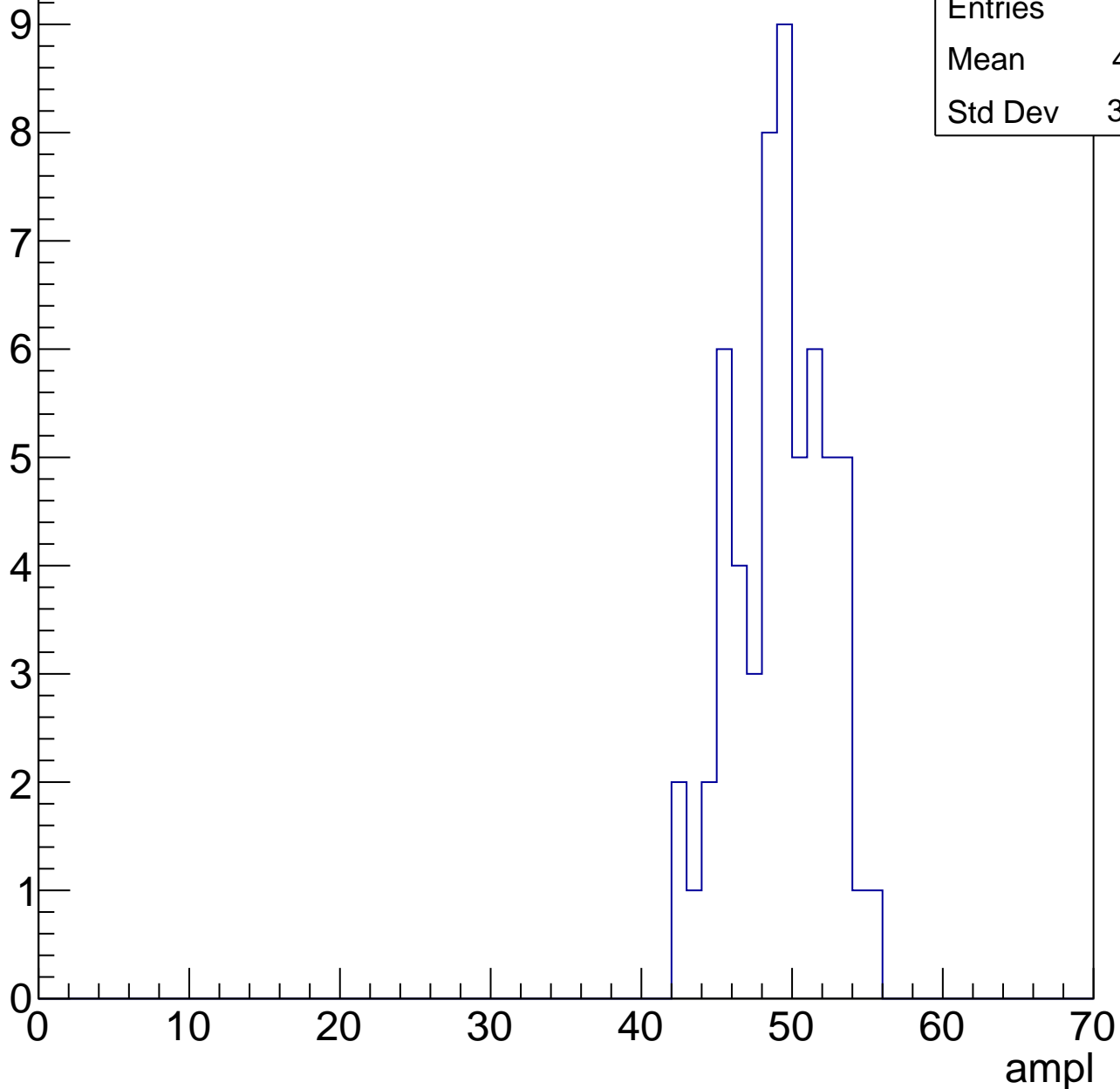
**Gaus Width: 3.8448**



# B0L000S, U10-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



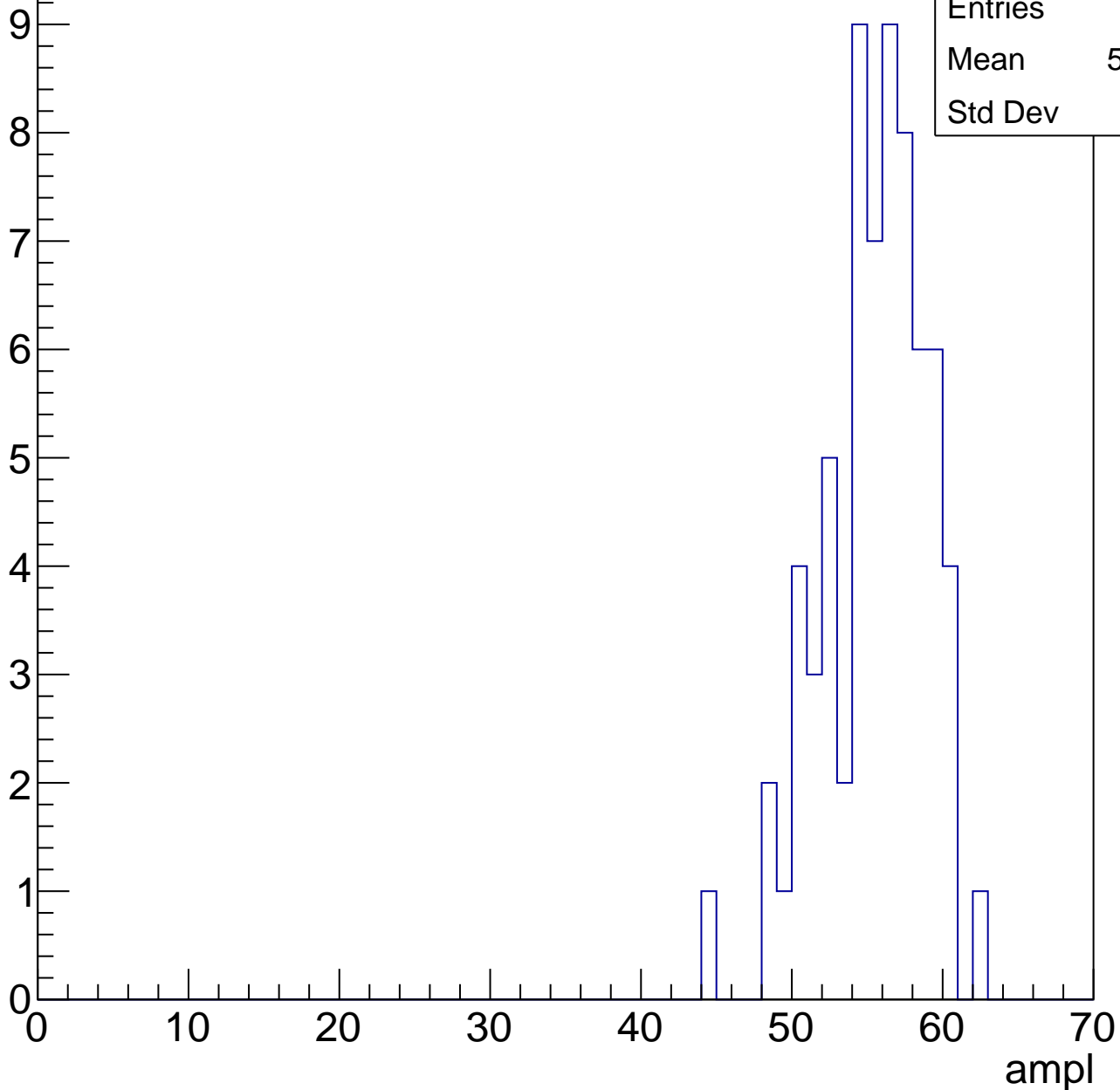
Entries	58
Mean	48.71
Std Dev	3.068

# B0L000S, U10-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	55.04
Std Dev	3.44

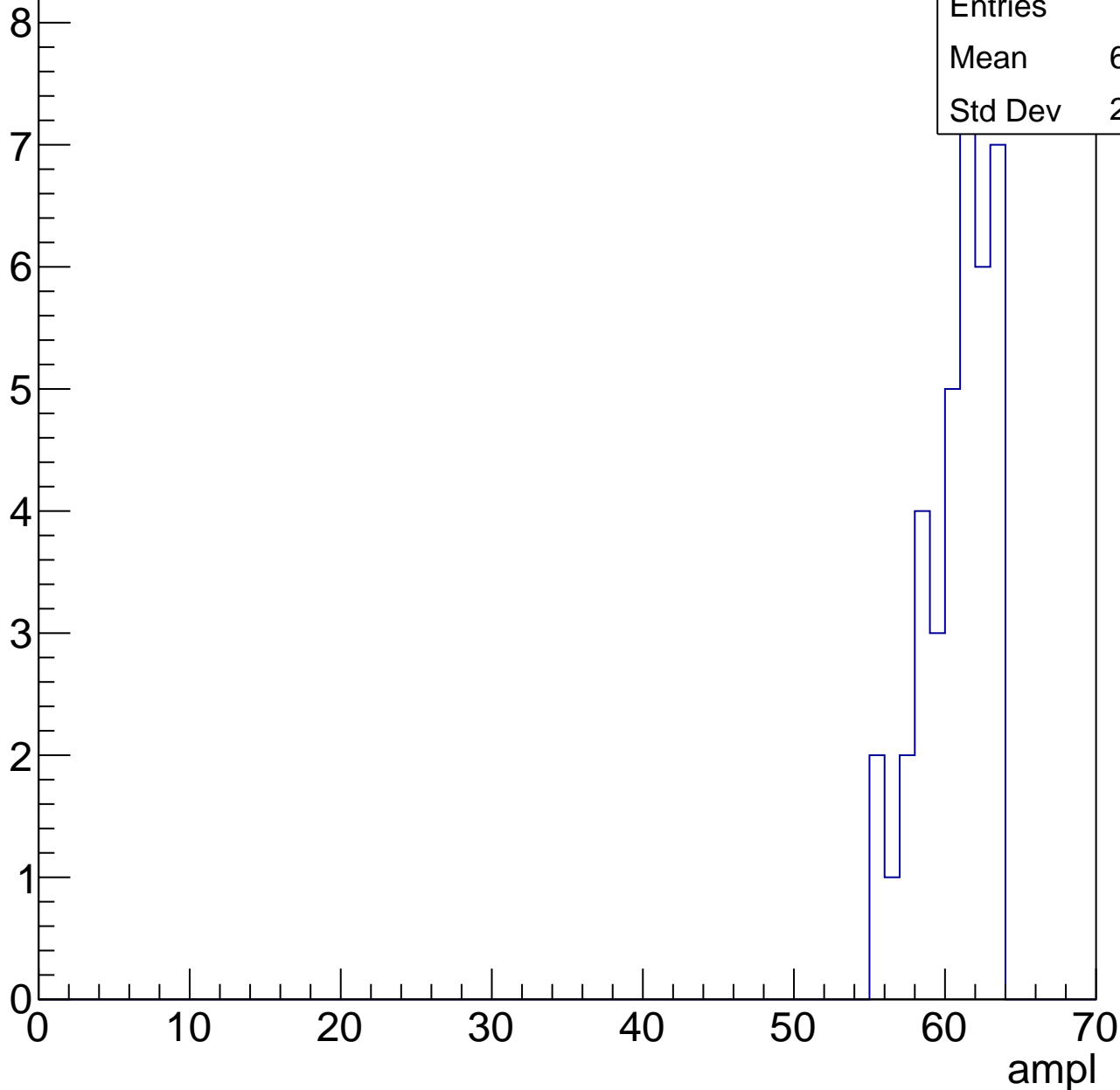


# B0L000S, U10-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

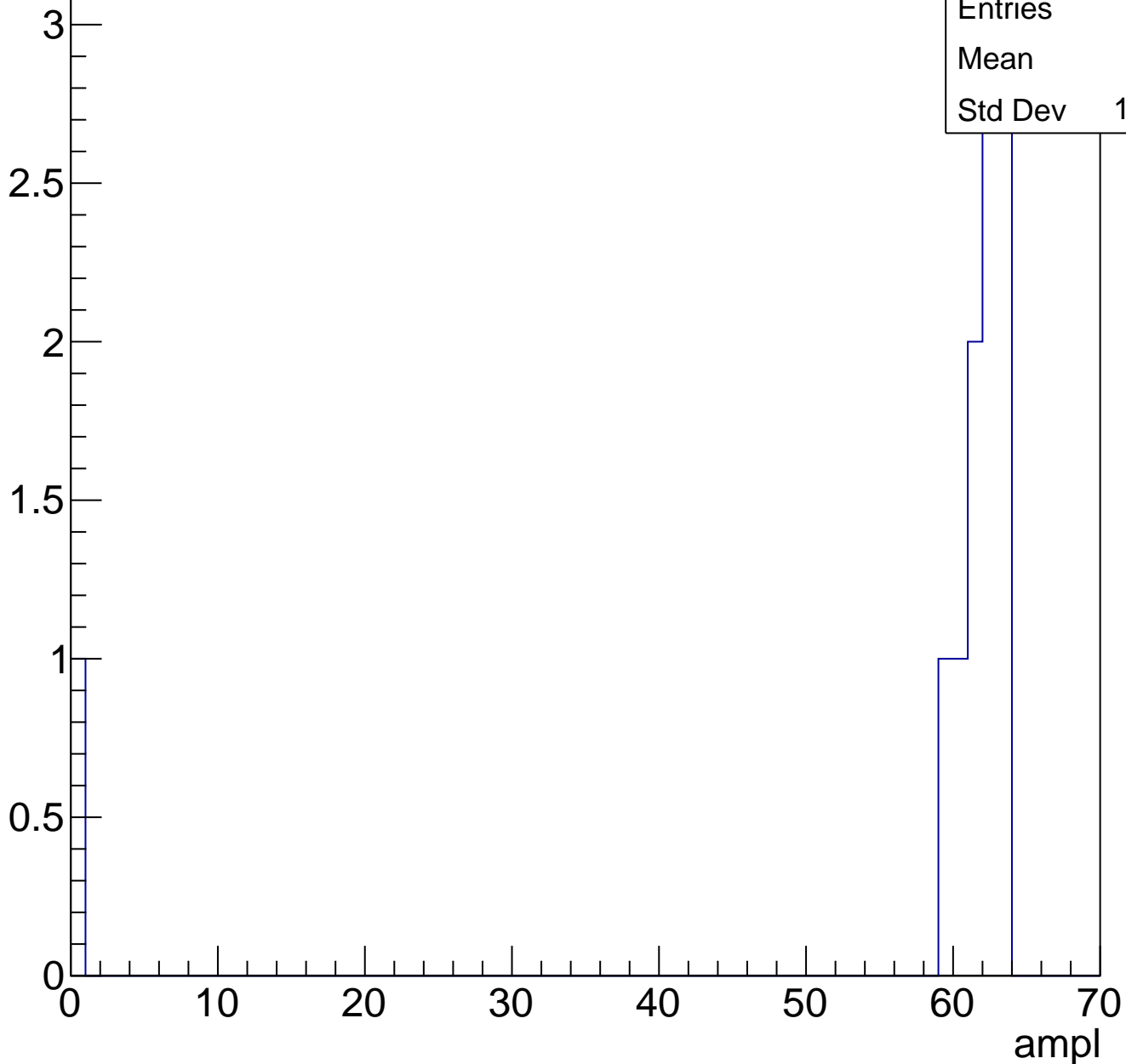
Entries	38
Mean	60.26
Std Dev	2.267



# B0L000S, U10-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

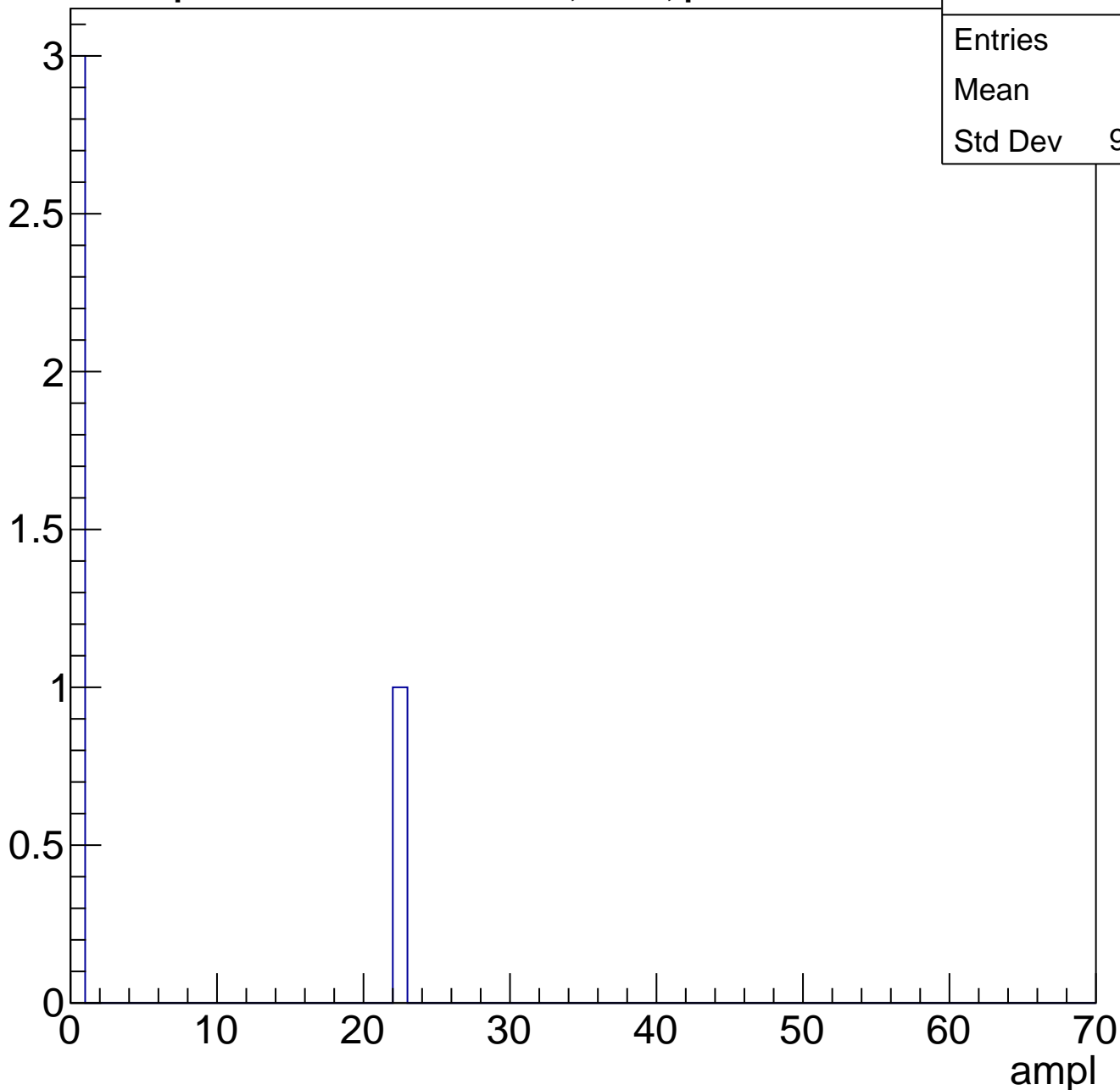




# B0L000S, U10-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch64, adc0

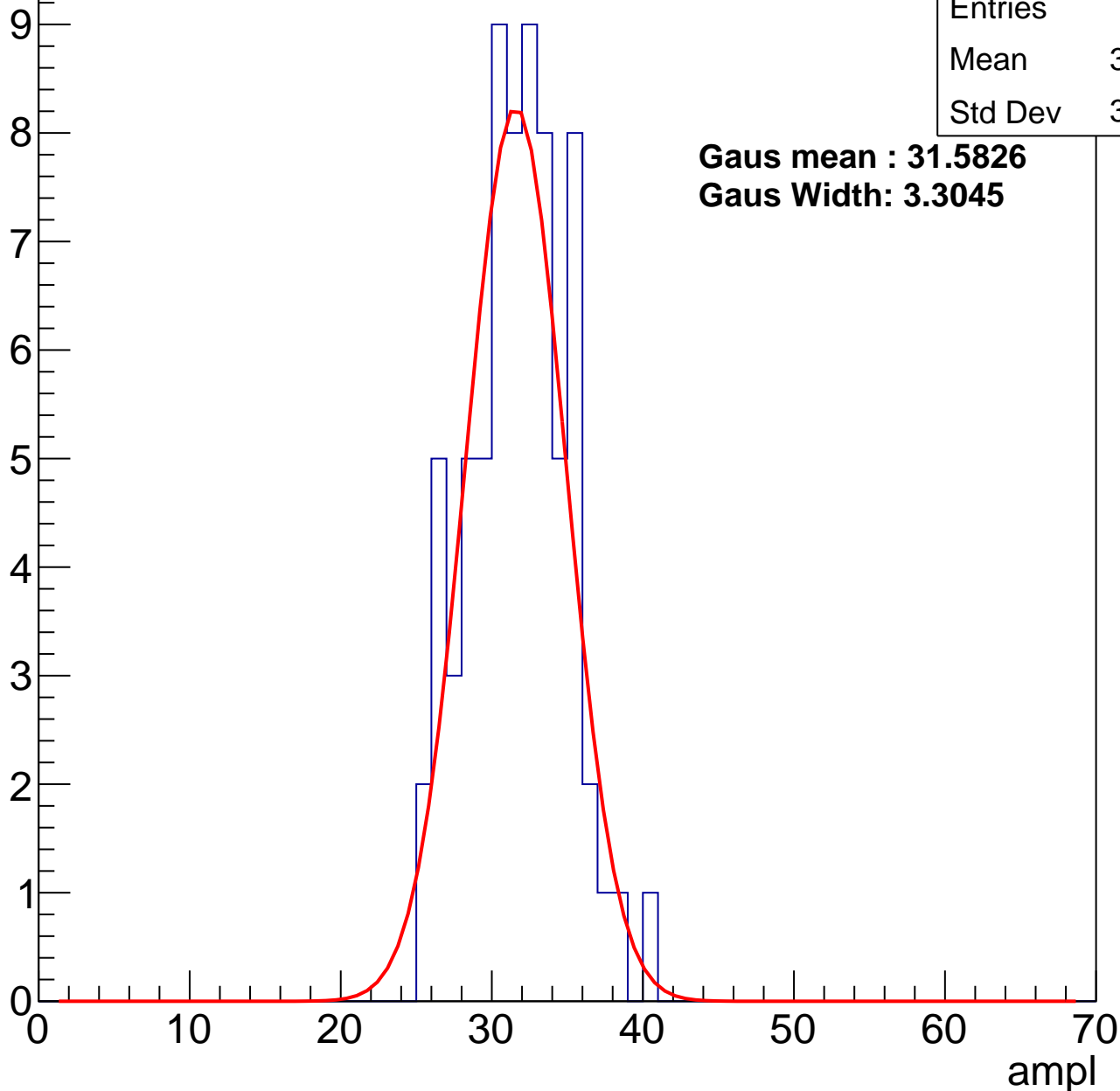
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	31.29
Std Dev	3.212

**Gaus mean : 31.5826**

**Gaus Width: 3.3045**



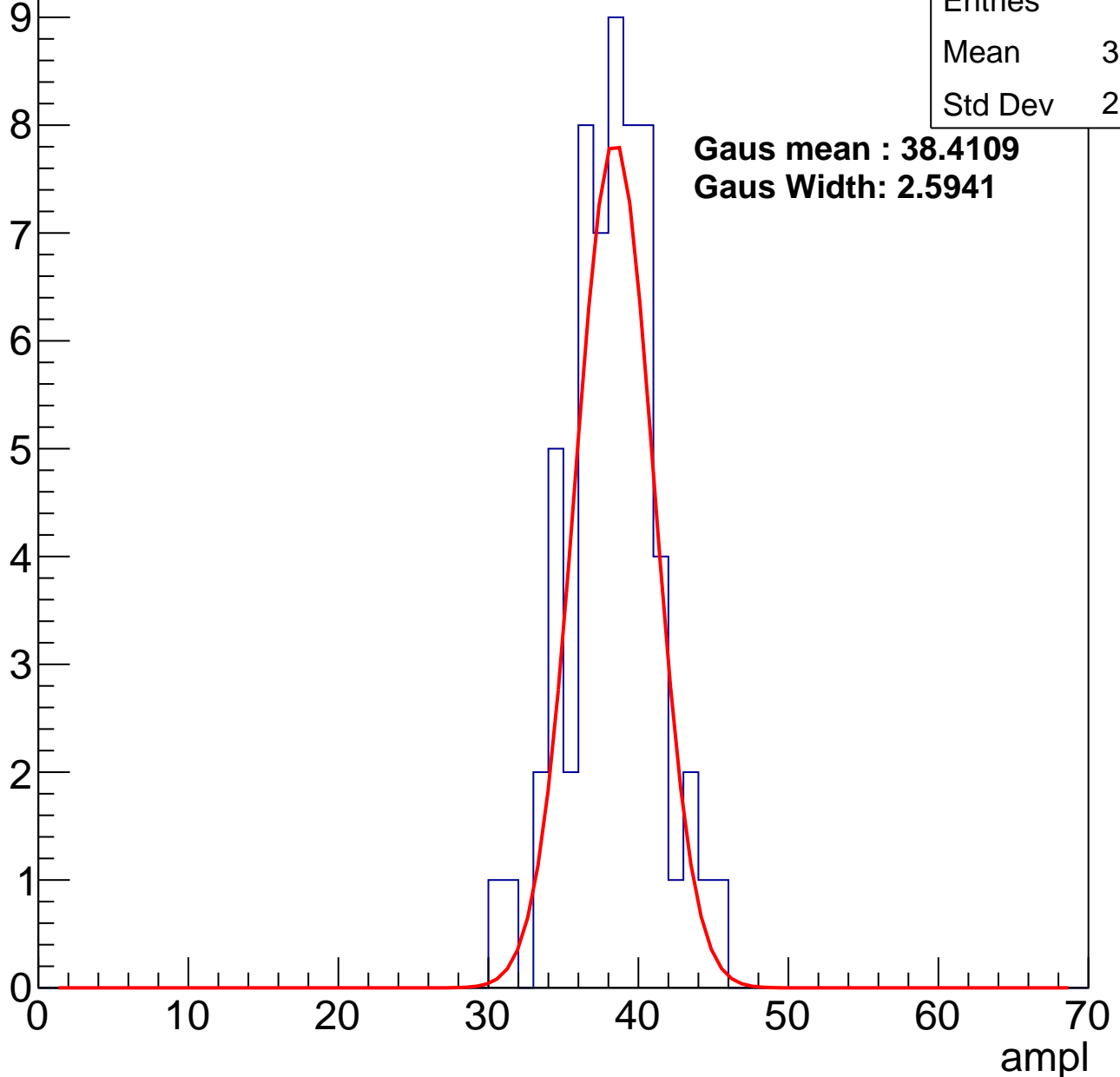
# B0L000S, U10-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	37.82
Std Dev	2.958

**Gaus mean : 38.4109**  
**Gaus Width: 2.5941**



# B0L000S, U10-ch64, adc2

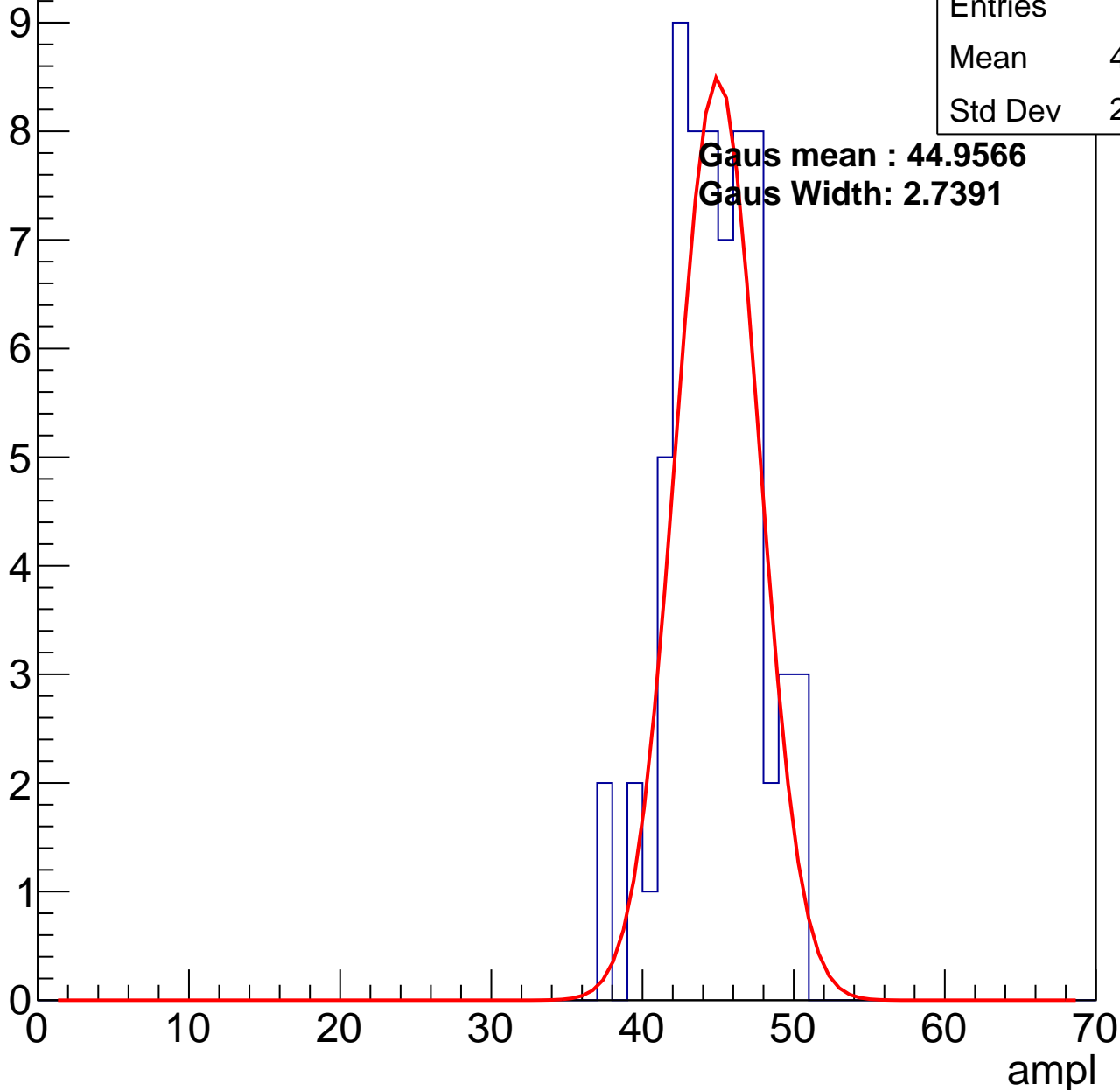
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.29
Std Dev	2.948

**Gaus mean : 44.9566**

**Gaus Width: 2.7391**

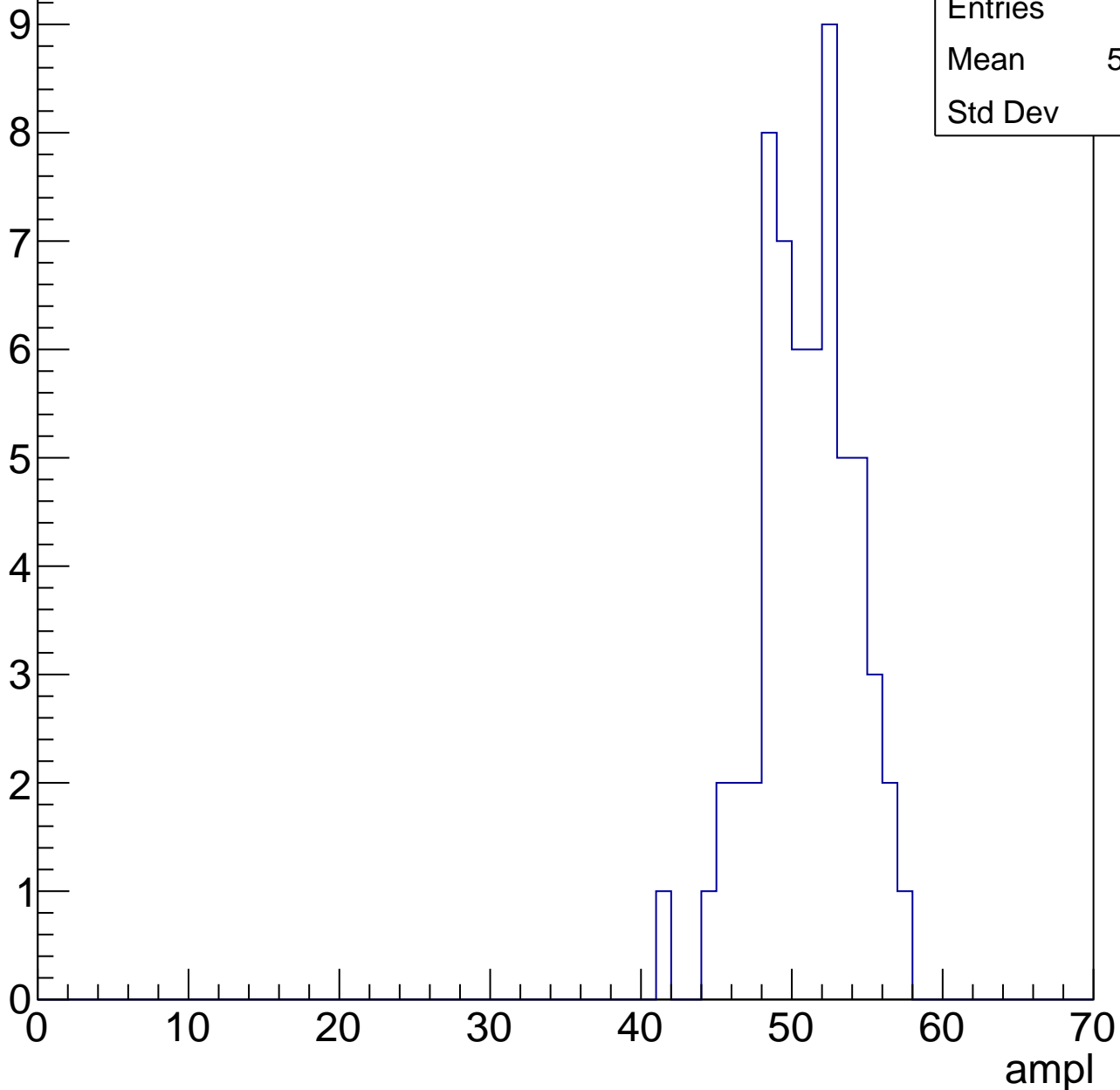


# B0L000S, U10-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.52
Std Dev	3.17

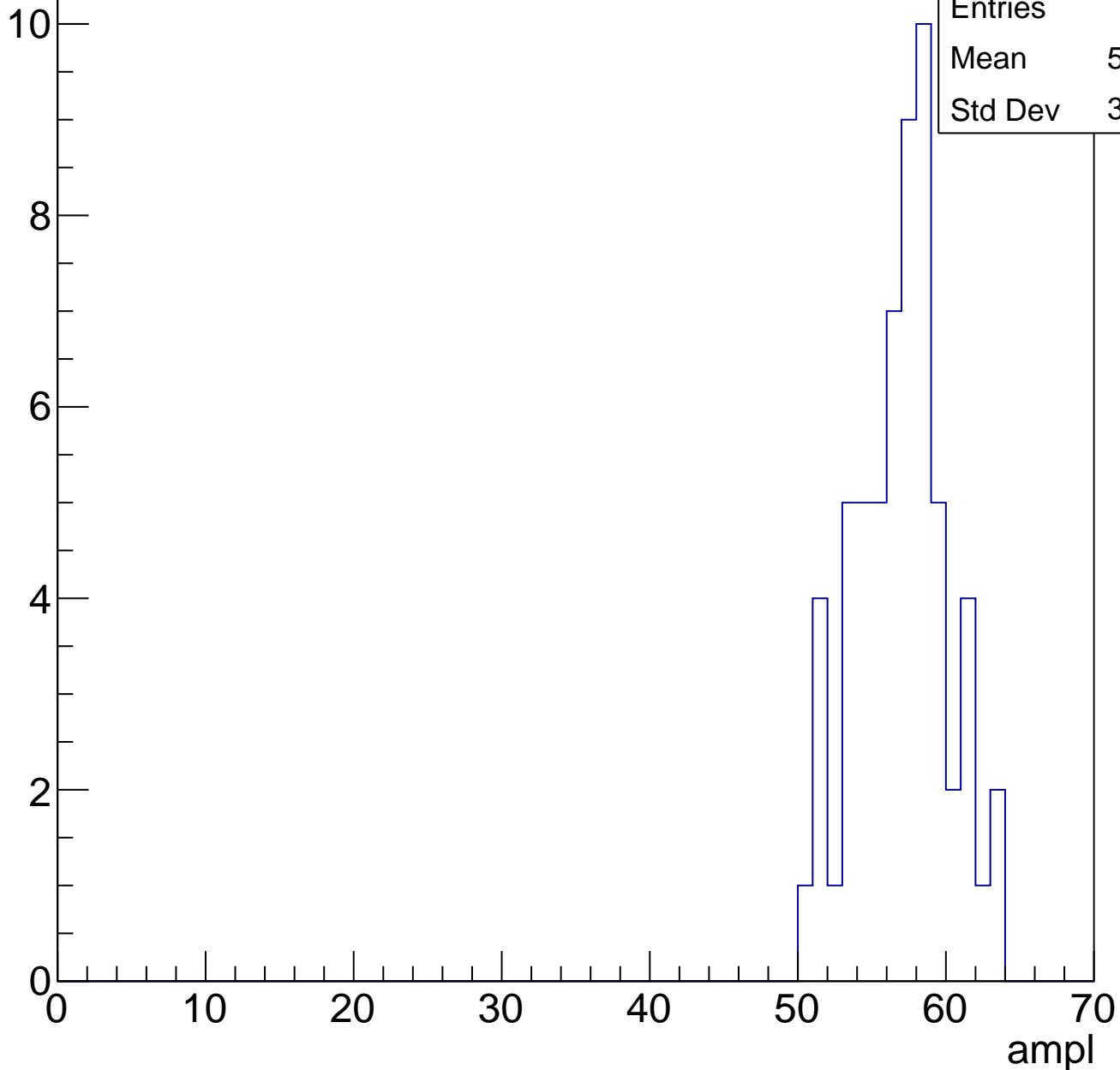


# B0L000S, U10-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	61
Mean	56.52
Std Dev	3.044

Entry

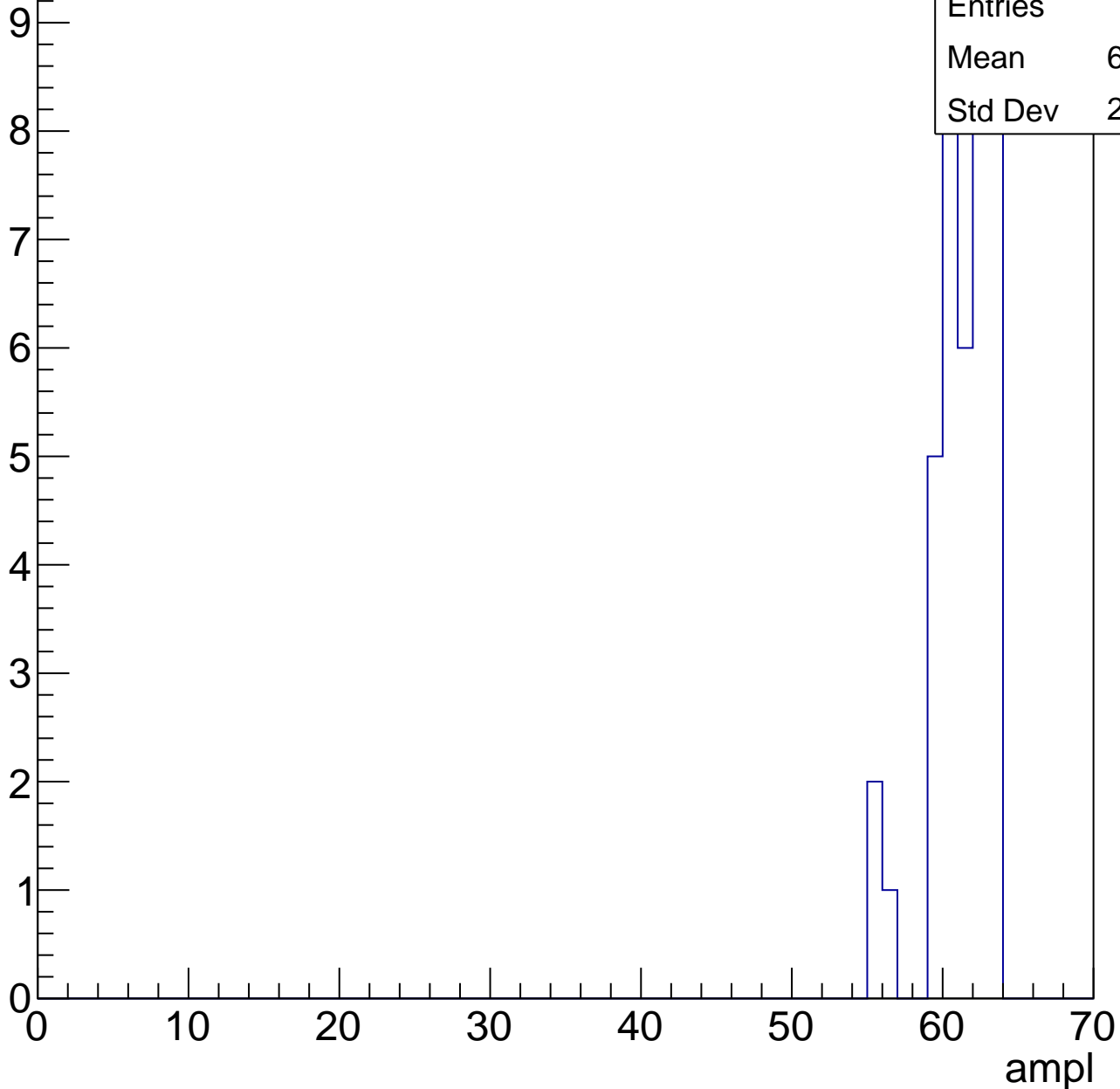


# B0L000S, U10-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	60.74
Std Dev	2.047



# B0L000S, U10-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

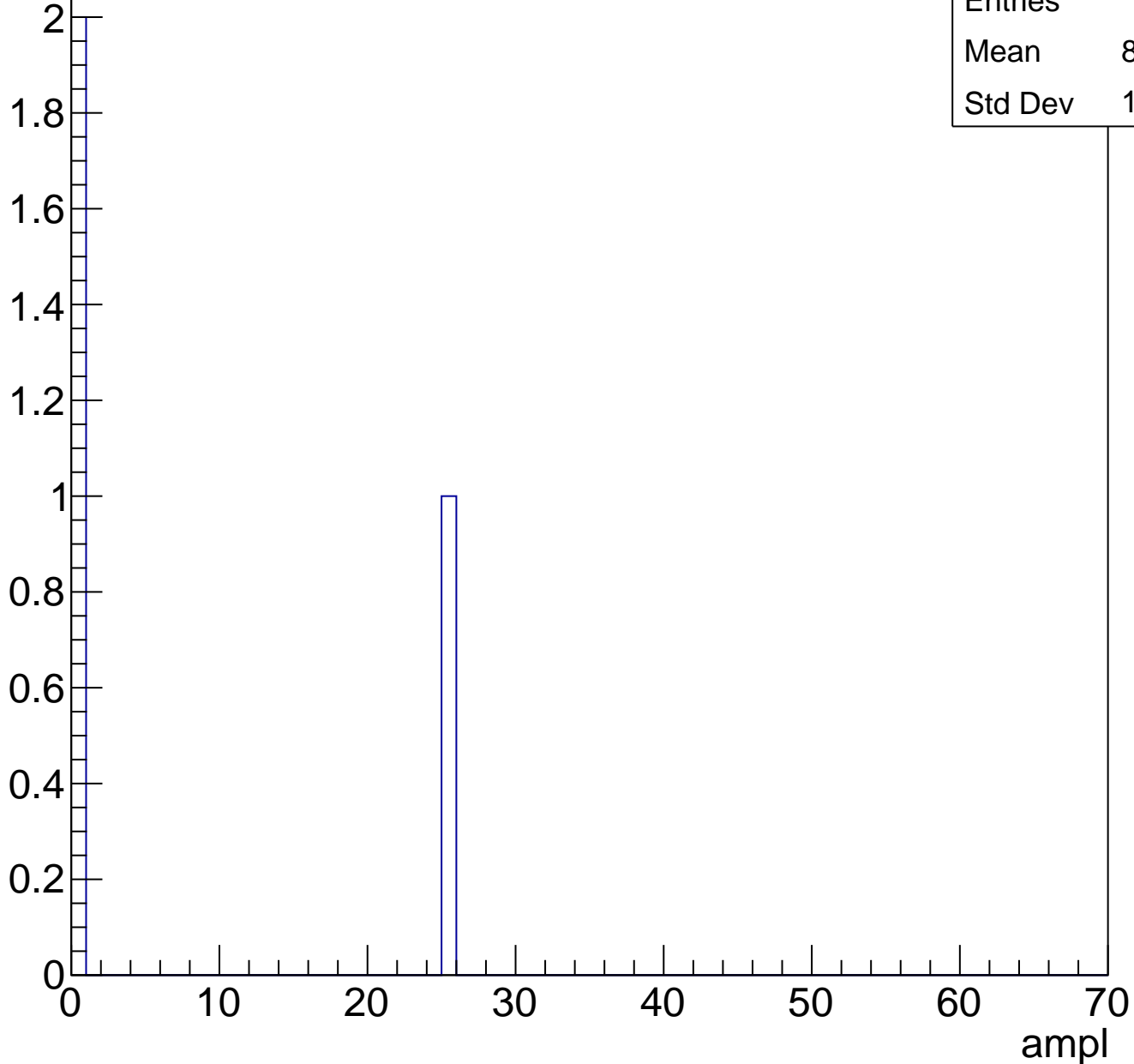




# B0L000S, U10-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

# B0L000S, U10-ch65, adc0

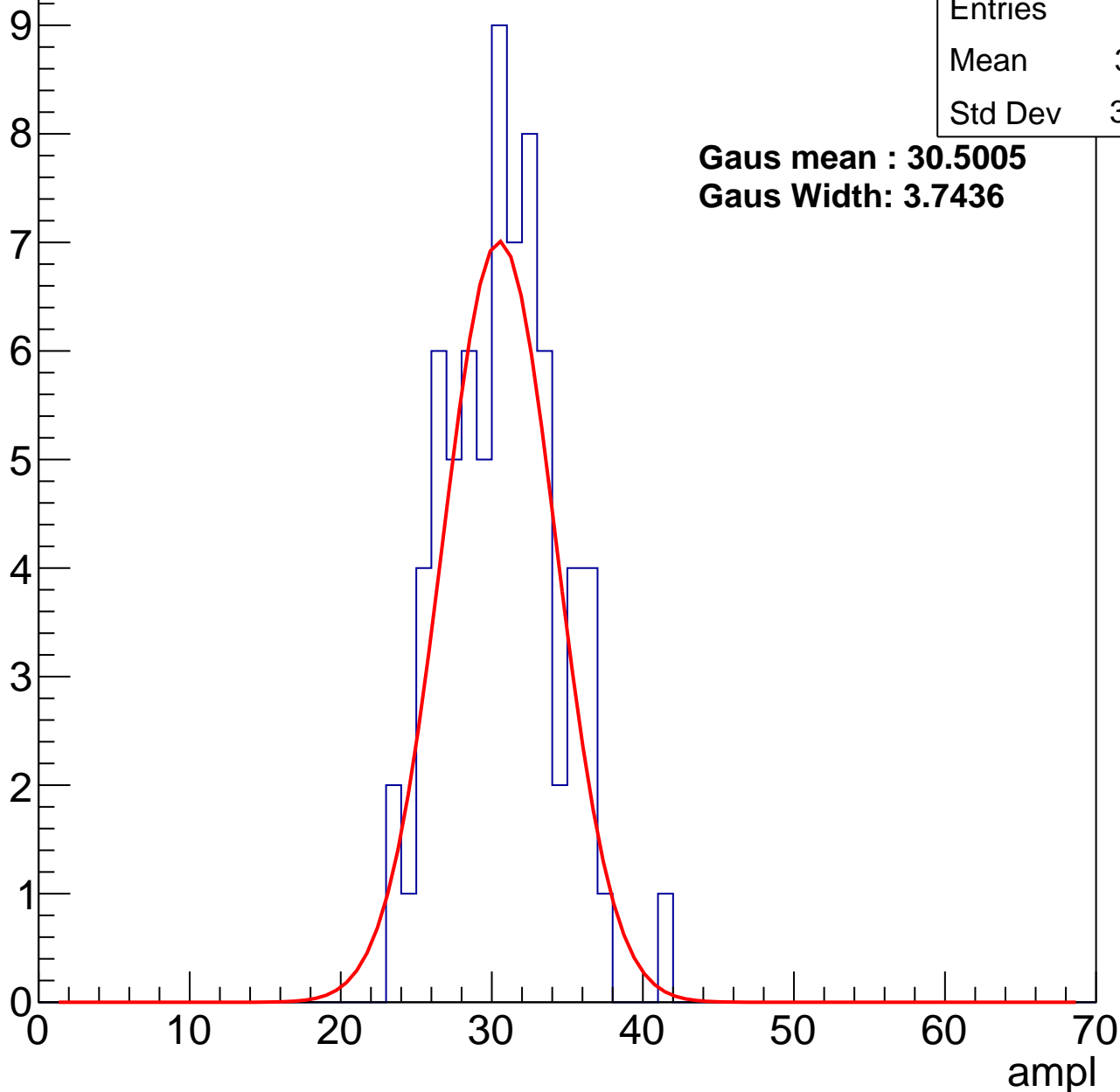
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.21
Std Dev	3.638

**Gaus mean : 30.5005**

**Gaus Width: 3.7436**



# B0L000S, U10-ch65, adc1

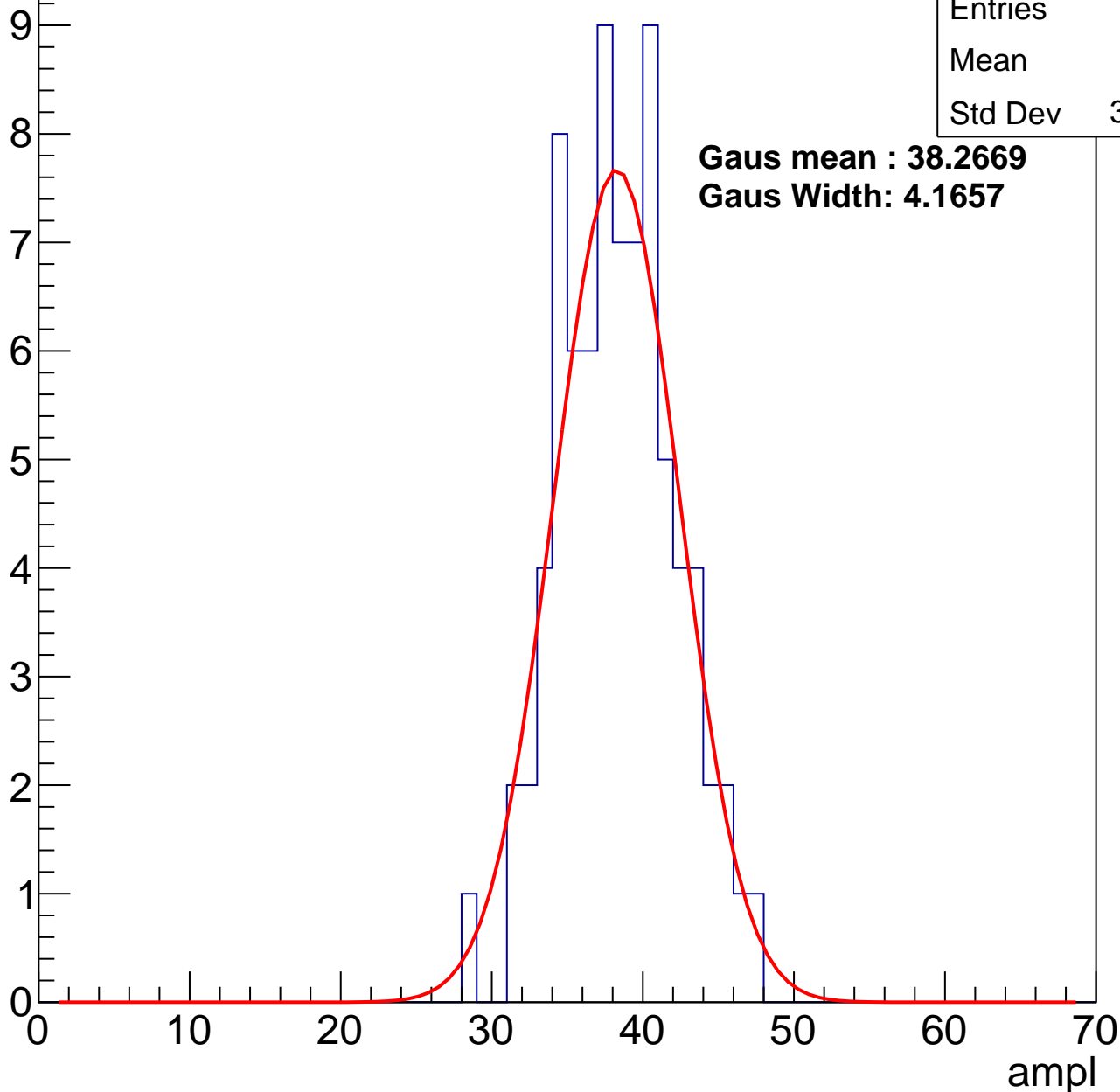
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	37.9
Std Dev	3.793

**Gaus mean : 38.2669**

**Gaus Width: 4.1657**



# B0L000S, U10-ch65, adc2

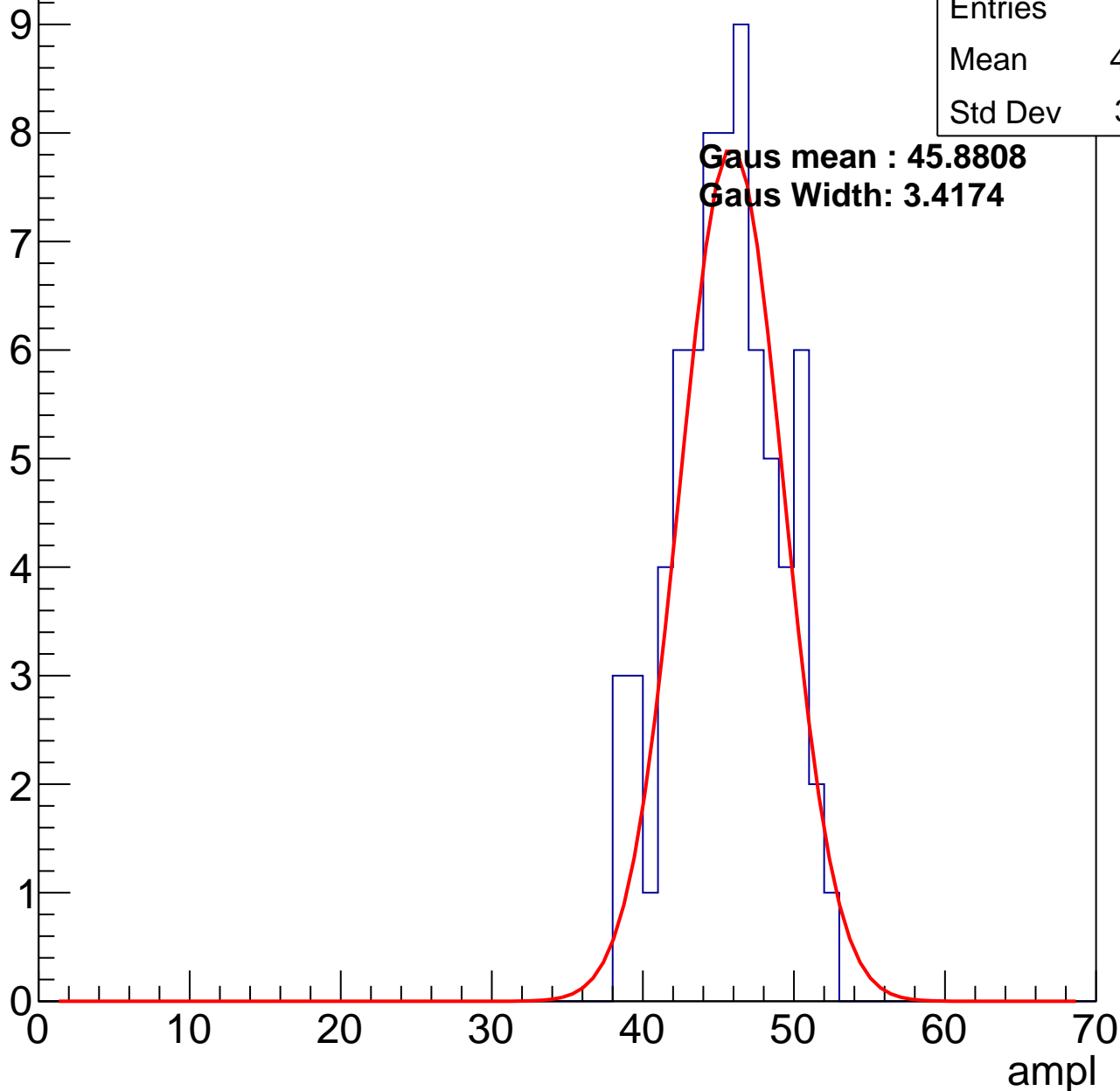
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	45.04
Std Dev	3.421

**Gaus mean : 45.8808**

**Gaus Width: 3.4174**

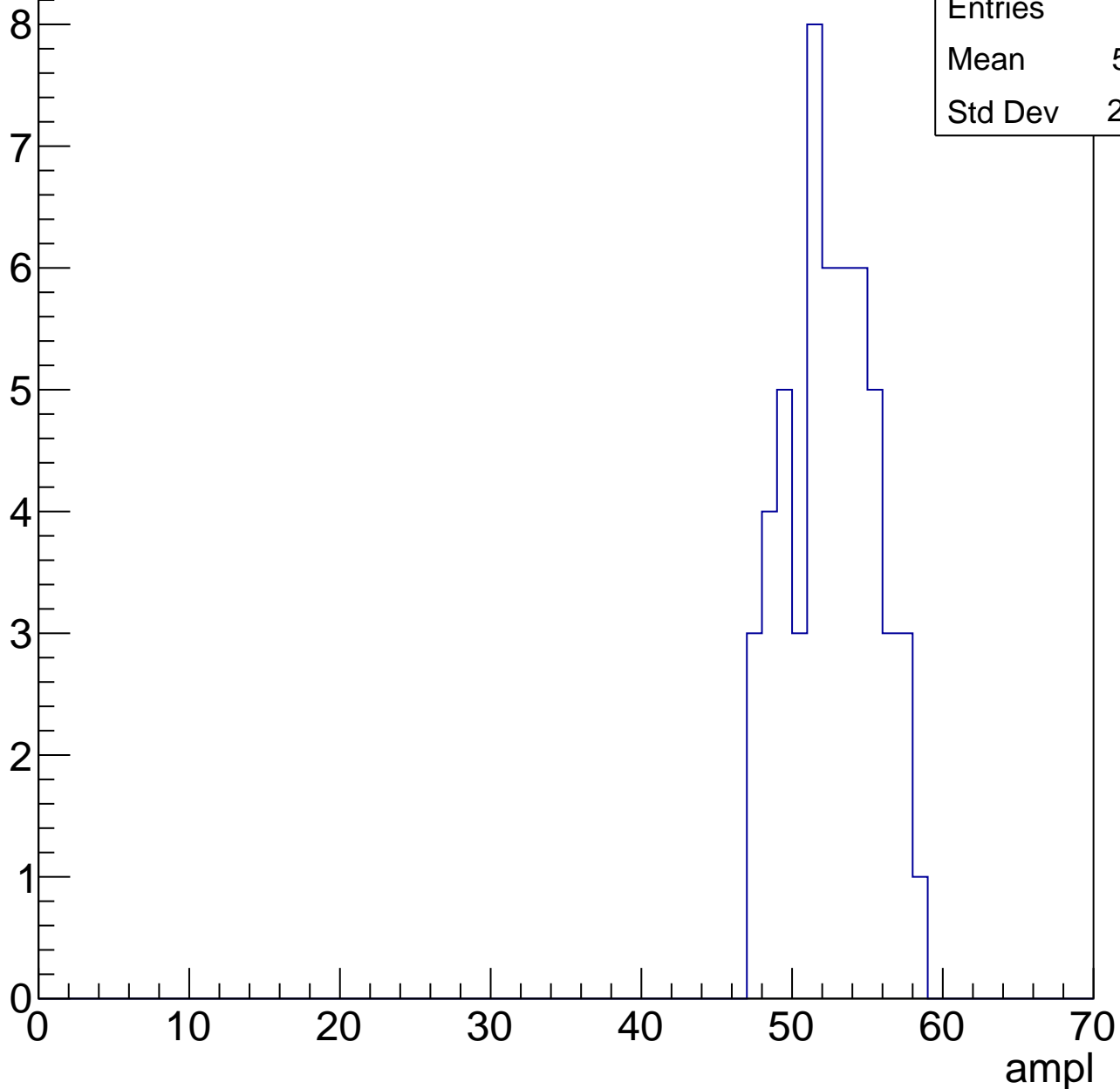


# B0L000S, U10-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	52.11
Std Dev	2.873

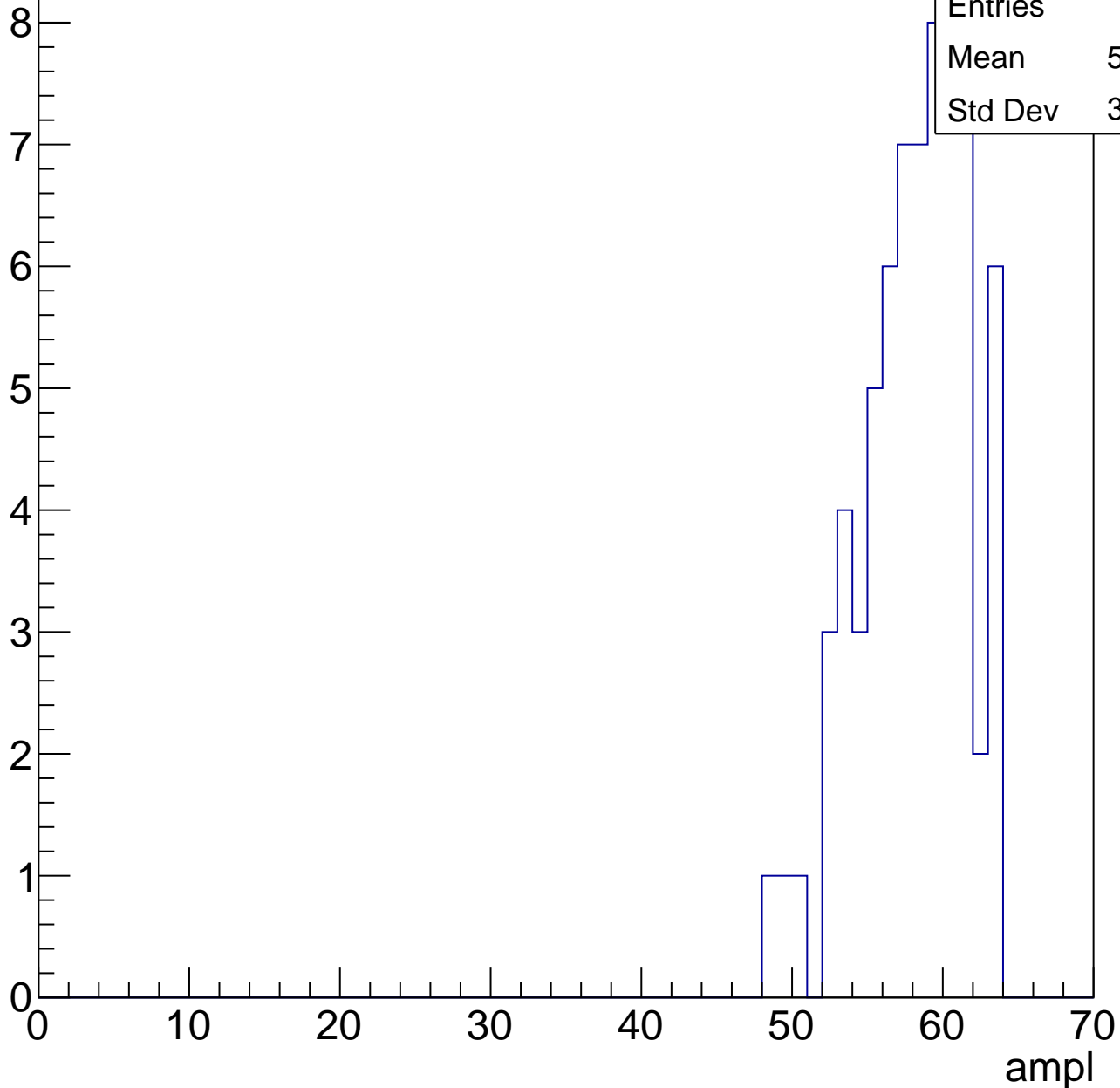


# B0L000S, U10-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	57.64
Std Dev	3.509

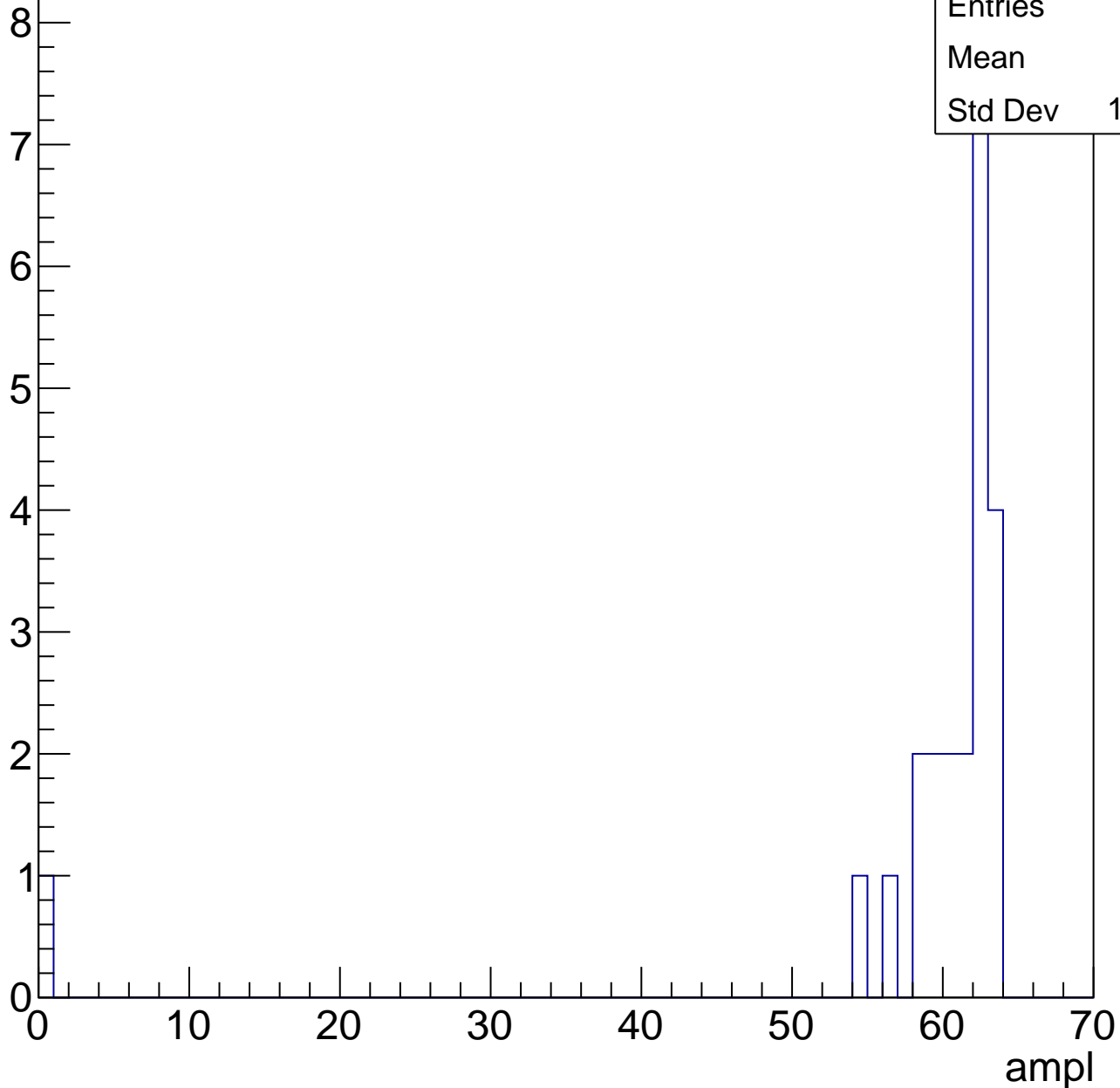


# B0L000S, U10-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58
Std Dev	12.58



# B0L000S, U10-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch66, adc0

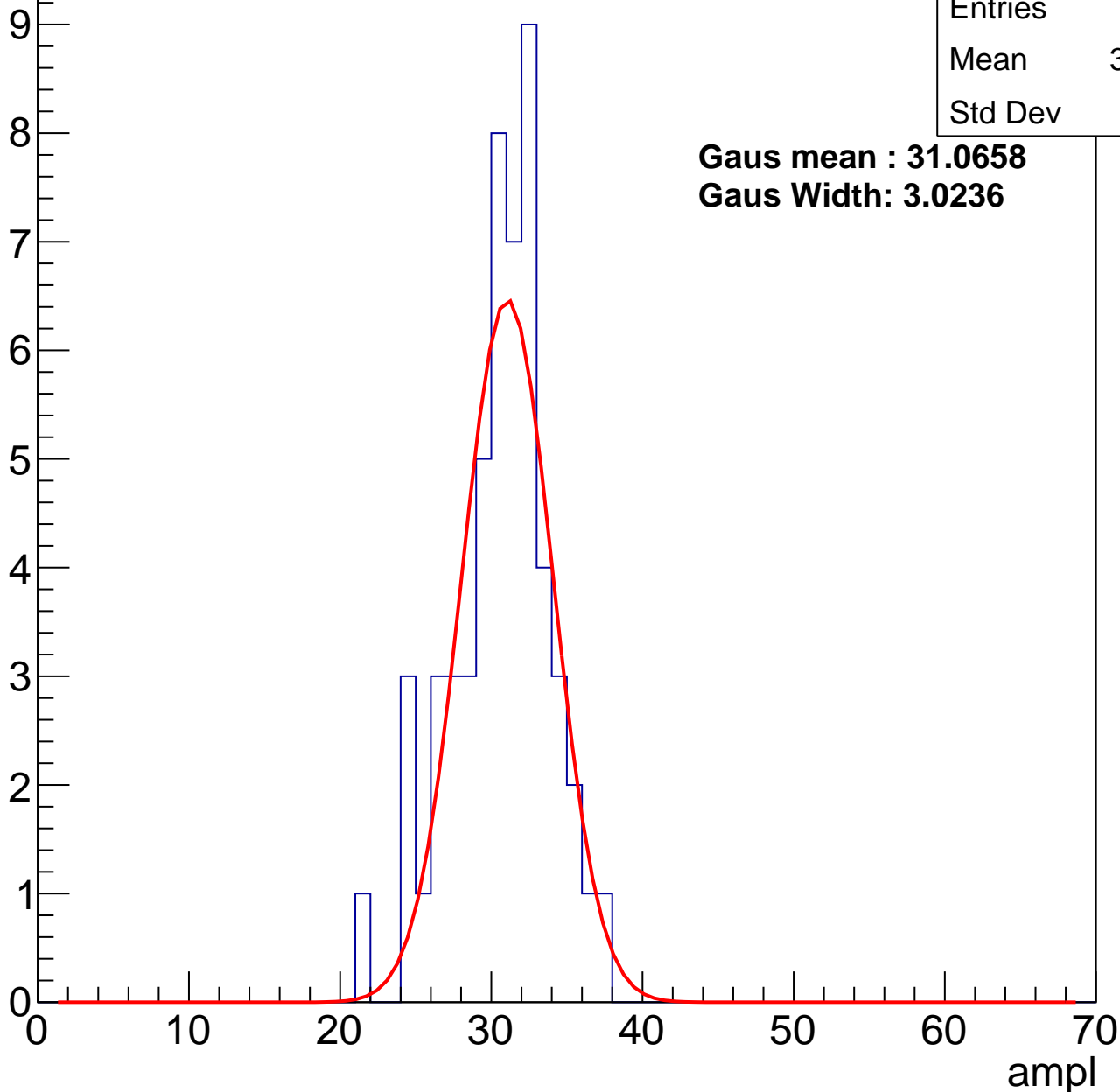
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	30.15
Std Dev	3.24

**Gaus mean : 31.0658**

**Gaus Width: 3.0236**



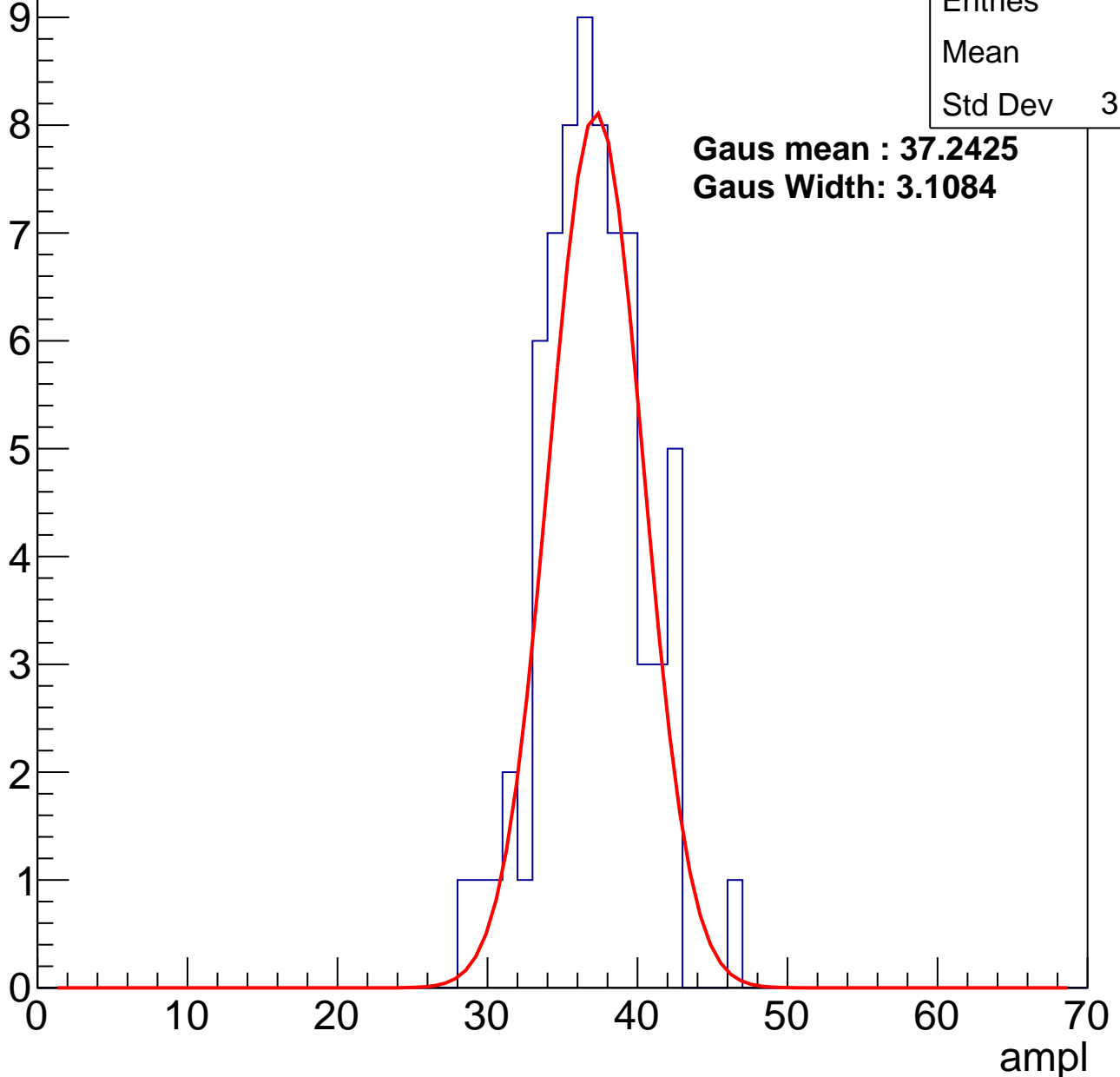
# B0L000S, U10-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.5
Std Dev	3.358

**Gaus mean : 37.2425**  
**Gaus Width: 3.1084**



# B0L000S, U10-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	43.68
Std Dev	3.76

**Gaus mean : 44.2456**

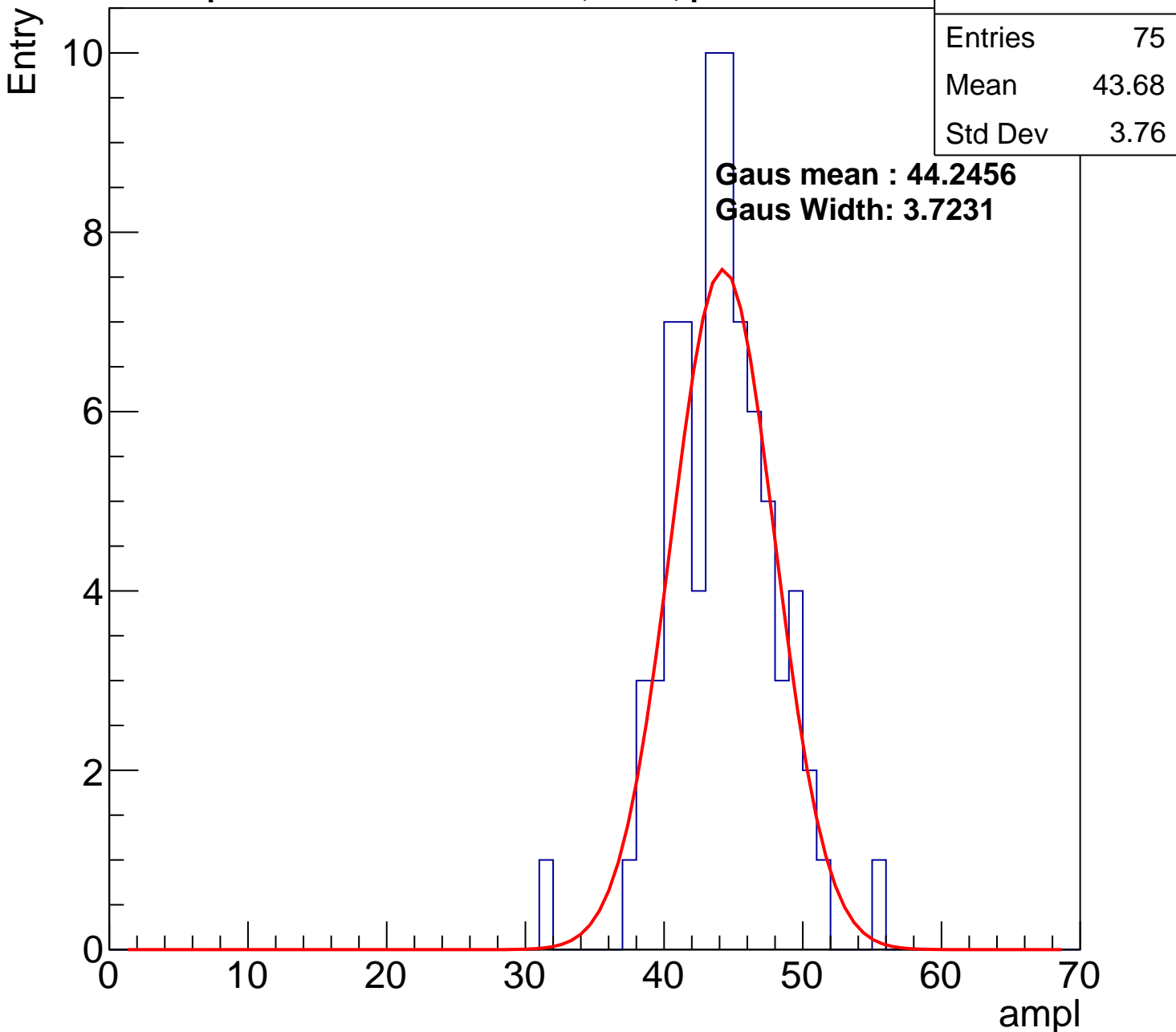
**Gaus Width: 3.7231**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

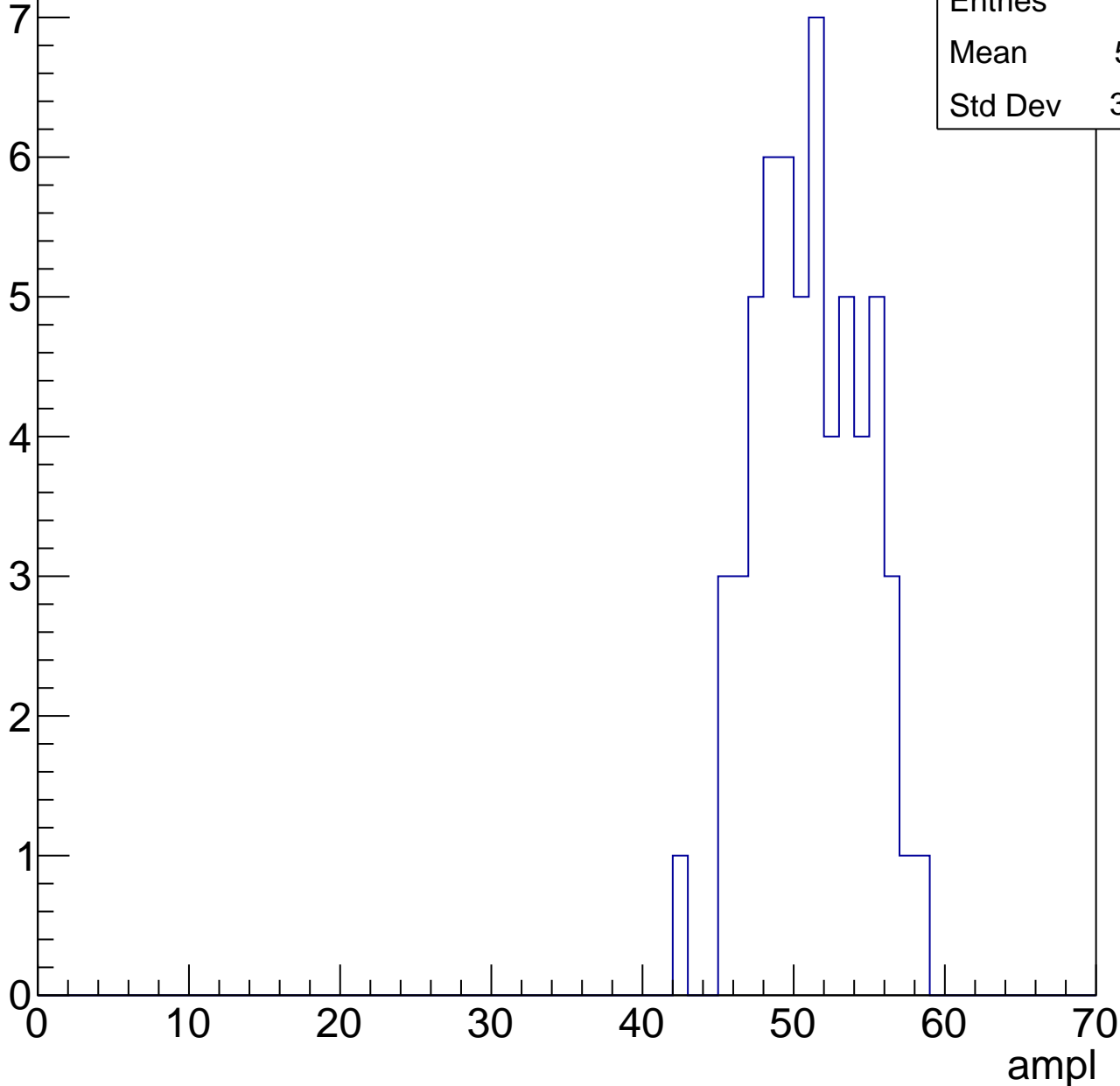


# B0L000S, U10-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.61
Std Dev	3.489

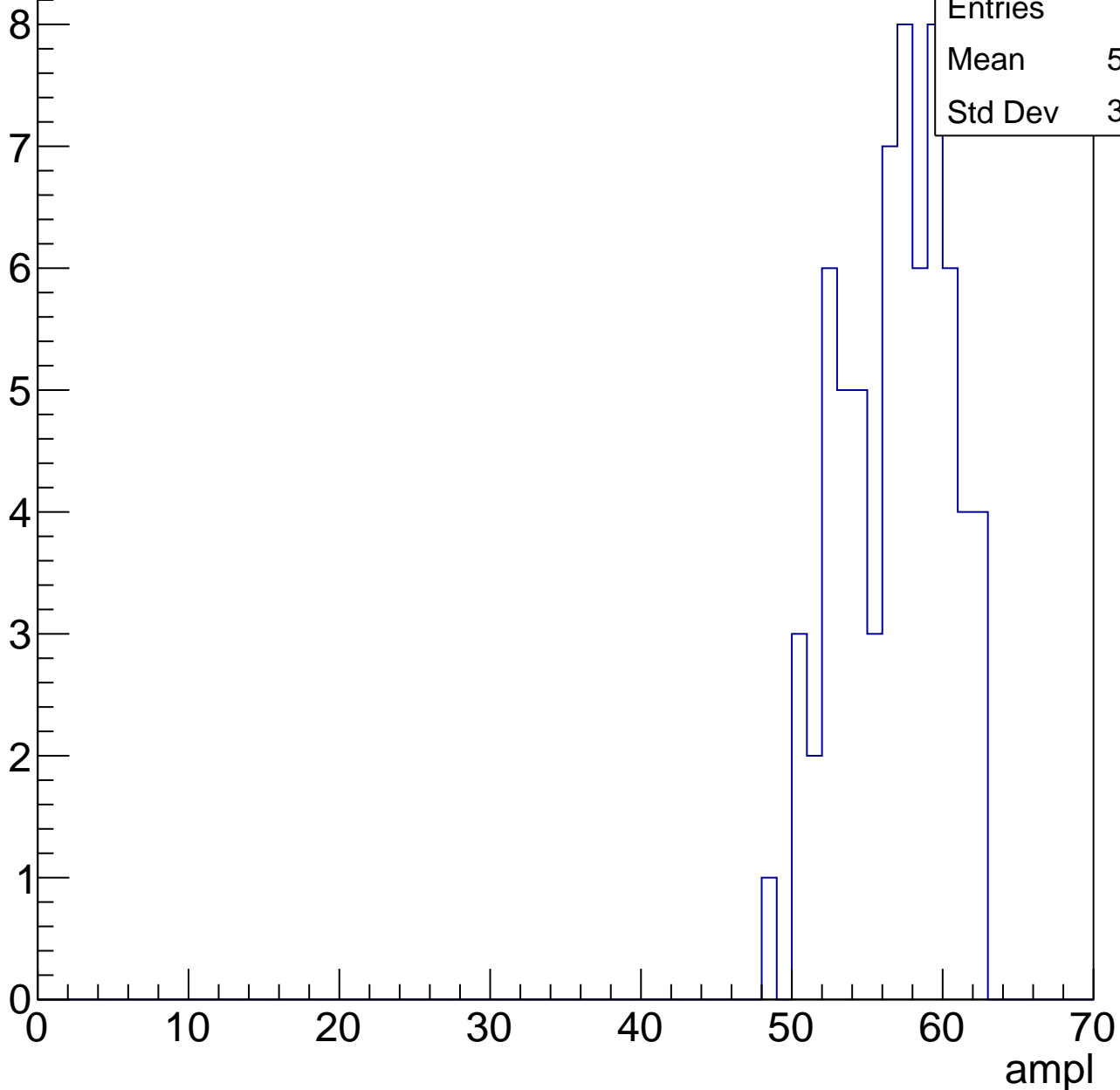


# B0L000S, U10-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	56.35
Std Dev	3.476

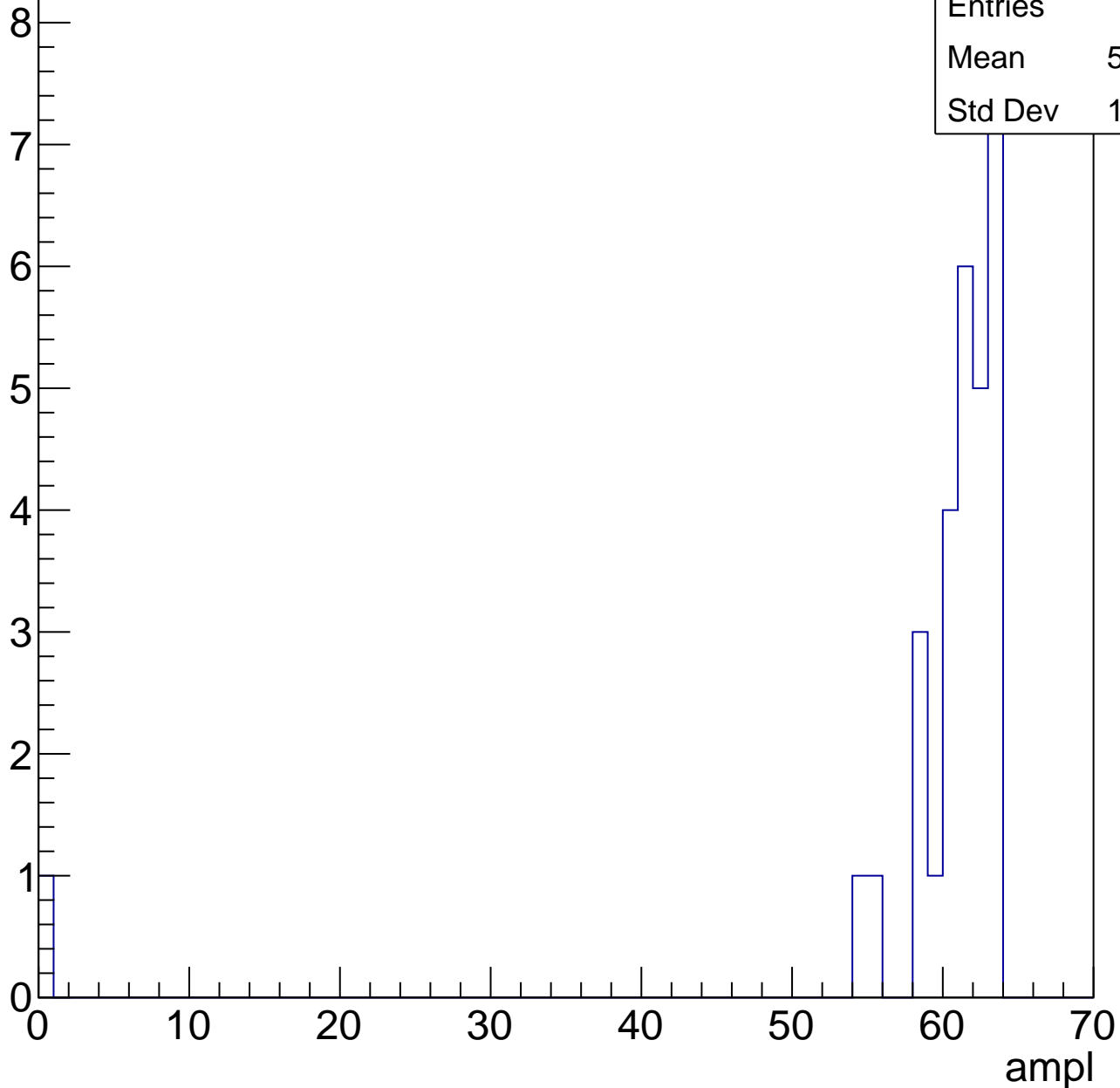


# B0L000S, U10-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	58.73
Std Dev	11.14



# B0L000S, U10-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

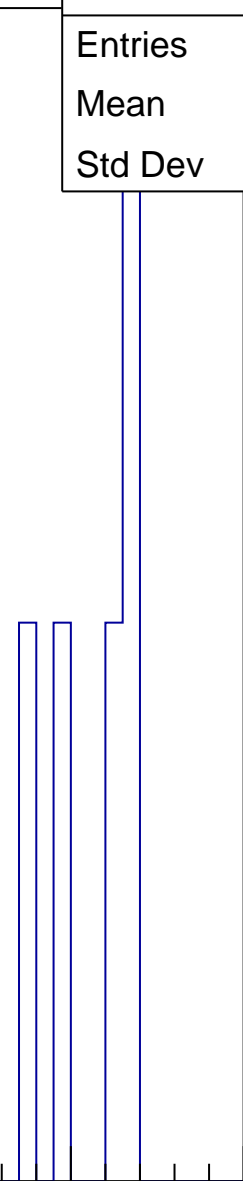
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	60.8
Std Dev	2.4

0 10 20 30 40 50 60 70

ampl





# B0L000S, U10-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.93
Std Dev	3.294

**Gaus mean : 30.6394**

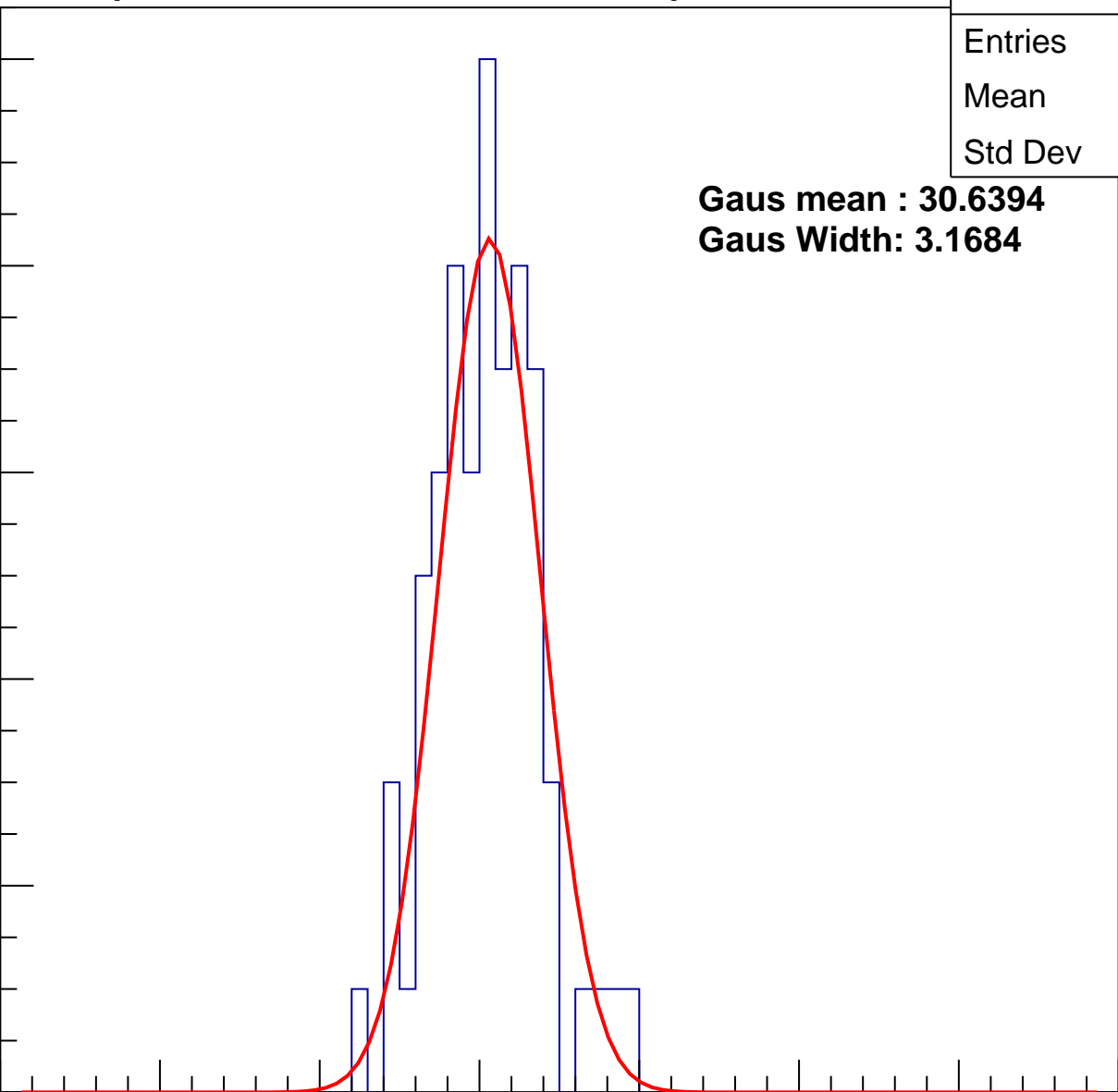
**Gaus Width: 3.1684**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch67, adc1

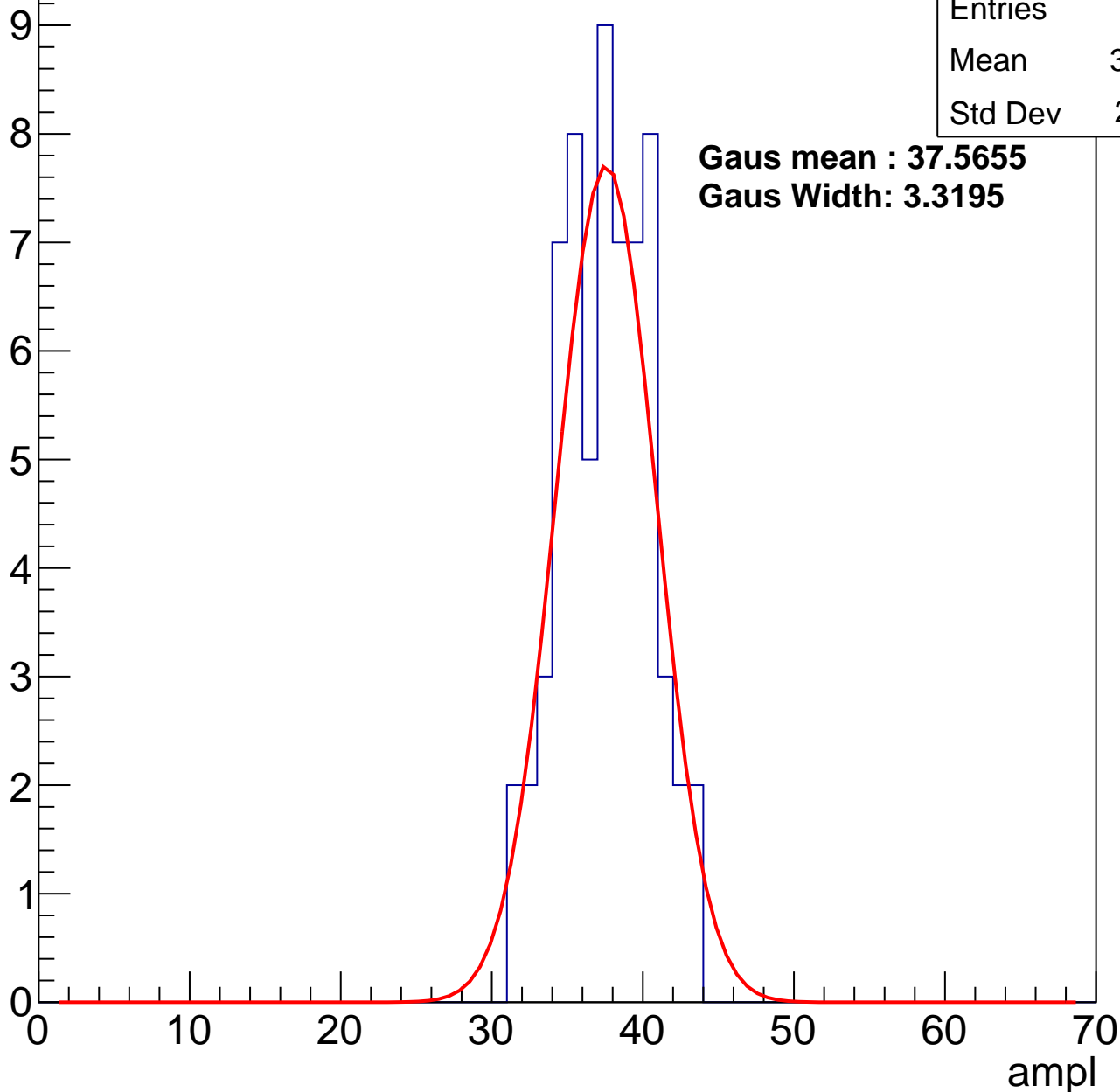
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.05
Std Dev	2.901

**Gaus mean : 37.5655**

**Gaus Width: 3.3195**



# B0L000S, U10-ch67, adc2

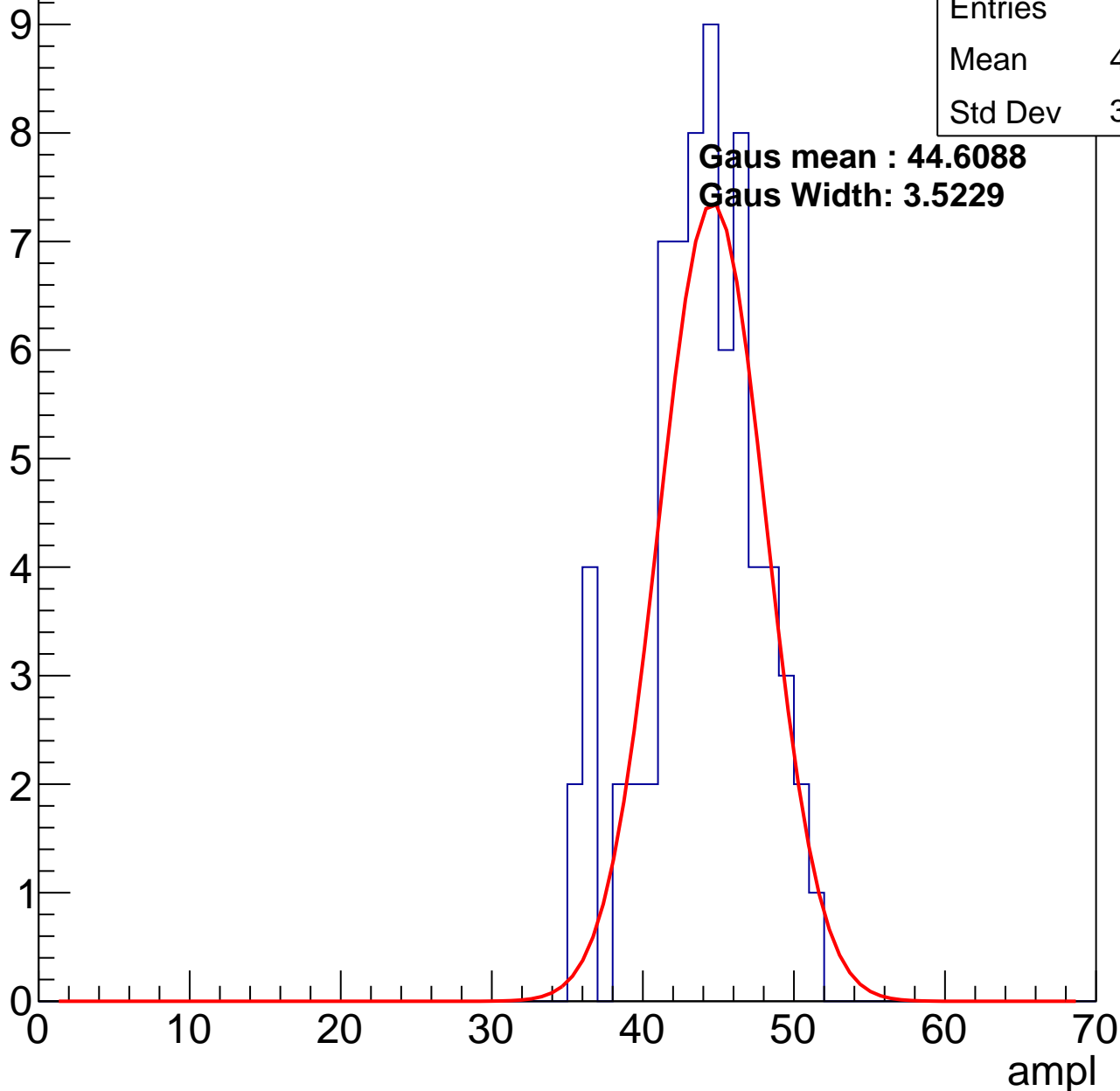
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	43.45
Std Dev	3.722

**Gaus mean : 44.6088**

**Gaus Width: 3.5229**

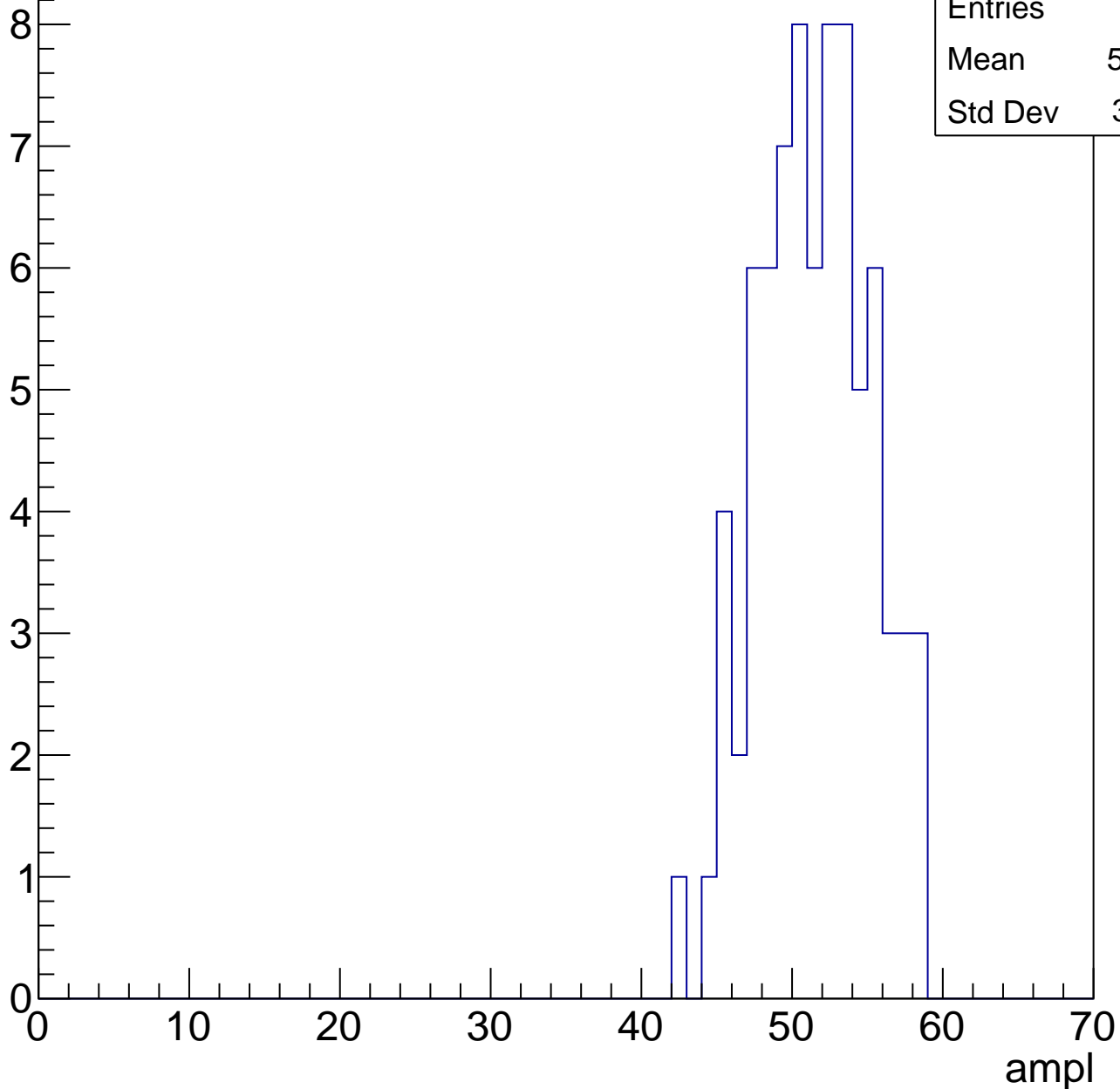


# B0L000S, U10-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	51.04
Std Dev	3.641

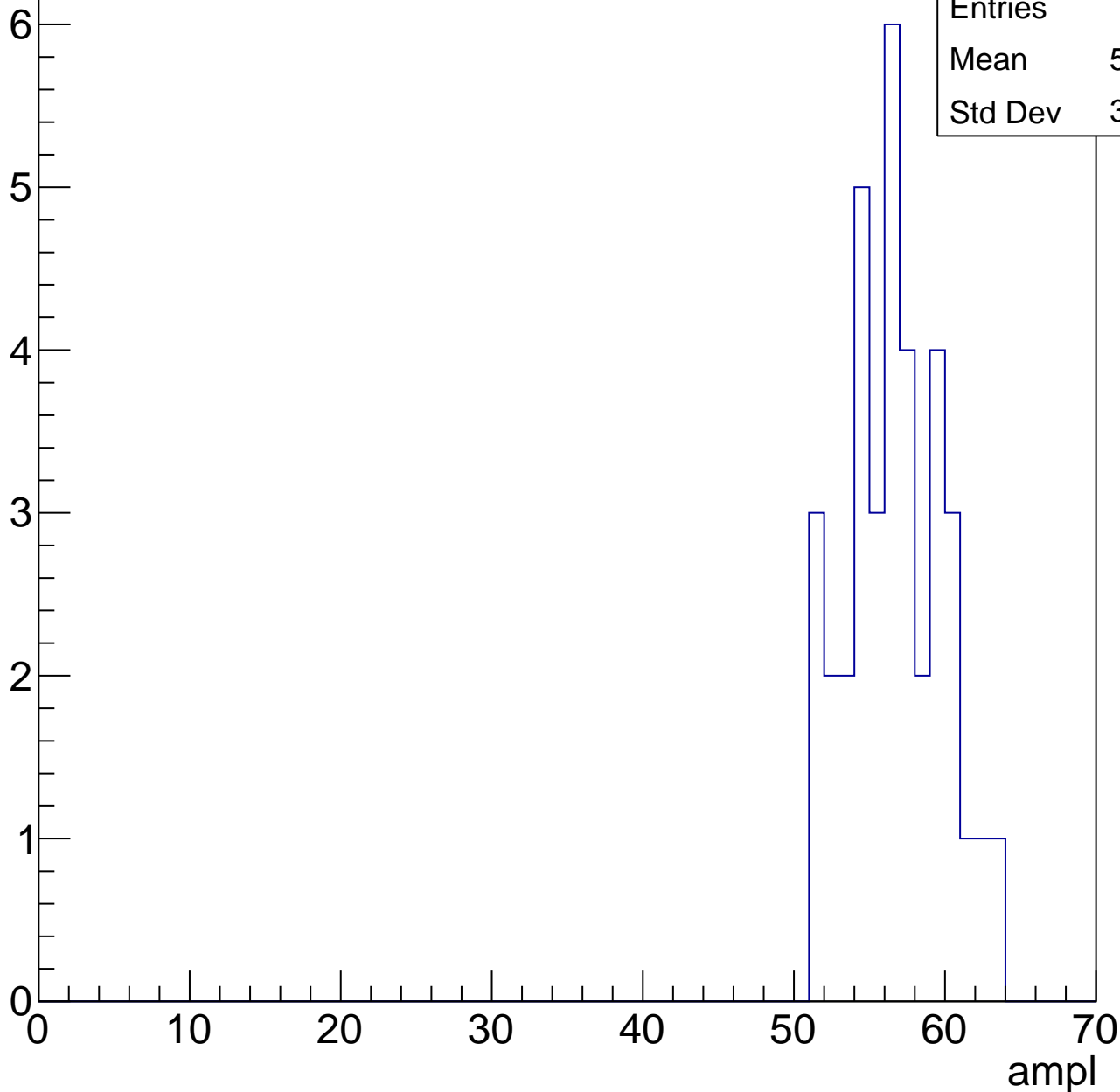


# B0L000S, U10-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	56.22
Std Dev	3.086

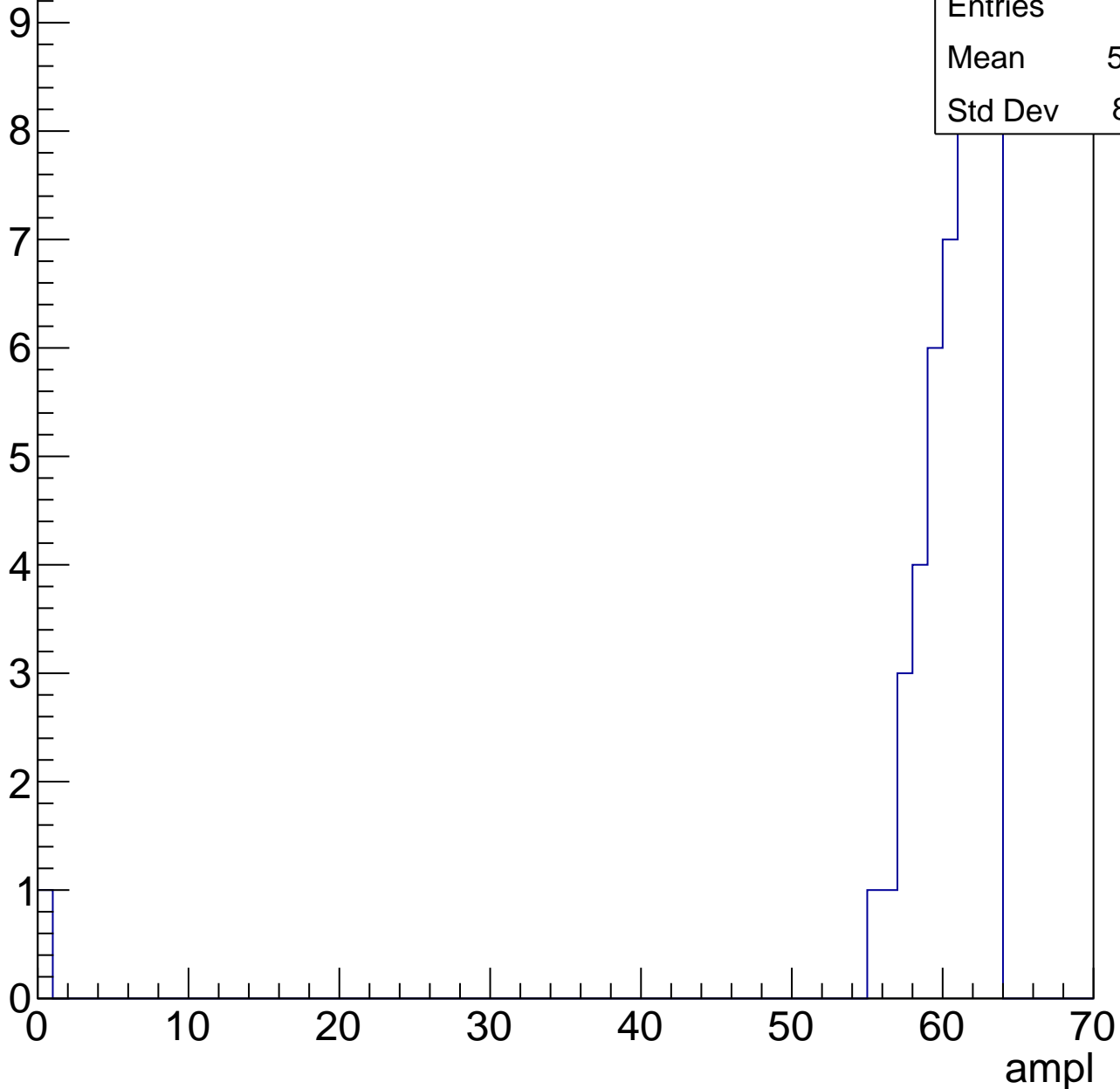


# B0L000S, U10-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

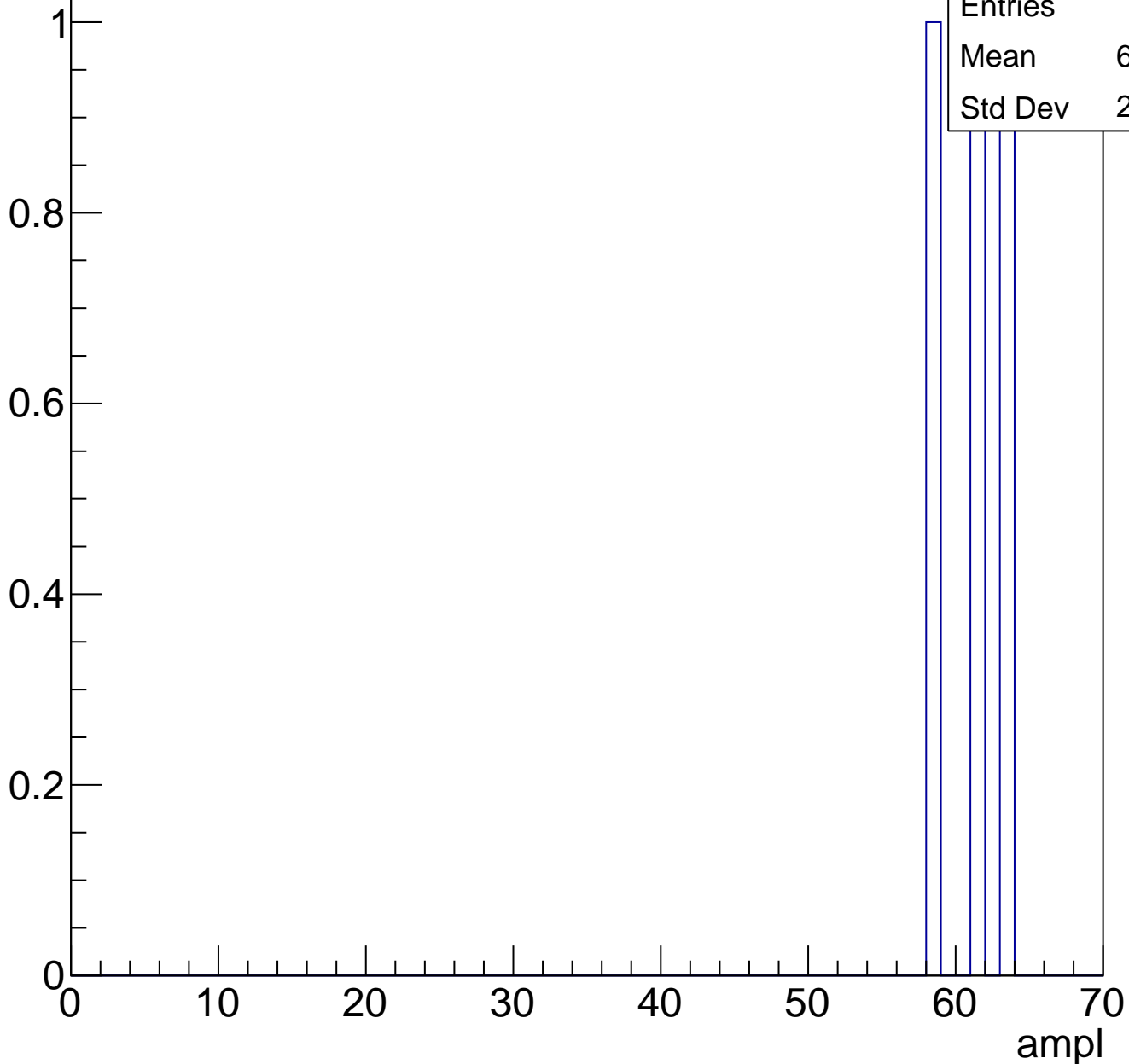
Entries	48
Mean	59.12
Std Dev	8.861



# B0L000S, U10-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch68, adc0

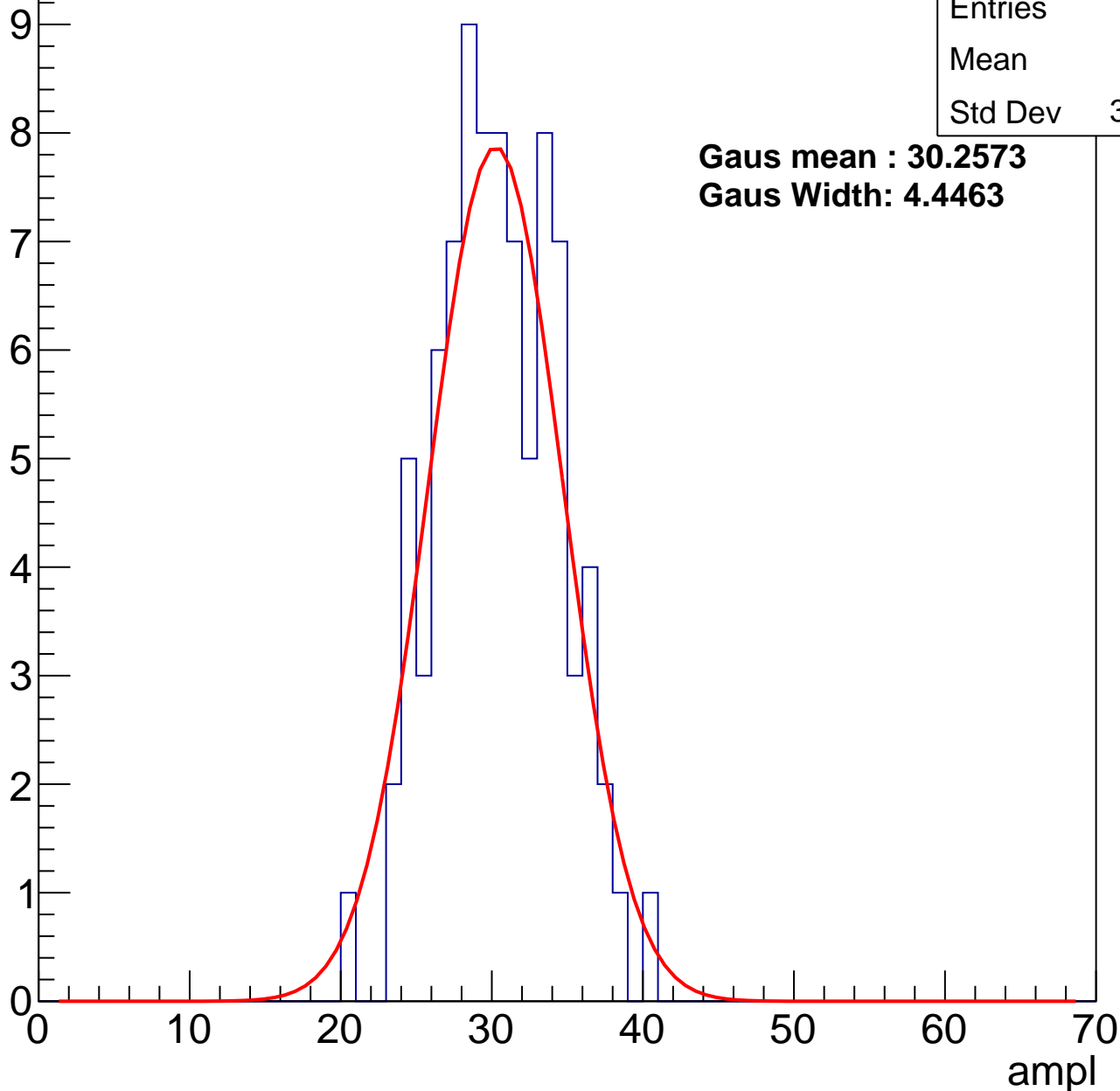
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	30
Std Dev	3.936

**Gaus mean : 30.2573**

**Gaus Width: 4.4463**



# B0L000S, U10-ch68, adc1

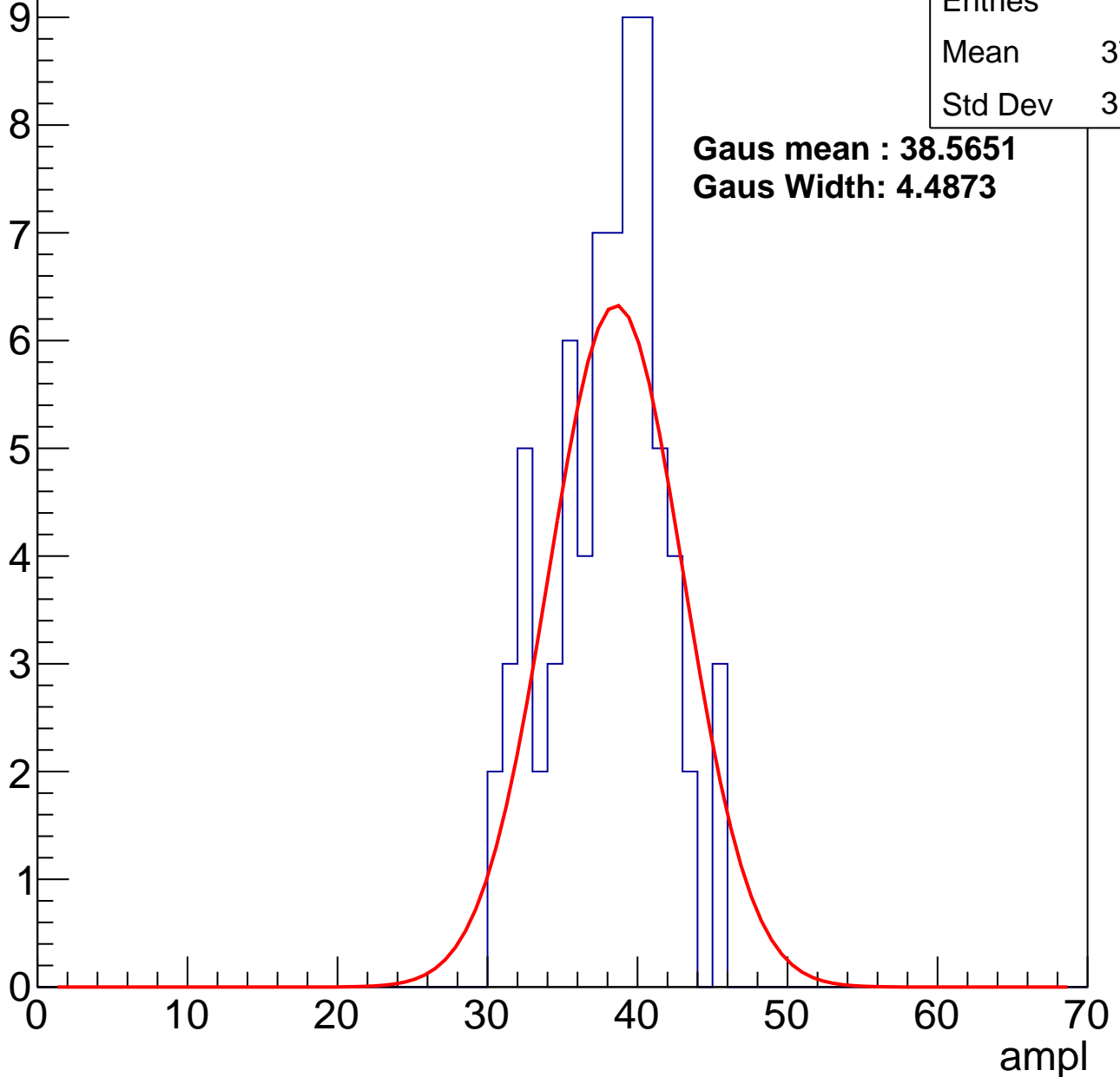
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.54
Std Dev	3.688

**Gaus mean : 38.5651**

**Gaus Width: 4.4873**



# B0L000S, U10-ch68, adc2

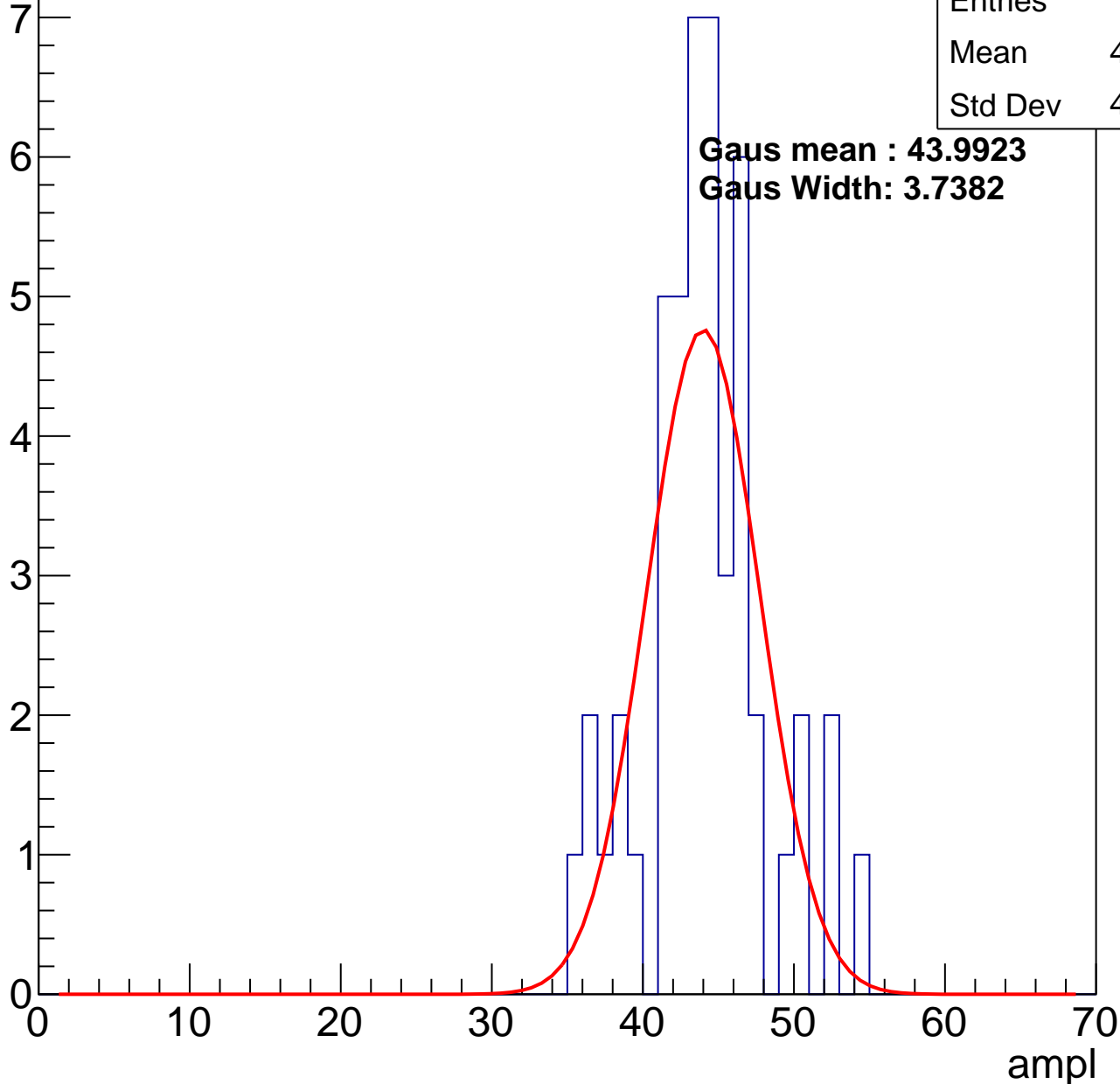
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	43.65
Std Dev	4.059

**Gaus mean : 43.9923**

**Gaus Width: 3.7382**

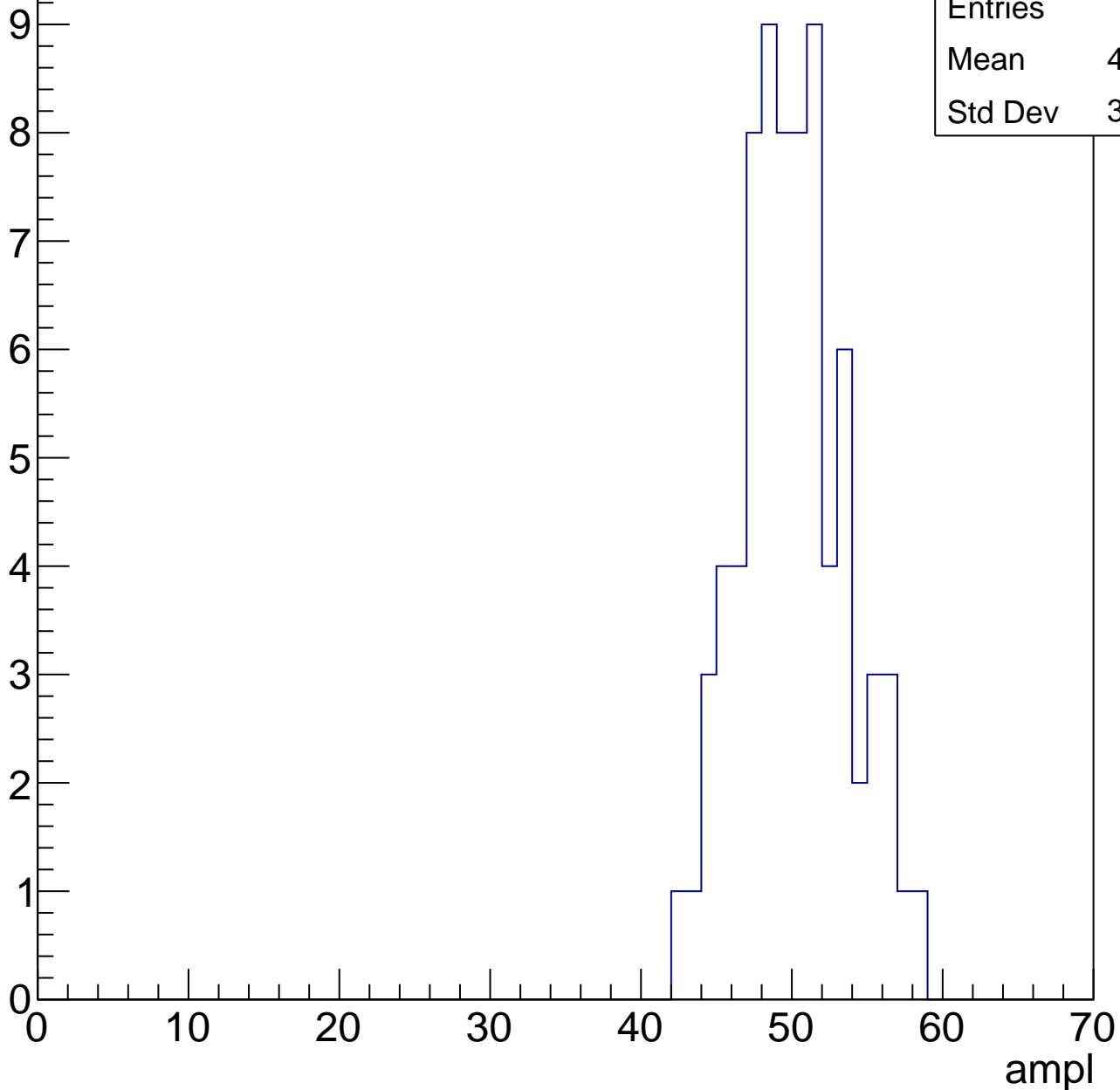


# B0L000S, U10-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	49.63
Std Dev	3.459

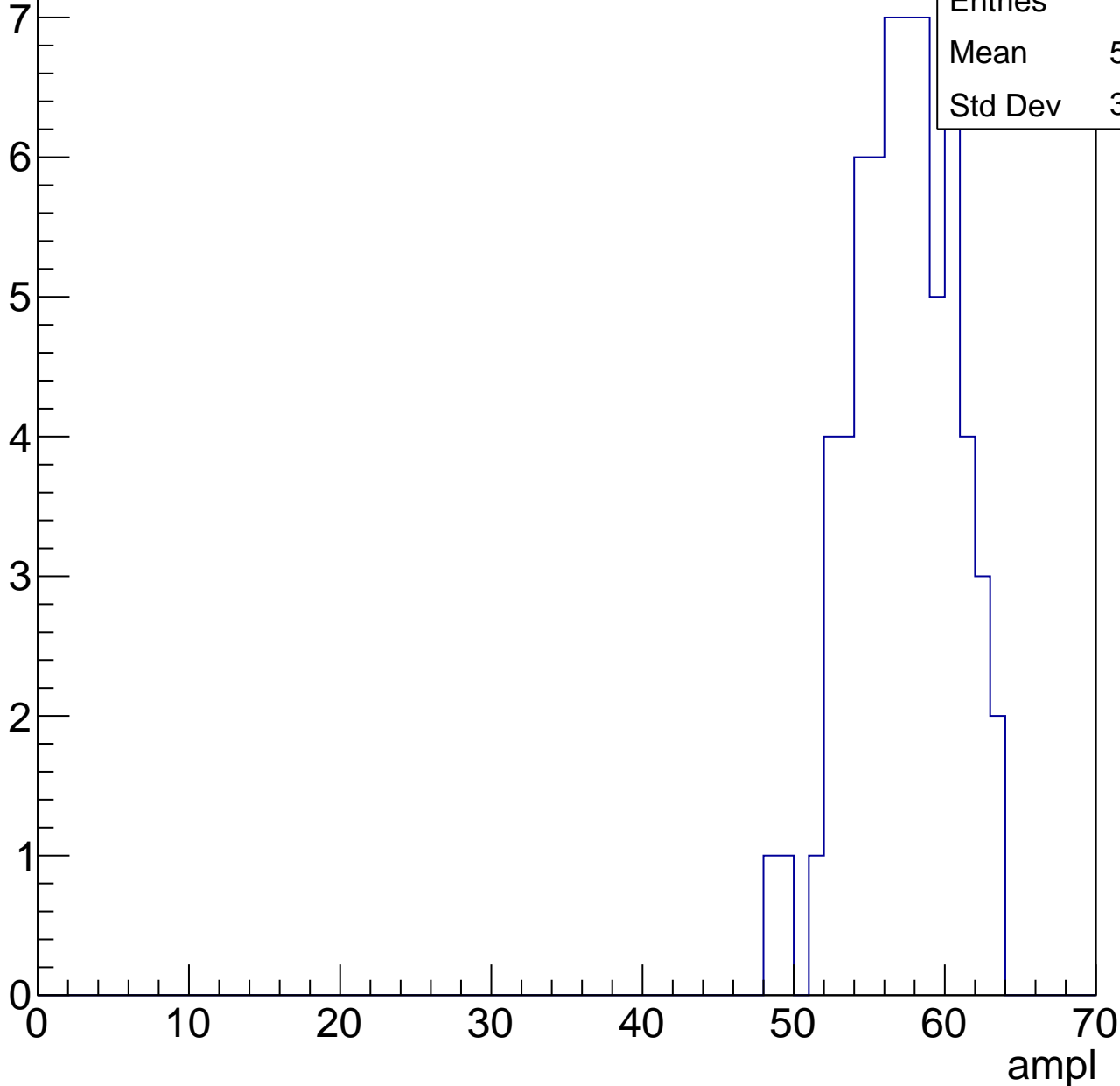


# B0L000S, U10-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.77
Std Dev	3.346

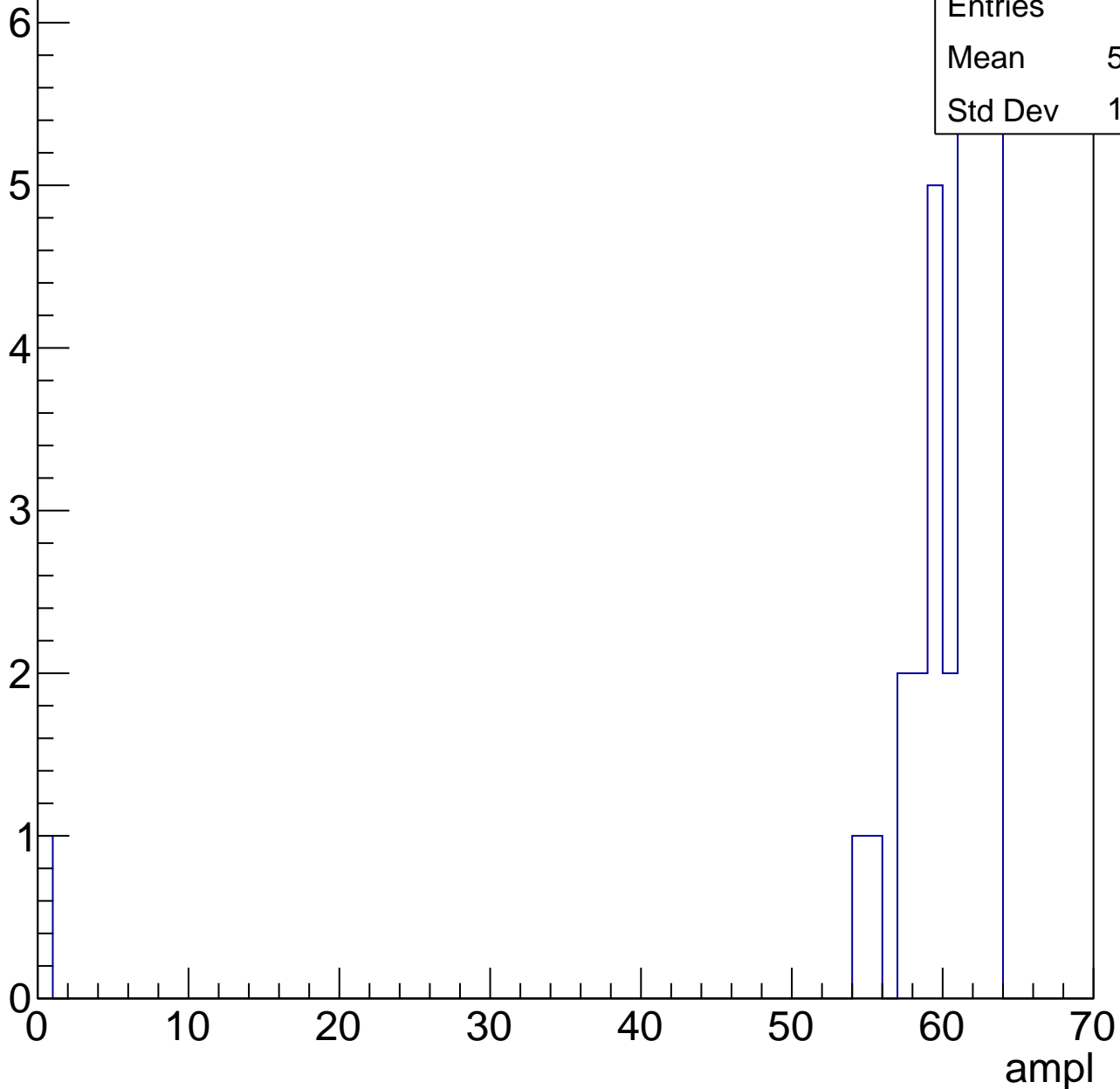


# B0L000S, U10-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.44
Std Dev	10.75



# B0L000S, U10-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch69, adc0

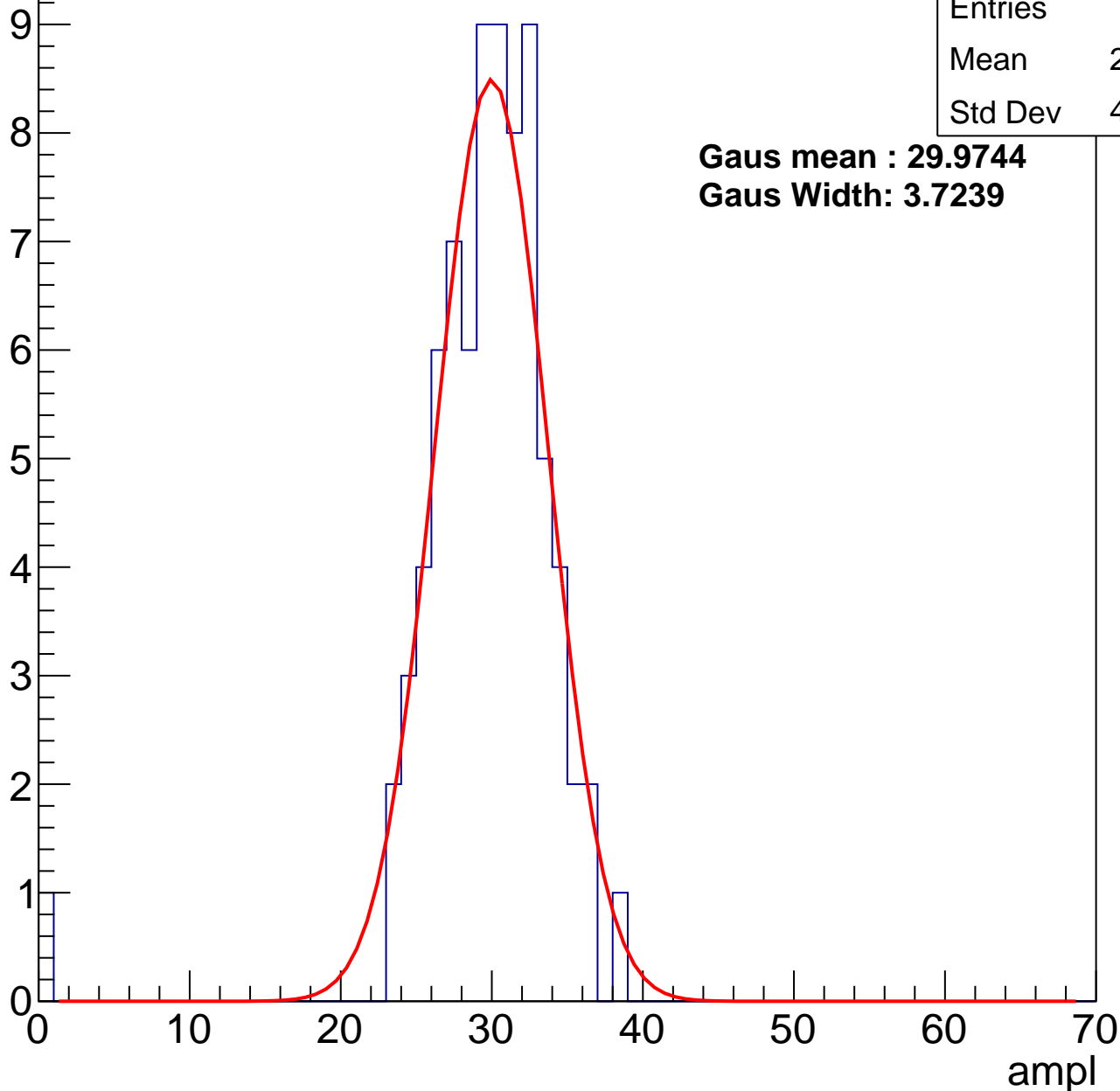
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	29.22
Std Dev	4.654

**Gaus mean : 29.9744**

**Gaus Width: 3.7239**



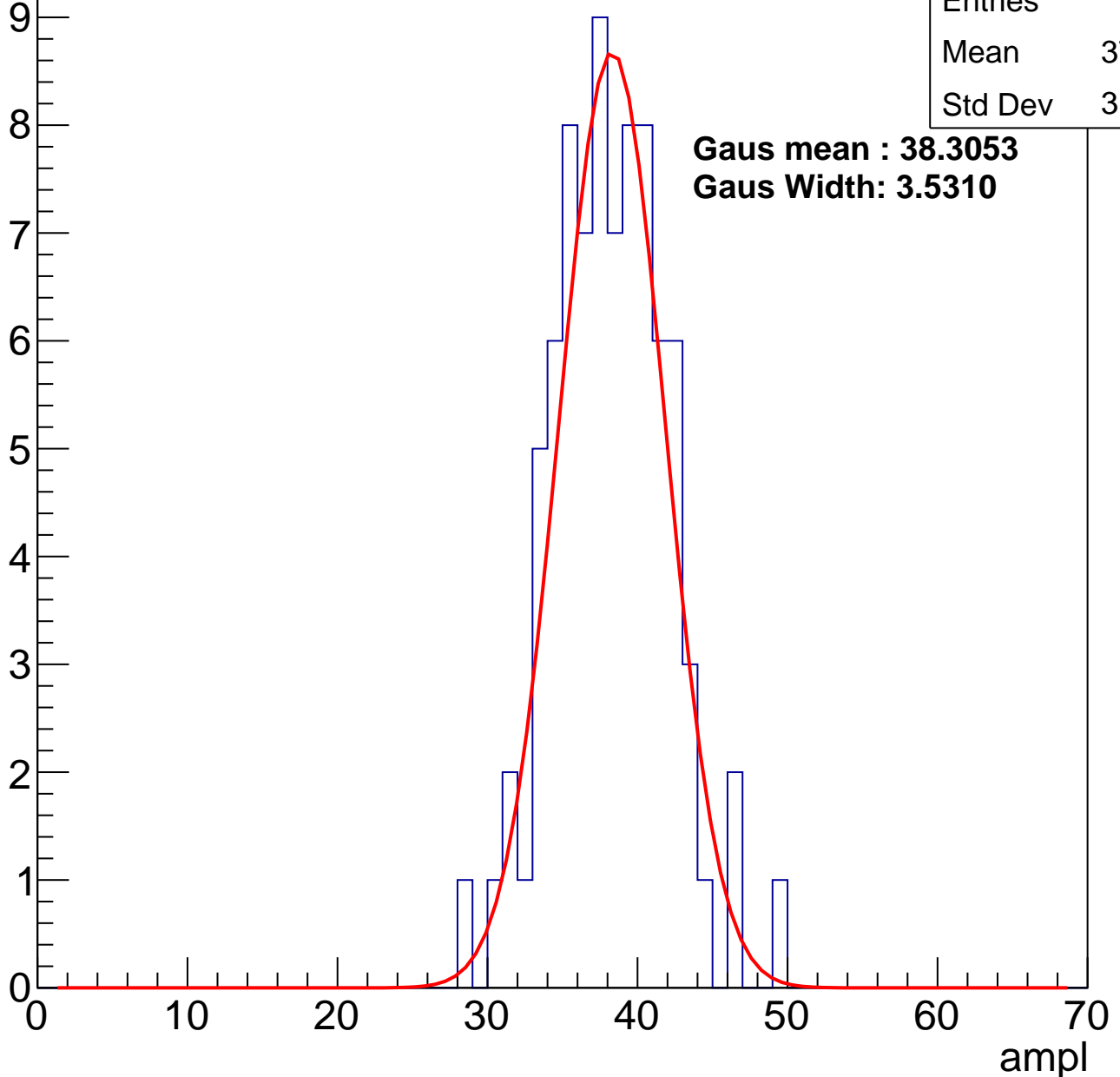
# B0L000S, U10-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	37.76
Std Dev	3.782

**Gaus mean : 38.3053**  
**Gaus Width: 3.5310**



# B0L000S, U10-ch69, adc2

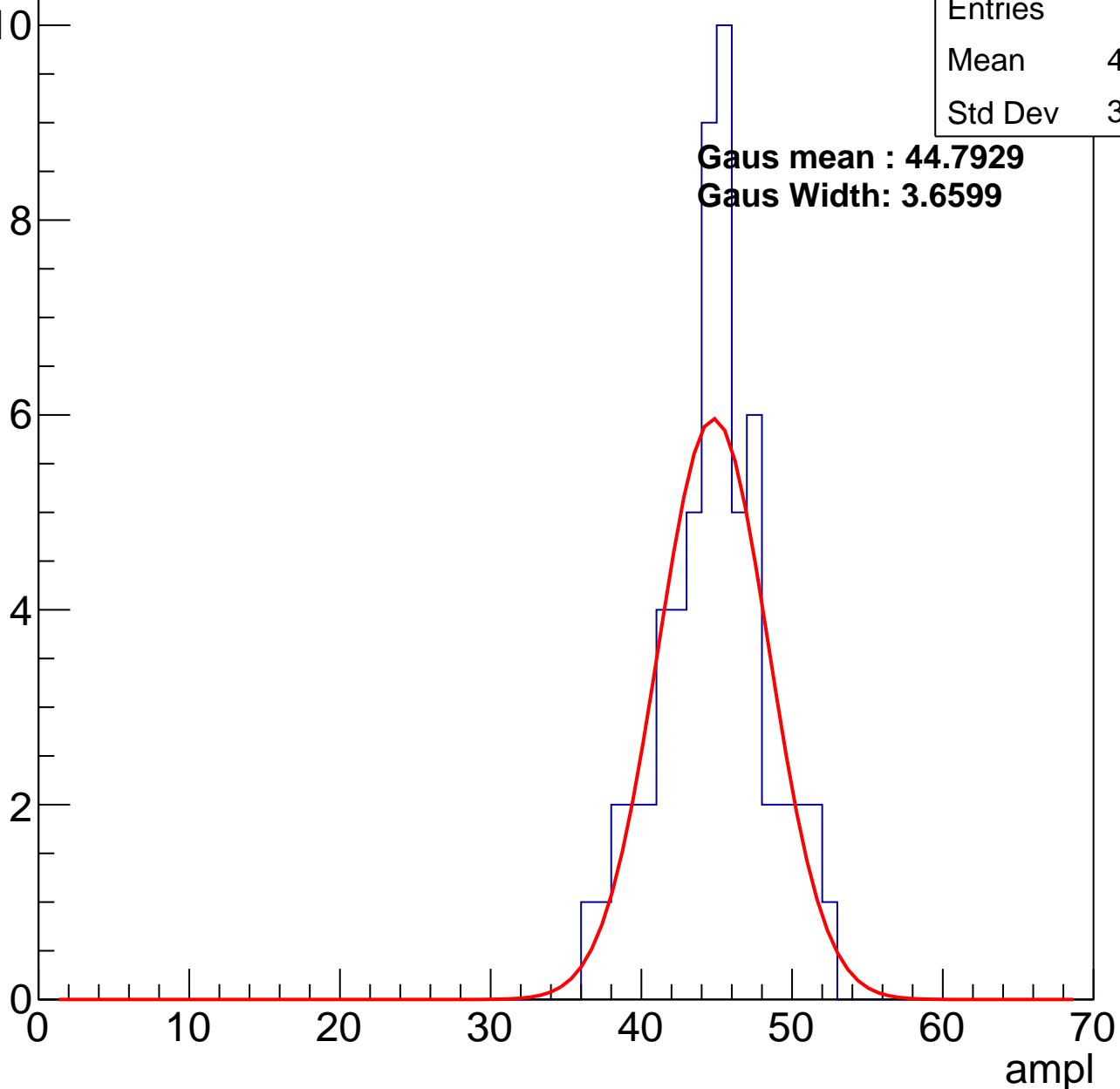
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.33
Std Dev	3.458

**Gaus mean : 44.7929**

**Gaus Width: 3.6599**

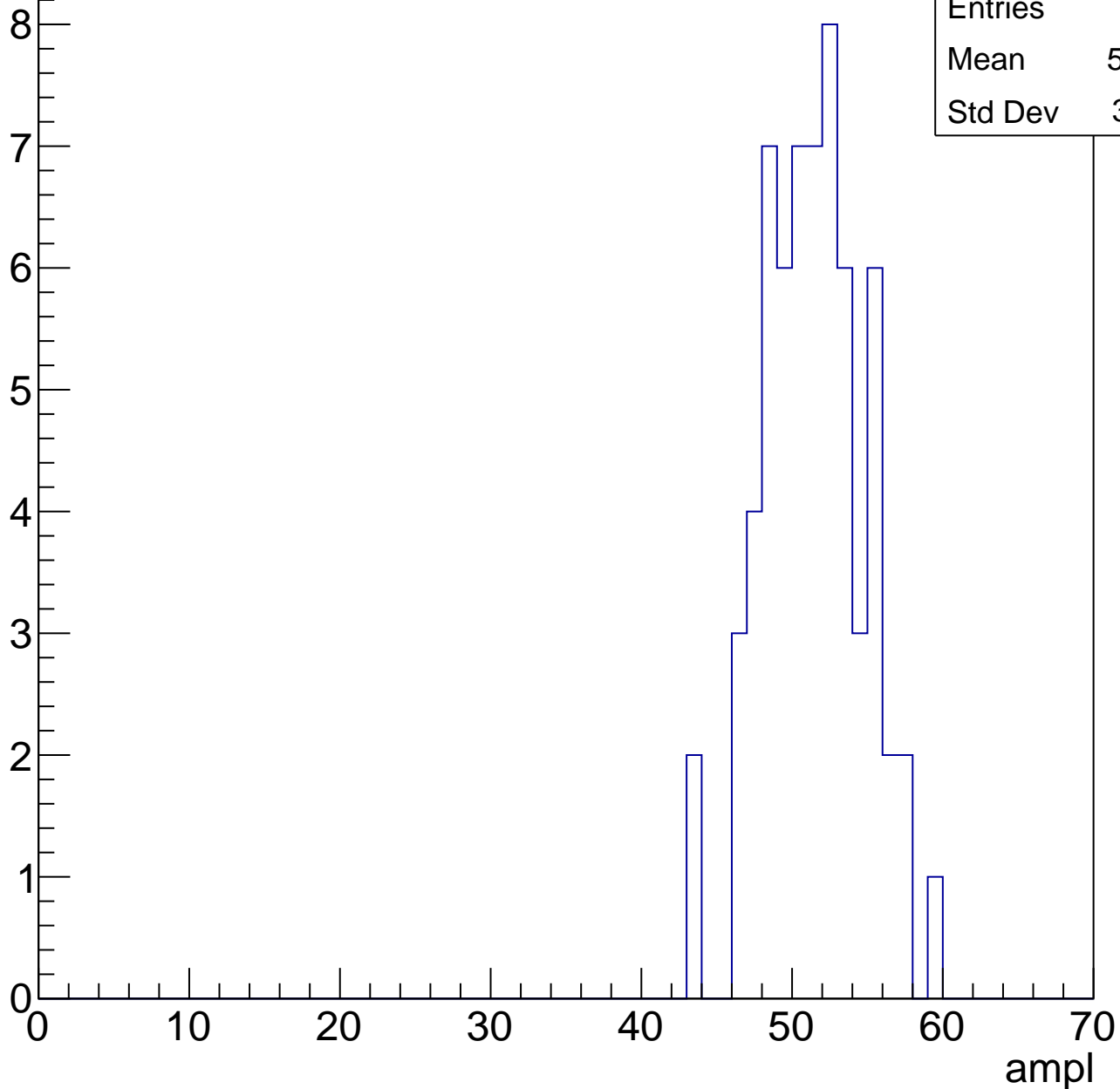


# B0L000S, U10-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

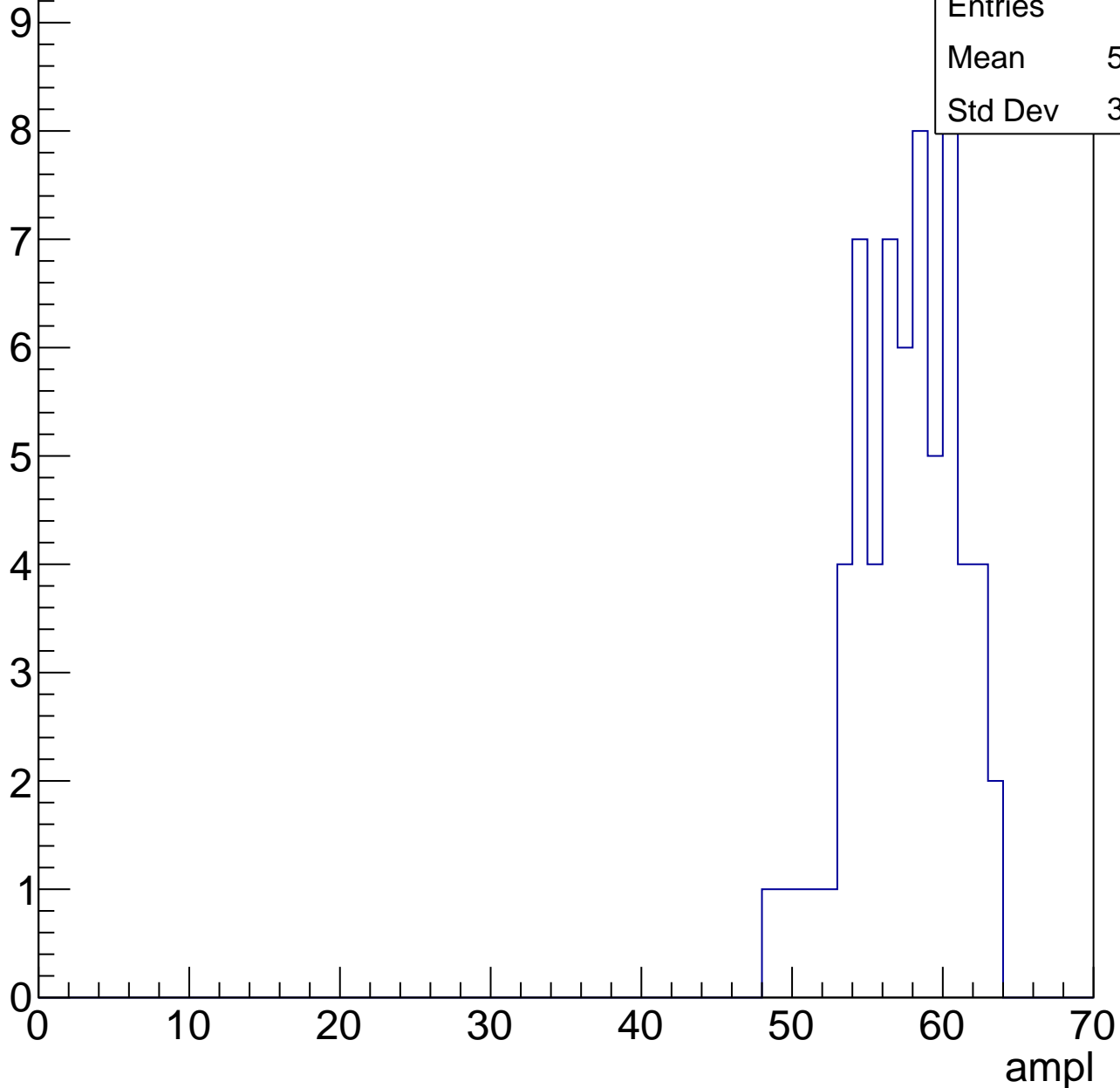
Entries	64
Mean	50.94
Std Dev	3.311



# B0L000S, U10-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

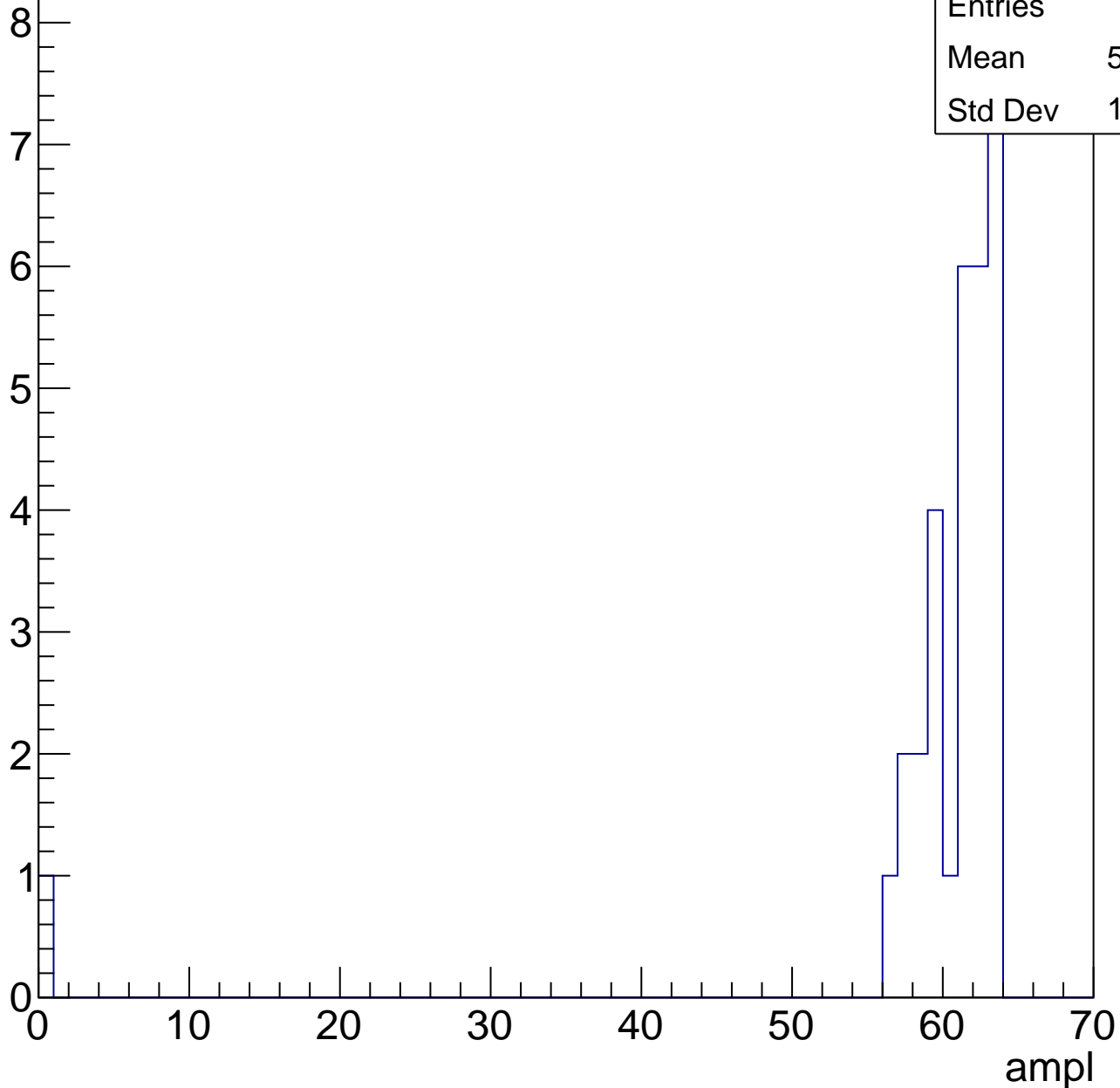


# B0L000S, U10-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	58.84
Std Dev	10.93



# B0L000S, U10-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch70, adc0

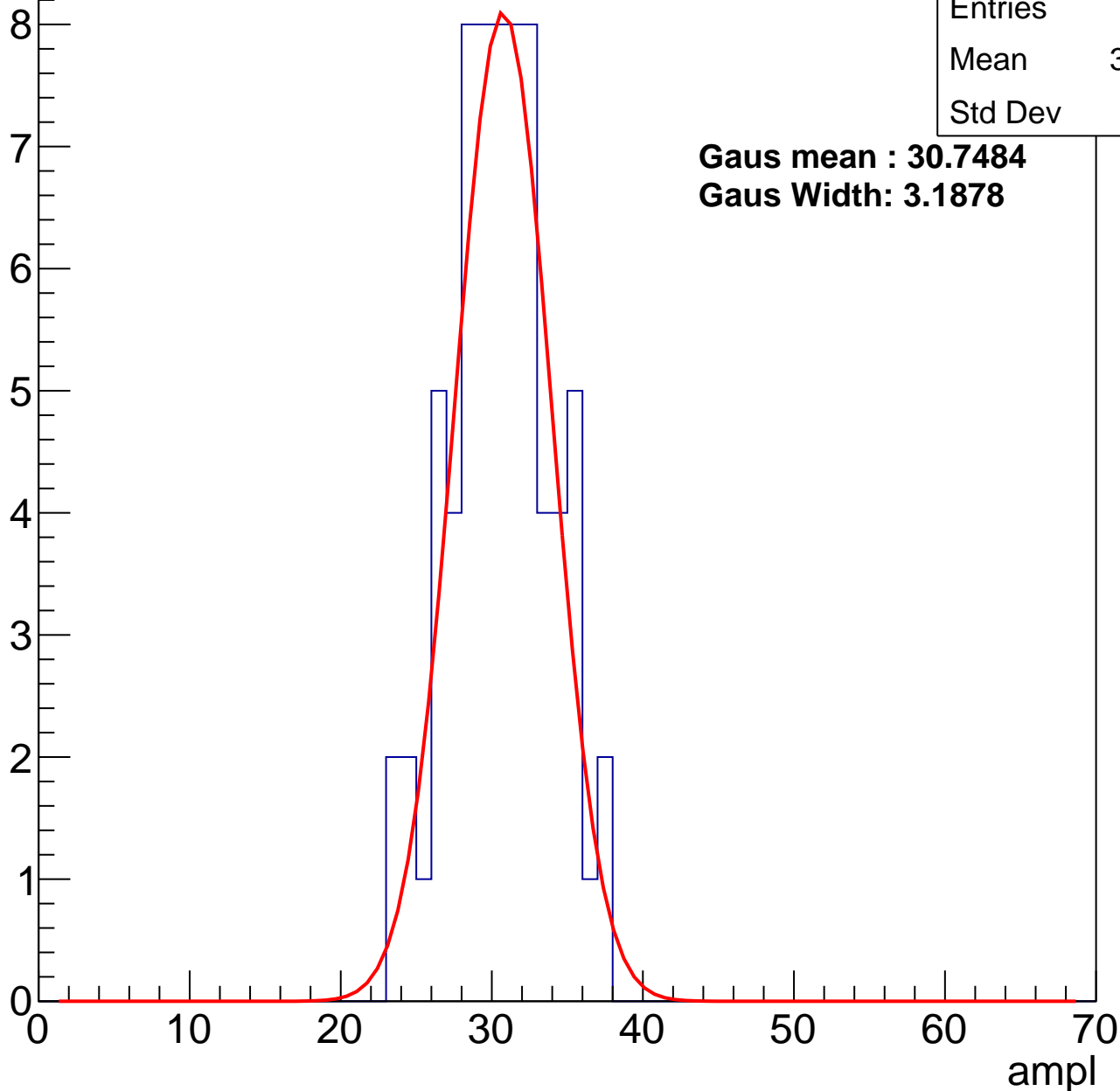
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.14
Std Dev	3.27

**Gaus mean : 30.7484**

**Gaus Width: 3.1878**



# B0L000S, U10-ch70, adc1

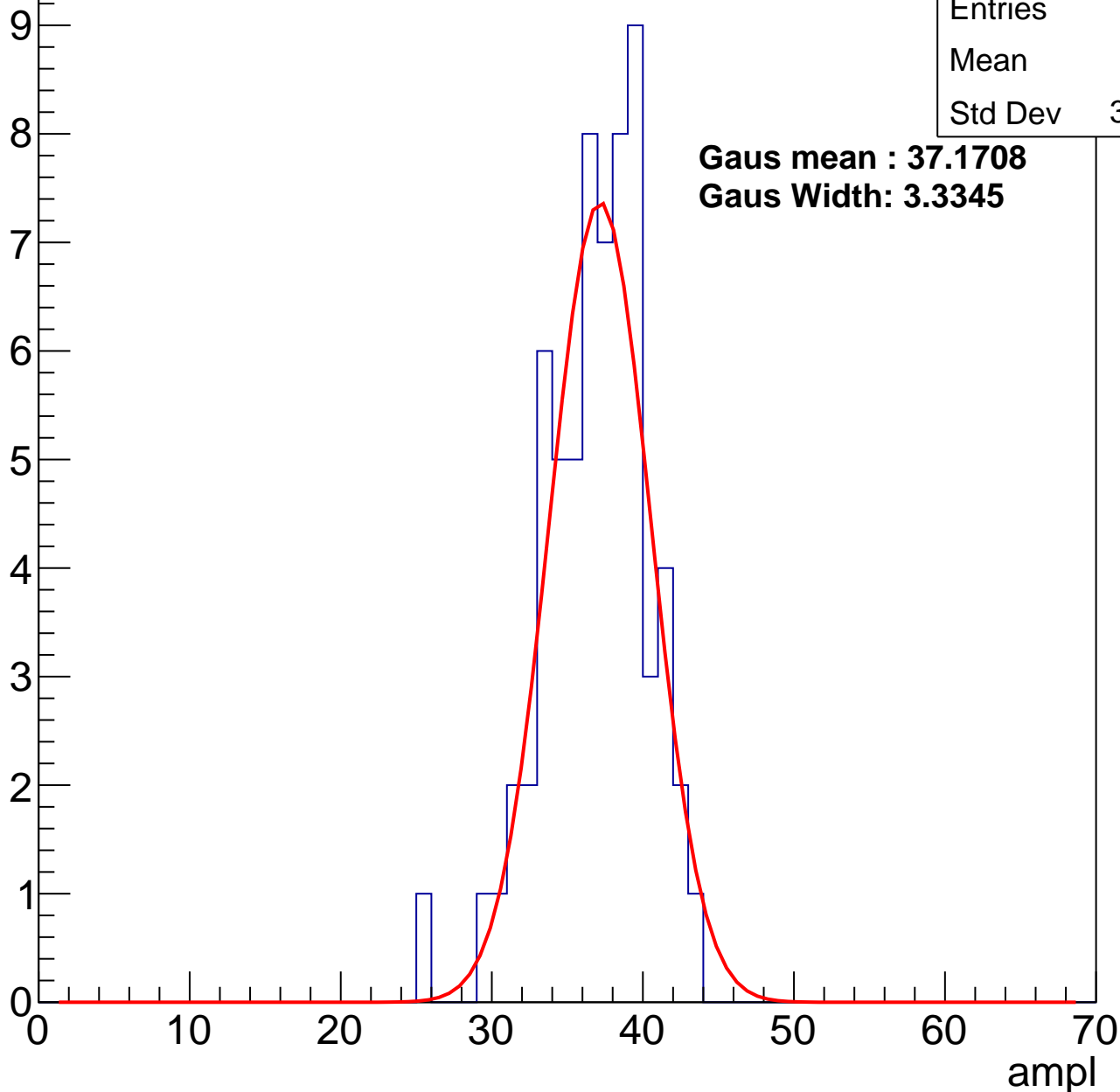
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.4
Std Dev	3.387

**Gaus mean : 37.1708**

**Gaus Width: 3.3345**



# B0L000S, U10-ch70, adc2

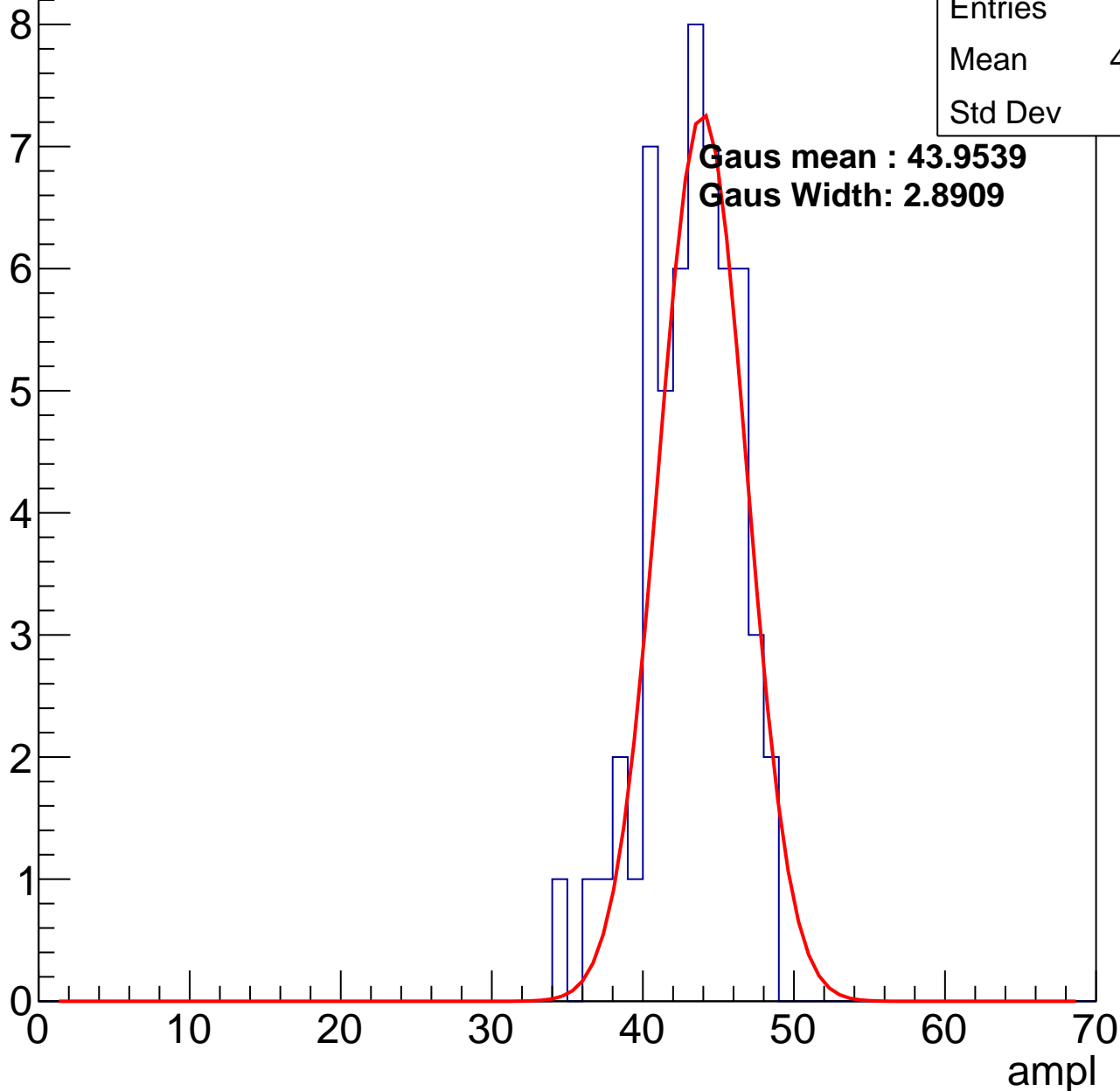
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.75
Std Dev	2.99

**Gaus mean : 43.9539**

**Gaus Width: 2.8909**

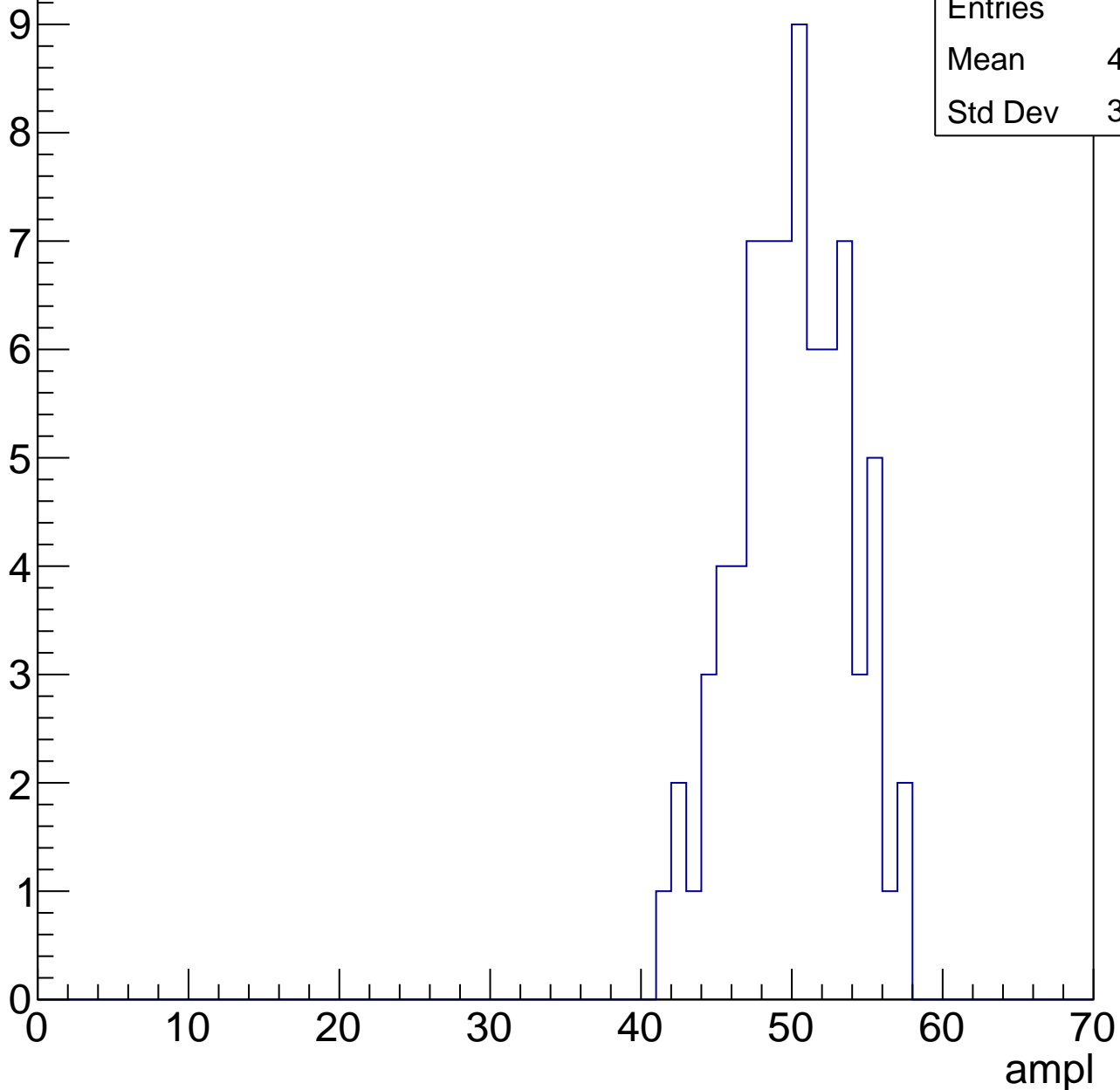


# B0L000S, U10-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	49.57
Std Dev	3.678

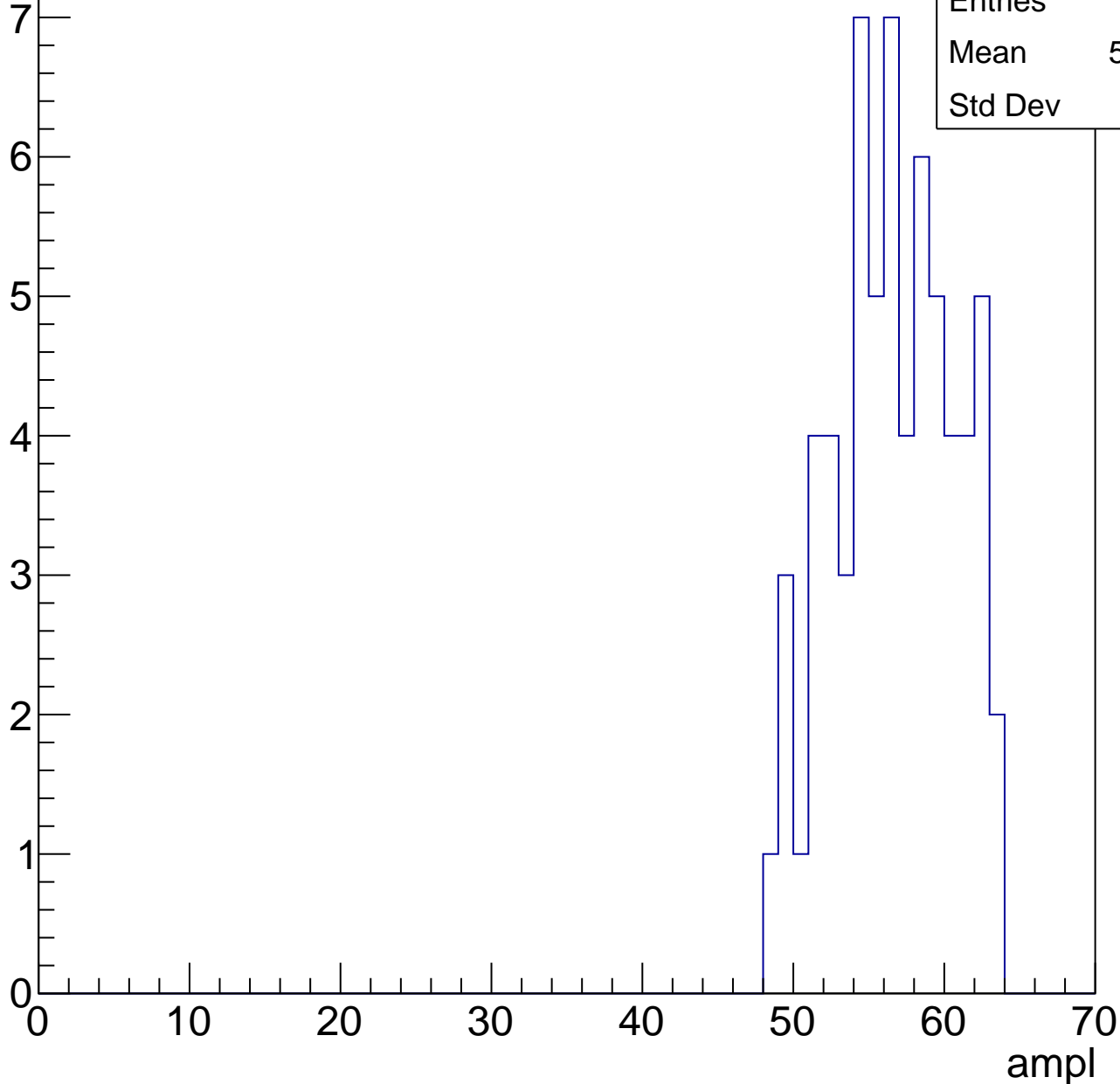


# B0L000S, U10-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	56.18
Std Dev	3.89

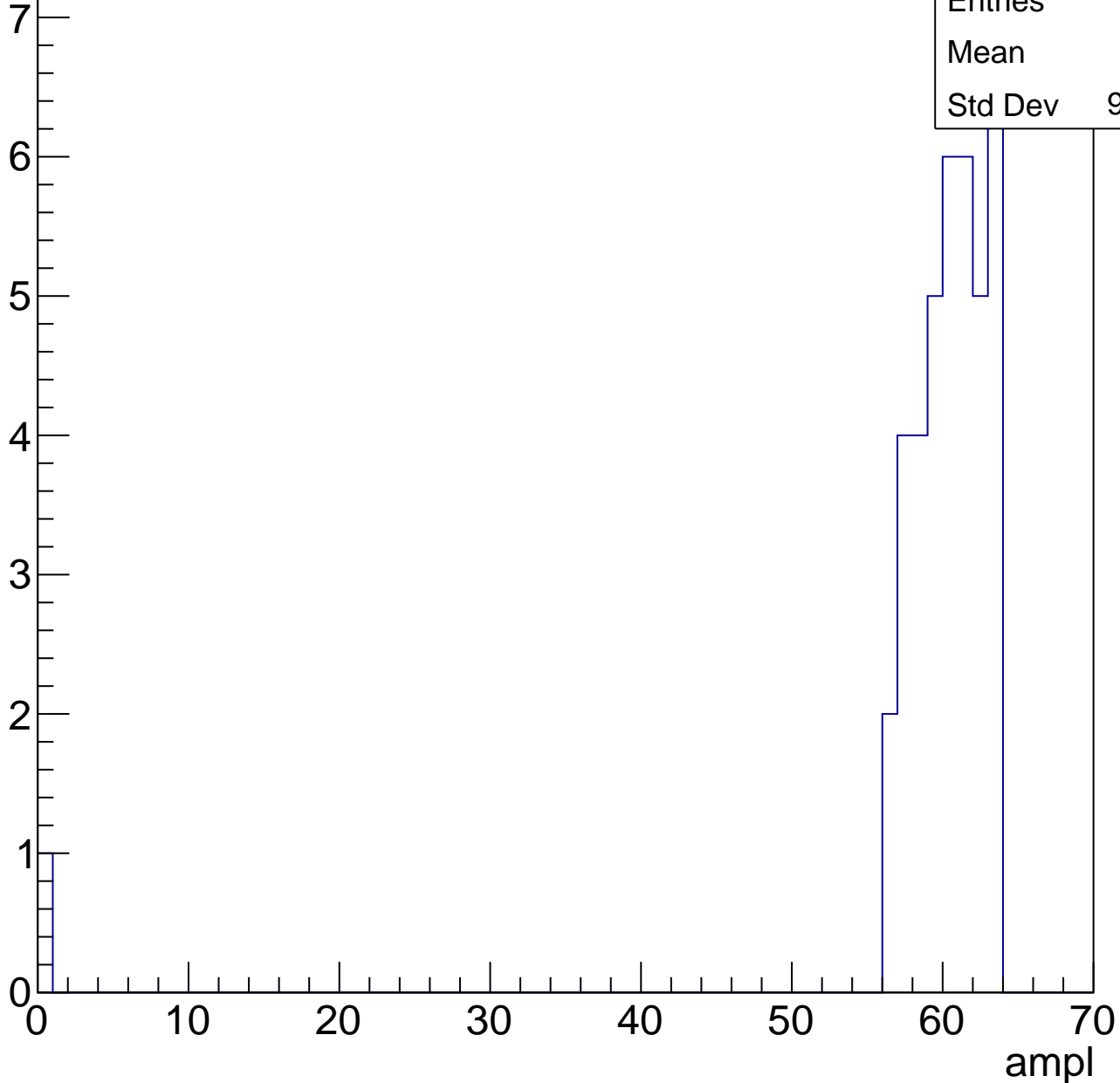


# B0L000S, U10-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.6
Std Dev	9.617



# B0L000S, U10-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U10-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch71, adc0

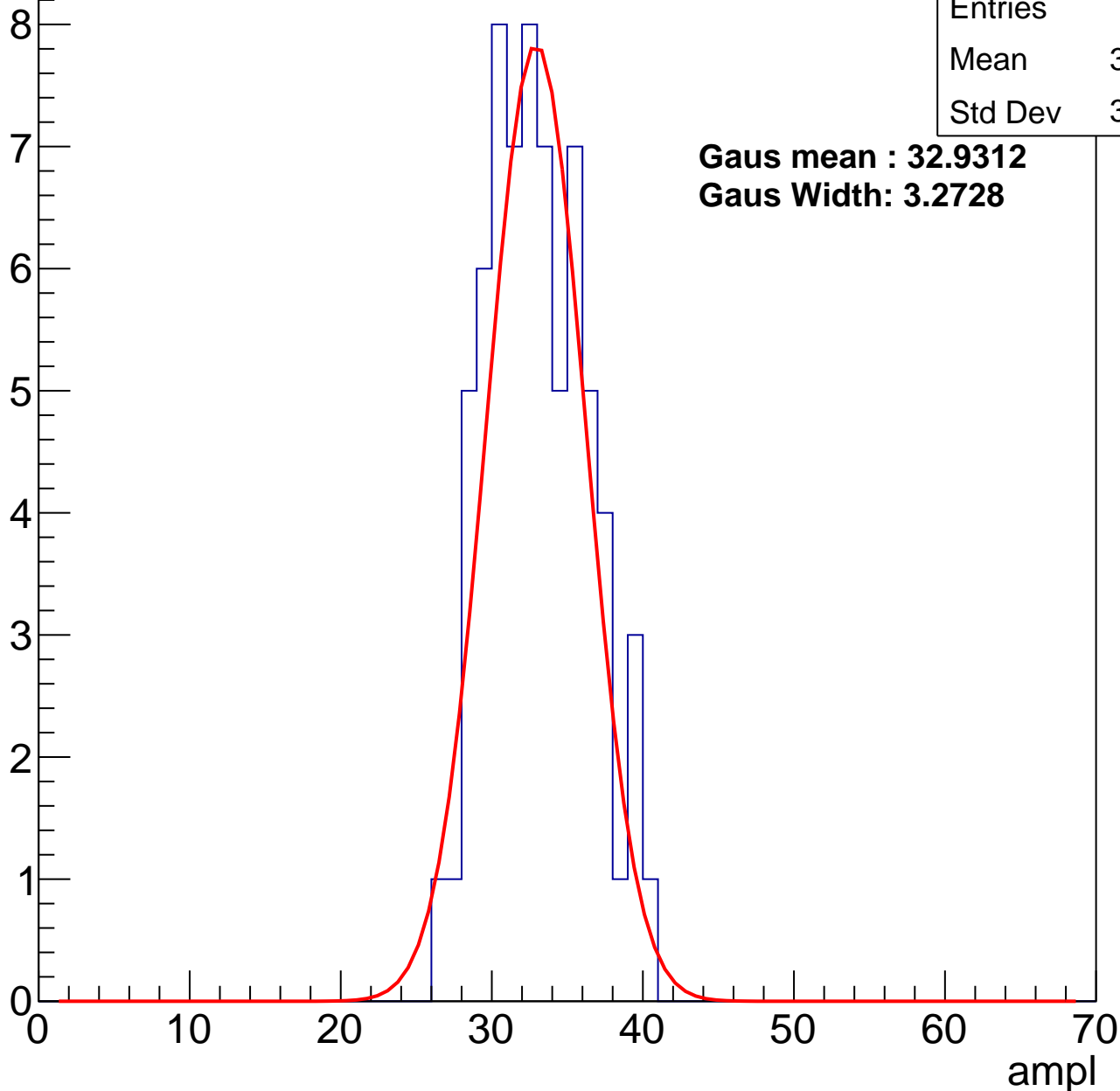
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	32.59
Std Dev	3.245

**Gaus mean : 32.9312**

**Gaus Width: 3.2728**



# B0L000S, U10-ch71, adc1

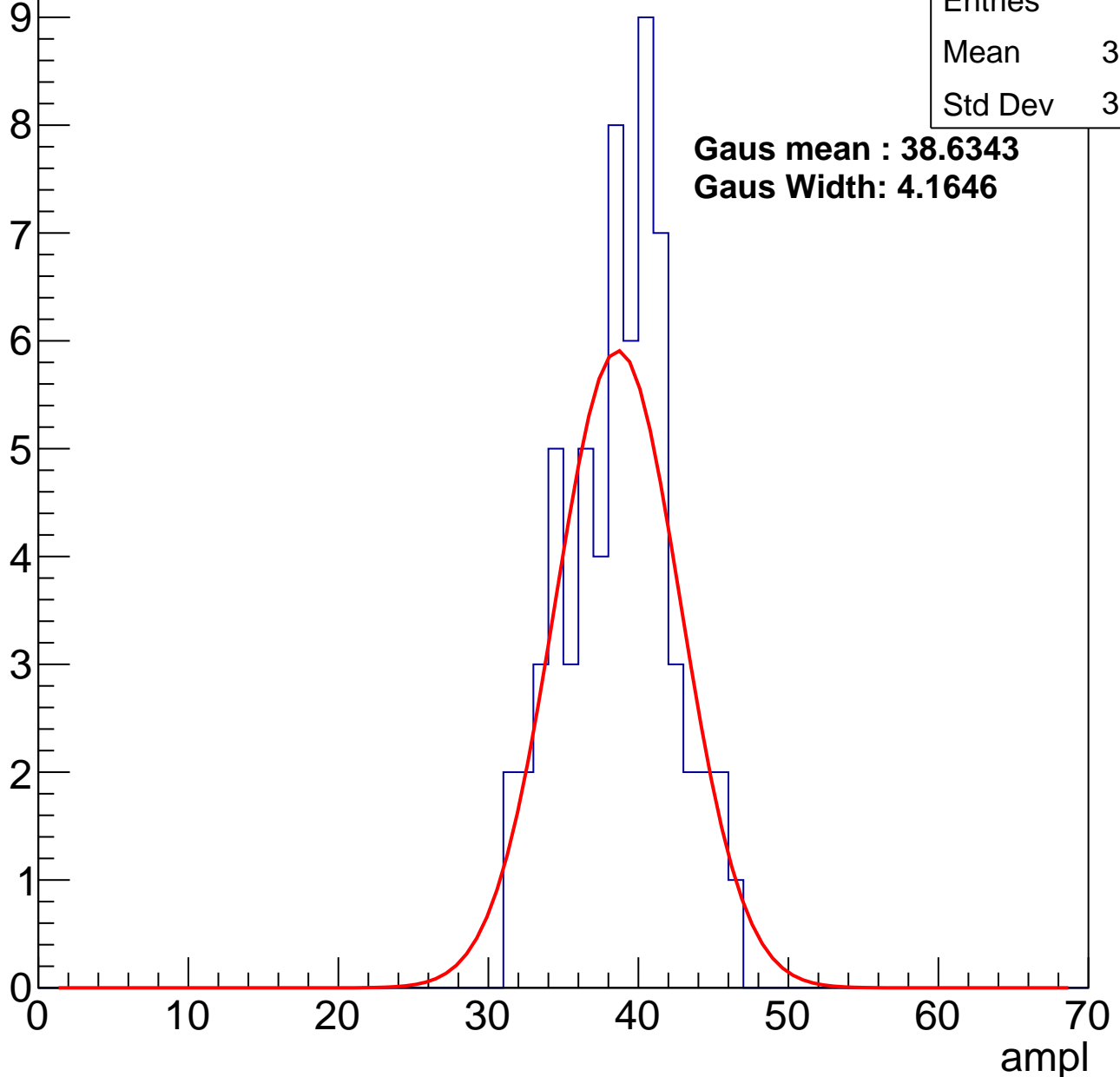
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	38.27
Std Dev	3.554

**Gaus mean : 38.6343**

**Gaus Width: 4.1646**



# B0L000S, U10-ch71, adc2

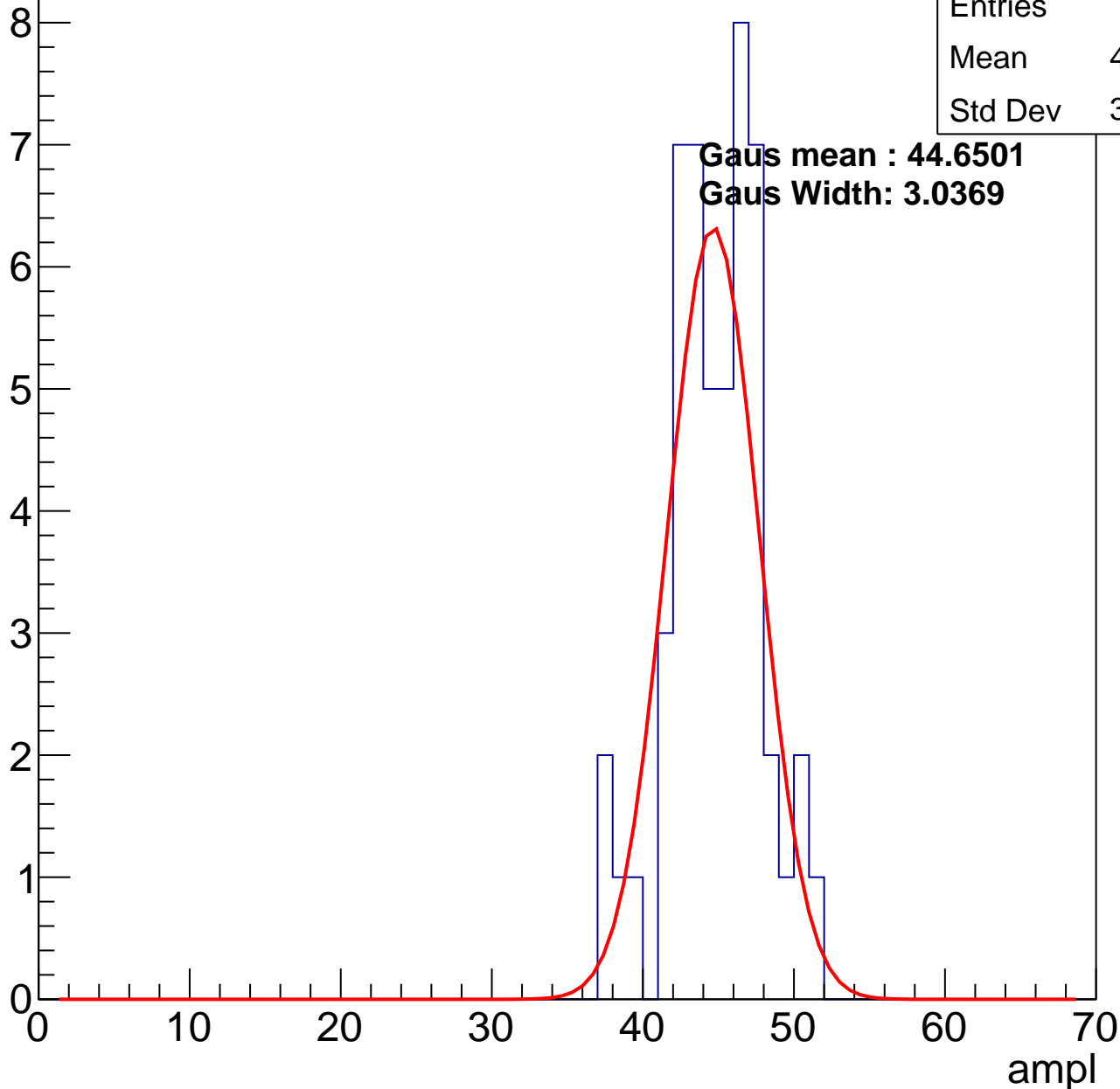
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.37
Std Dev	3.076

**Gaus mean : 44.6501**

**Gaus Width: 3.0369**

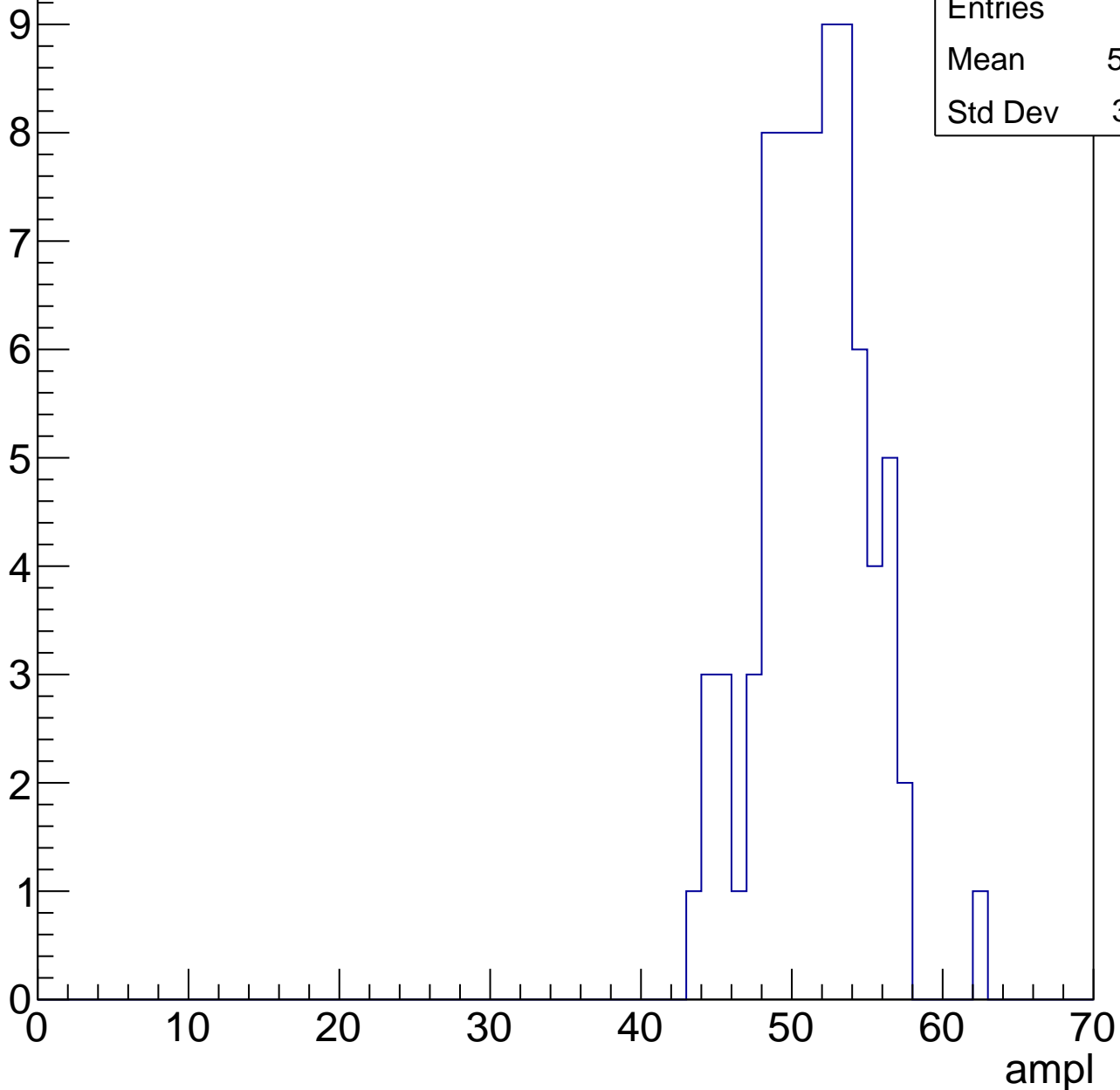


# B0L000S, U10-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	50.96
Std Dev	3.531



# B0L000S, U10-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	56.98
Std Dev	7.611

Entry

10

8

6

4

2

0

0

10

20

30

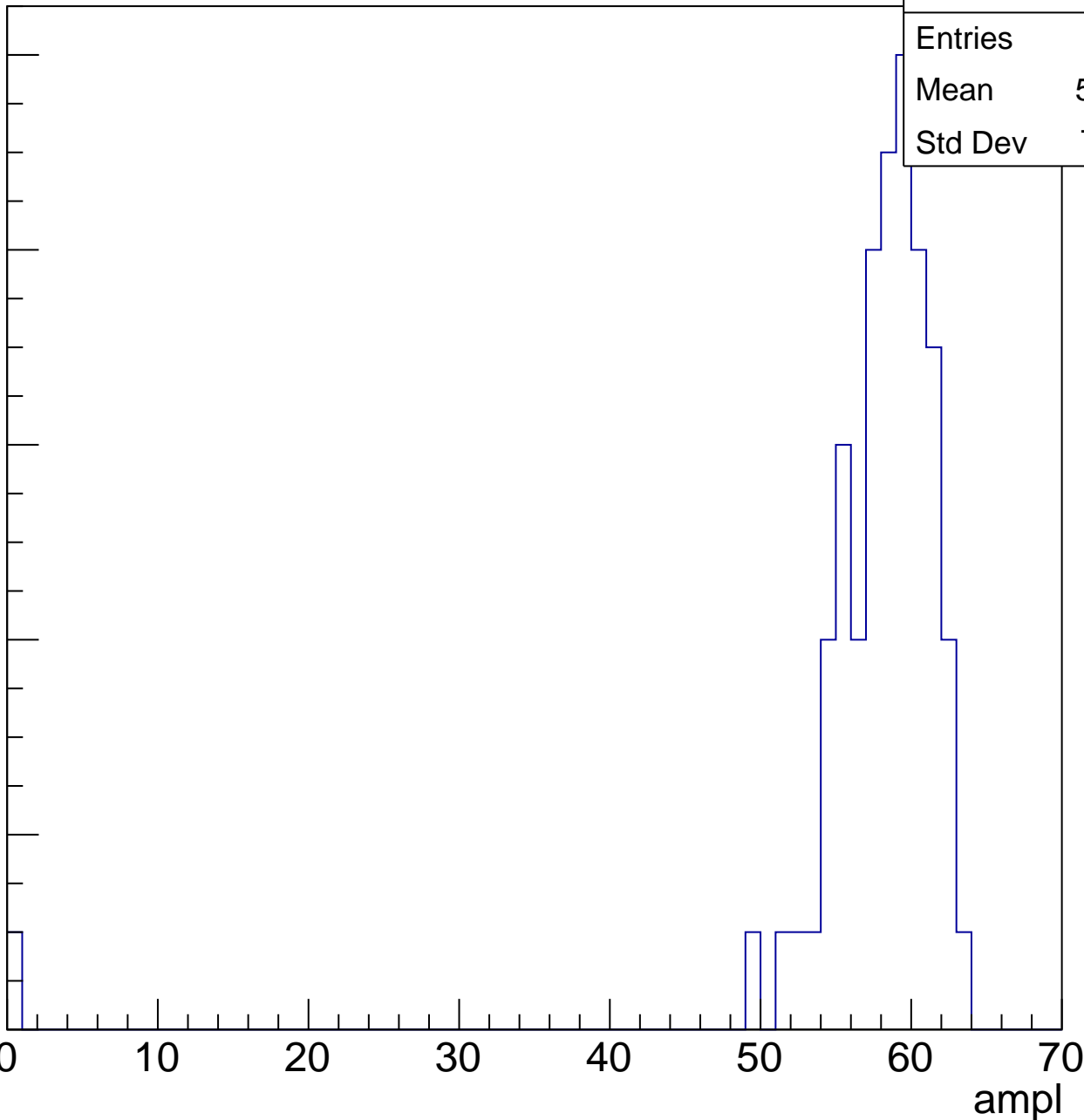
40

50

60

70

ampl

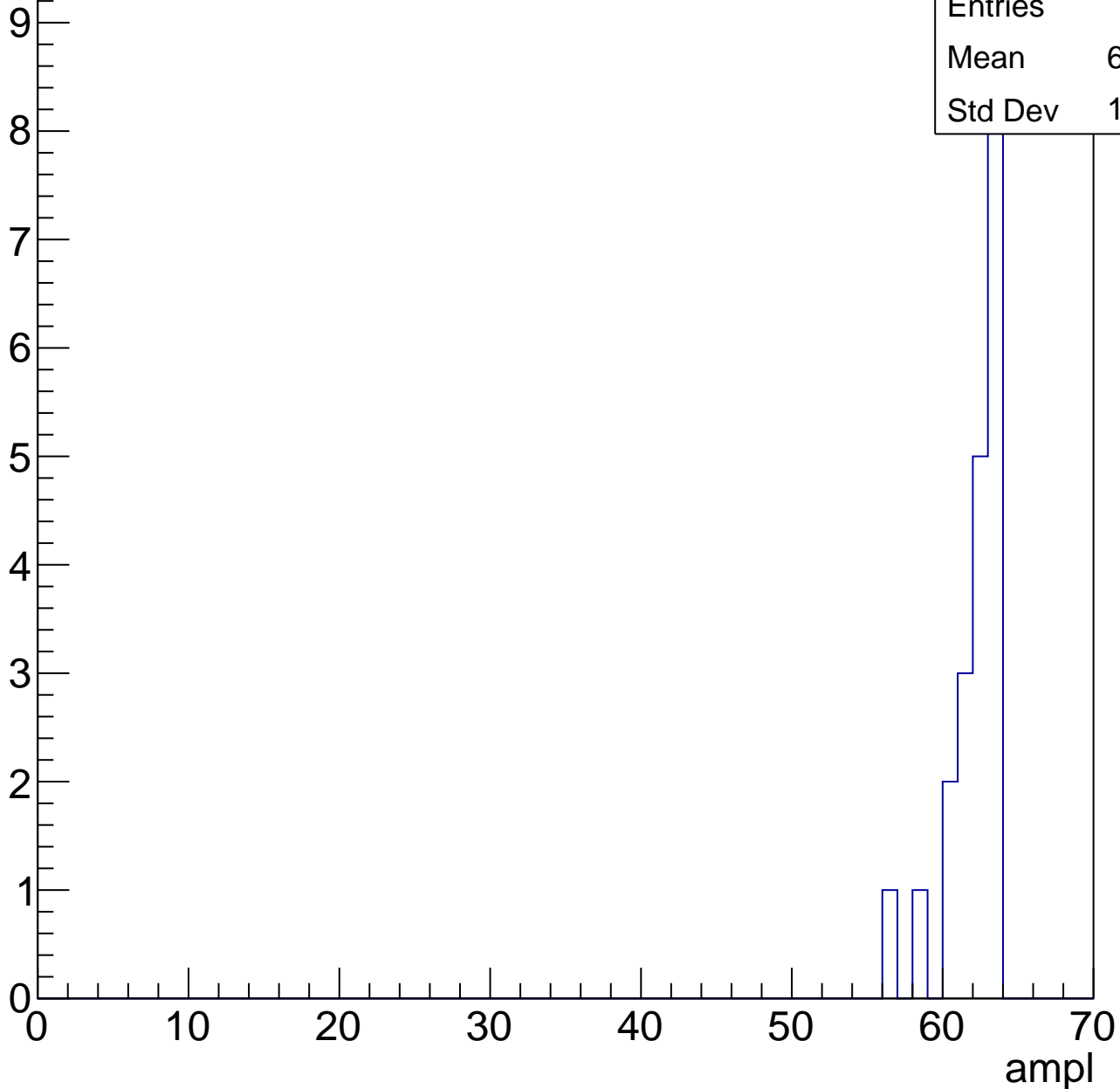


# B0L000S, U10-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	61.62
Std Dev	1.812



# B0L000S, U10-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch72, adc0

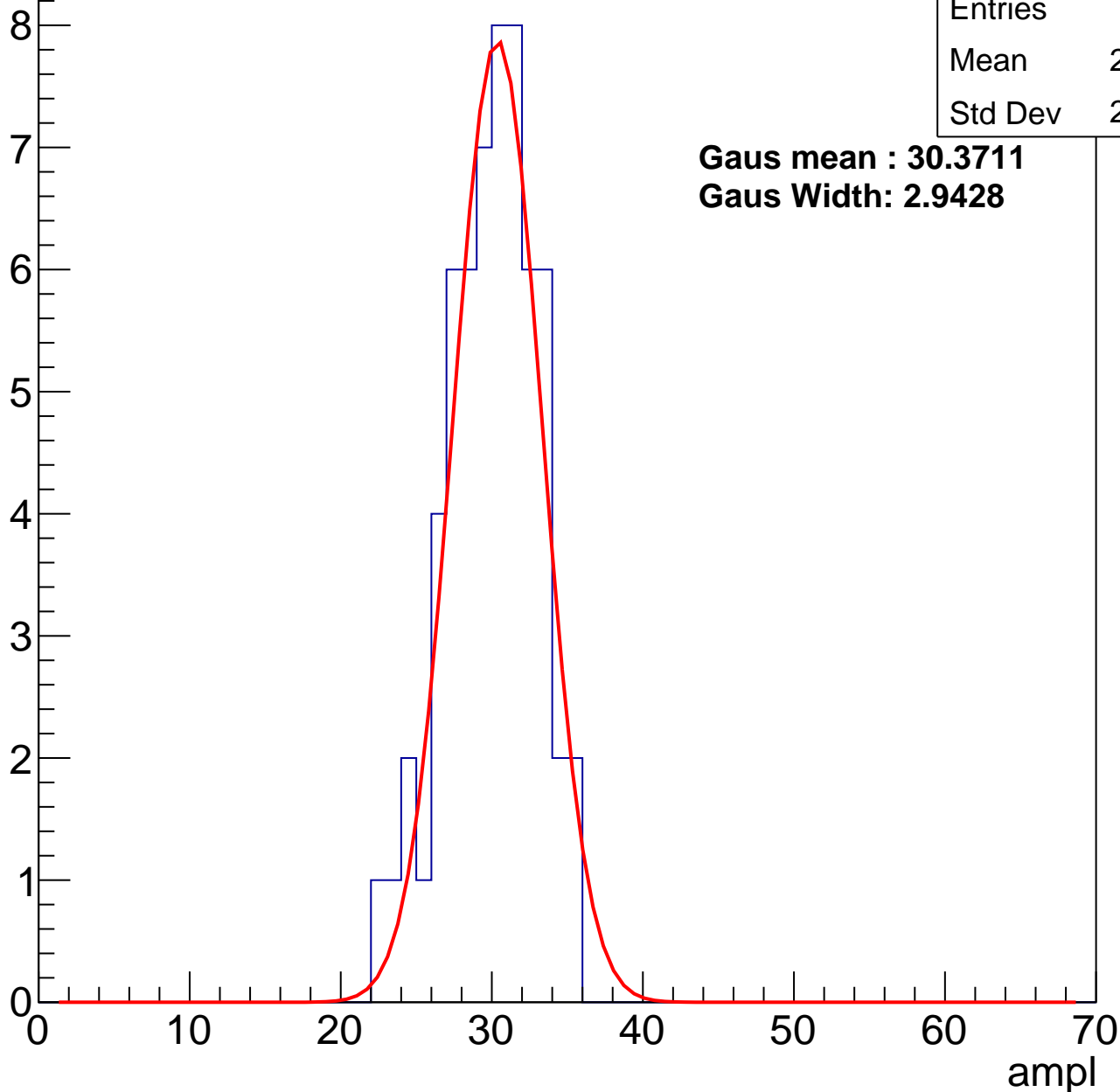
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.52
Std Dev	2.924

**Gaus mean : 30.3711**

**Gaus Width: 2.9428**



# B0L000S, U10-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	36.82
Std Dev	3.185

**Gaus mean : 37.3951**

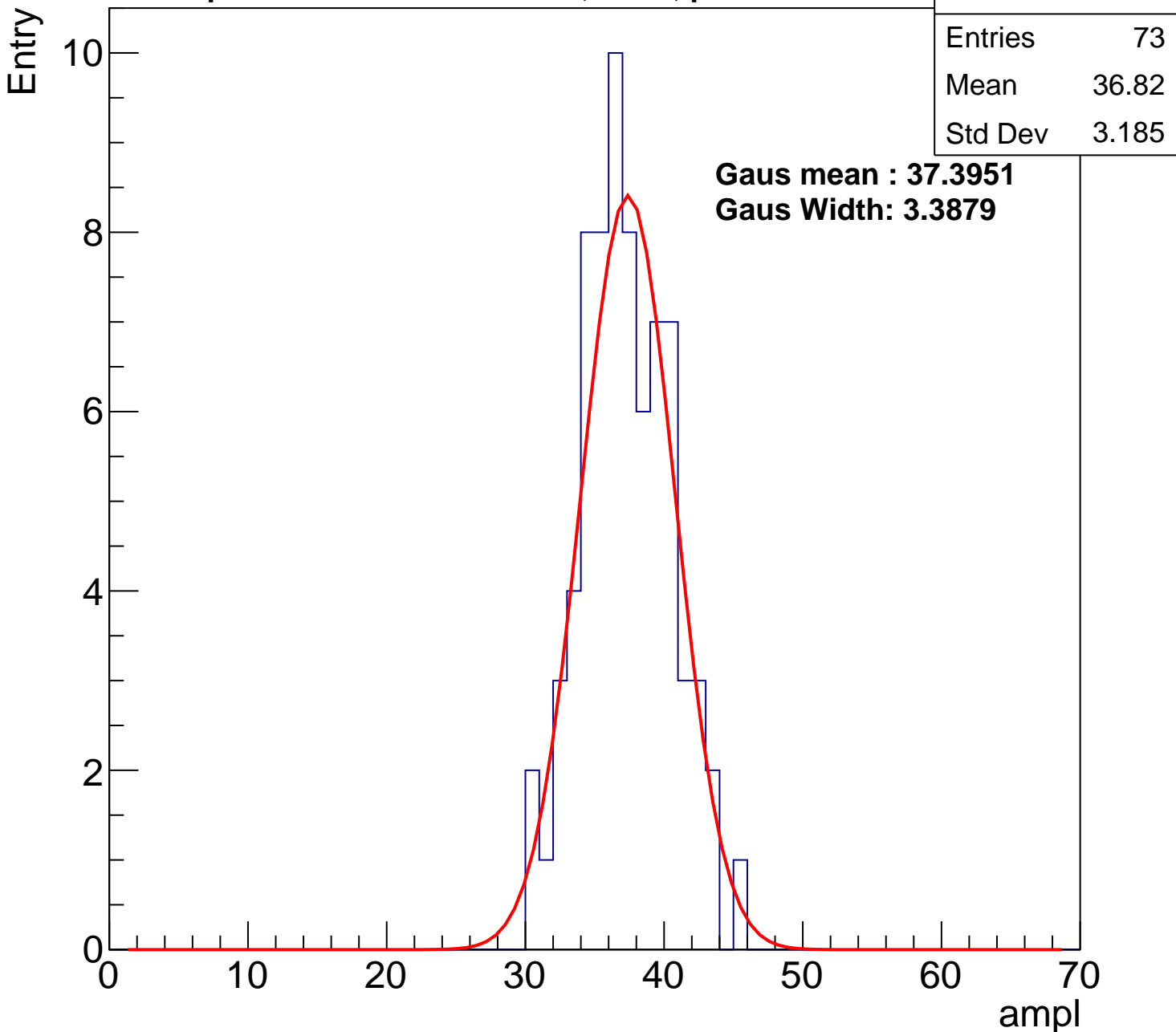
**Gaus Width: 3.3879**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch72, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	43.82
Std Dev	3.429

**Gaus mean : 44.1937**

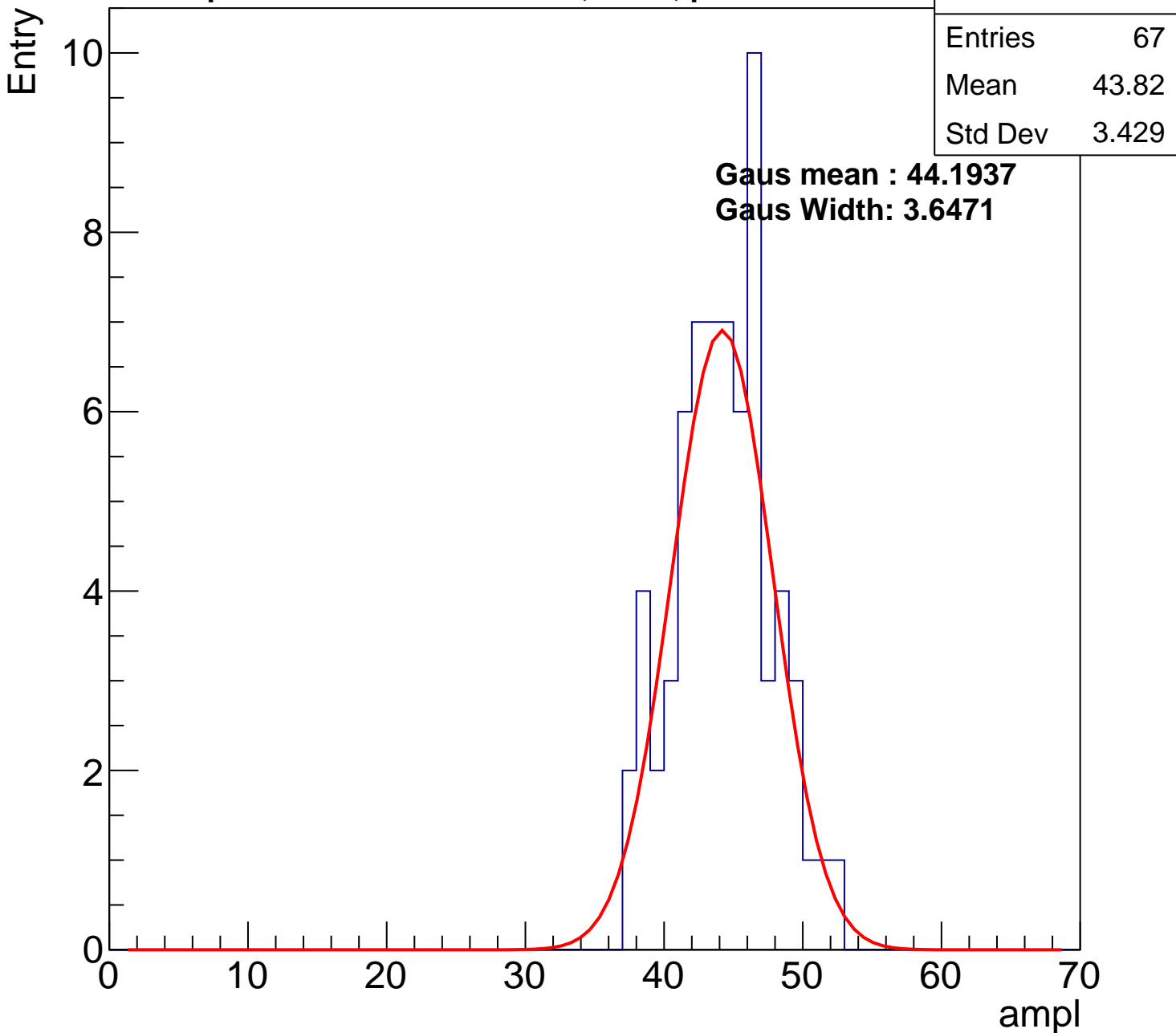
**Gaus Width: 3.6471**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

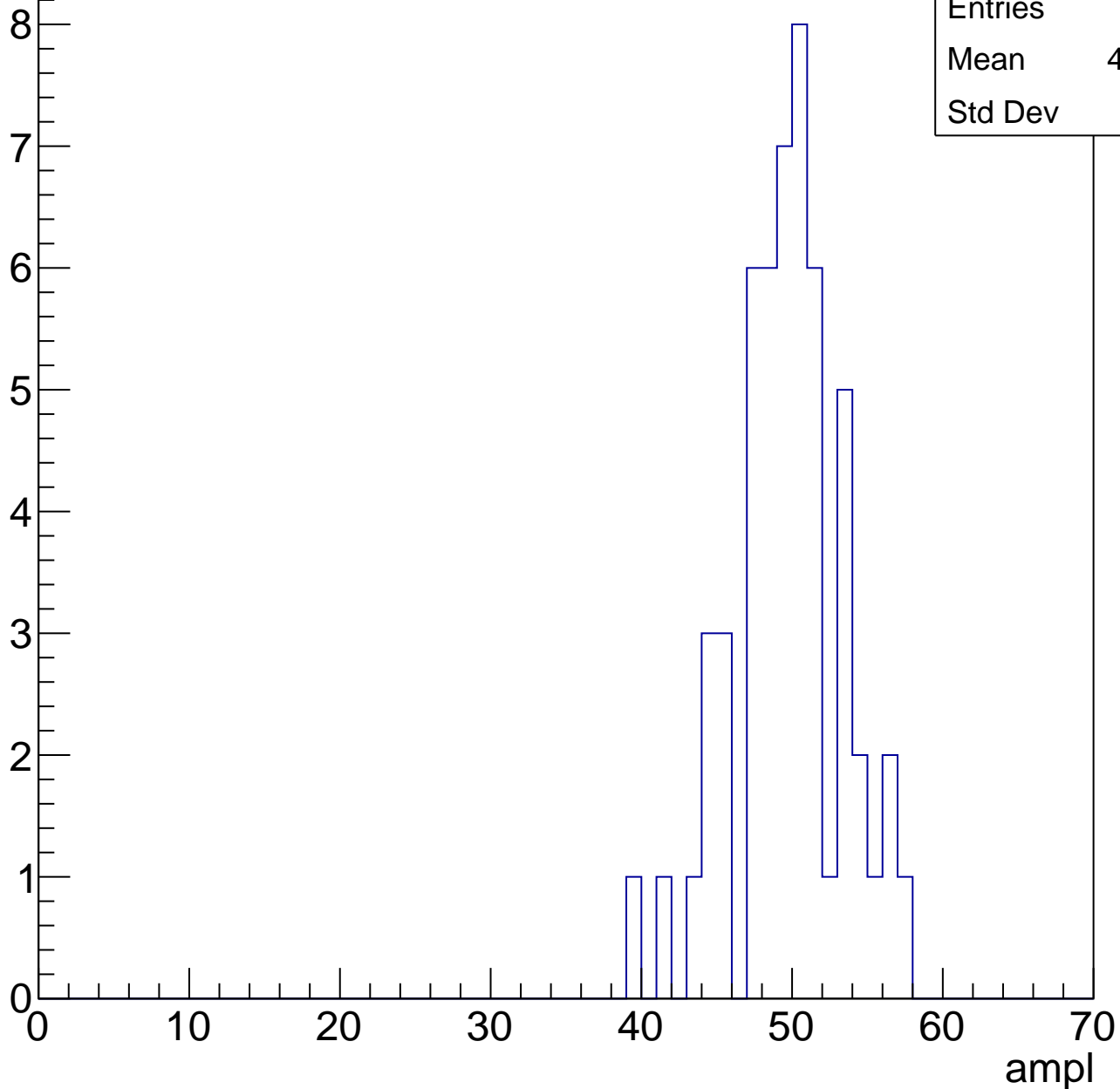


# B0L000S, U10-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	49.22
Std Dev	3.65

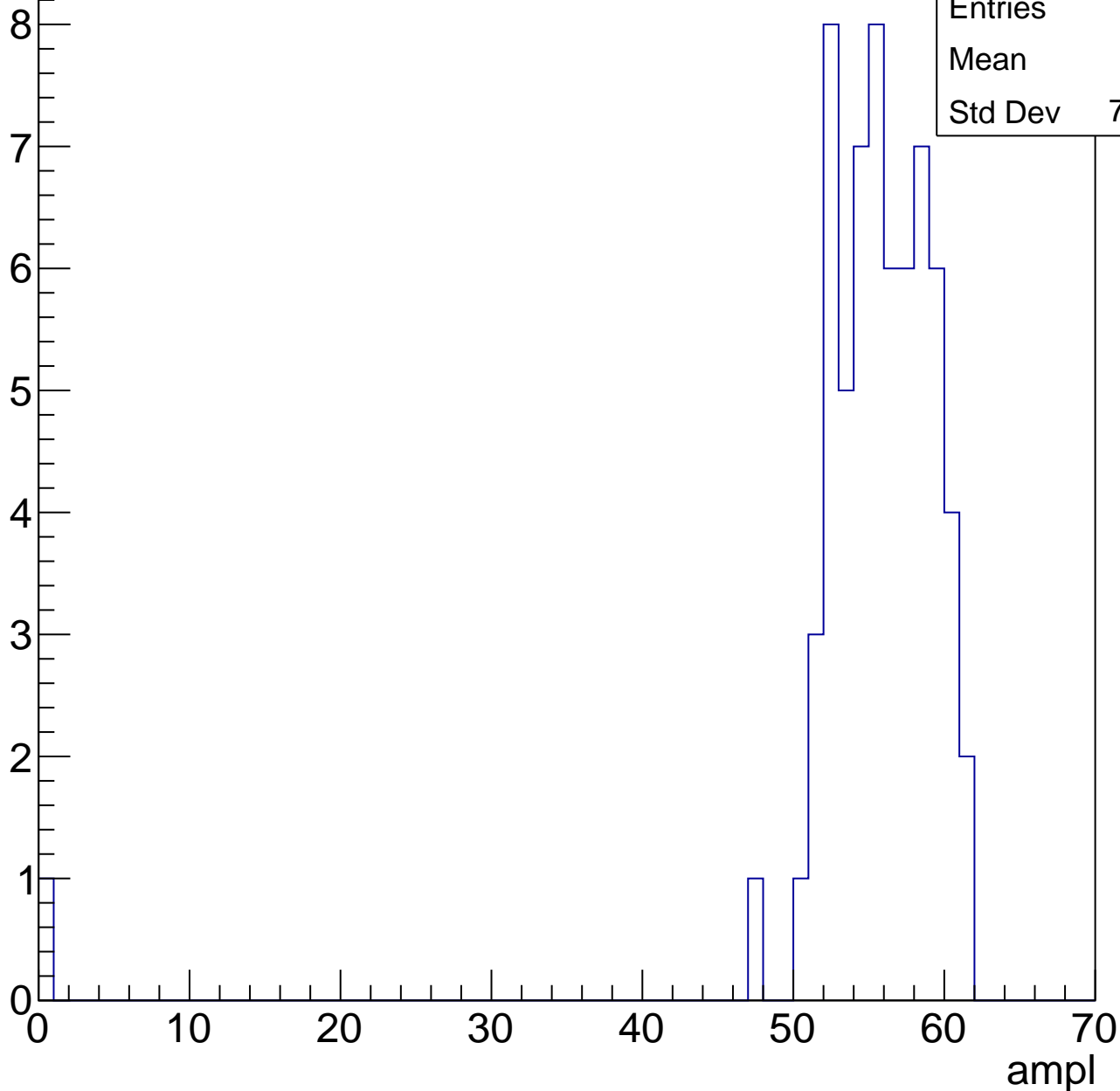


# B0L000S, U10-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	54.6
Std Dev	7.454

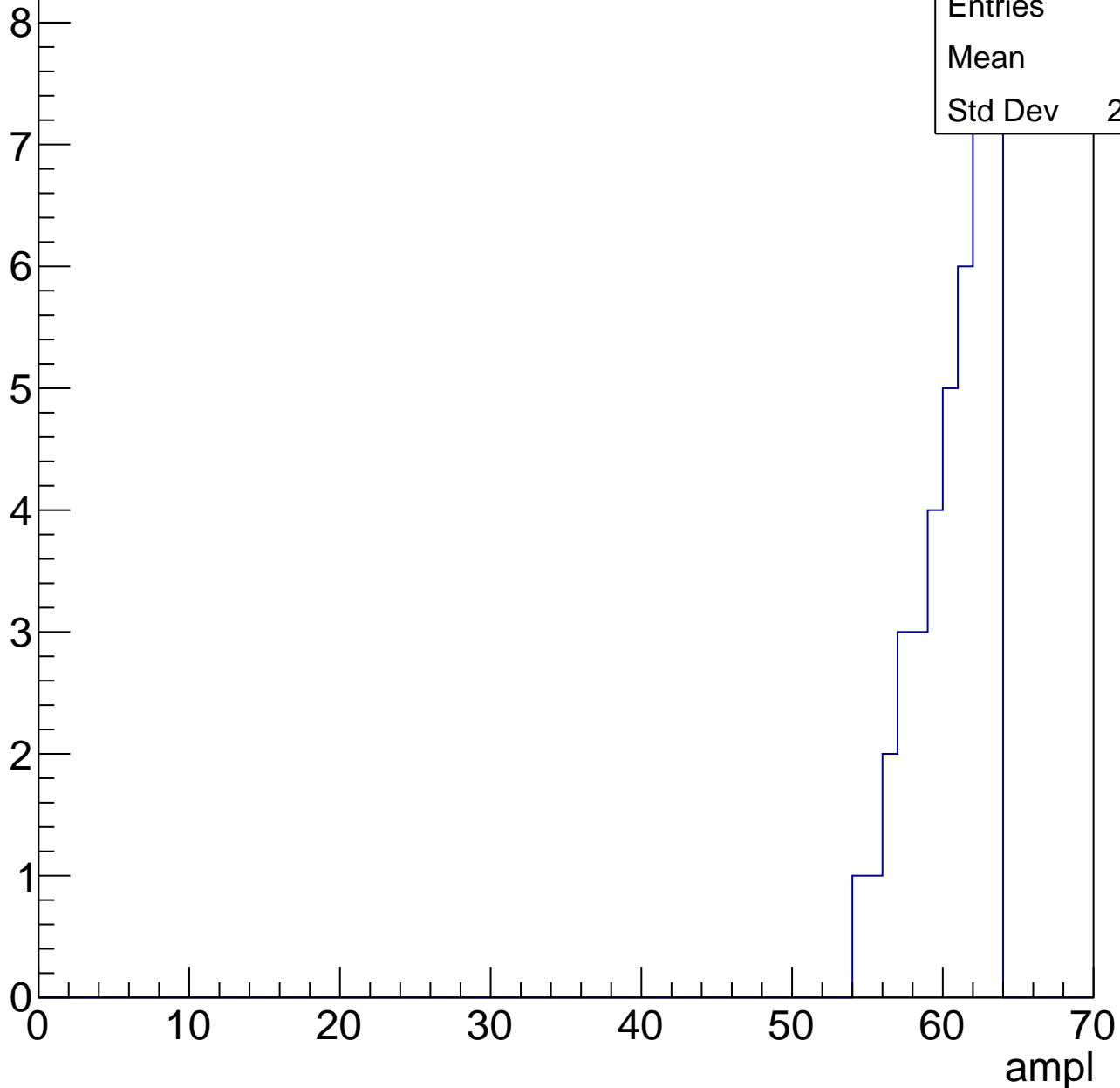


# B0L000S, U10-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	60.2
Std Dev	2.442



# B0L000S, U10-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.71
Std Dev	1.03

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	29.61
Std Dev	4.905

**Gaus mean : 30.5304**

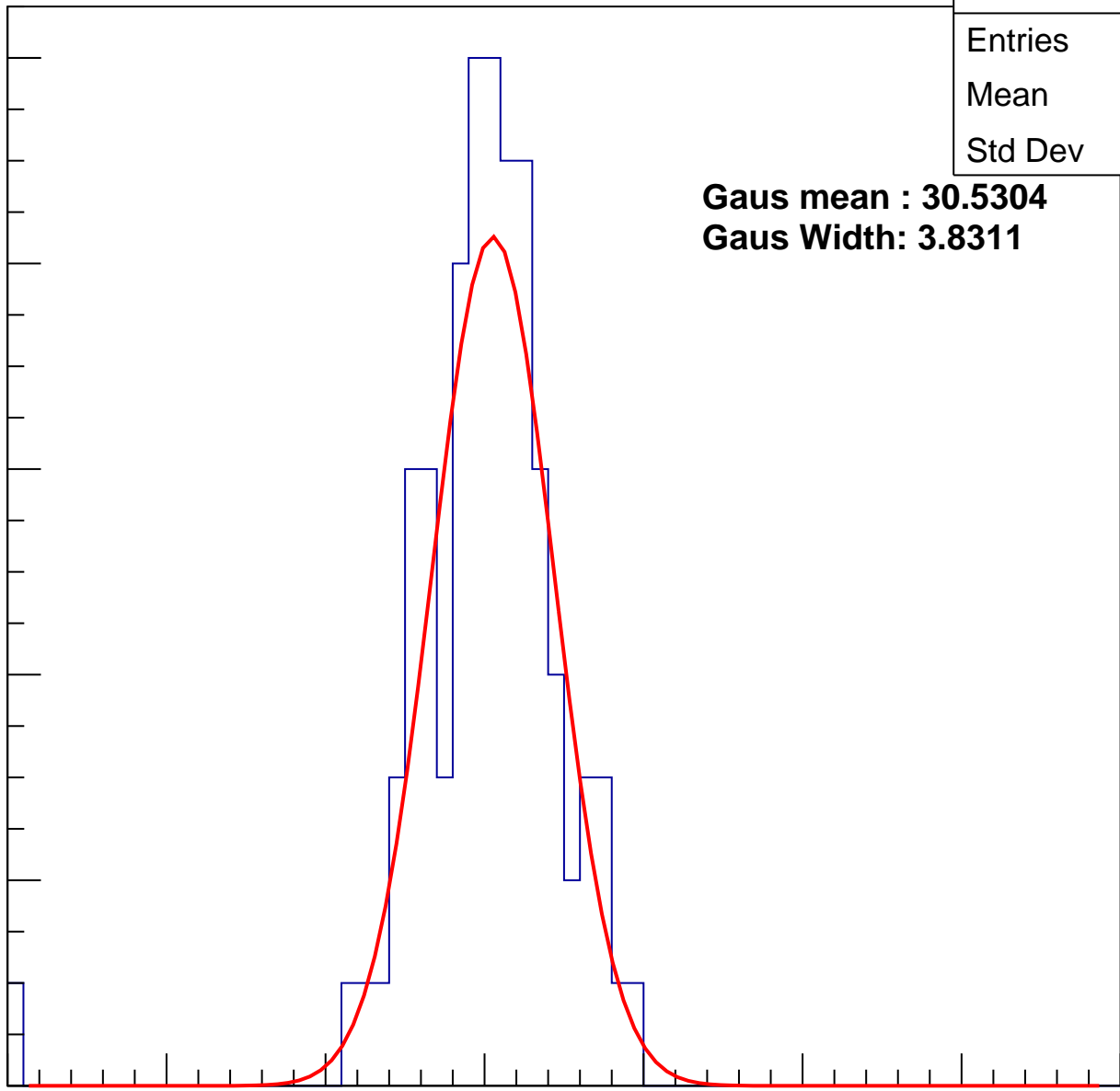
**Gaus Width: 3.8311**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch73, adc1

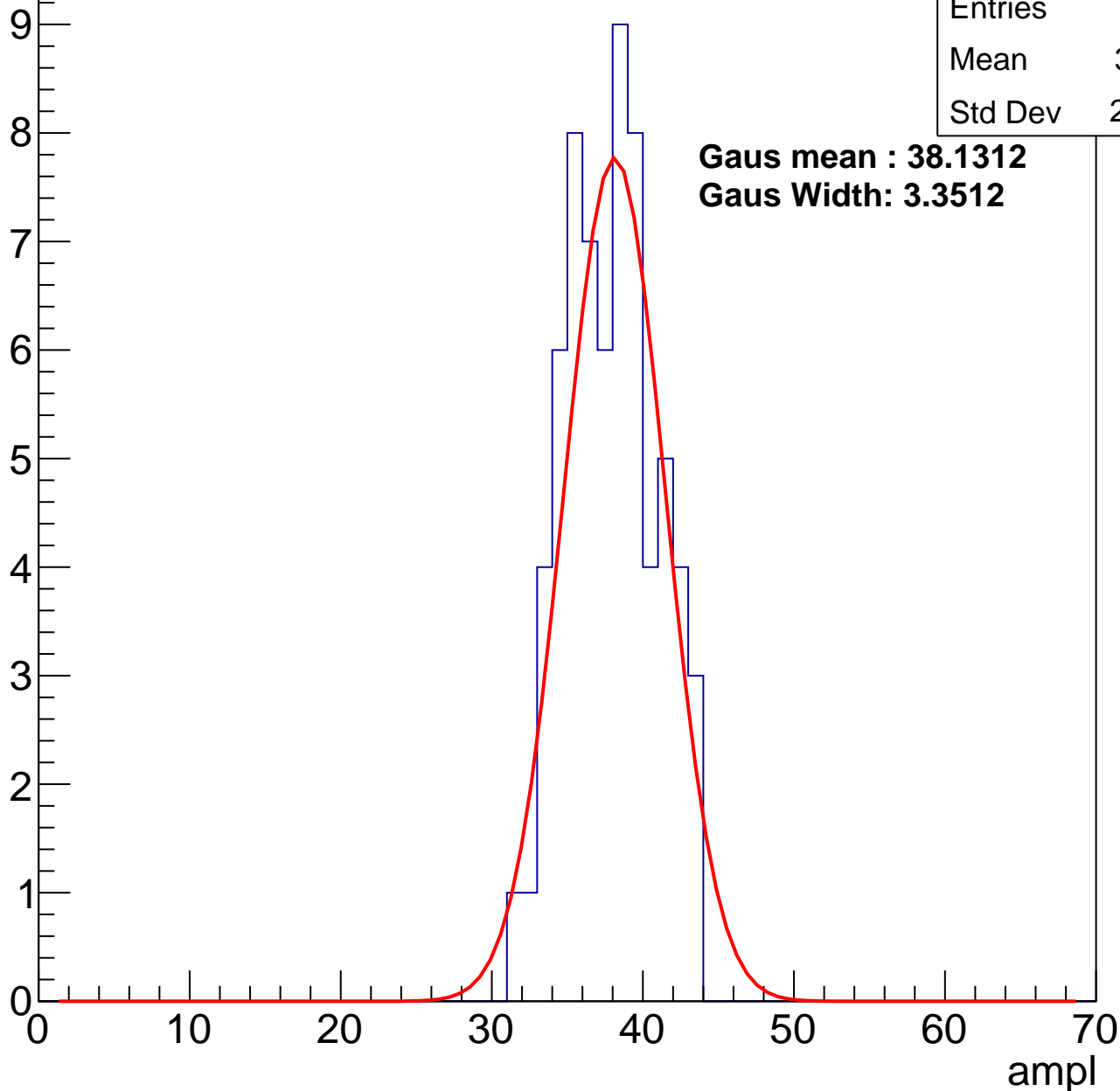
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.41
Std Dev	2.944

**Gaus mean : 38.1312**

**Gaus Width: 3.3512**



# B0L000S, U10-ch73, adc2

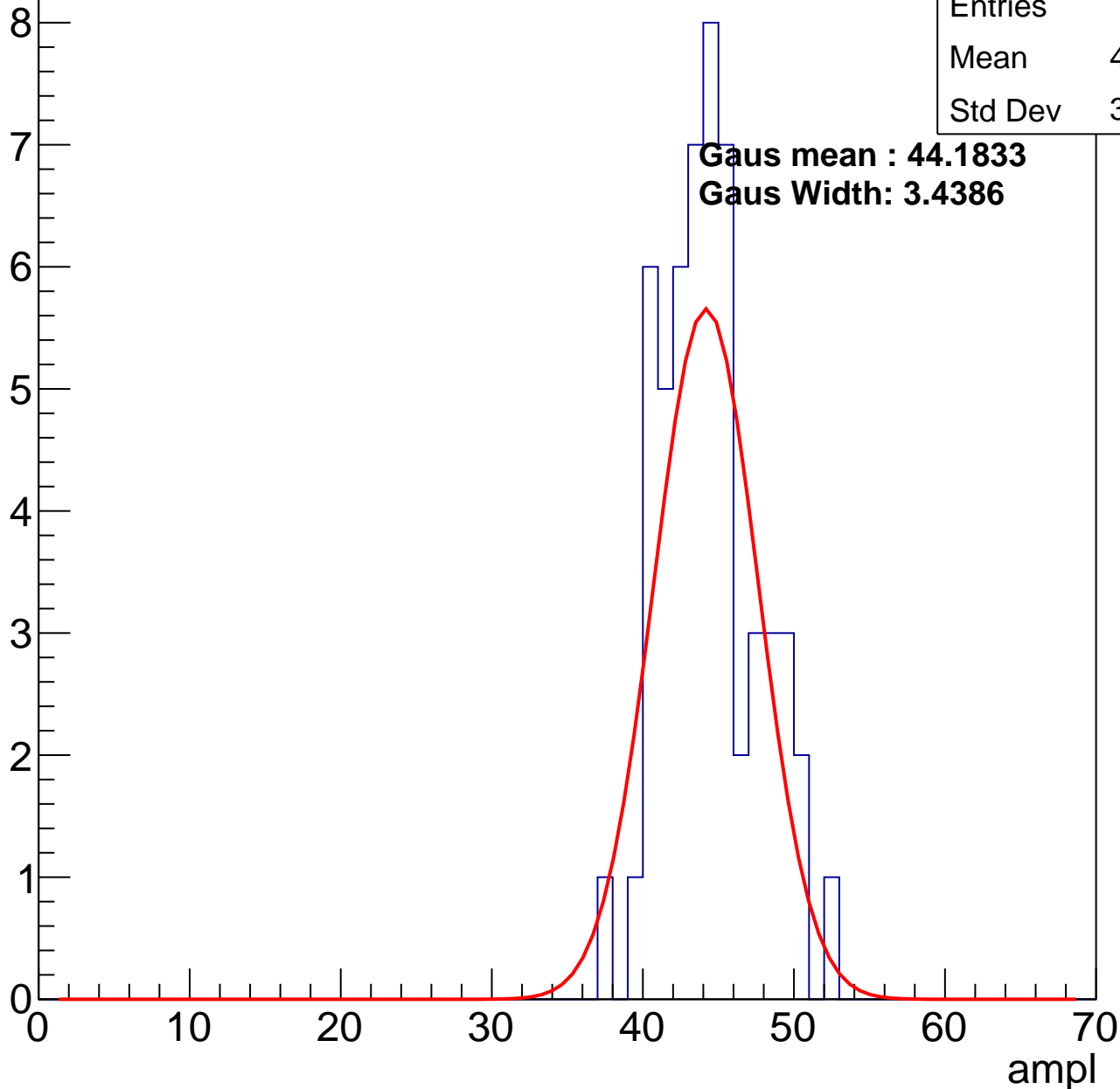
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	43.95
Std Dev	3.153

**Gaus mean : 44.1833**

**Gaus Width: 3.4386**

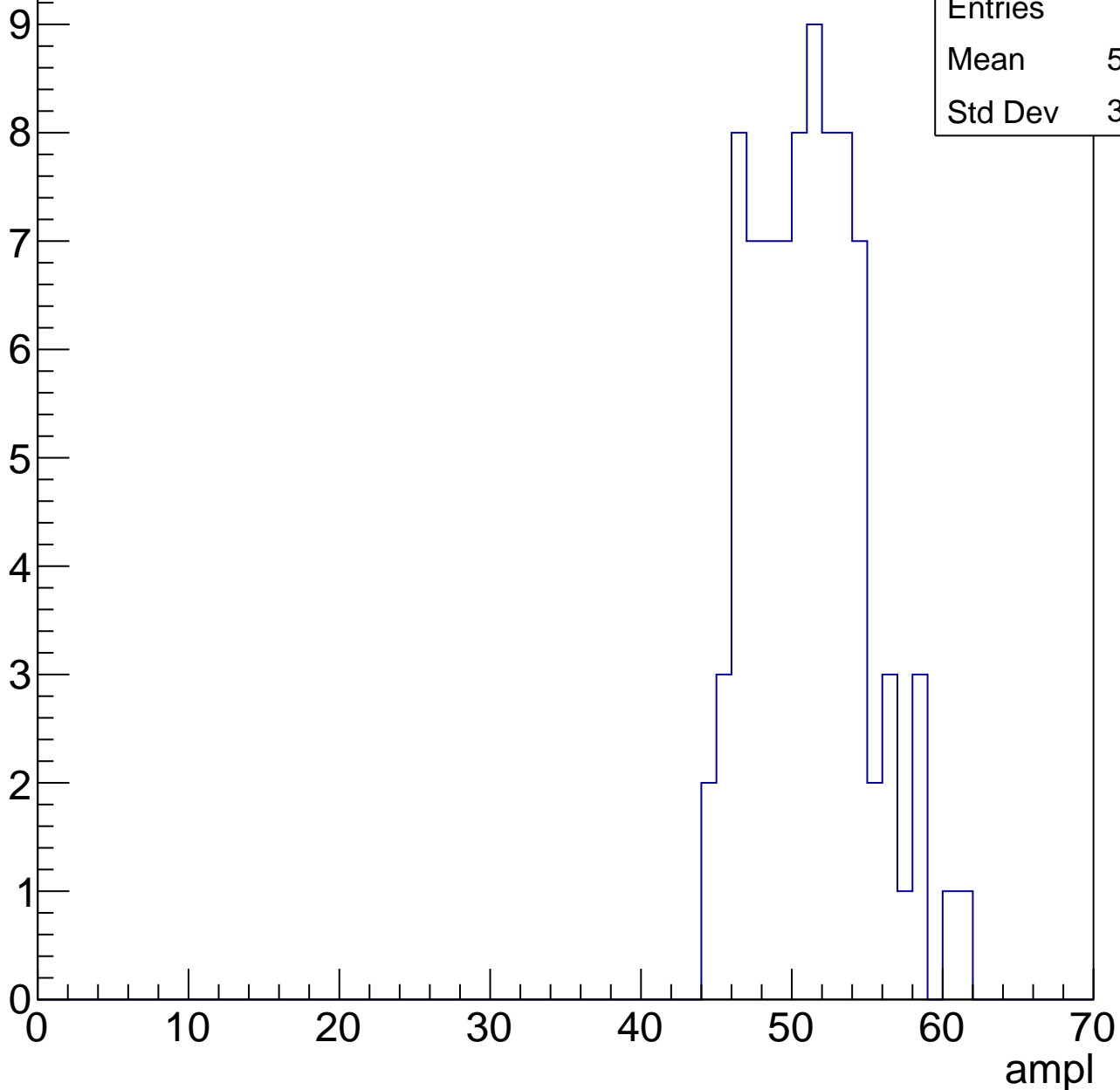


# B0L000S, U10-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	50.66
Std Dev	3.728

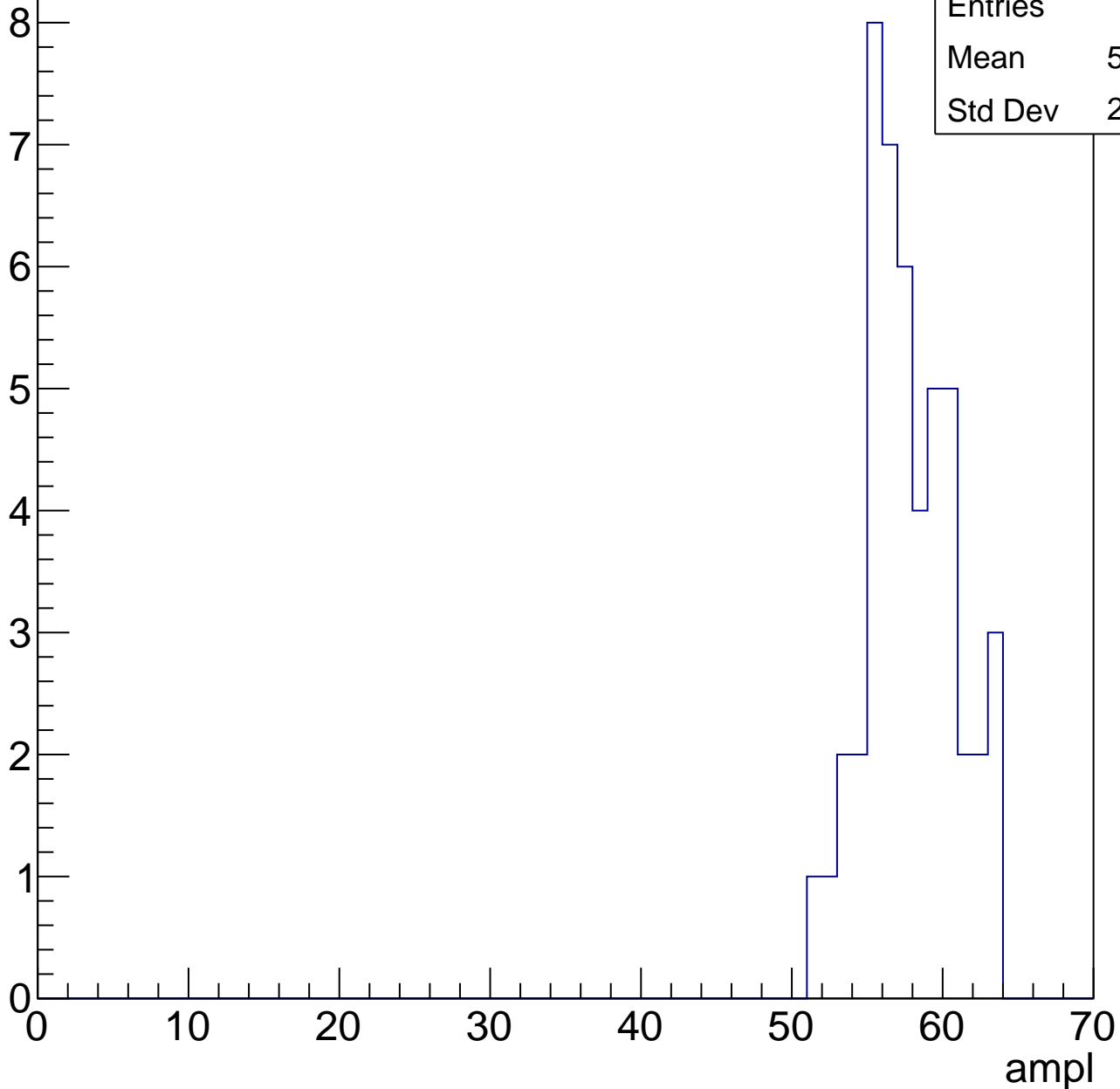


# B0L000S, U10-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	57.35
Std Dev	2.897

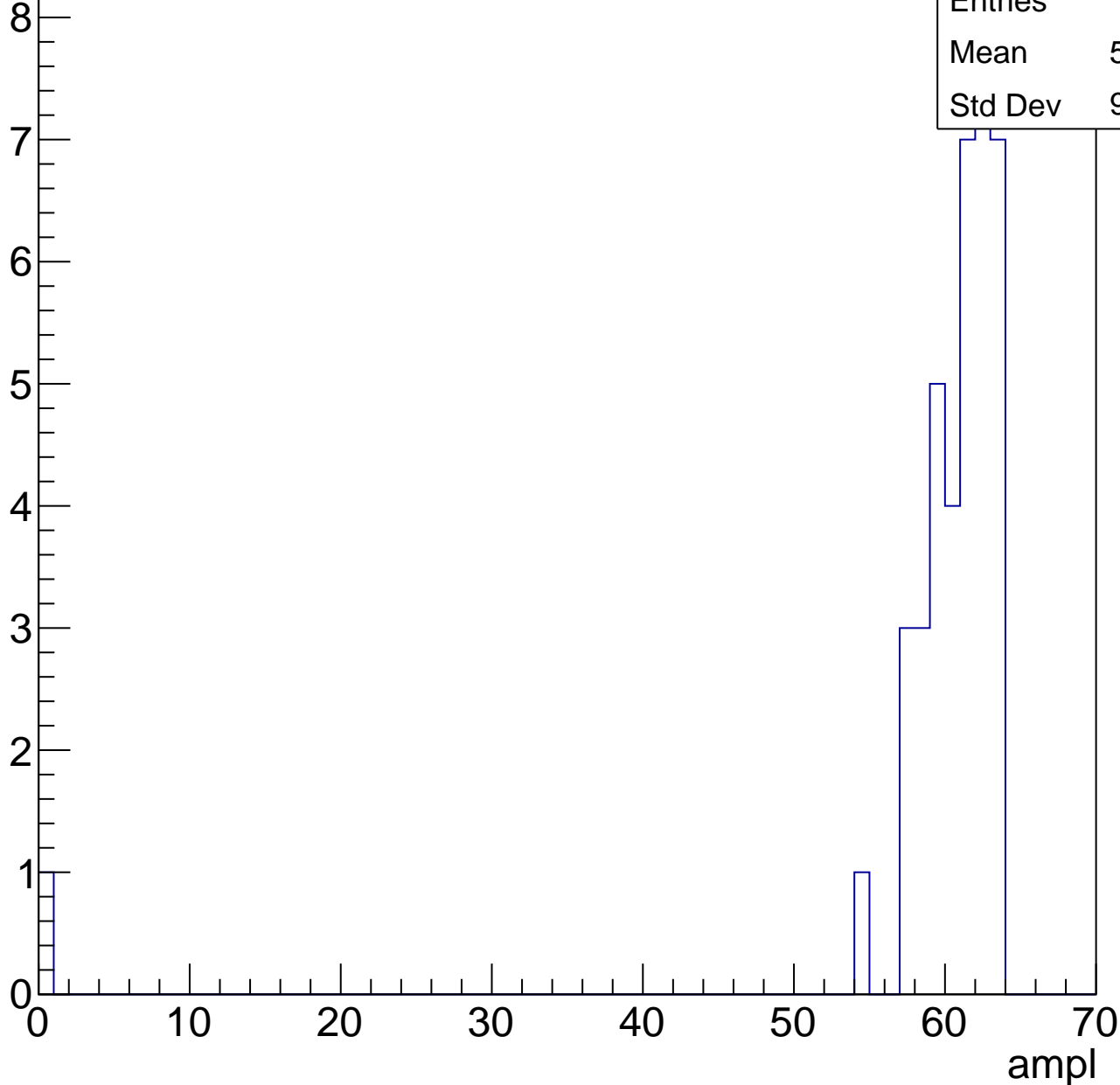


# B0L000S, U10-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.92
Std Dev	9.788



# B0L000S, U10-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch74, adc0

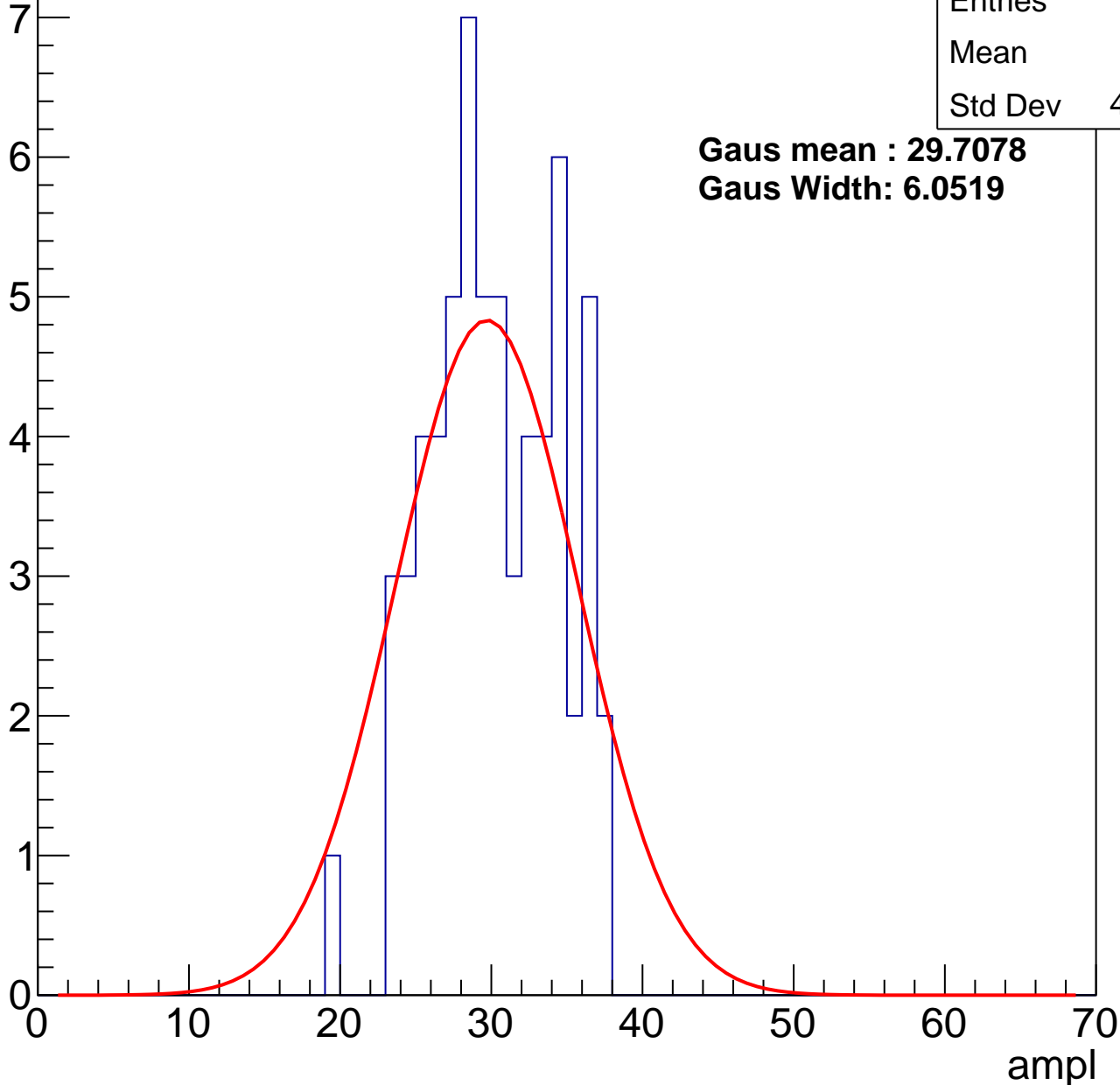
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.7
Std Dev	4.162

**Gaus mean : 29.7078**

**Gaus Width: 6.0519**



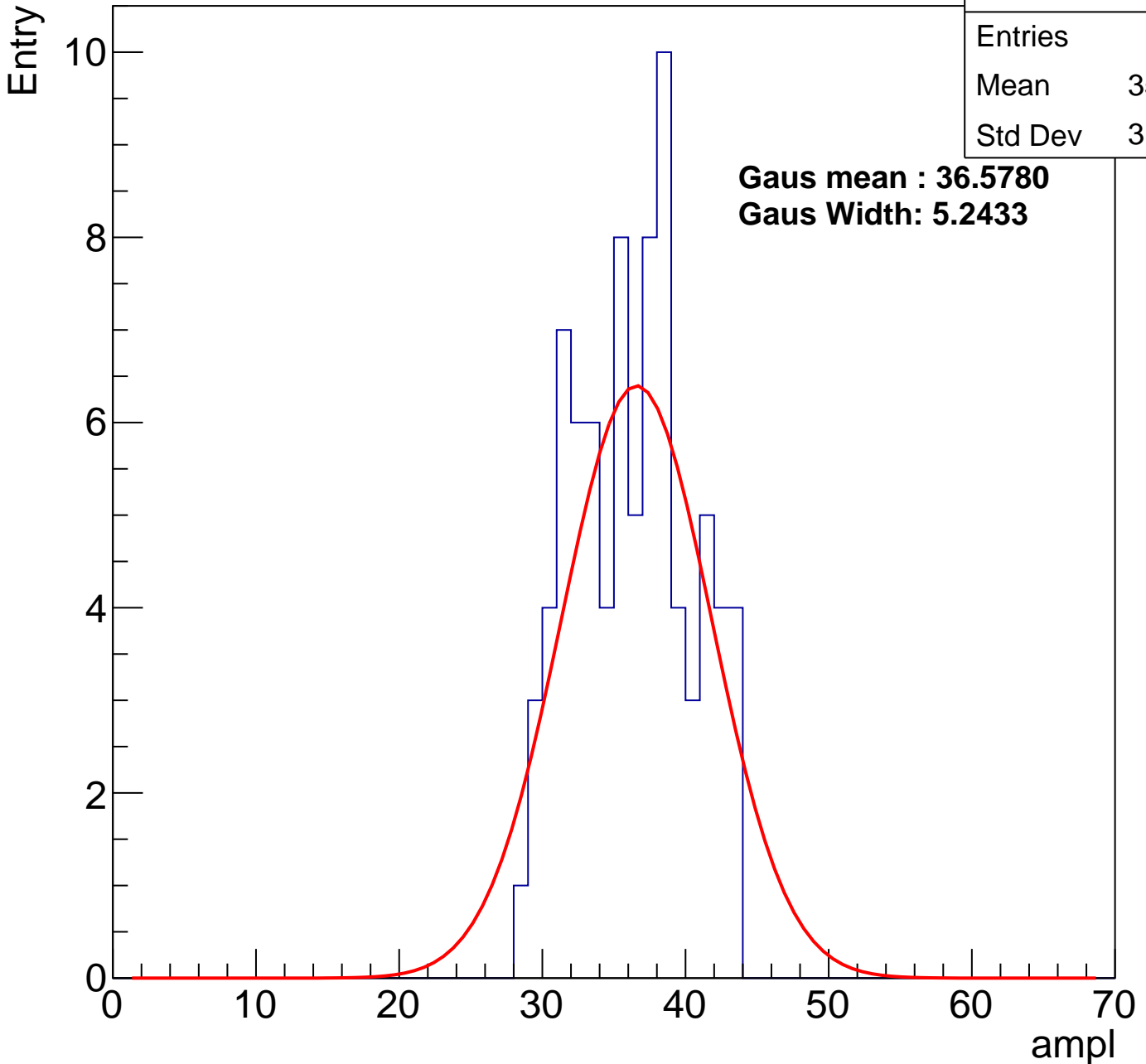
# B0L000S, U10-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	35.79
Std Dev	3.978

**Gaus mean : 36.5780**

**Gaus Width: 5.2433**



# B0L000S, U10-ch74, adc2

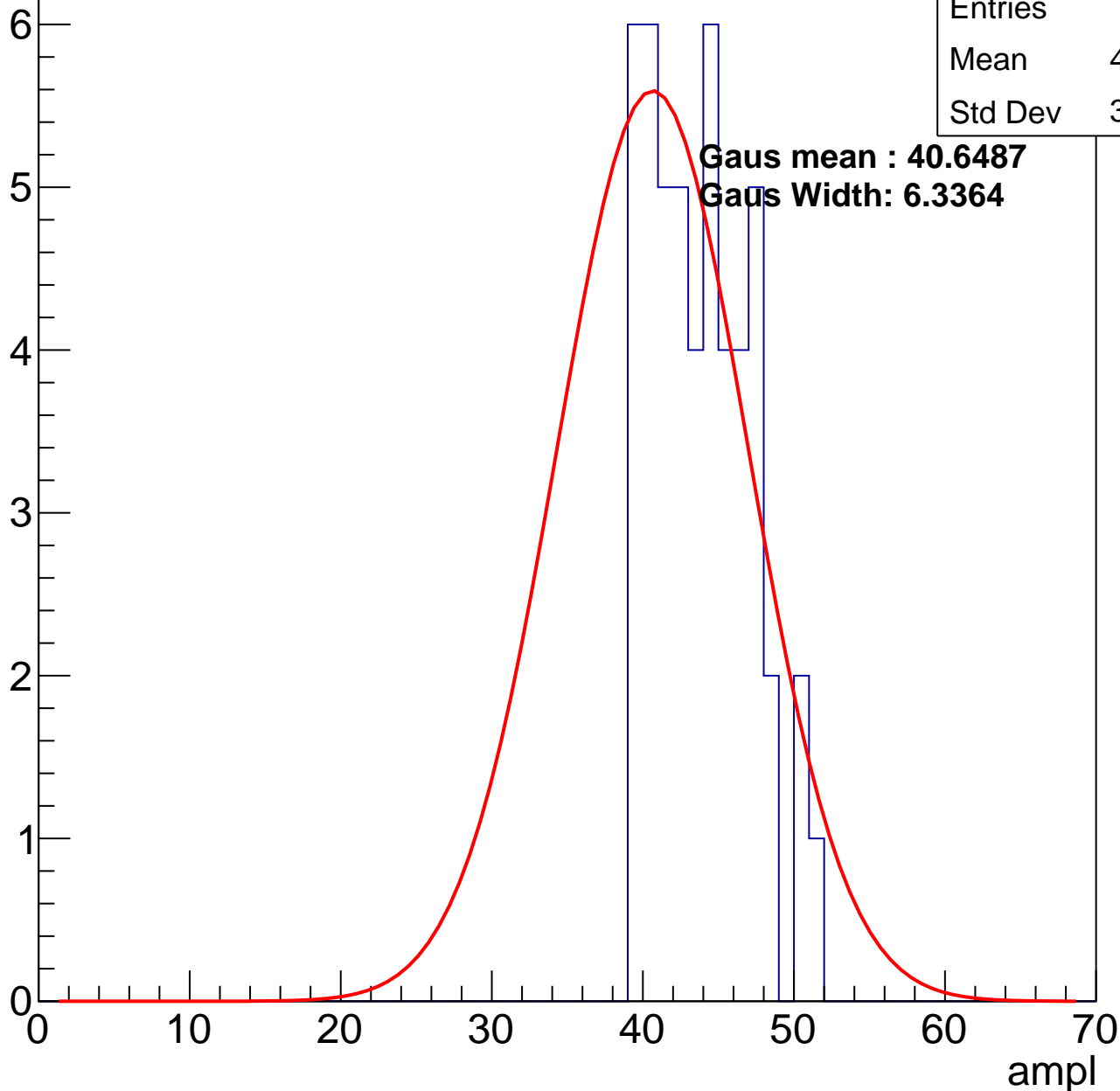
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	43.42
Std Dev	3.213

**Gaus mean : 40.6487**

**Gaus Width: 6.3364**

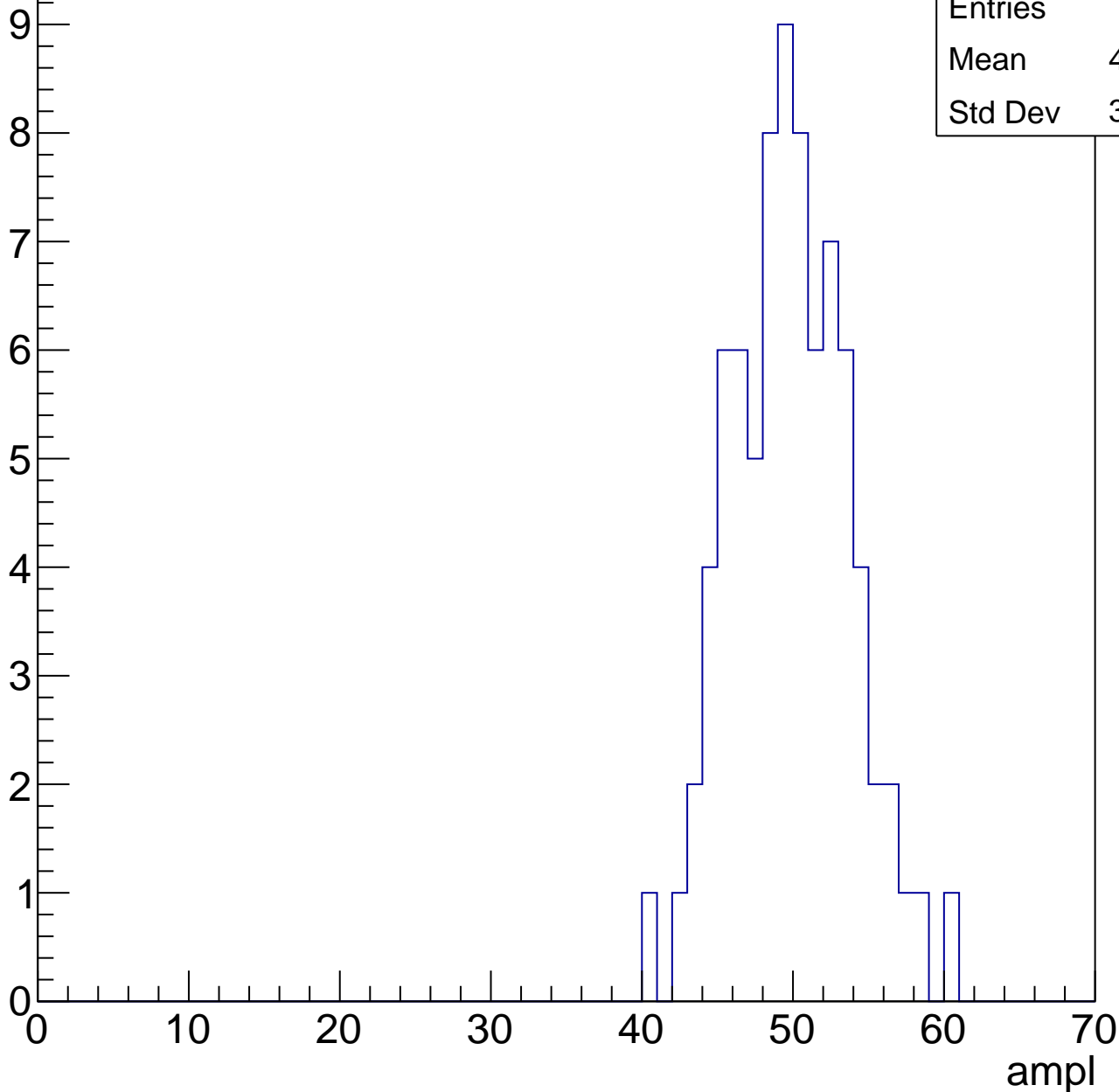


# B0L000S, U10-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	49.39
Std Dev	3.865

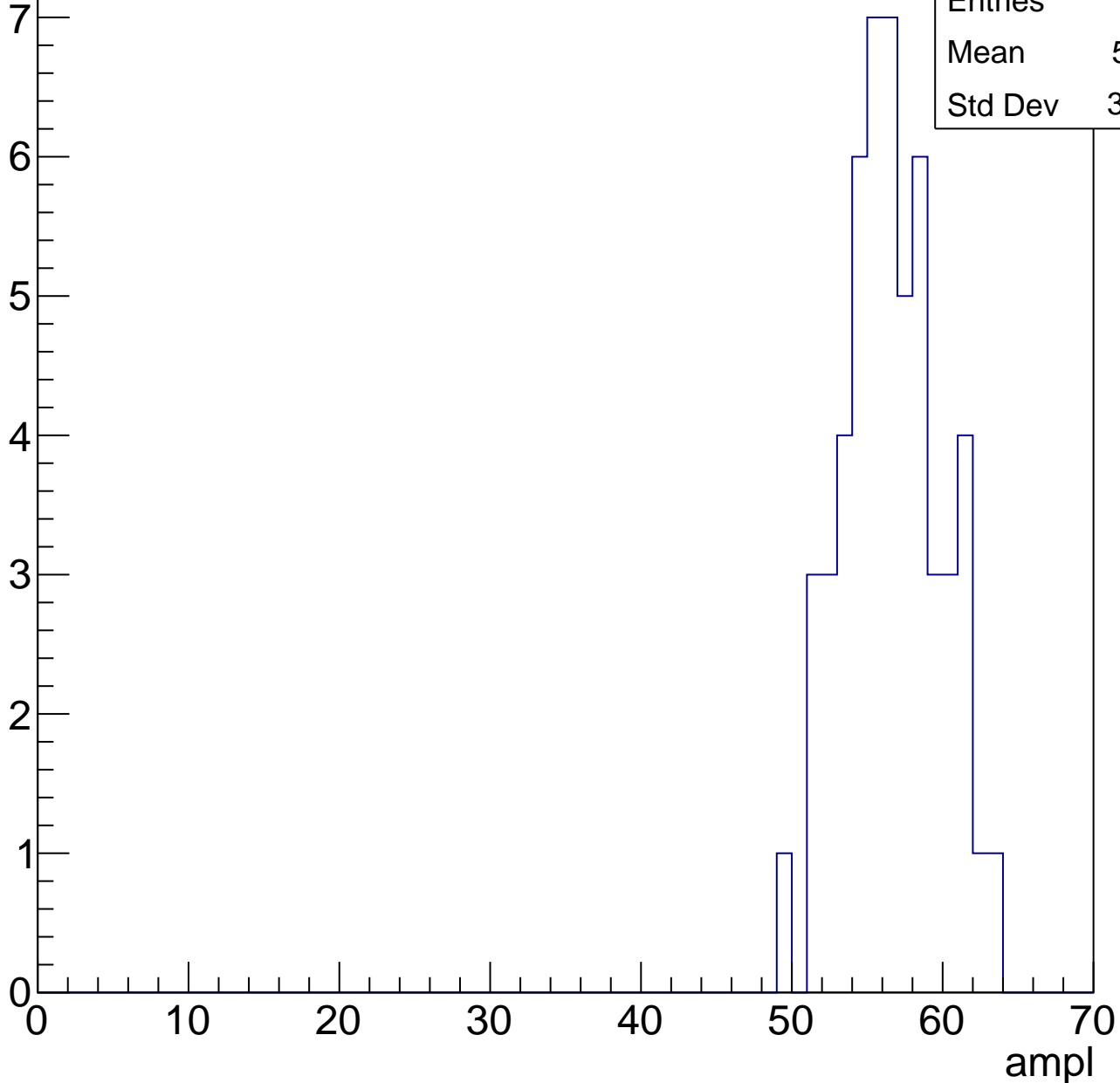


# B0L000S, U10-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

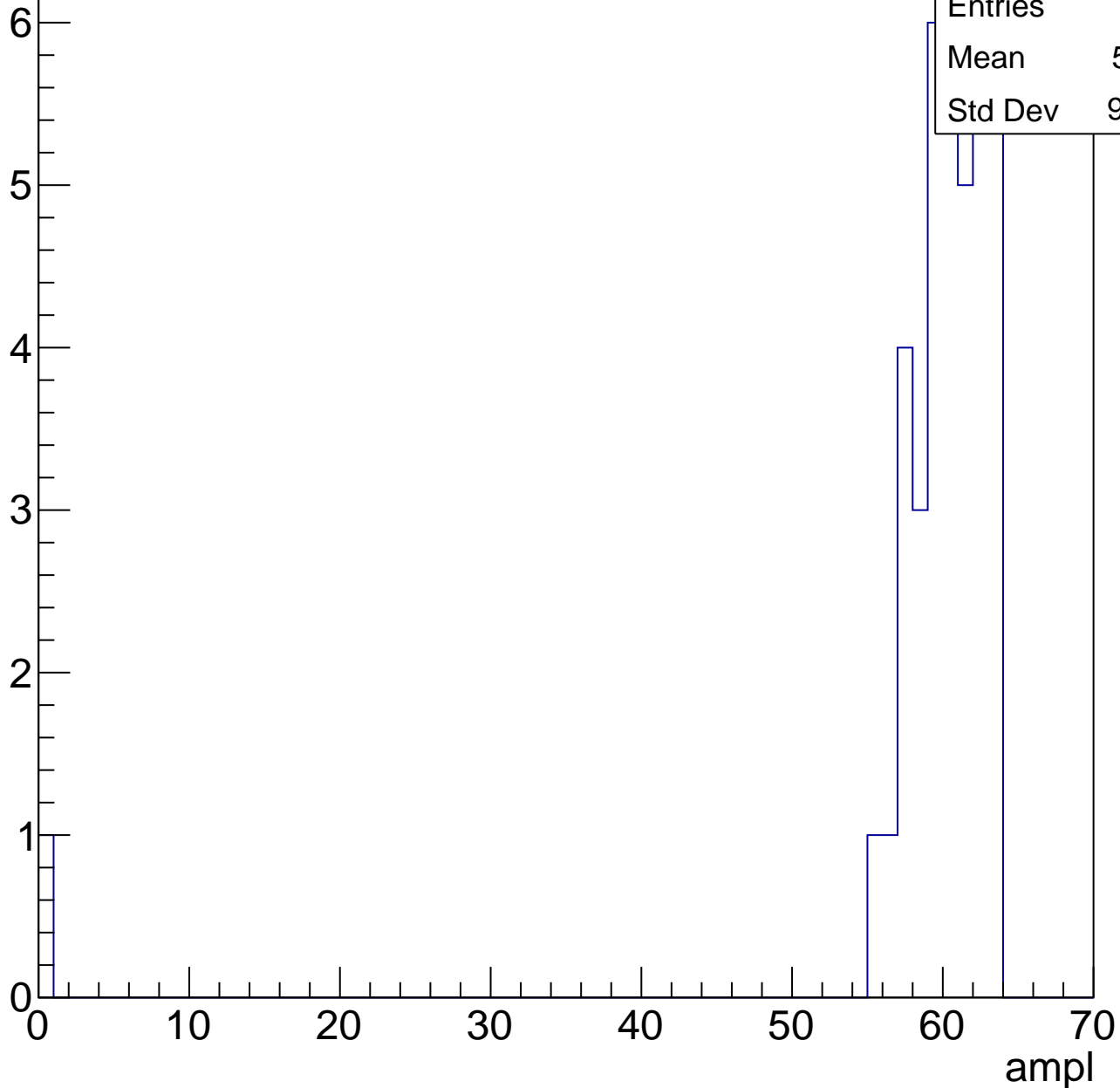
Entries	54
Mean	56.11
Std Dev	3.125



# B0L000S, U10-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L000S, U10-ch75, adc0

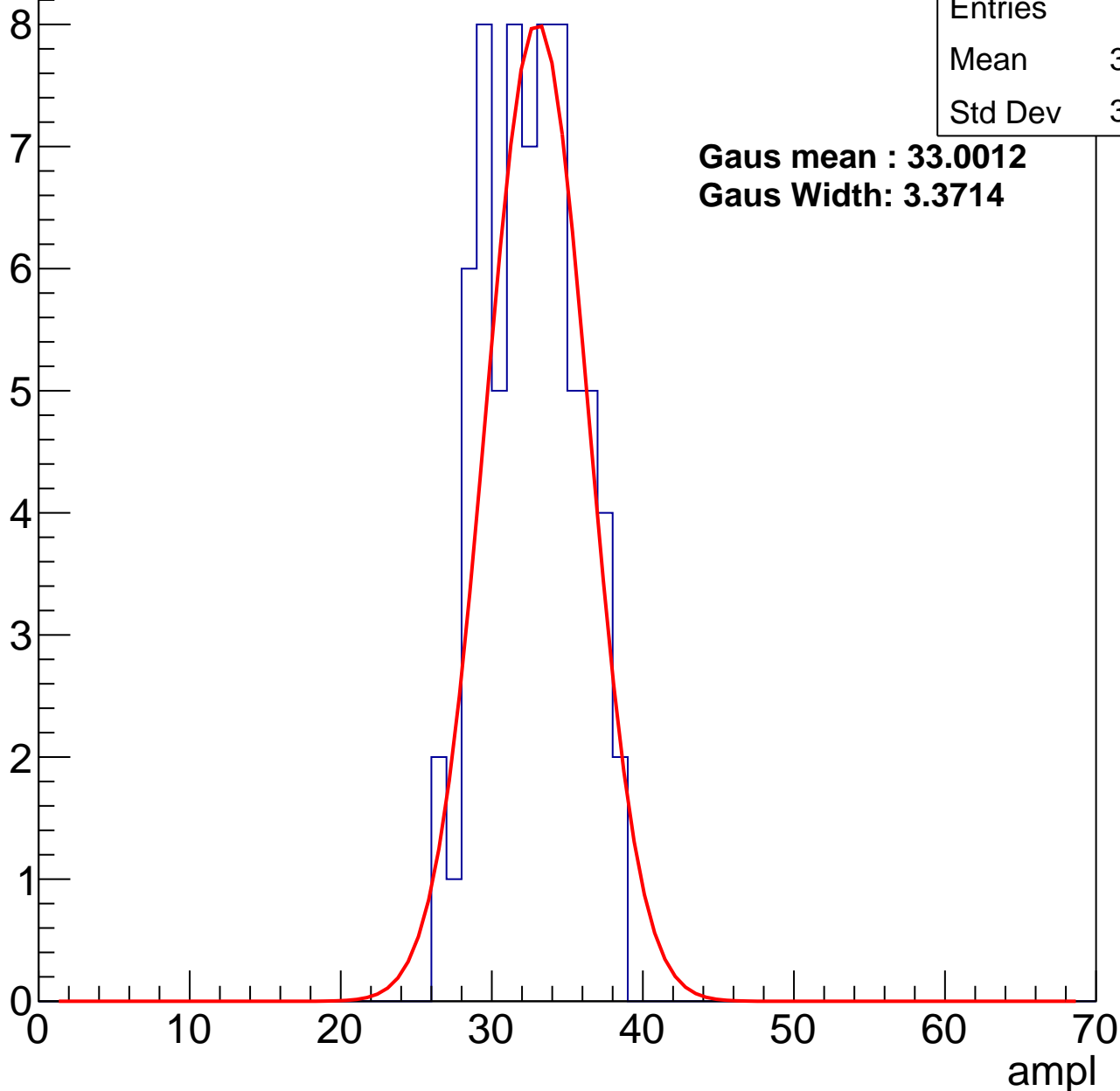
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	32.12
Std Dev	3.019

**Gaus mean : 33.0012**

**Gaus Width: 3.3714**



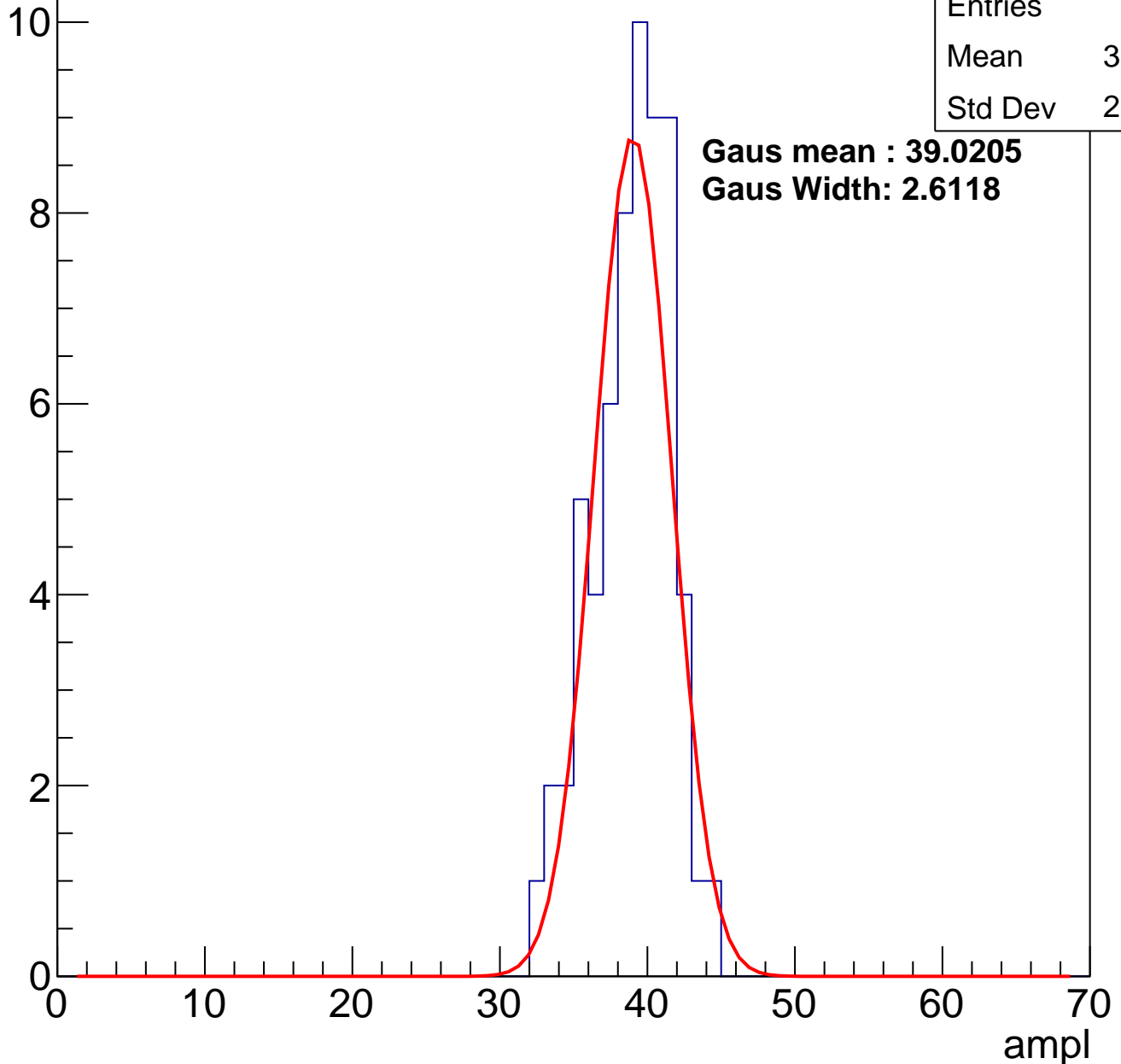
# B0L000S, U10-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	38.47
Std Dev	2.613

**Gaus mean : 39.0205**  
**Gaus Width: 2.6118**

Entry



# B0L000S, U10-ch75, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	45.97
Std Dev	3.192

**Gaus mean : 46.5783**

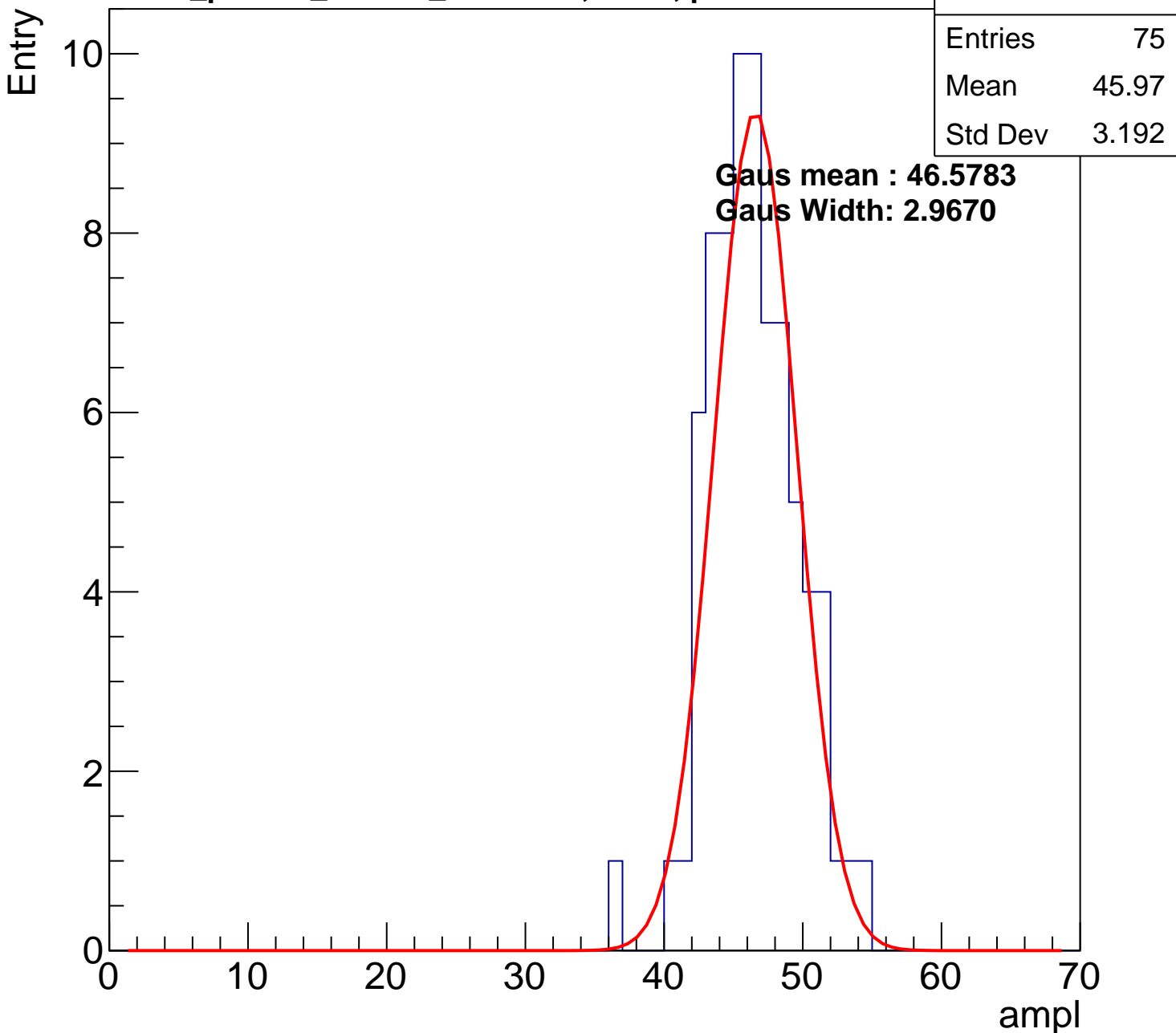
**Gaus Width: 2.9670**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

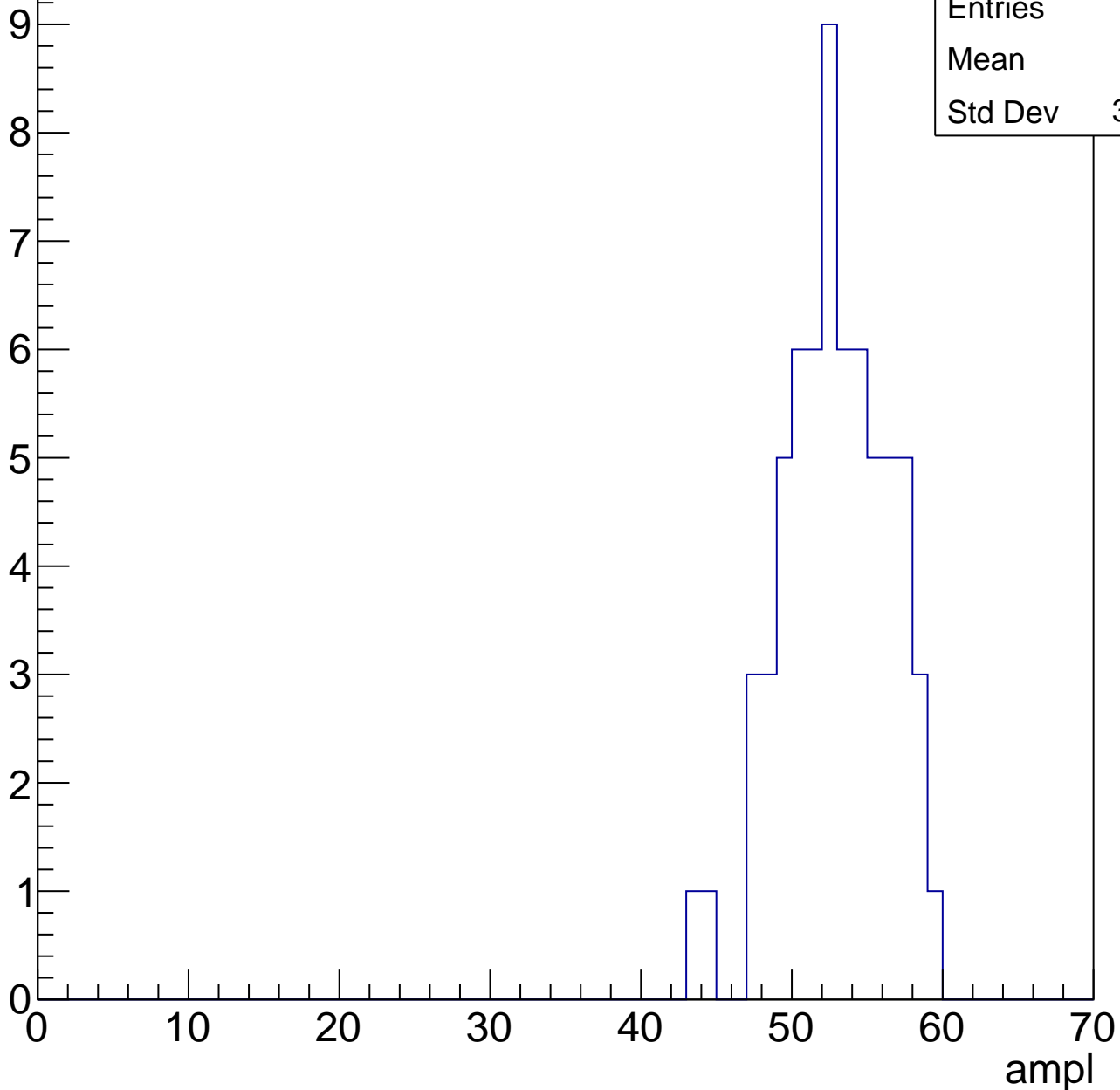


# B0L000S, U10-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	52.4
Std Dev	3.441

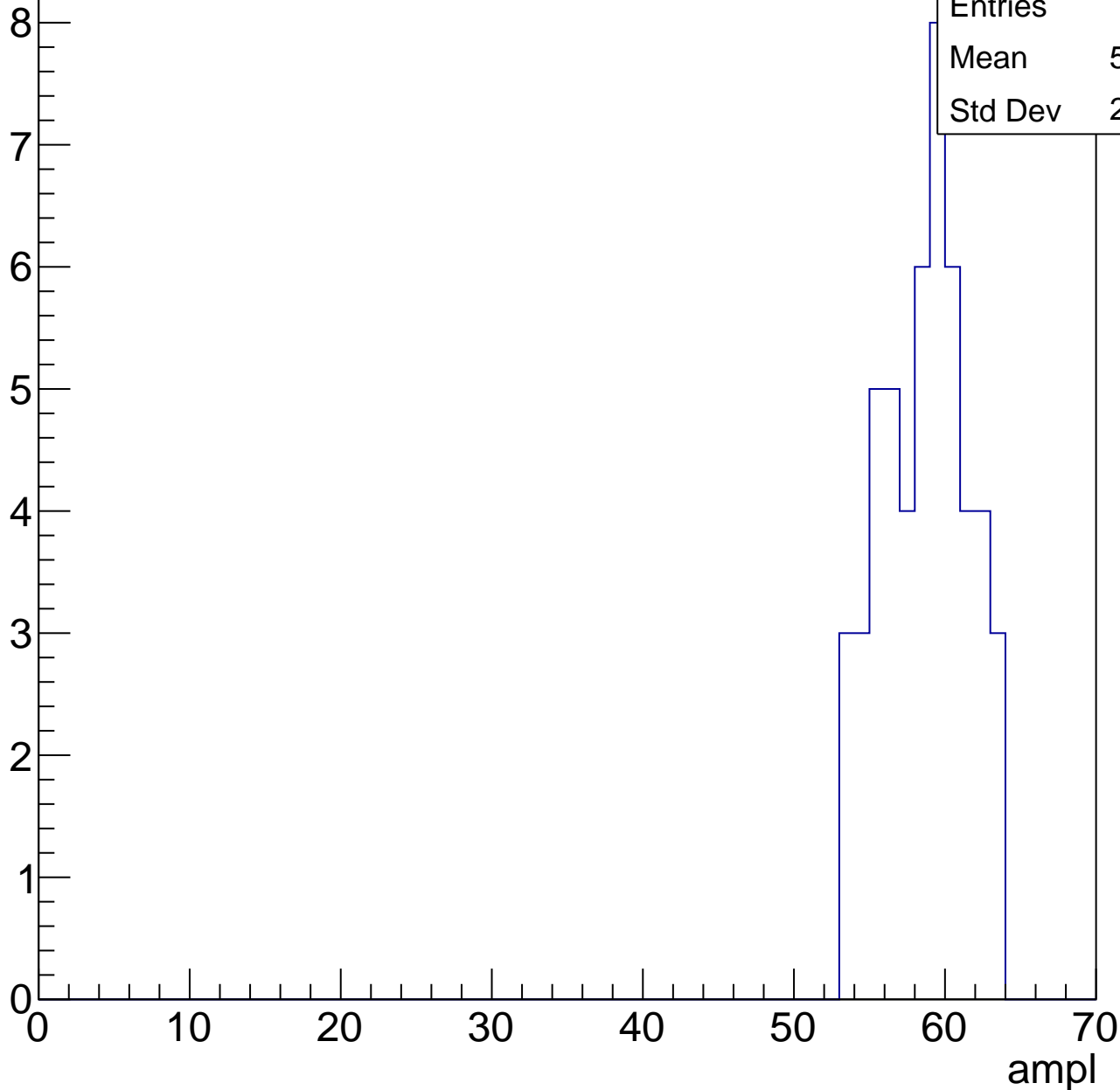


# B0L000S, U10-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	58.14
Std Dev	2.794

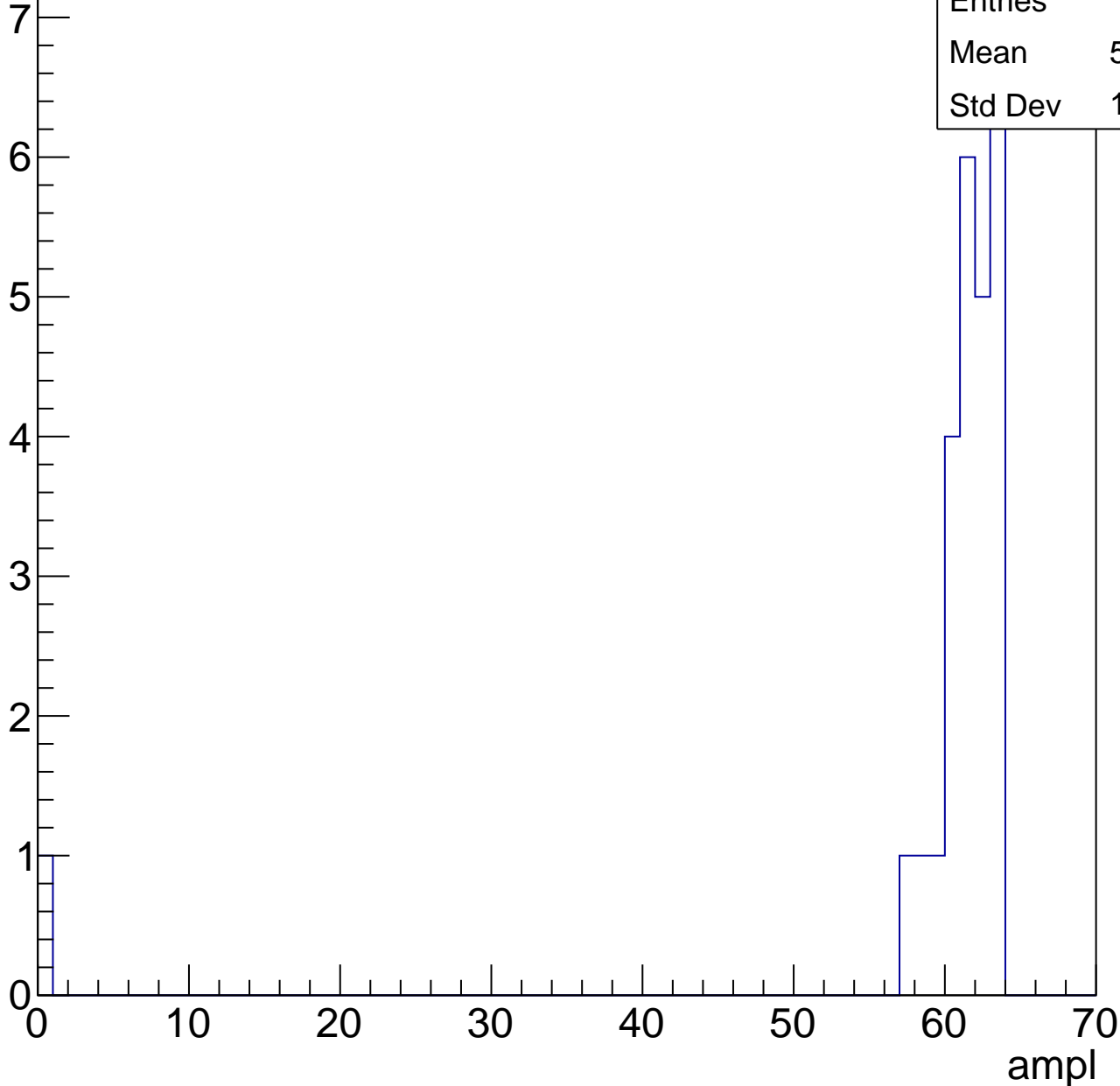


# B0L000S, U10-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.88
Std Dev	11.88



# B0L000S, U10-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch76, adc0

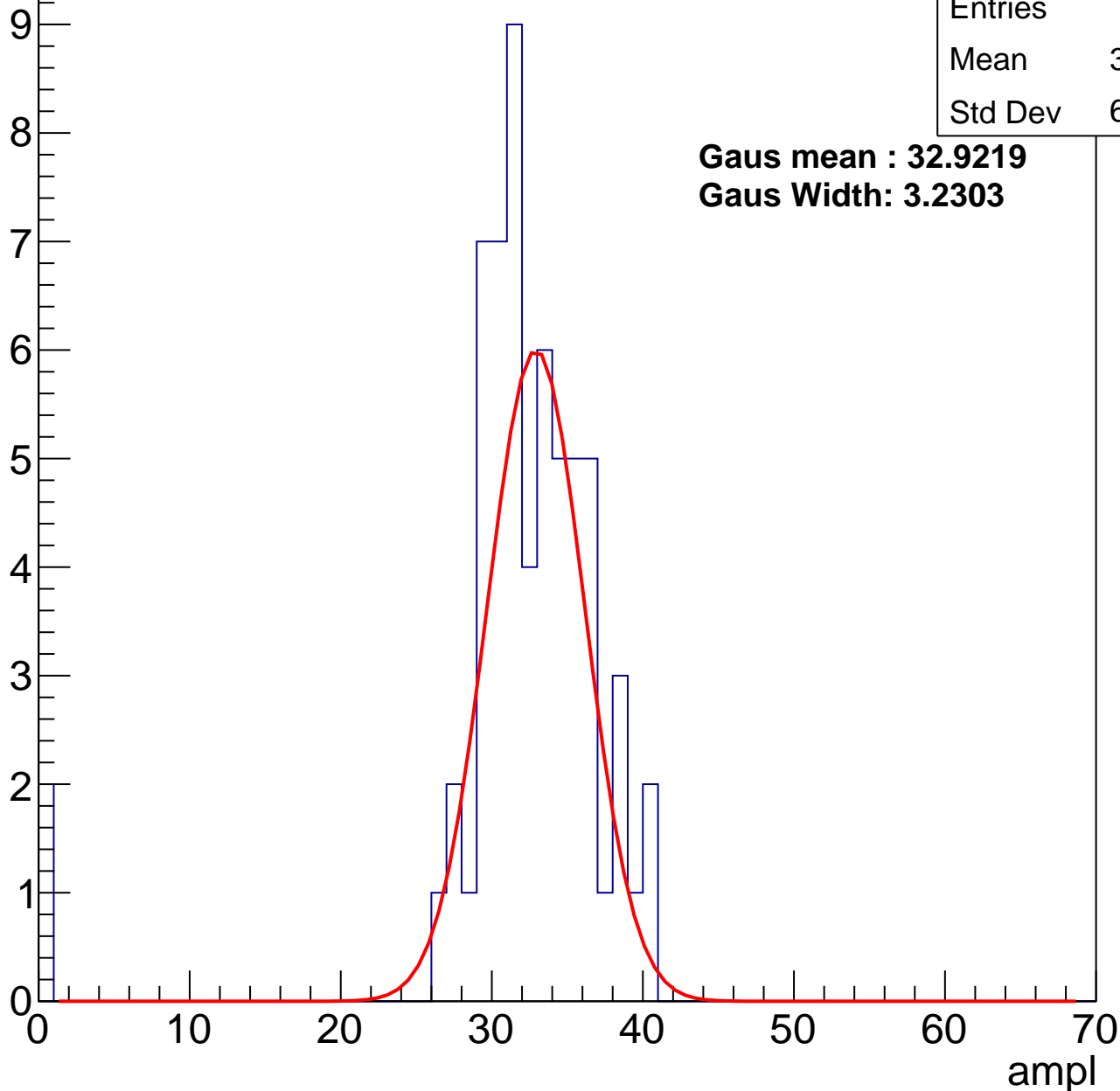
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	31.49
Std Dev	6.647

**Gaus mean : 32.9219**

**Gaus Width: 3.2303**



# B0L000S, U10-ch76, adc1

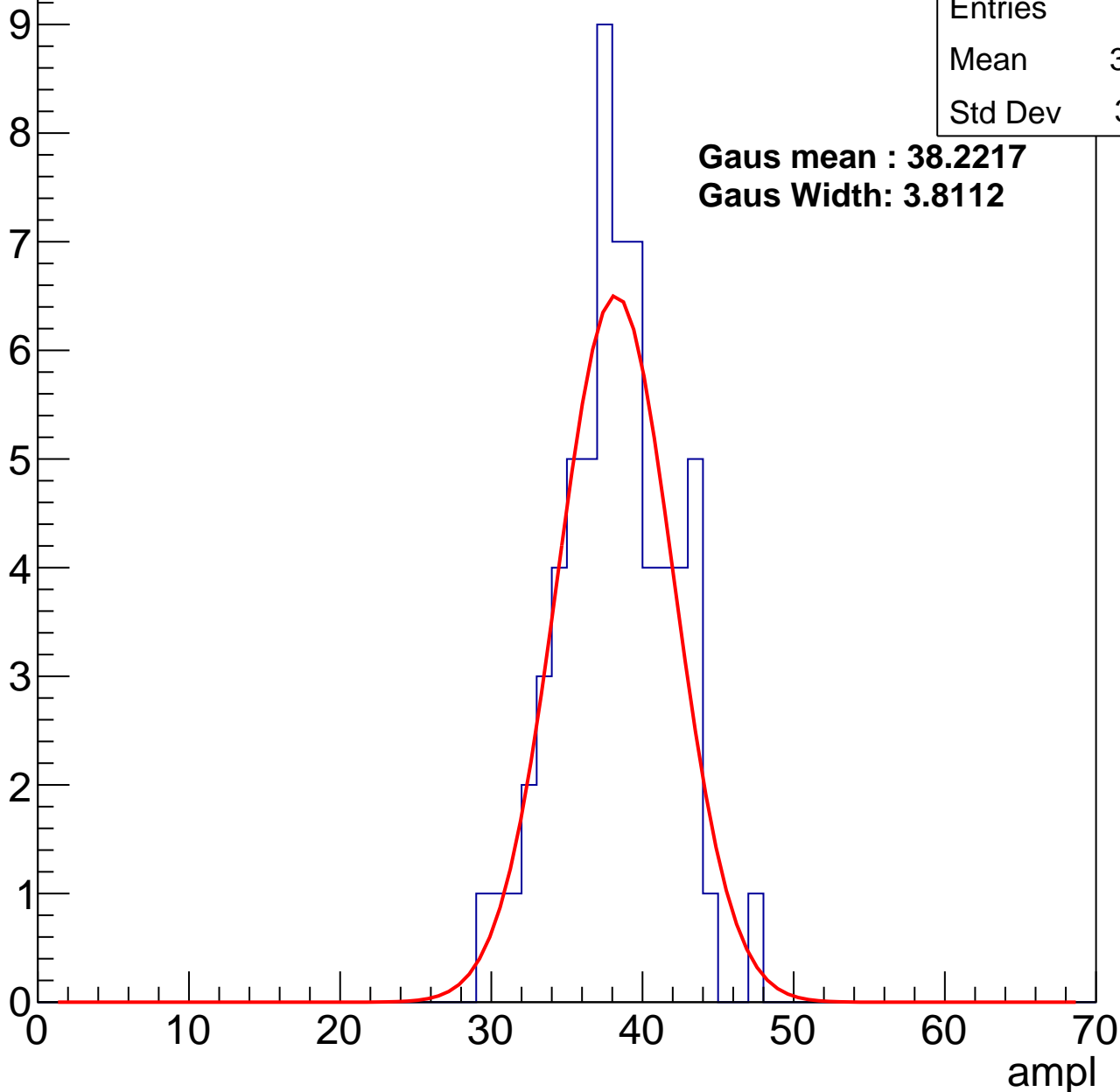
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.72
Std Dev	3.621

**Gaus mean : 38.2217**

**Gaus Width: 3.8112**



# B0L000S, U10-ch76, adc2

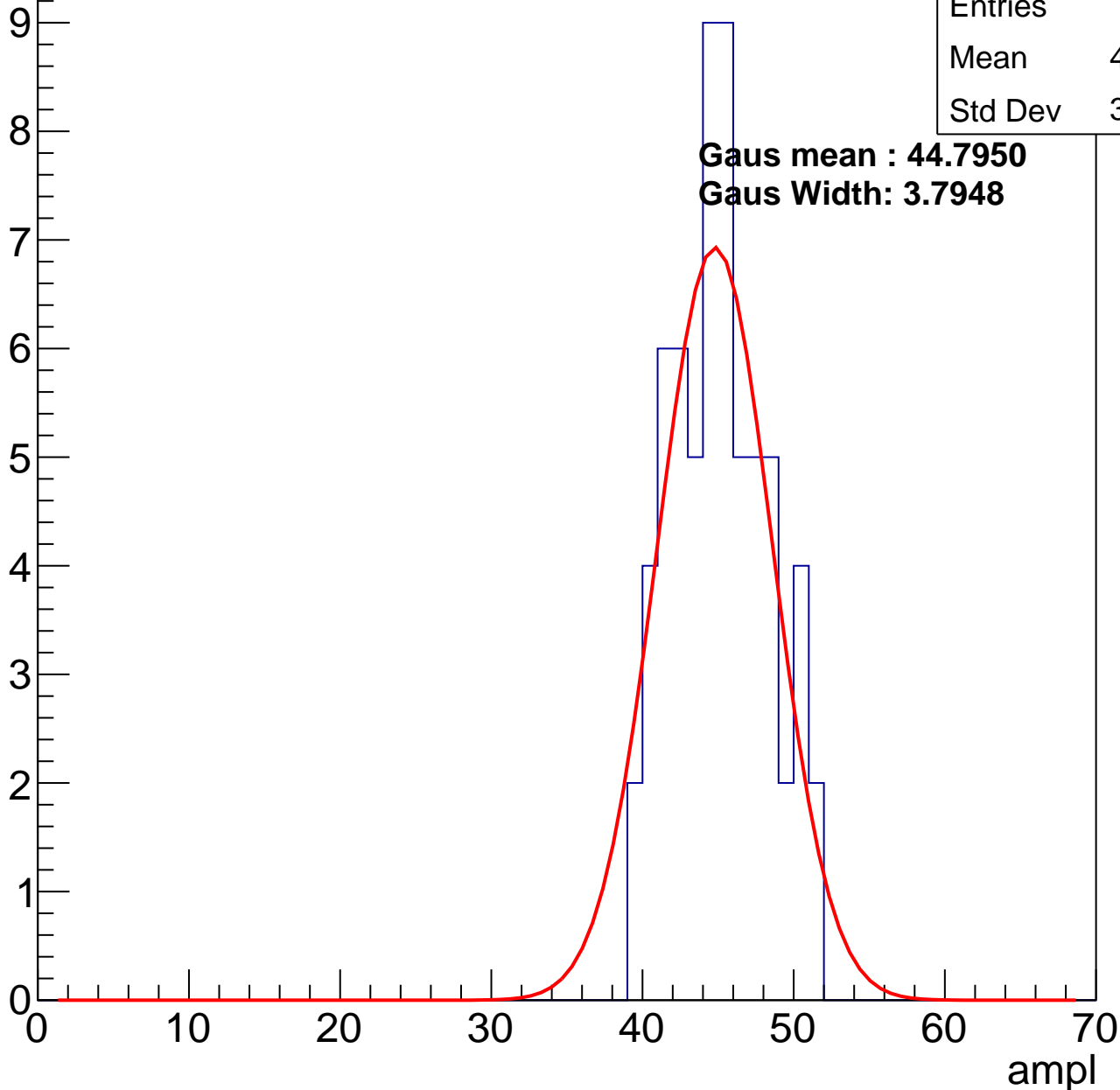
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	44.64
Std Dev	3.104

**Gaus mean : 44.7950**

**Gaus Width: 3.7948**



# B0L000S, U10-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	51.81
Std Dev	3.531

Entry

10

8

6

4

2

0

0

10

20

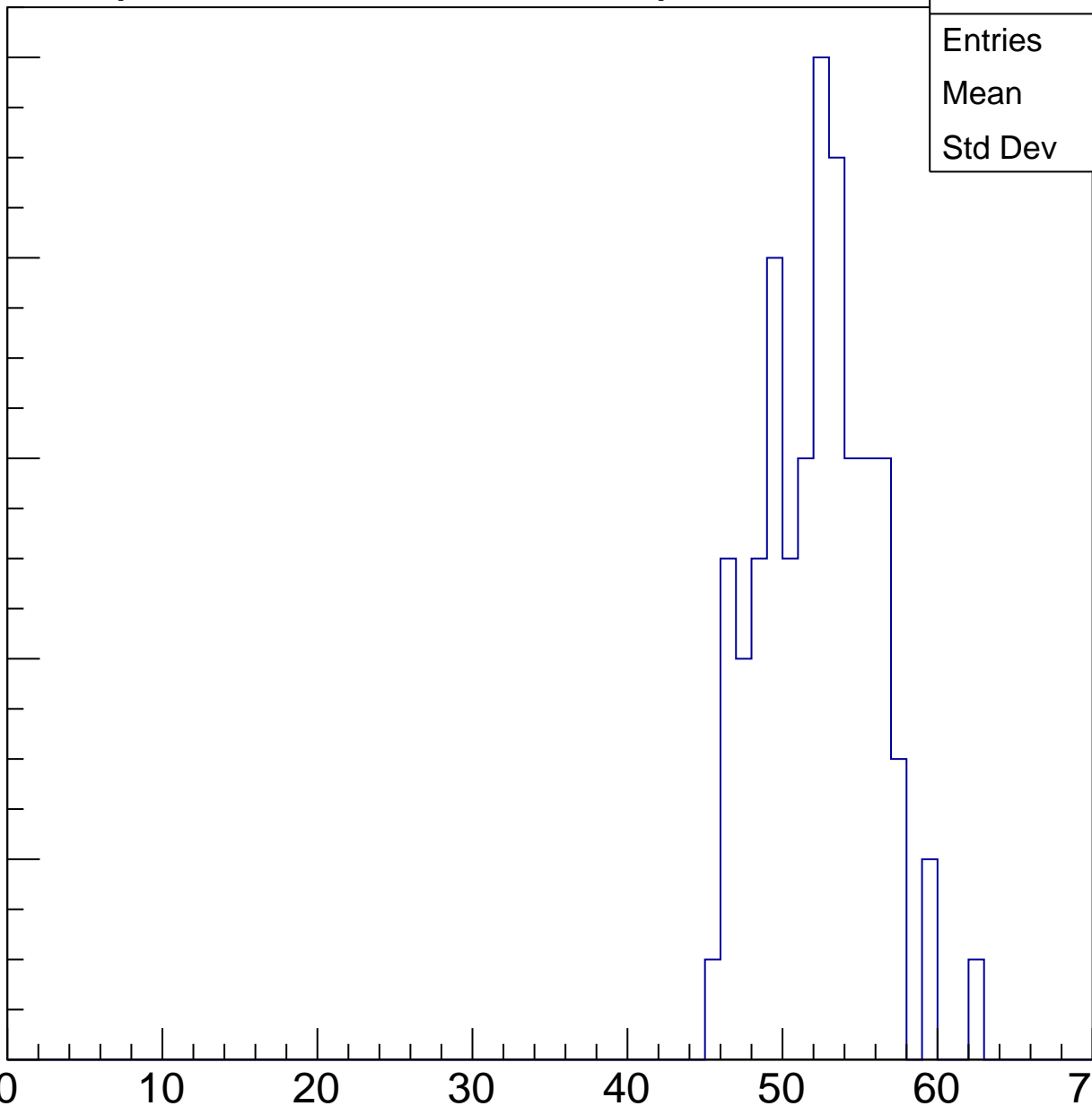
30

40

50

60

ampl

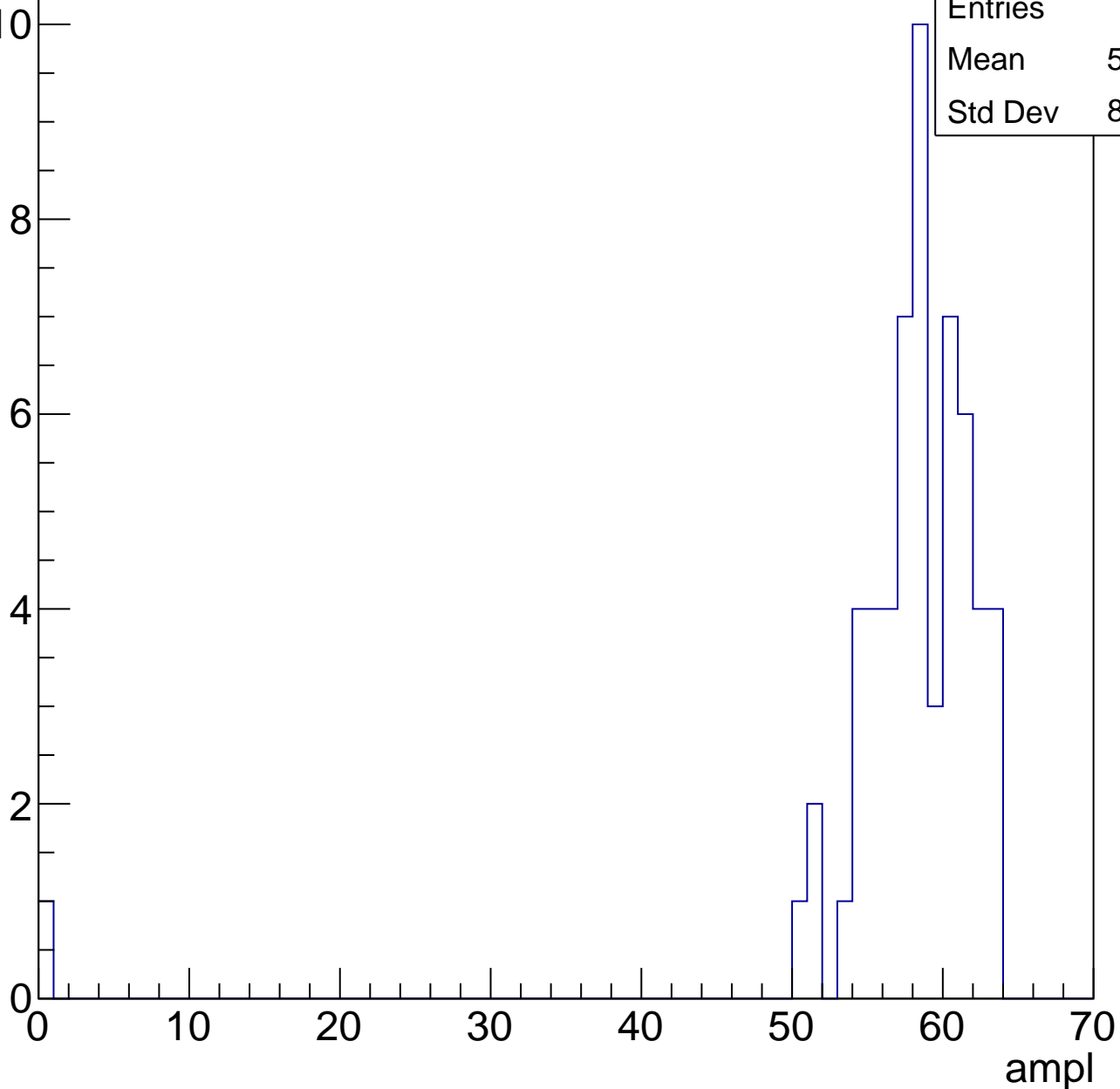


# B0L000S, U10-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

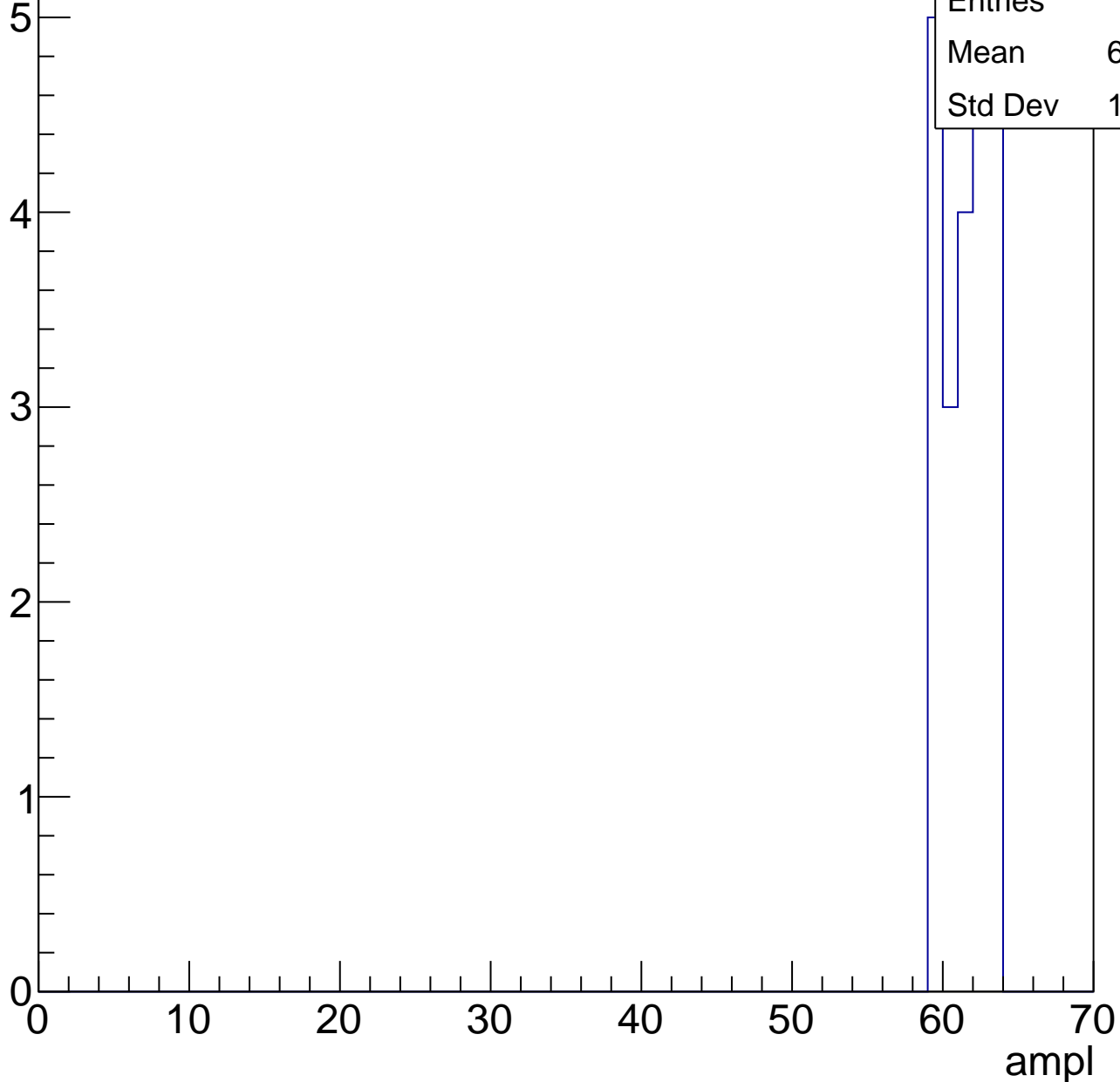
Entries	58
Mean	57.02
Std Dev	8.165



# B0L000S, U10-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



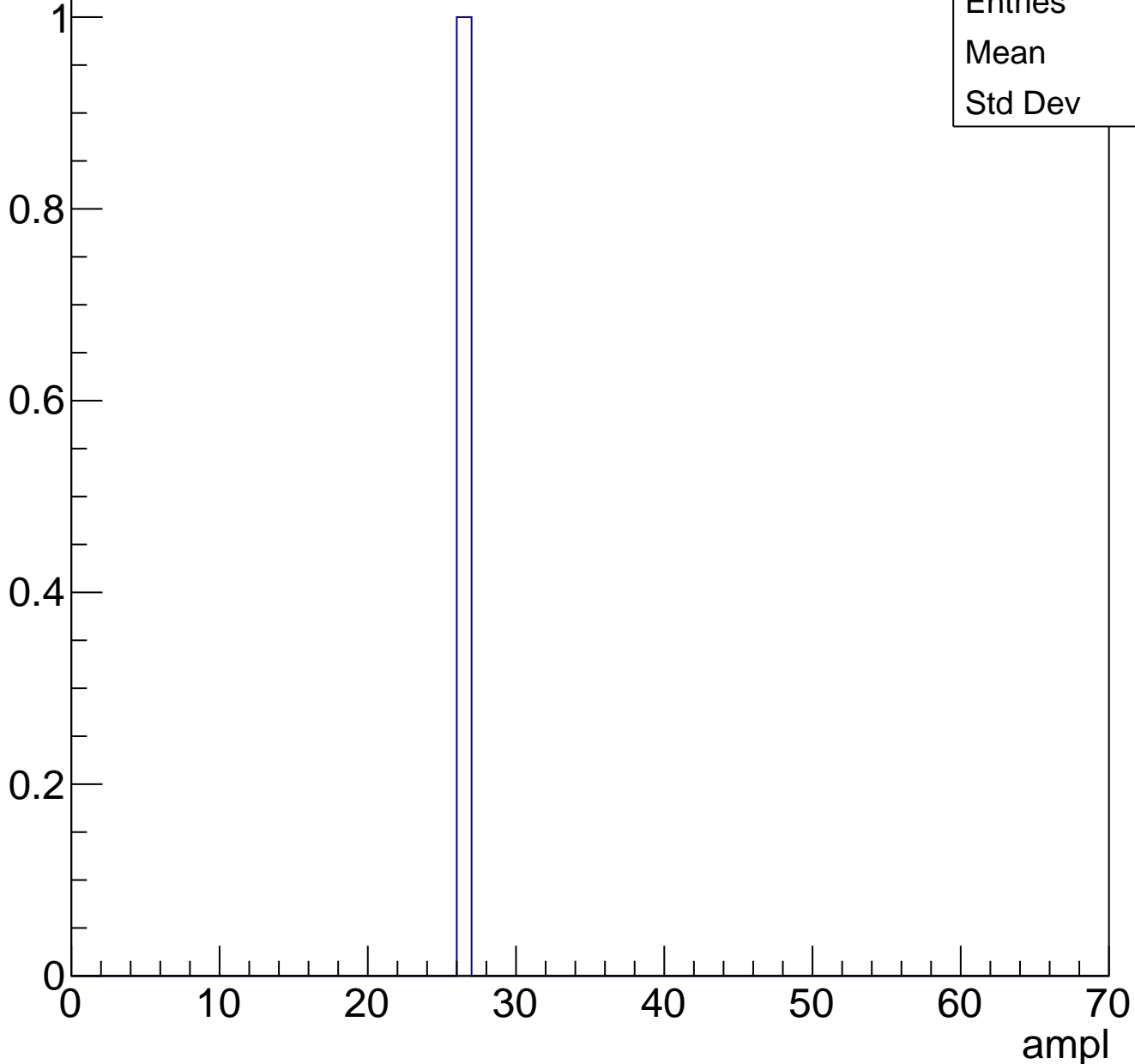
Entries	1
Mean	63
Std Dev	0



# B0L000S, U10-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	26
Std Dev	0

# B0L000S, U10-ch77, adc0

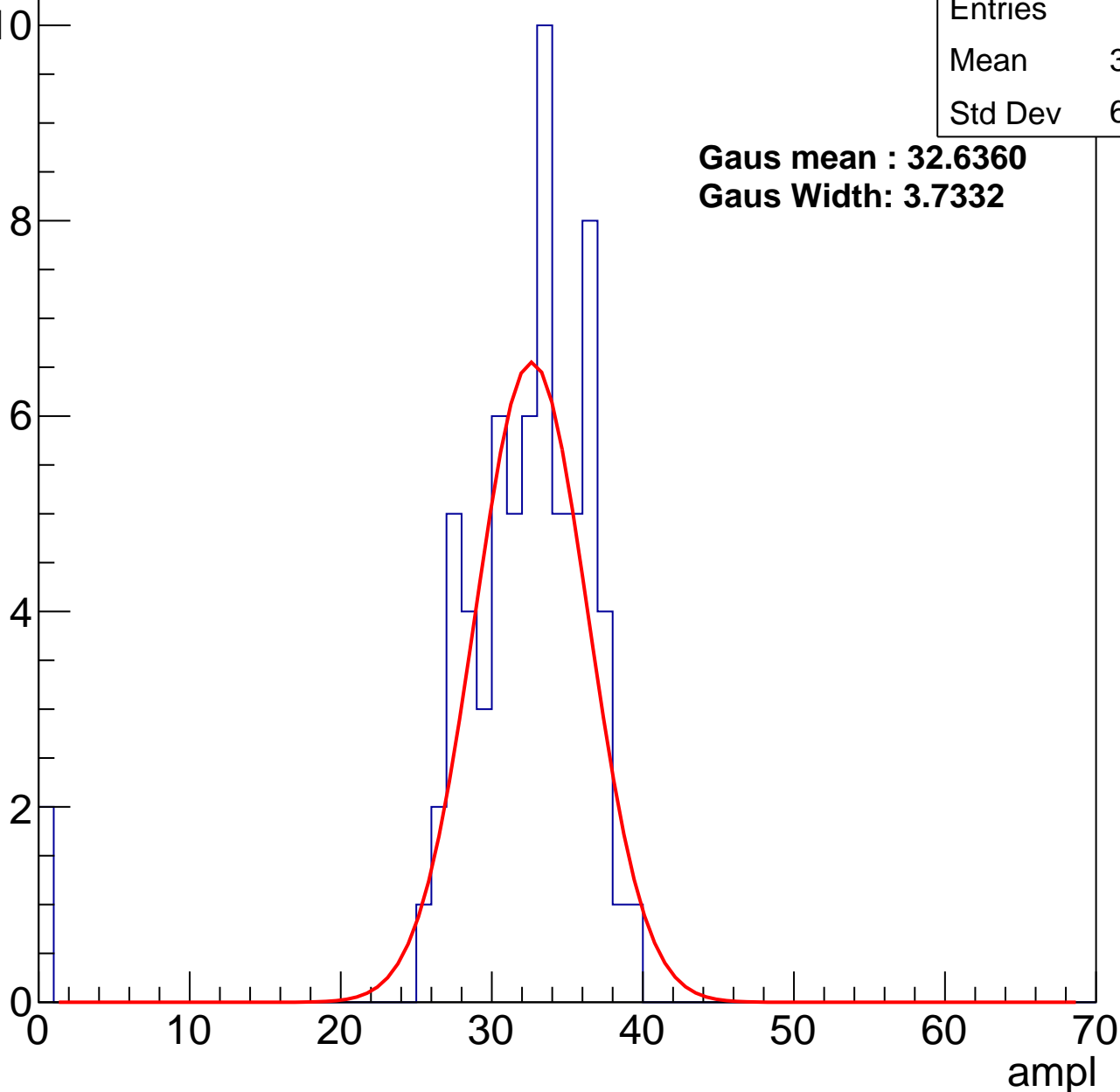
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	31.26
Std Dev	6.377

**Gaus mean : 32.6360**

**Gaus Width: 3.7332**



# B0L000S, U10-ch77, adc1

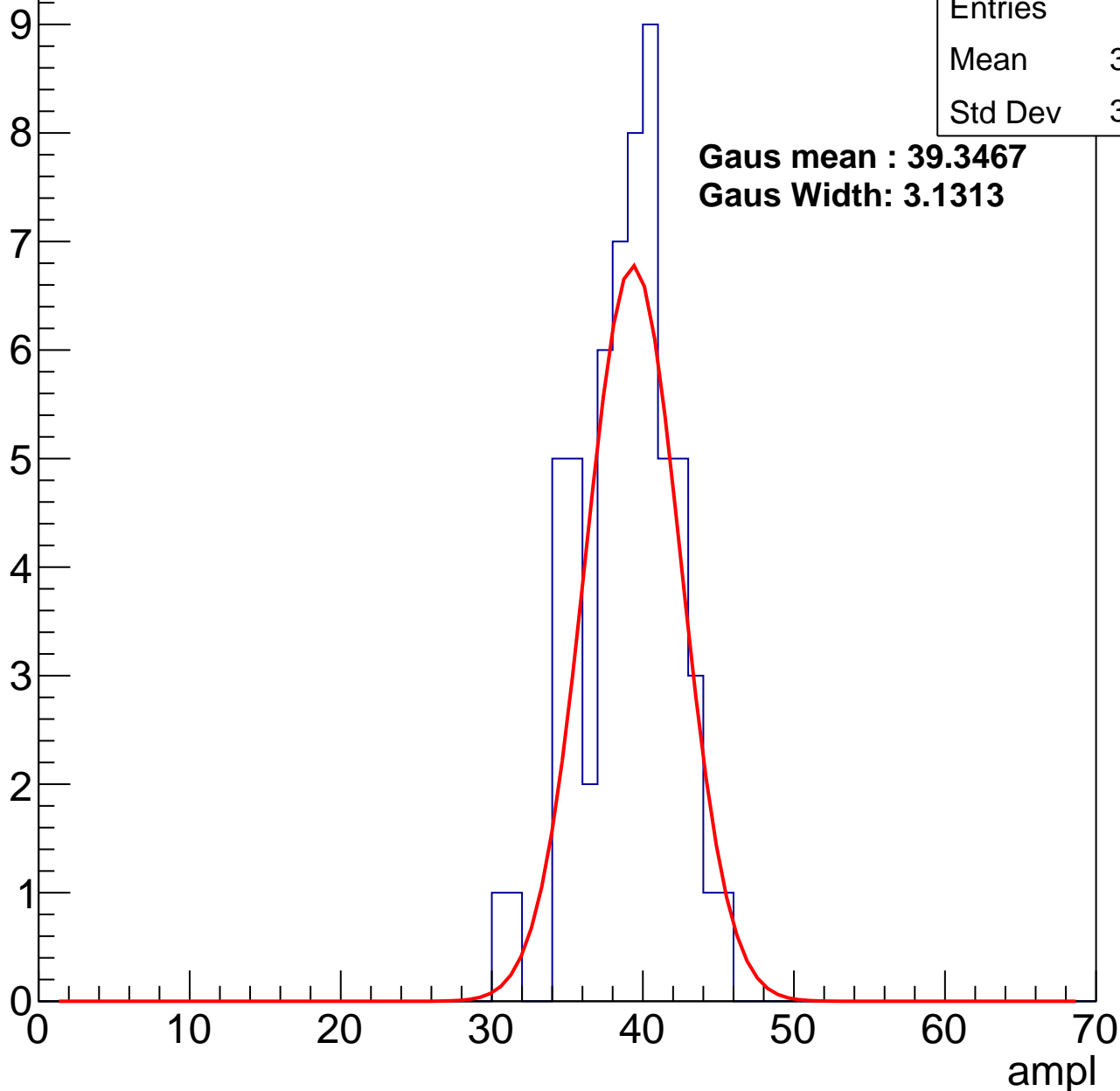
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	38.49
Std Dev	3.099

**Gaus mean : 39.3467**

**Gaus Width: 3.1313**



# B0L000S, U10-ch77, adc2

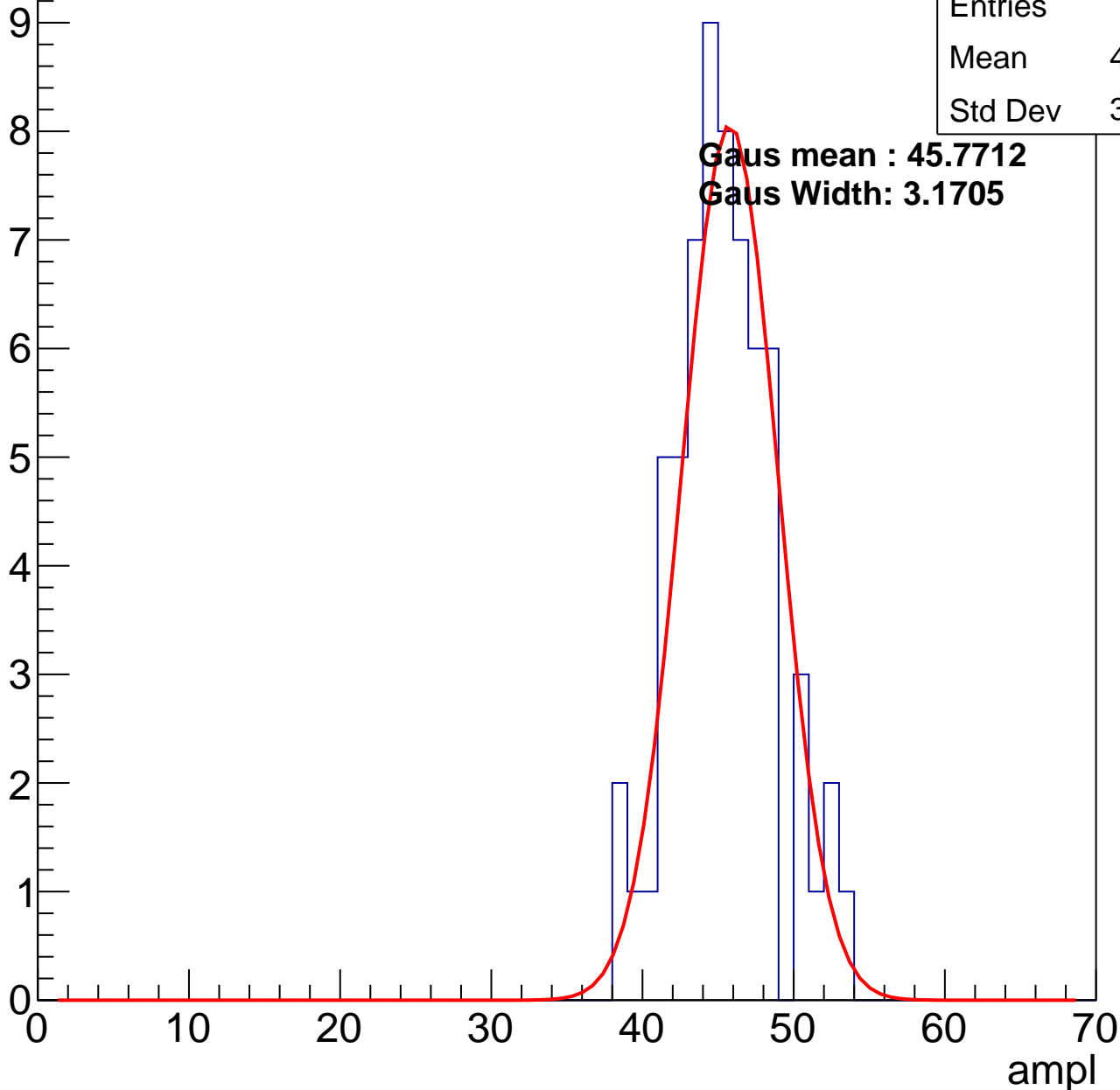
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	44.95
Std Dev	3.257

**Gaus mean : 45.7712**

**Gaus Width: 3.1705**



# B0L000S, U10-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	51.09
Std Dev	2.832

Entry

10

8

6

4

2

0

0

10

20

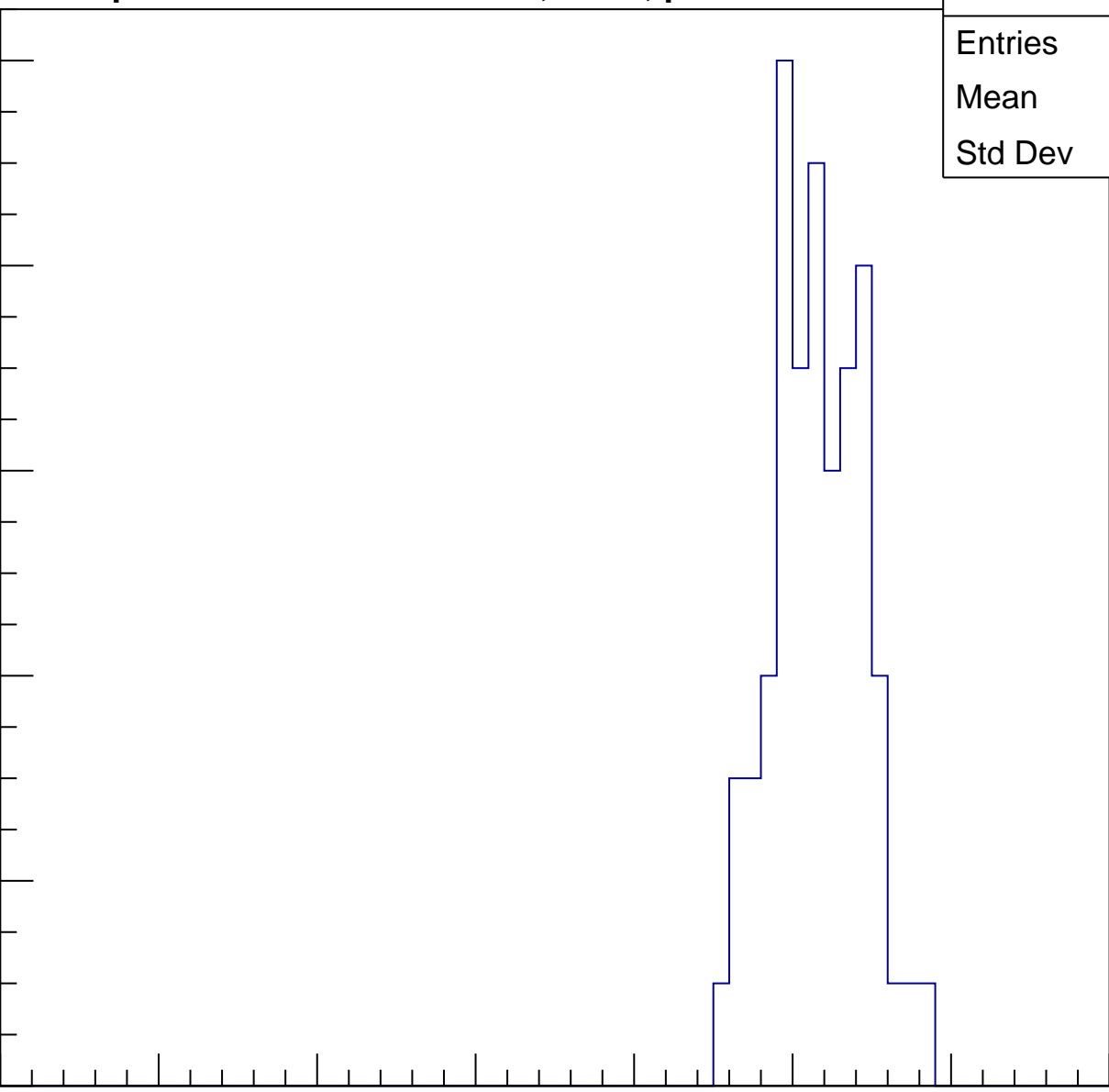
30

40

50

60

ampl

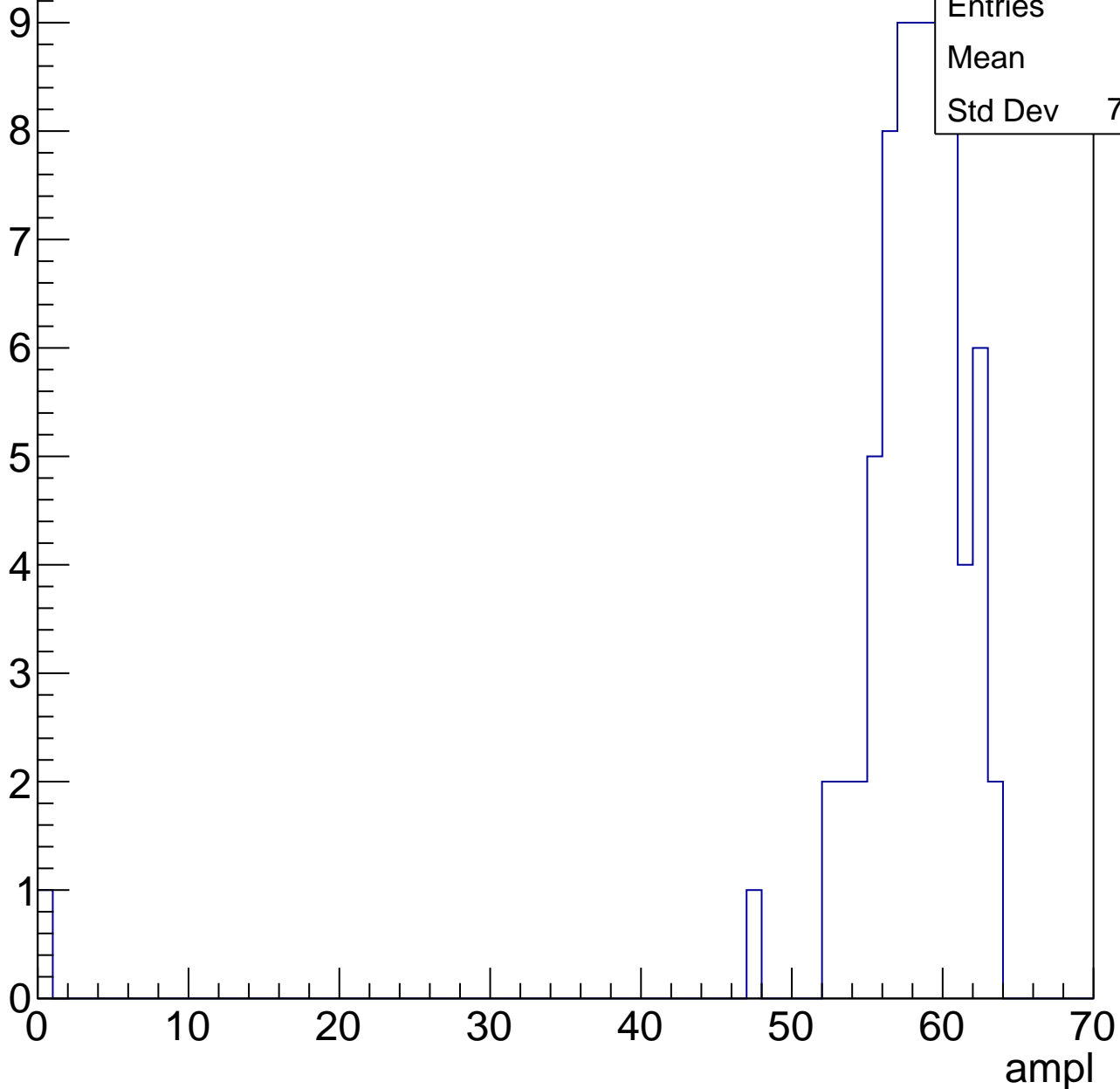


# B0L000S, U10-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	57
Std Dev	7.554

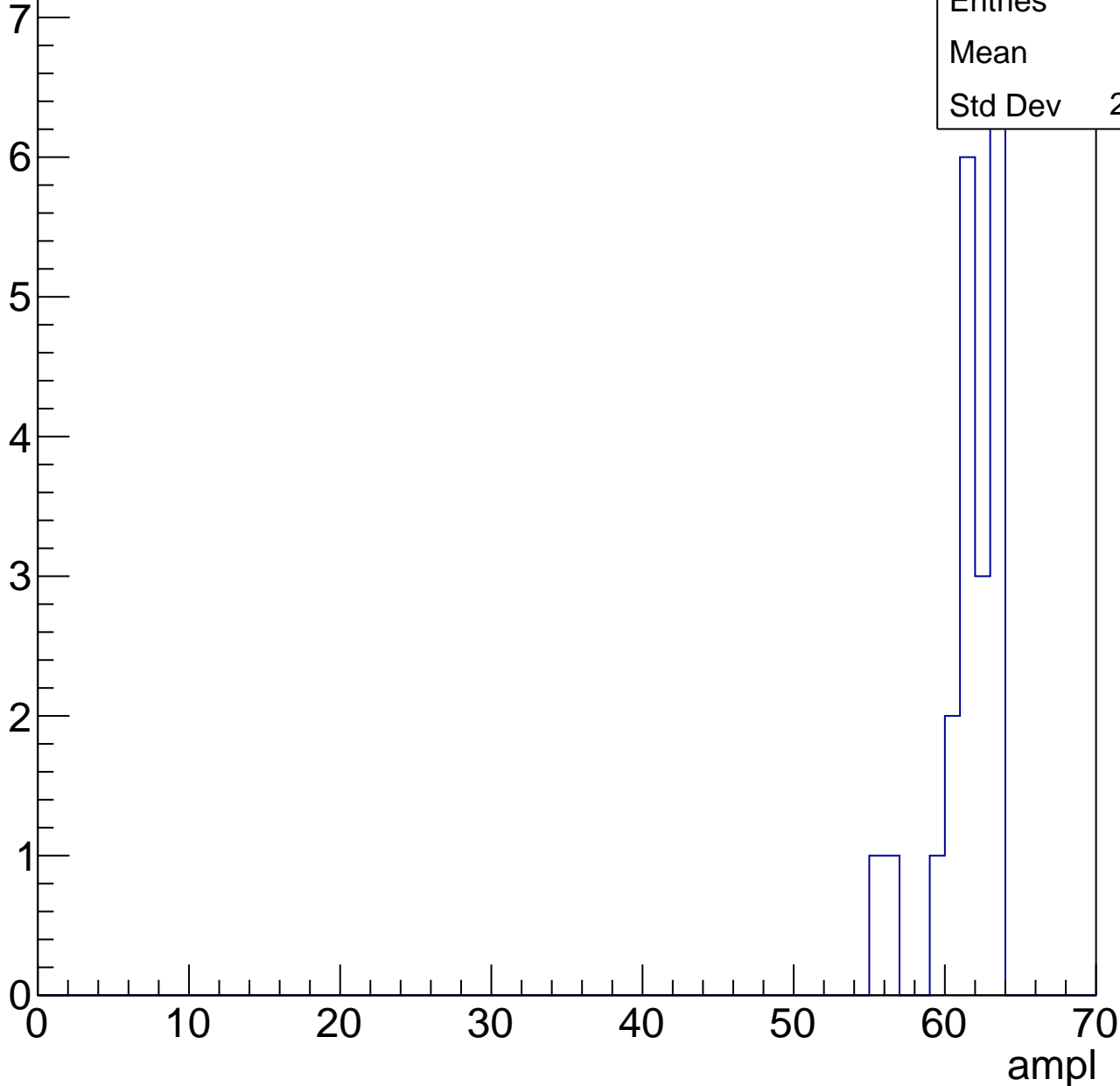


# B0L000S, U10-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	61.1
Std Dev	2.158



# B0L000S, U10-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

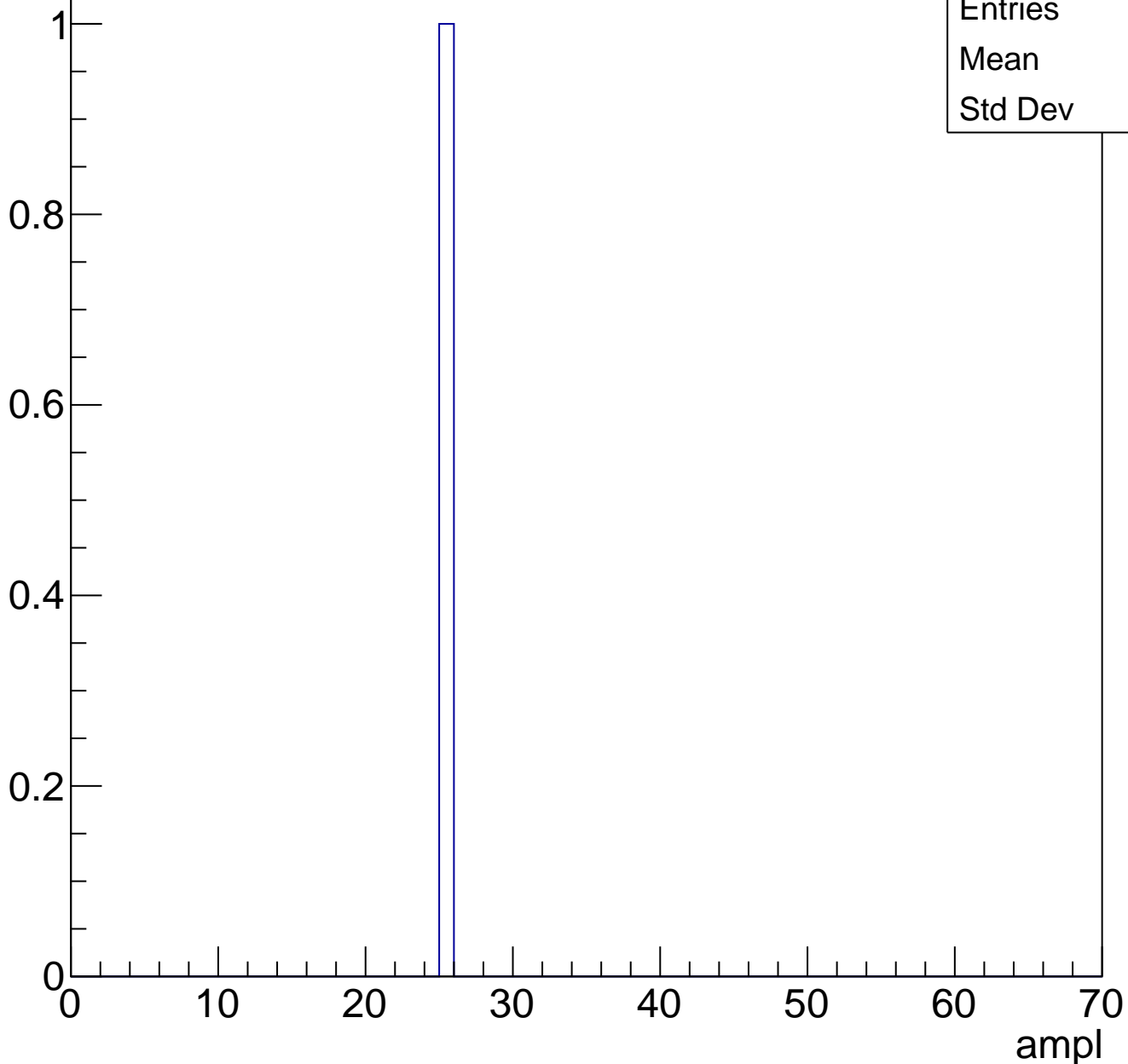




# B0L000S, U10-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L000S, U10-ch78, adc0

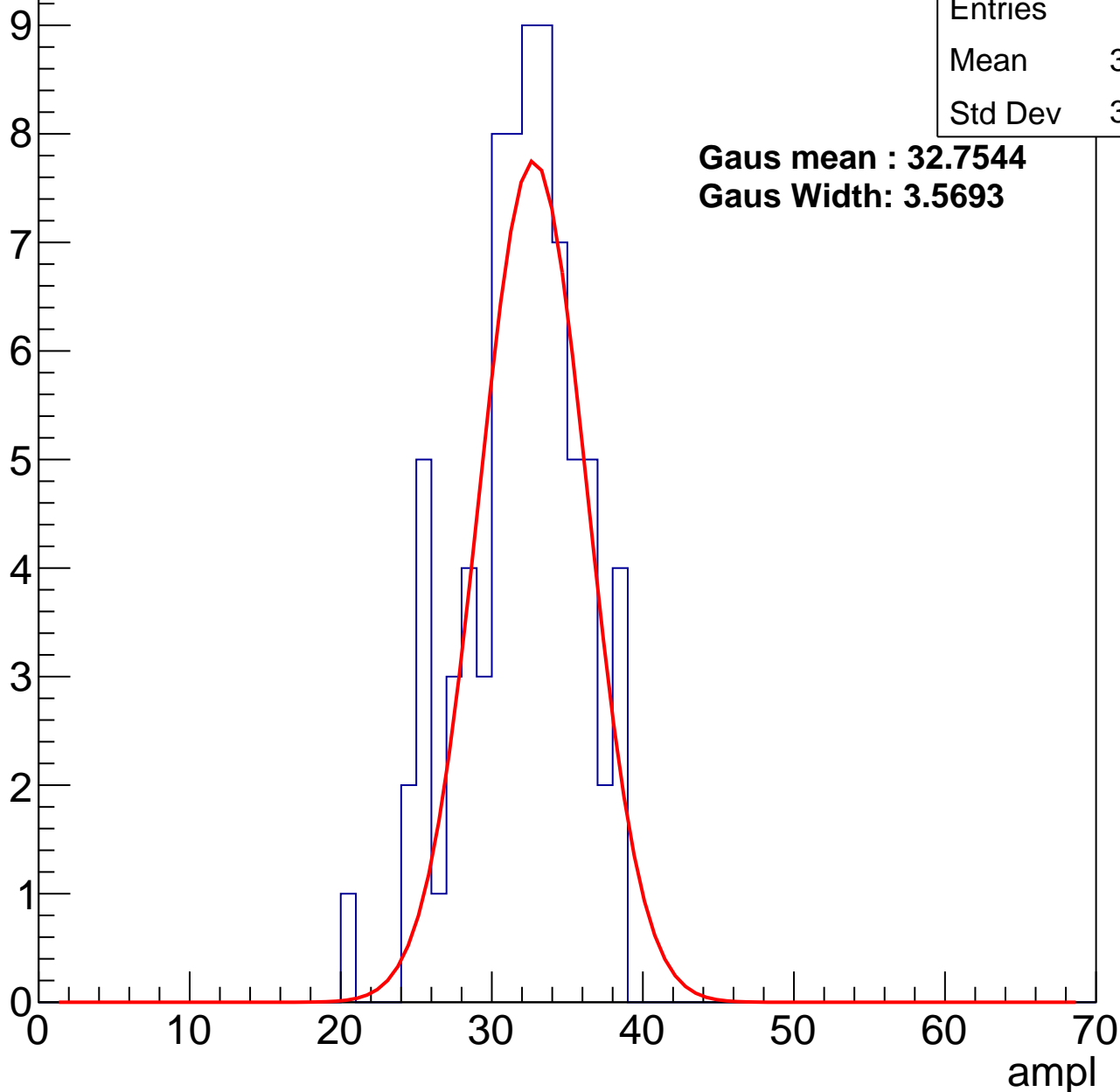
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	31.46
Std Dev	3.792

**Gaus mean : 32.7544**

**Gaus Width: 3.5693**



# B0L000S, U10-ch78, adc1

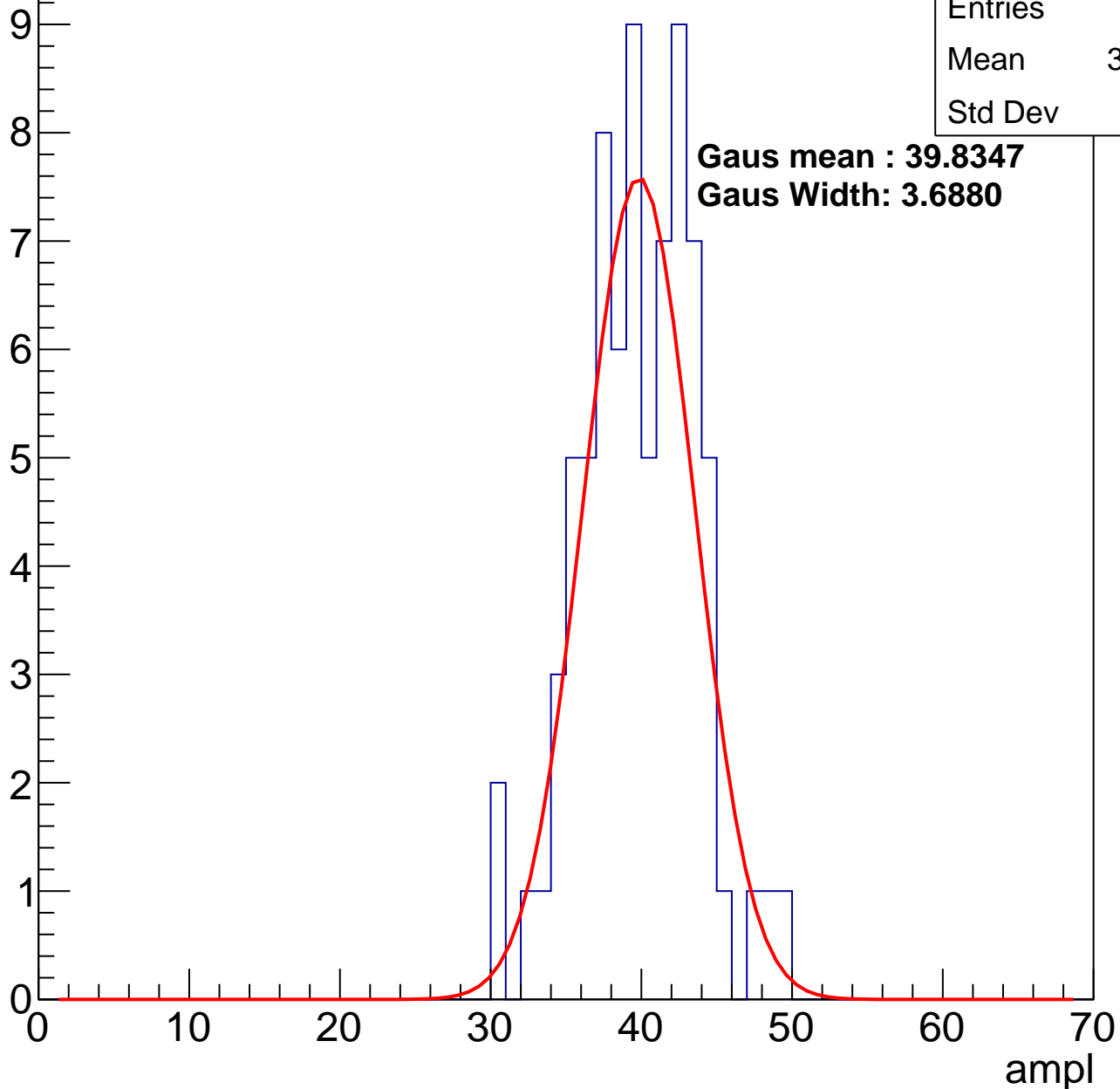
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	39.38
Std Dev	3.79

**Gaus mean : 39.8347**

**Gaus Width: 3.6880**



# B0L000S, U10-ch78, adc2

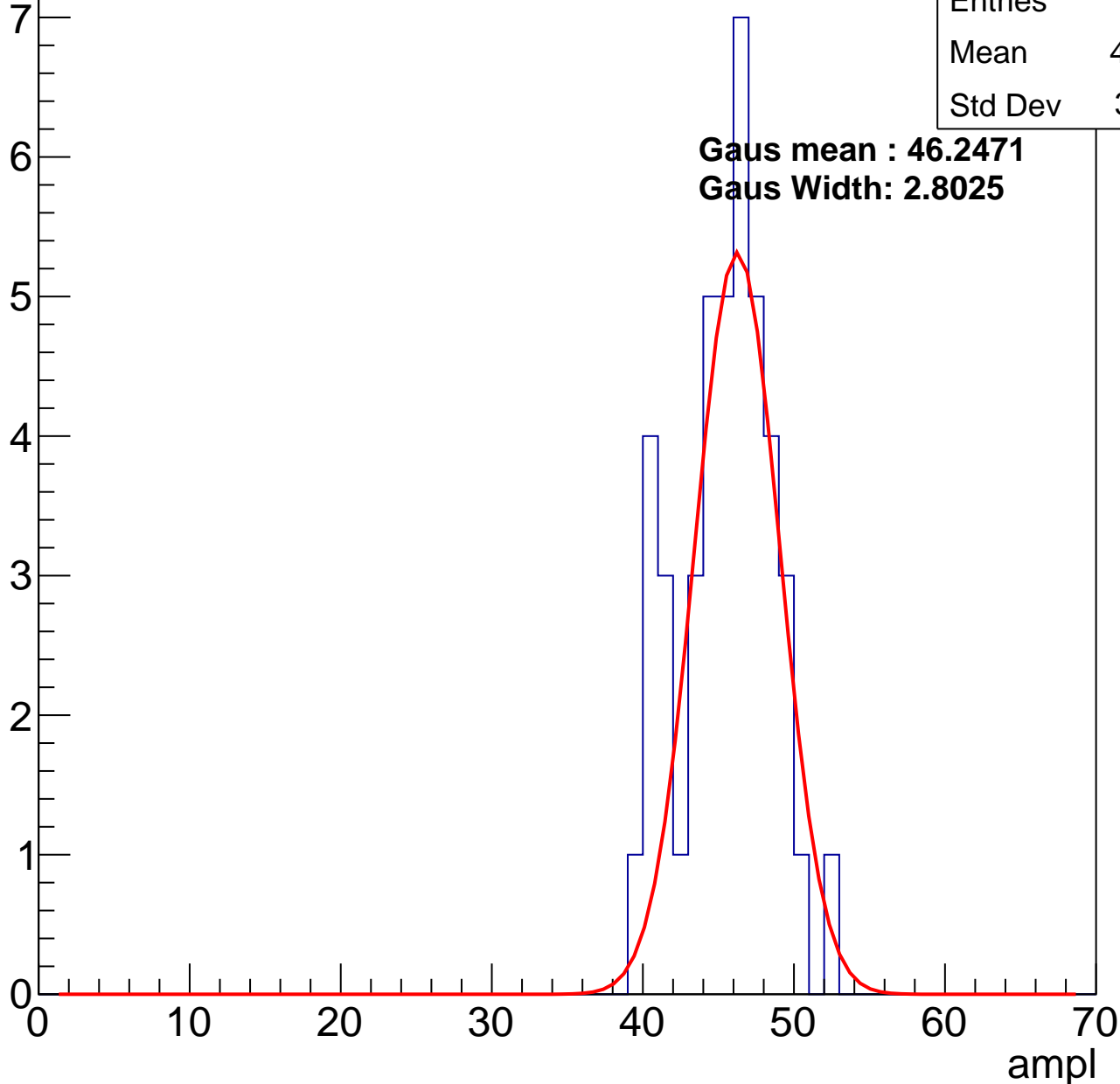
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	45.02
Std Dev	3.031

**Gaus mean : 46.2471**

**Gaus Width: 2.8025**

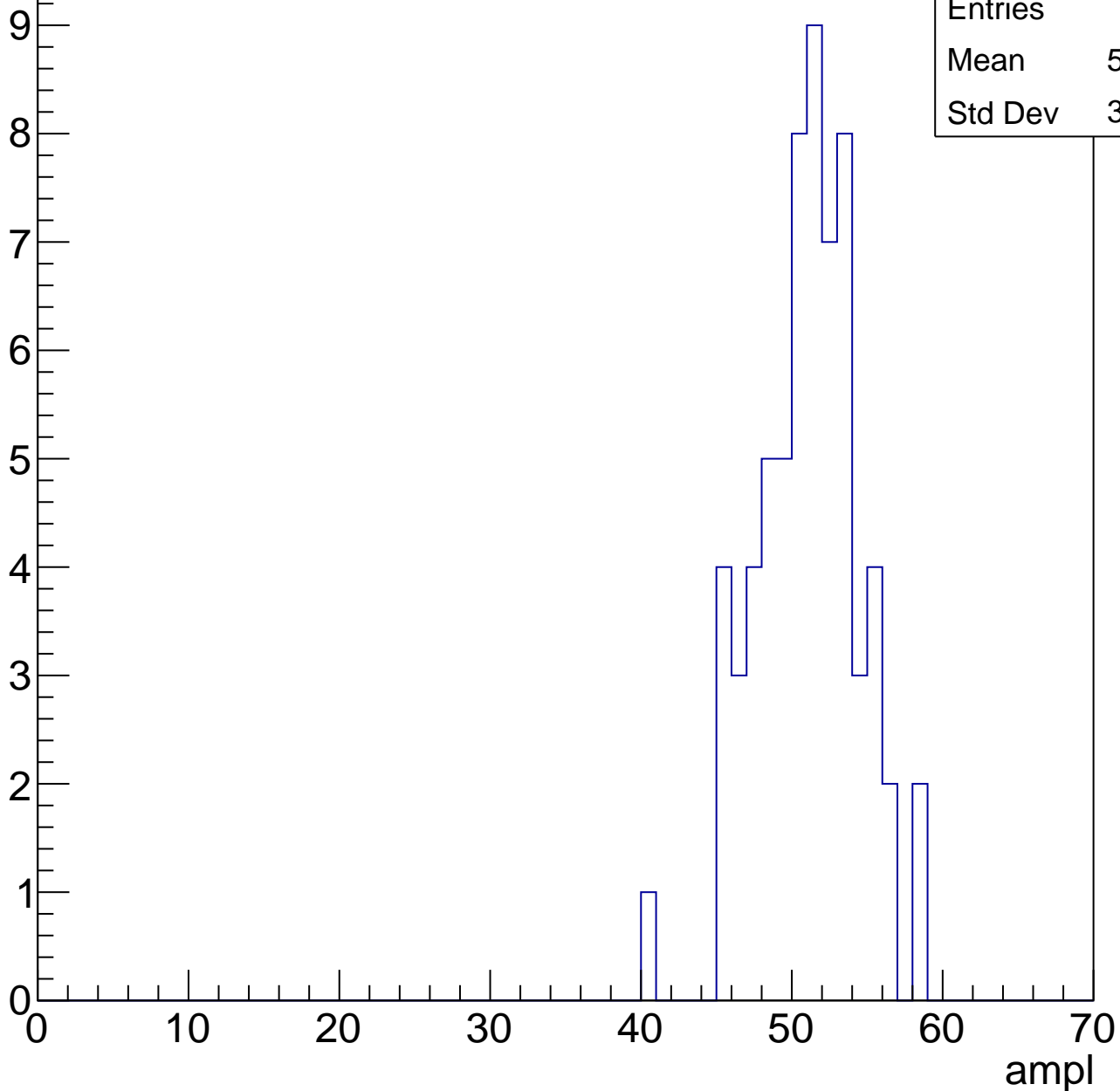


# B0L000S, U10-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	50.58
Std Dev	3.383

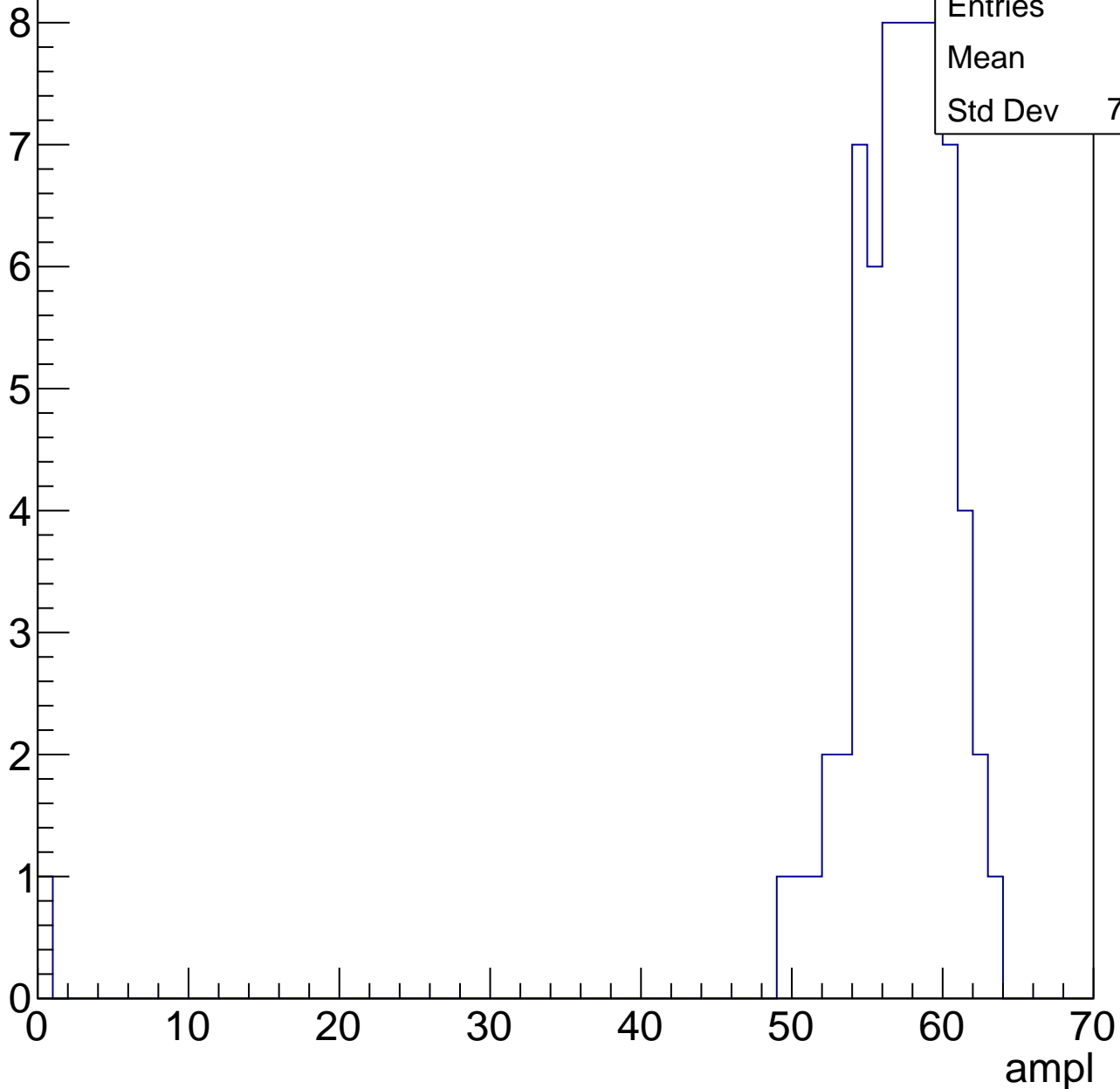


# B0L000S, U10-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	56.1
Std Dev	7.505

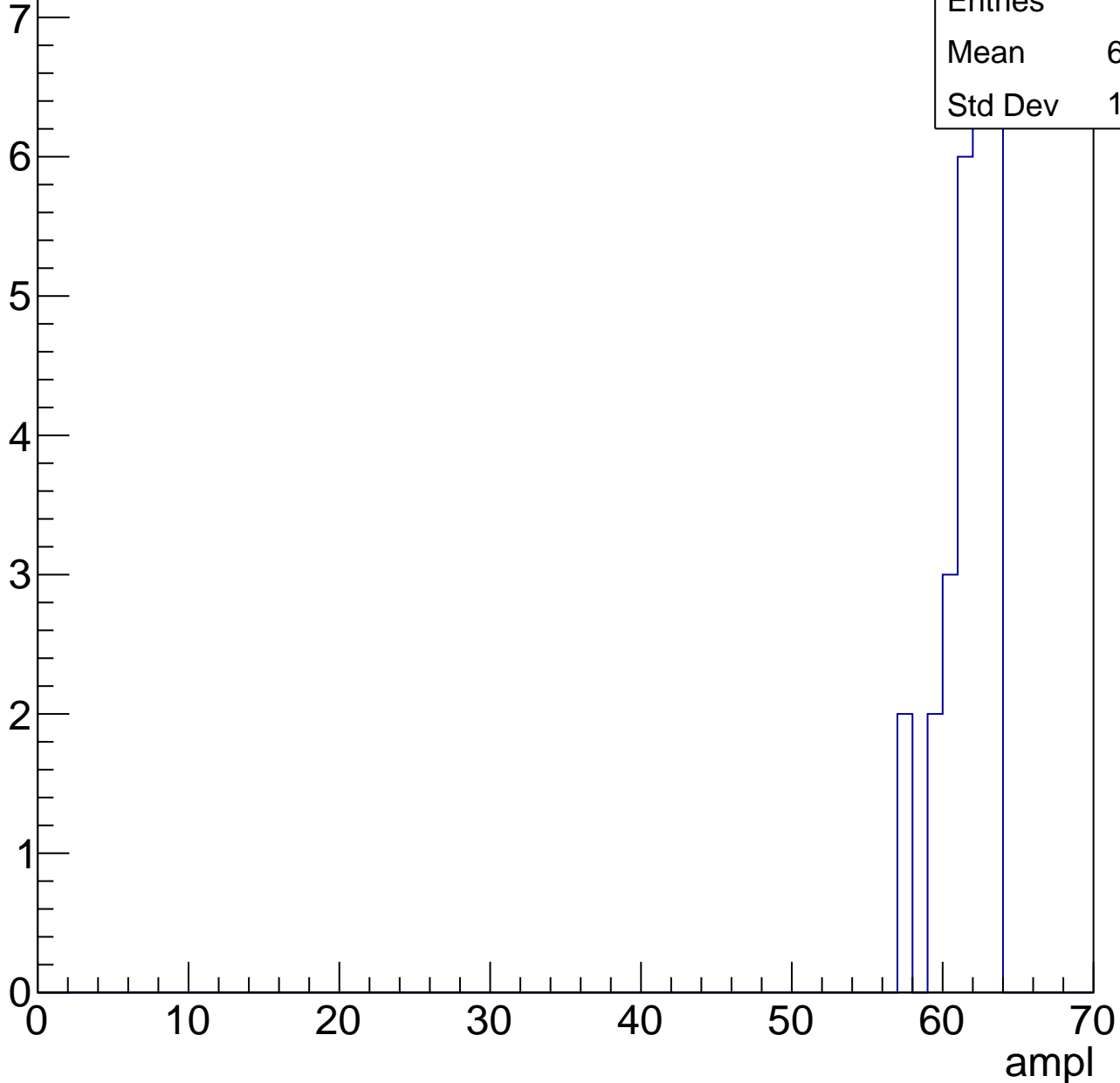


# B0L000S, U10-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	61.22
Std Dev	1.685



# B0L000S, U10-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch79, adc0

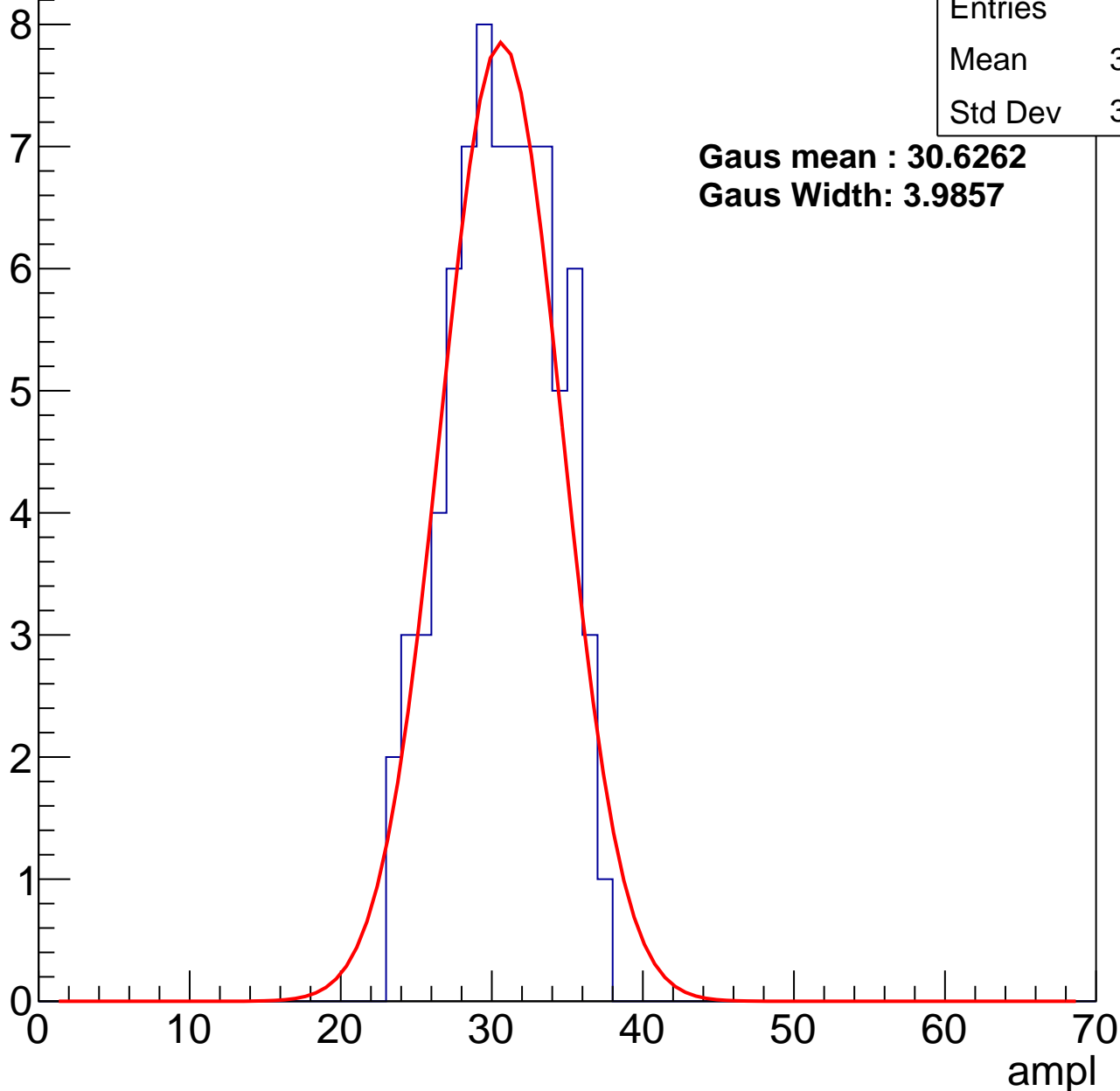
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.18
Std Dev	3.474

**Gaus mean : 30.6262**

**Gaus Width: 3.9857**



# B0L000S, U10-ch79, adc1

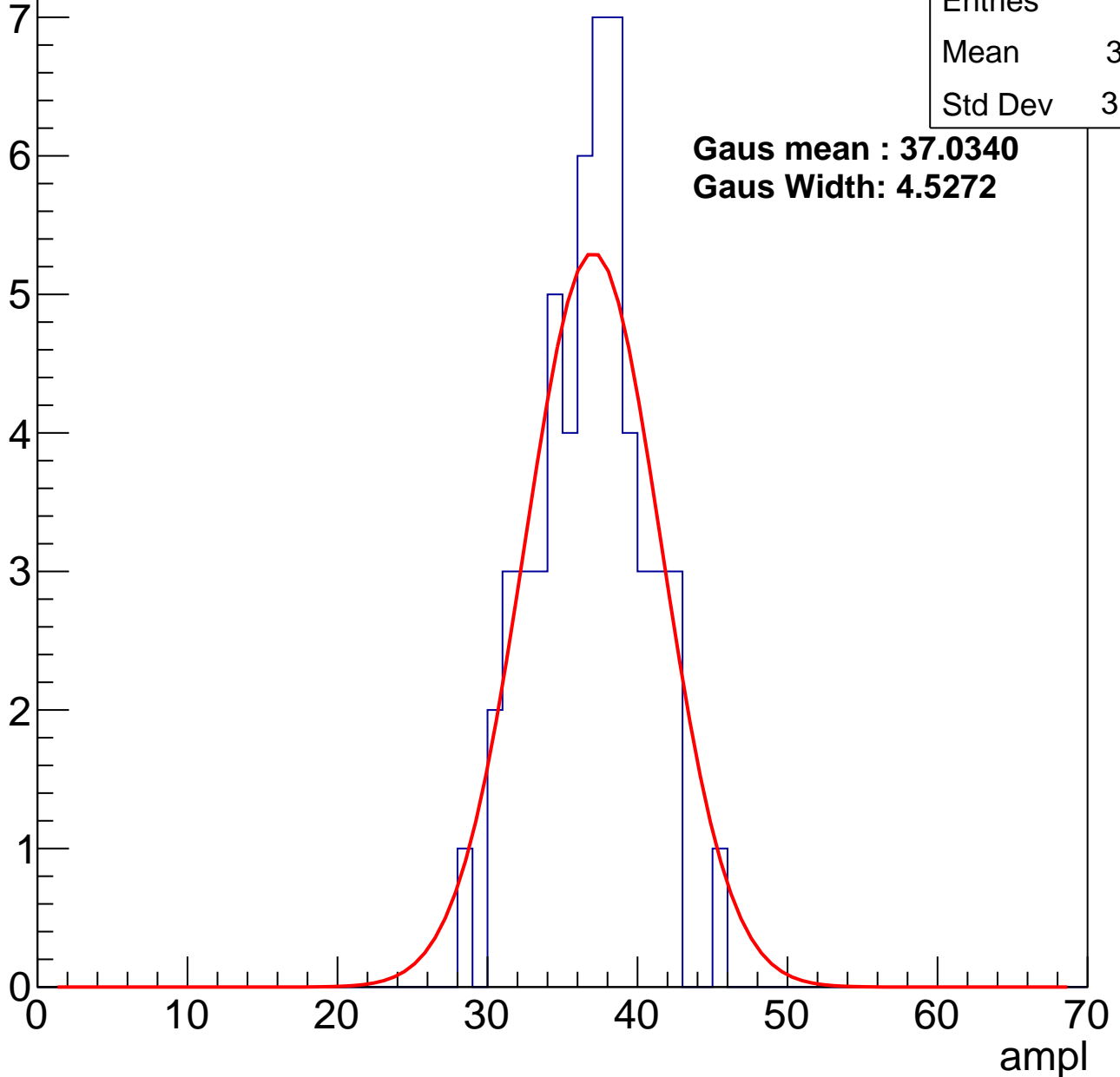
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	36.31
Std Dev	3.536

**Gaus mean : 37.0340**

**Gaus Width: 4.5272**



# B0L000S, U10-ch79, adc2

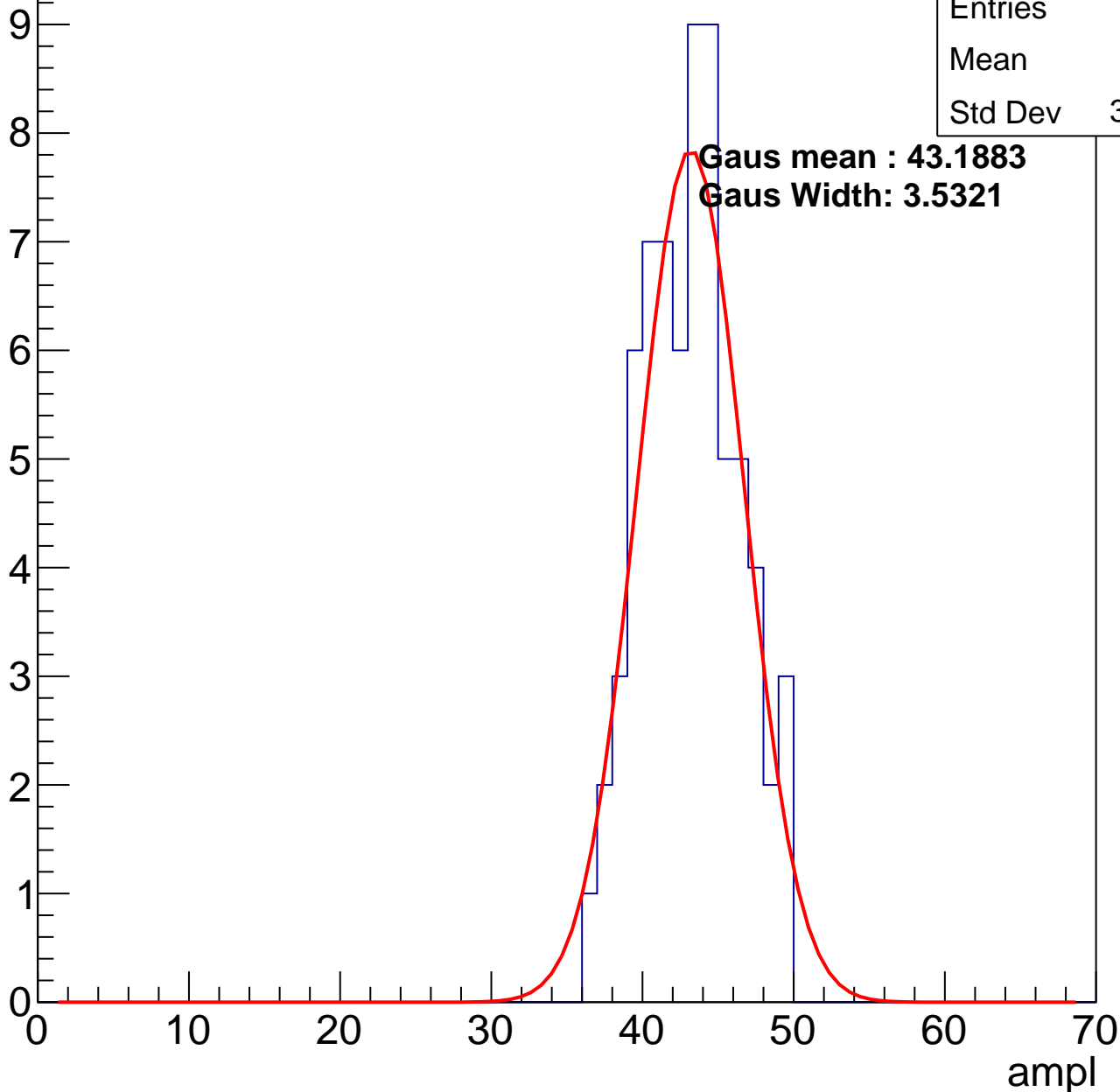
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.7
Std Dev	3.136

**Gaus mean : 43.1883**

**Gaus Width: 3.5321**

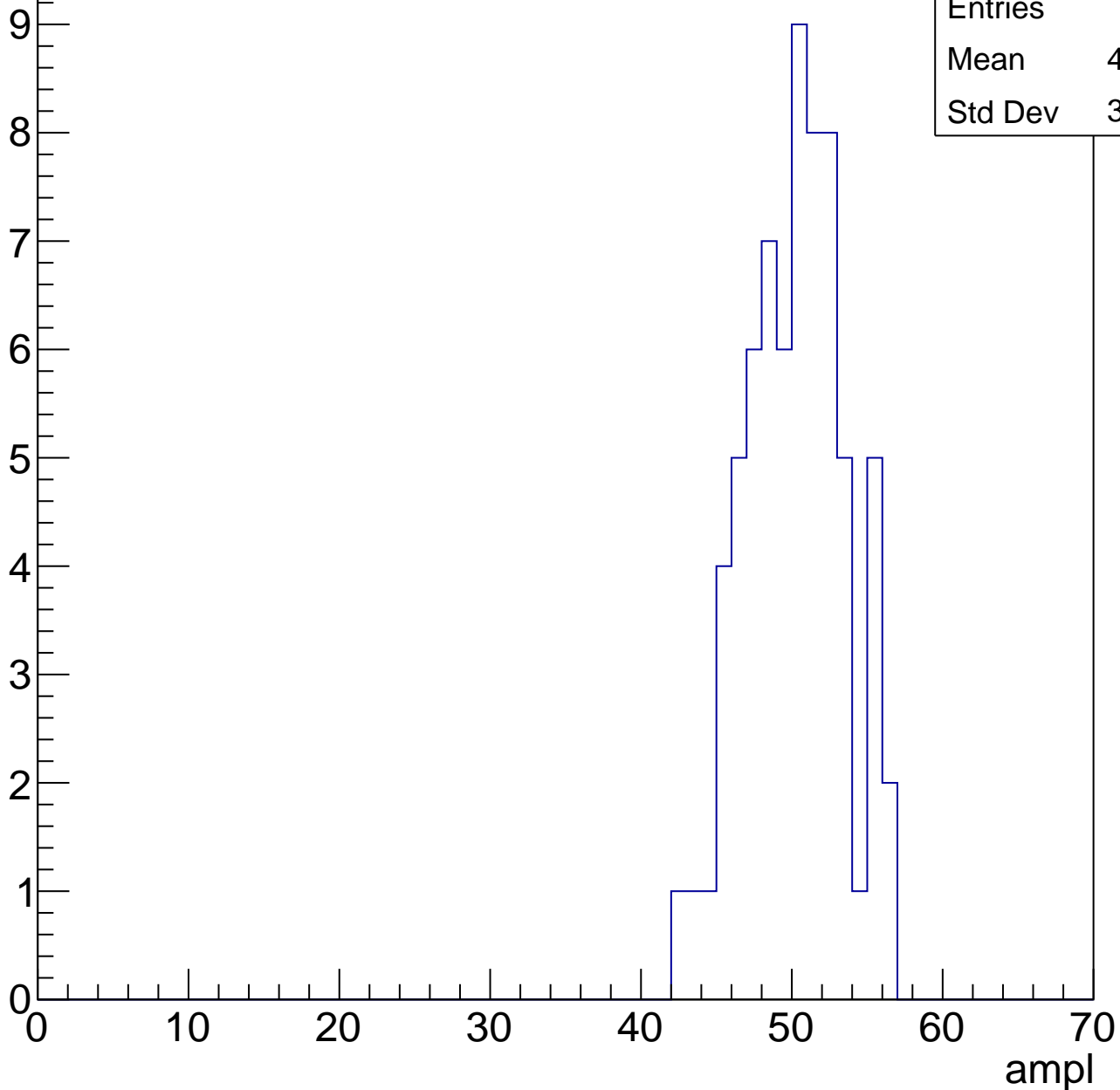


# B0L000S, U10-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.72
Std Dev	3.207

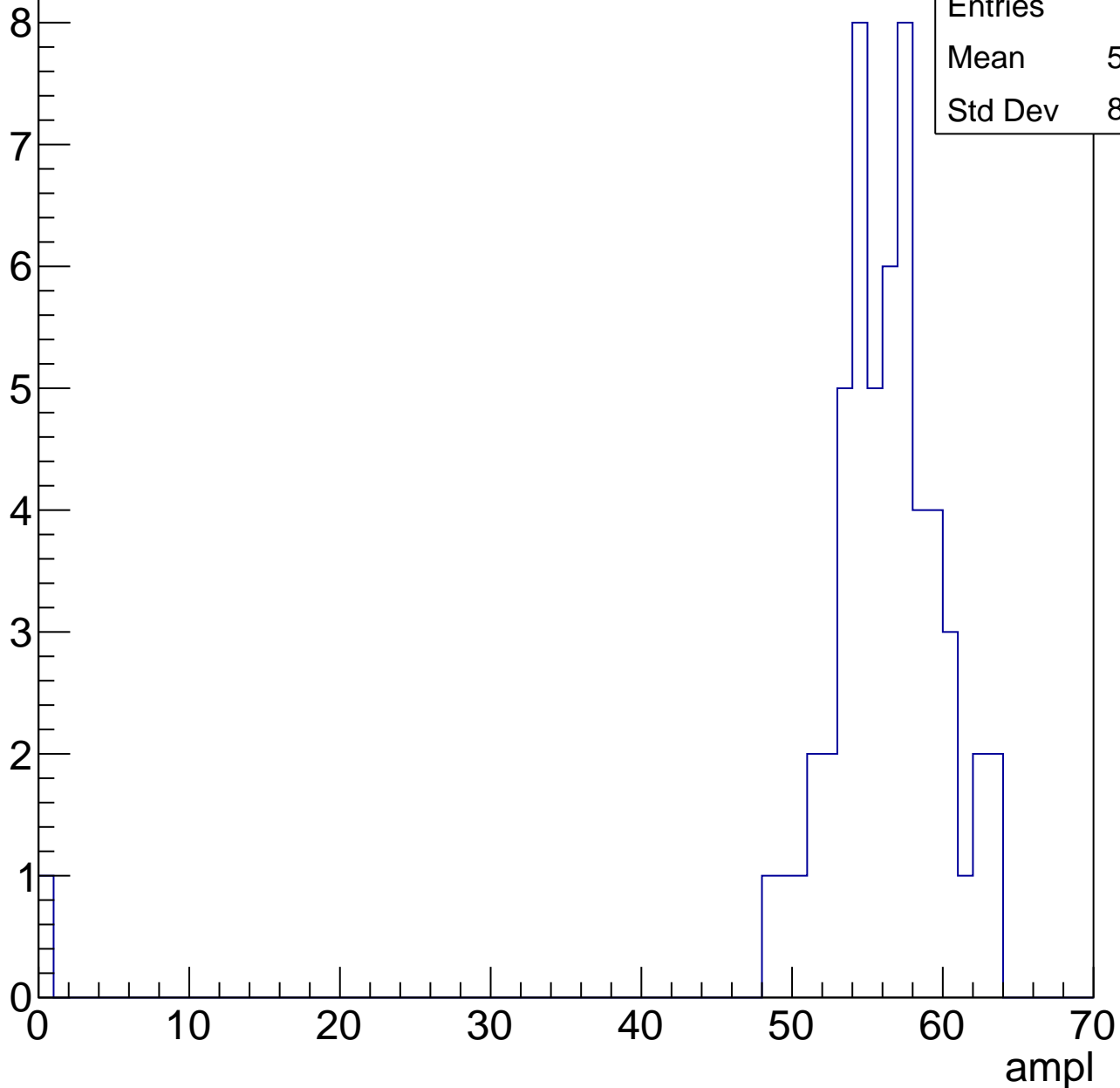


# B0L000S, U10-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	54.93
Std Dev	8.115

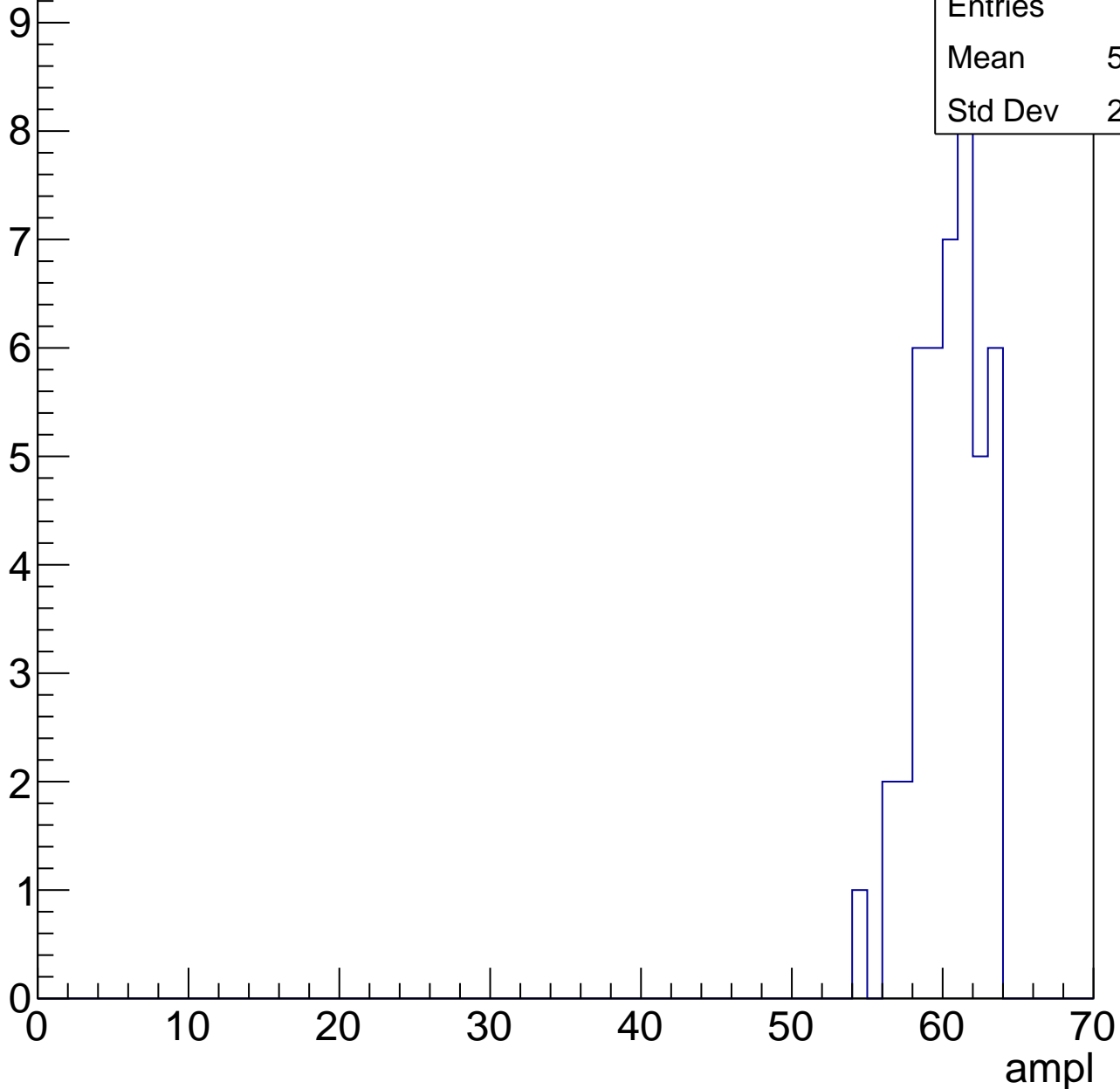


# B0L000S, U10-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

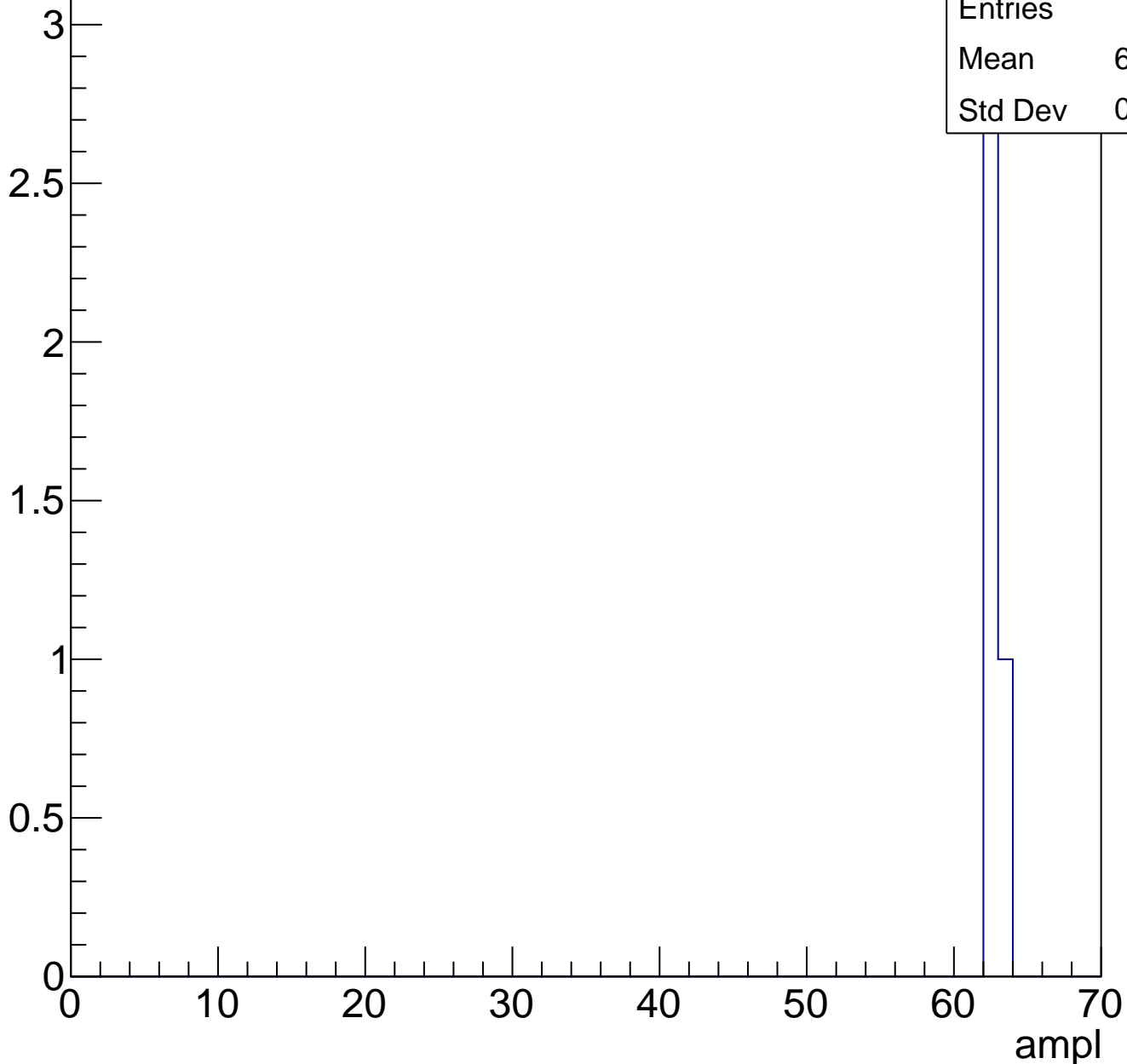
Entries	44
Mean	59.98
Std Dev	2.127



# B0L000S, U10-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0

# B0L000S, U10-ch80, adc0

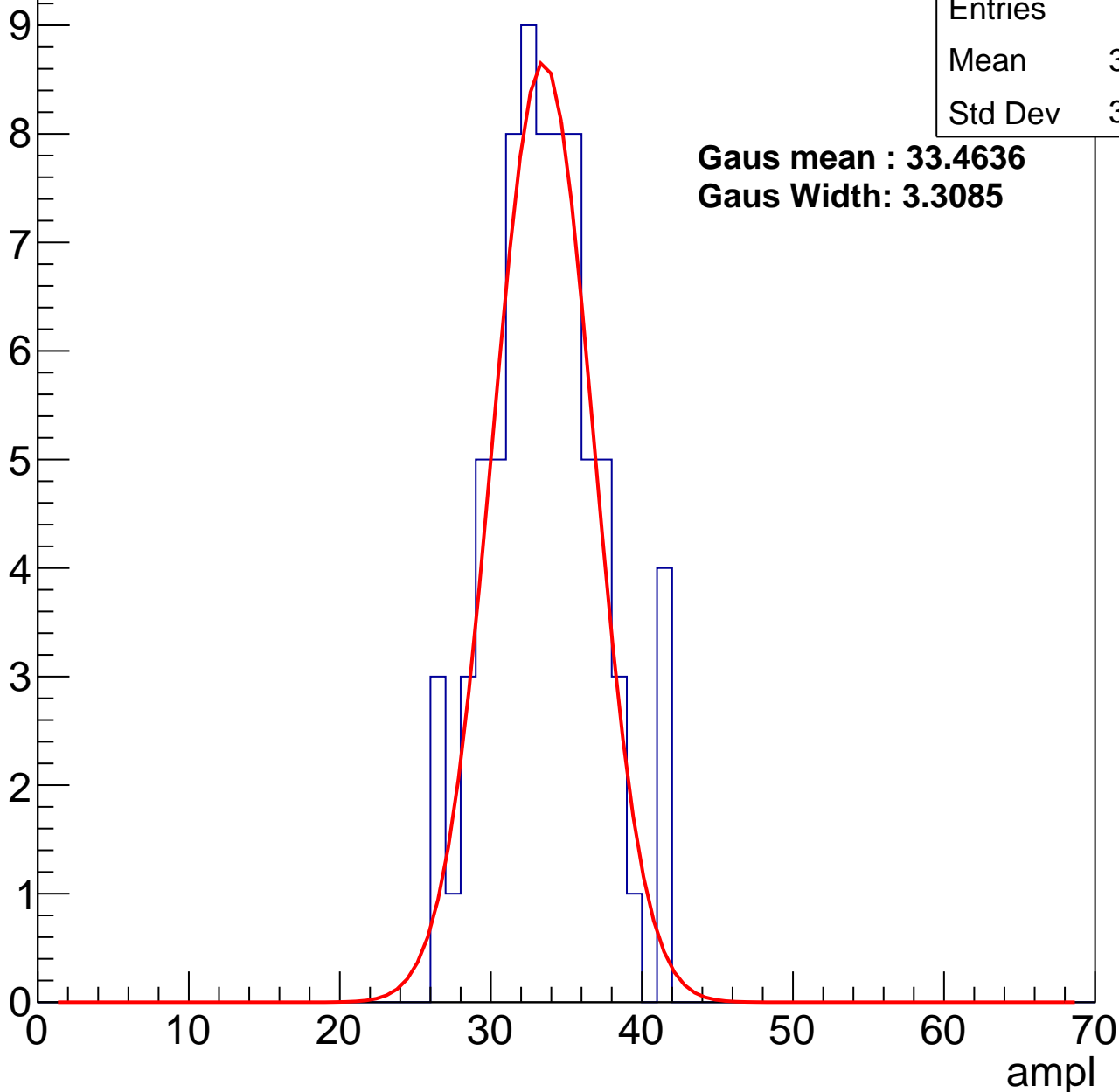
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	33.13
Std Dev	3.544

**Gaus mean : 33.4636**

**Gaus Width: 3.3085**



# B0L000S, U10-ch80, adc1

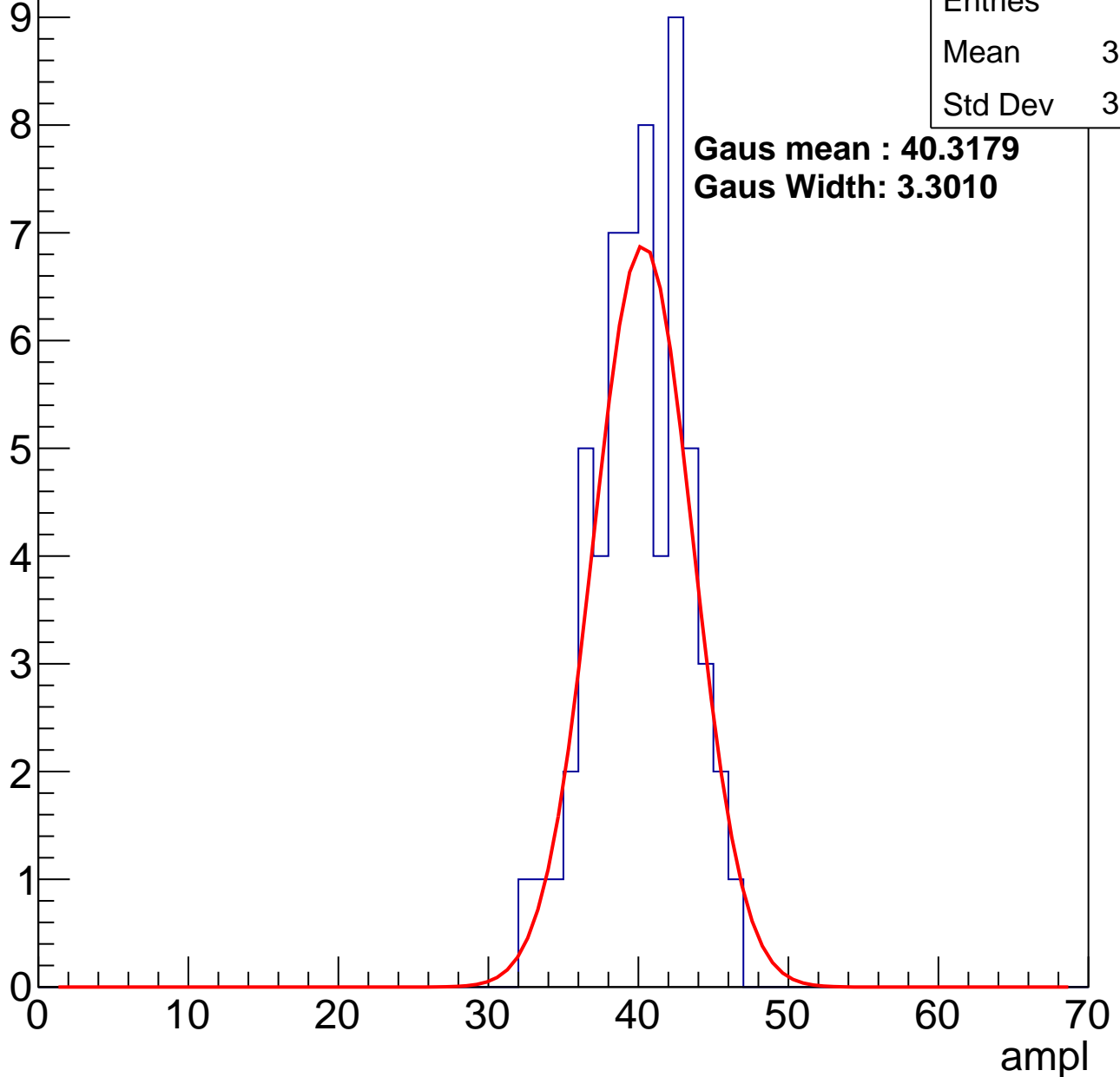
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	39.68
Std Dev	3.063

**Gaus mean : 40.3179**

**Gaus Width: 3.3010**



# B0L000S, U10-ch80, adc2

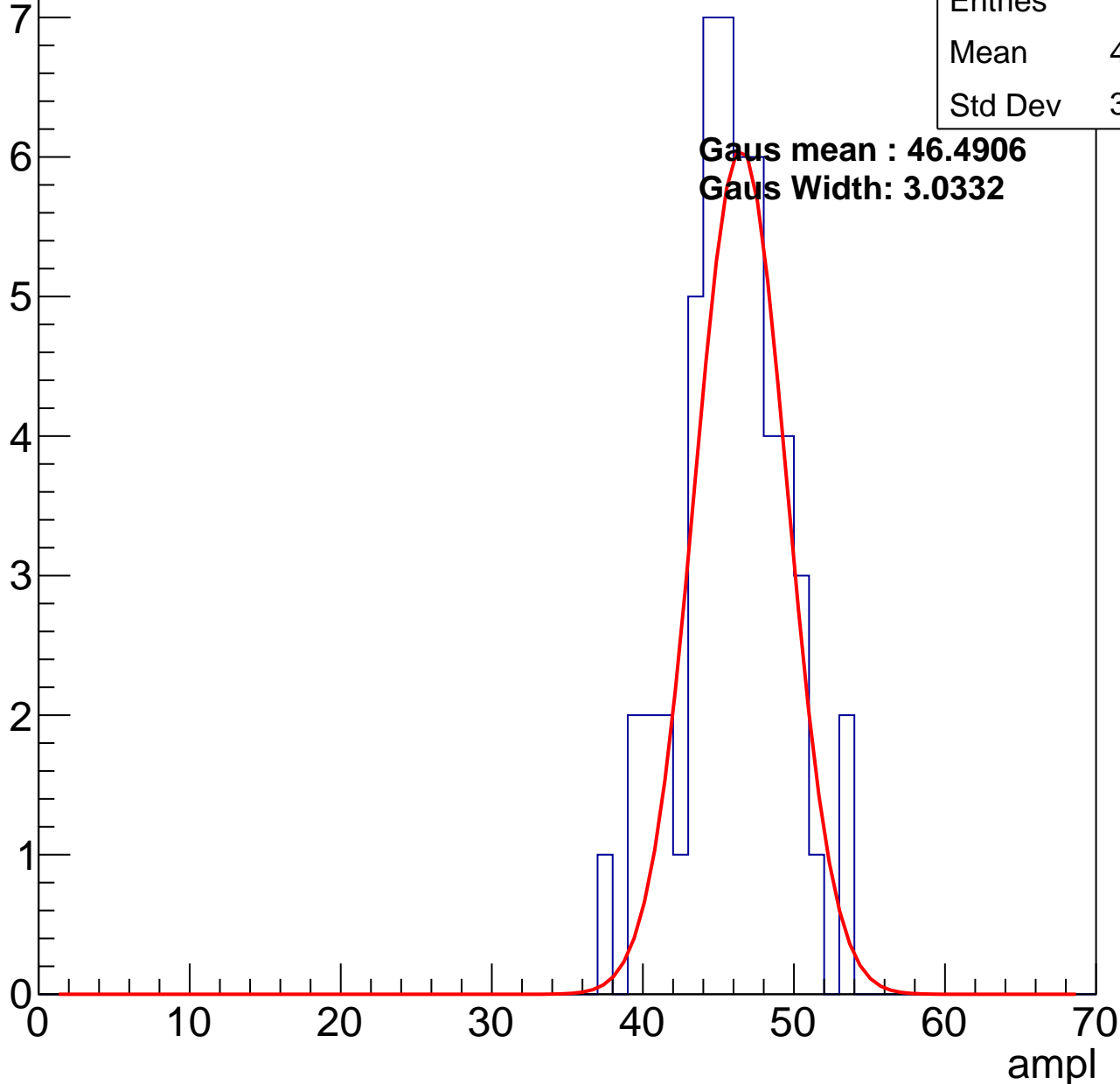
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	45.47
Std Dev	3.396

**Gaus mean : 46.4906**

**Gaus Width: 3.0332**

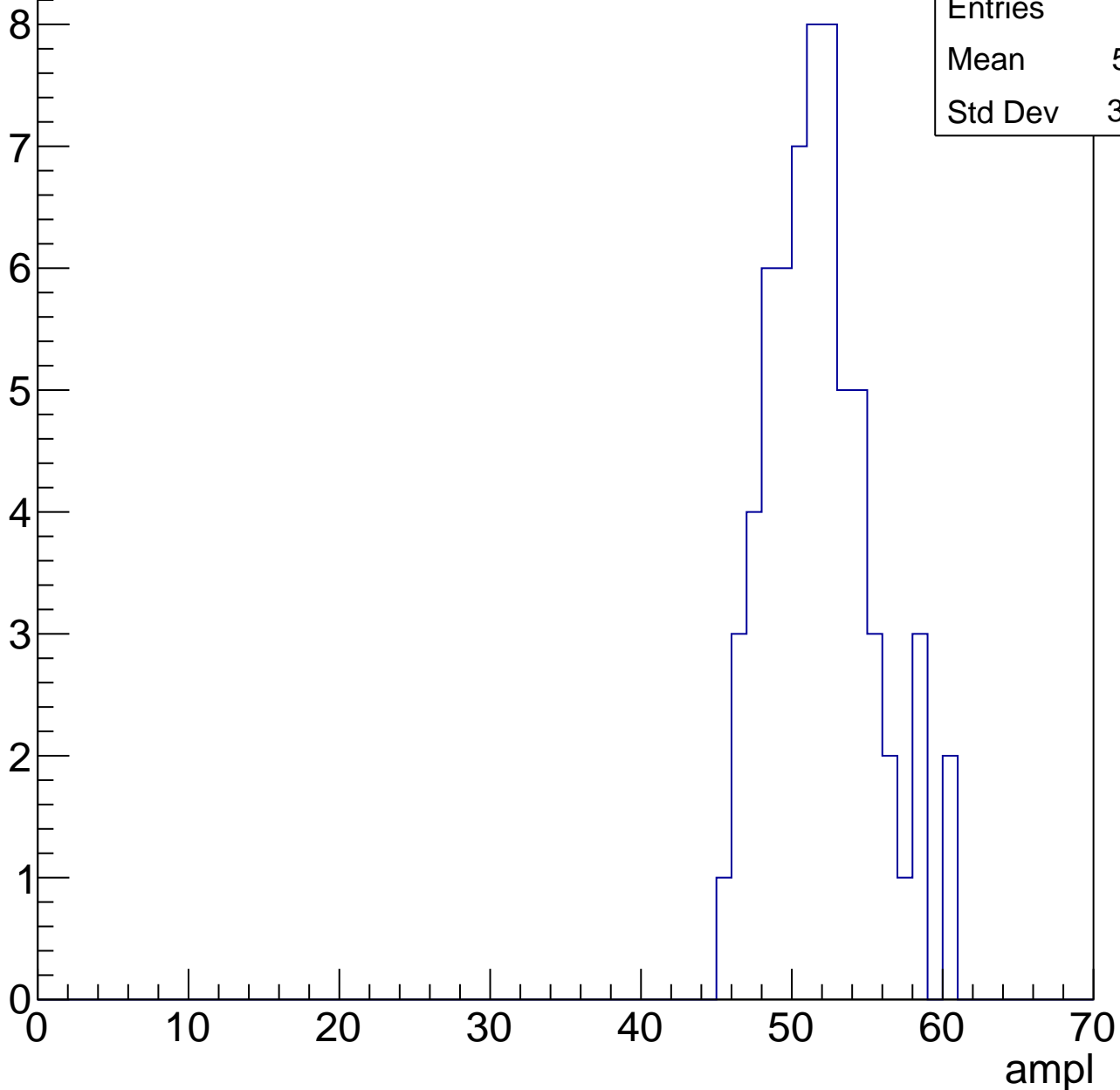


# B0L000S, U10-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.41
Std Dev	3.458

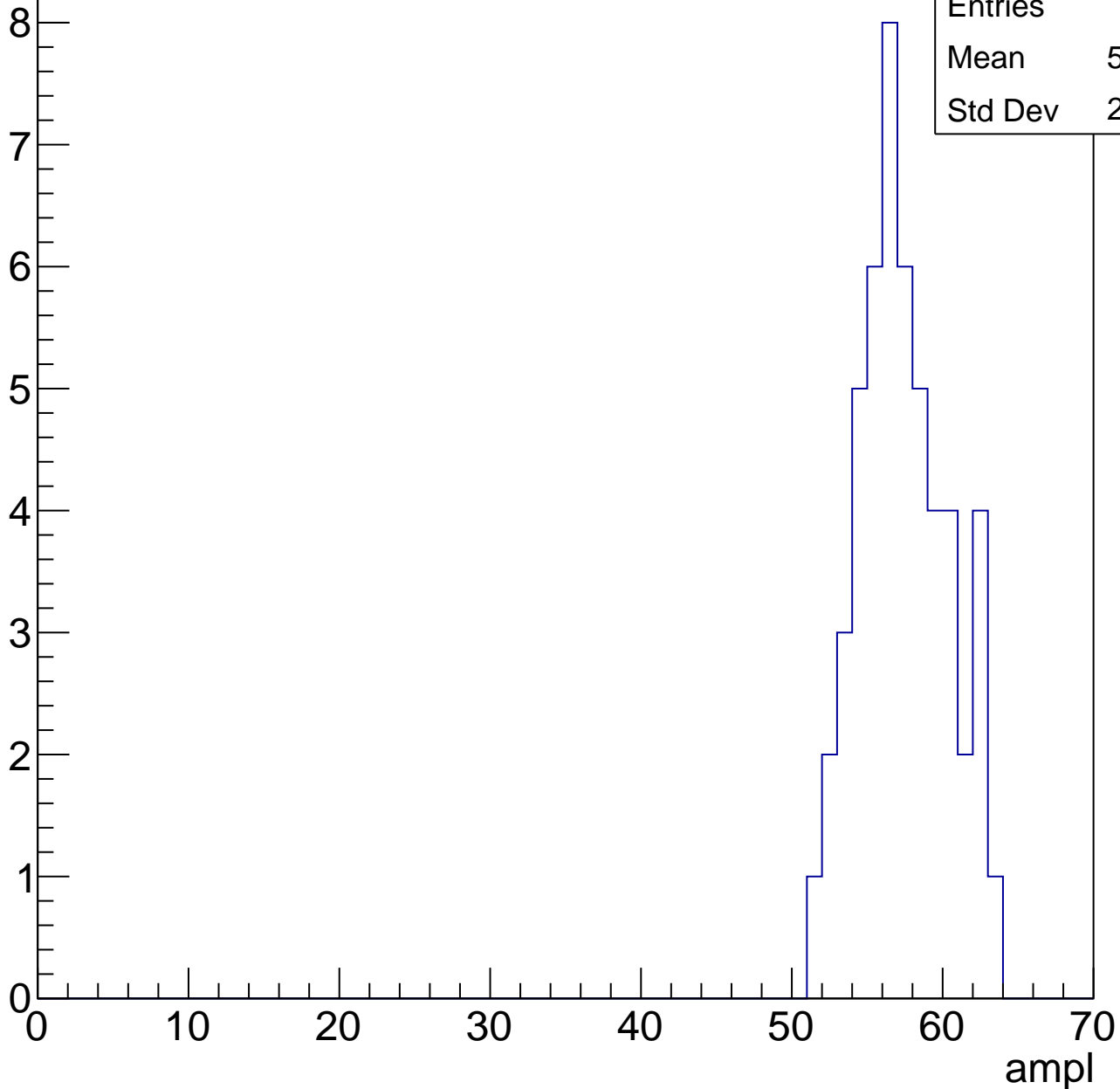


# B0L000S, U10-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	56.92
Std Dev	2.923

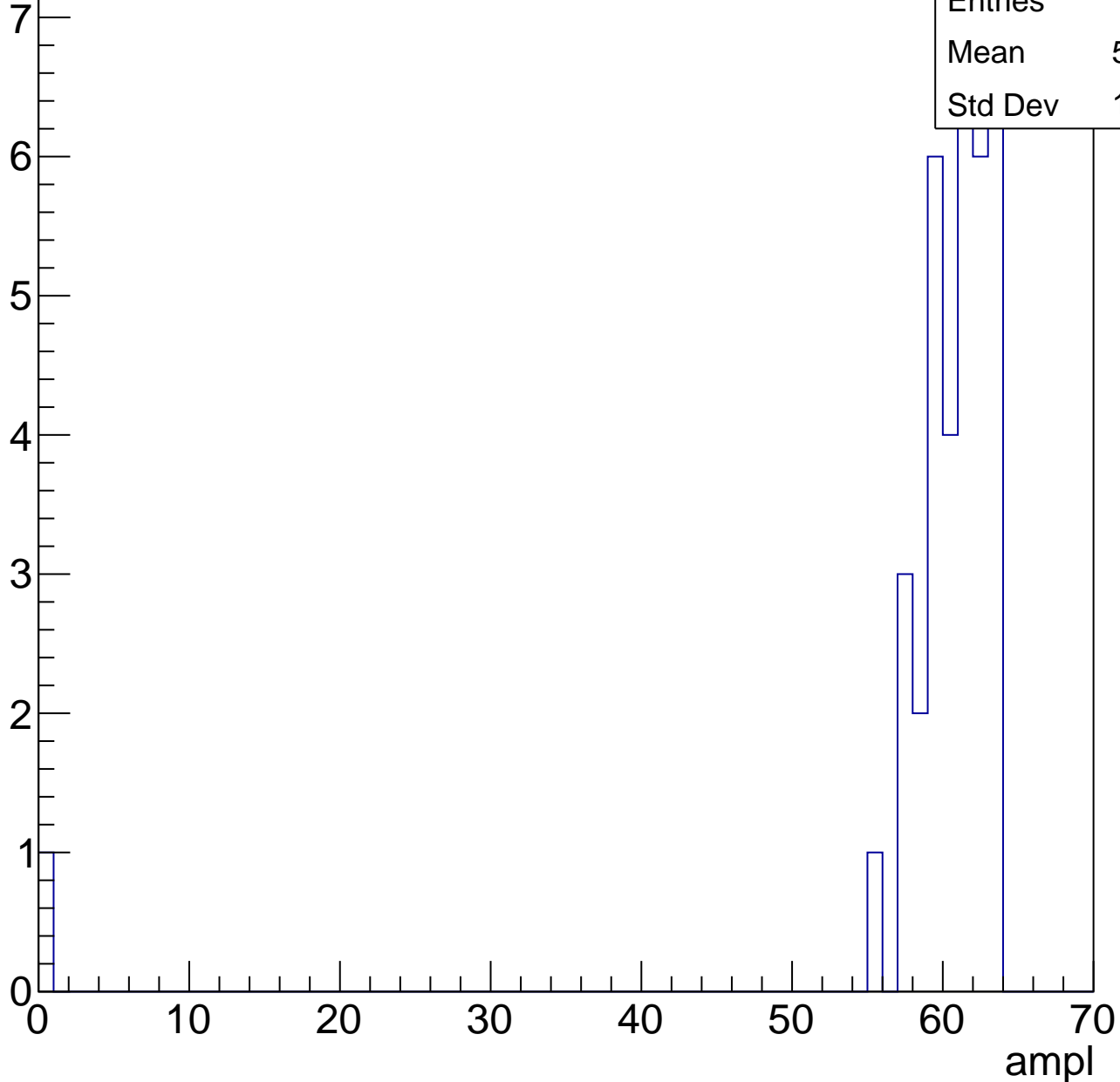


# B0L000S, U10-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.81
Std Dev	10.01



# B0L000S, U10-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch80, adc7

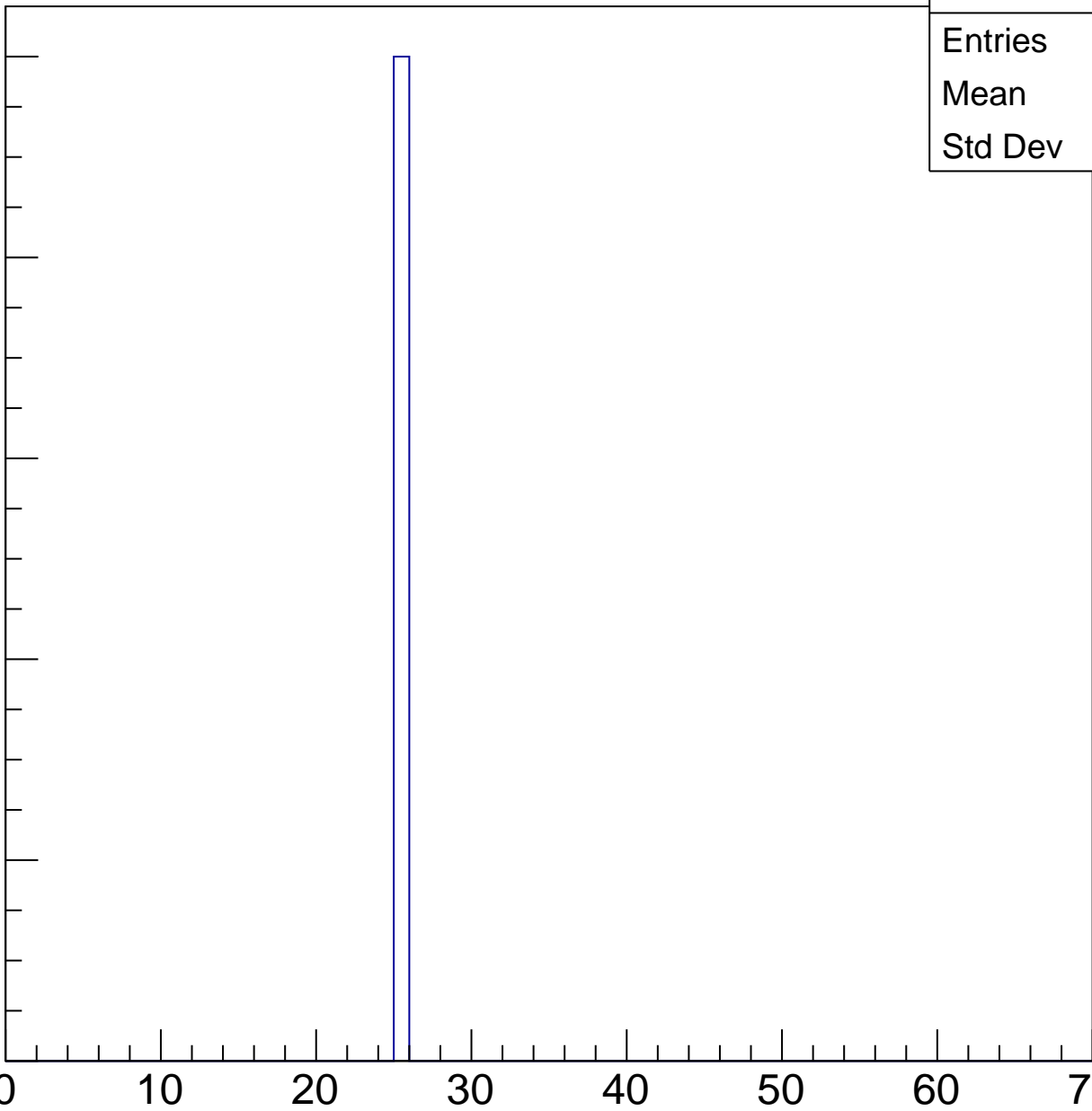
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl



# B0L000S, U10-ch81, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	29.09
Std Dev	5.237

**Gaus mean : 30.3579**

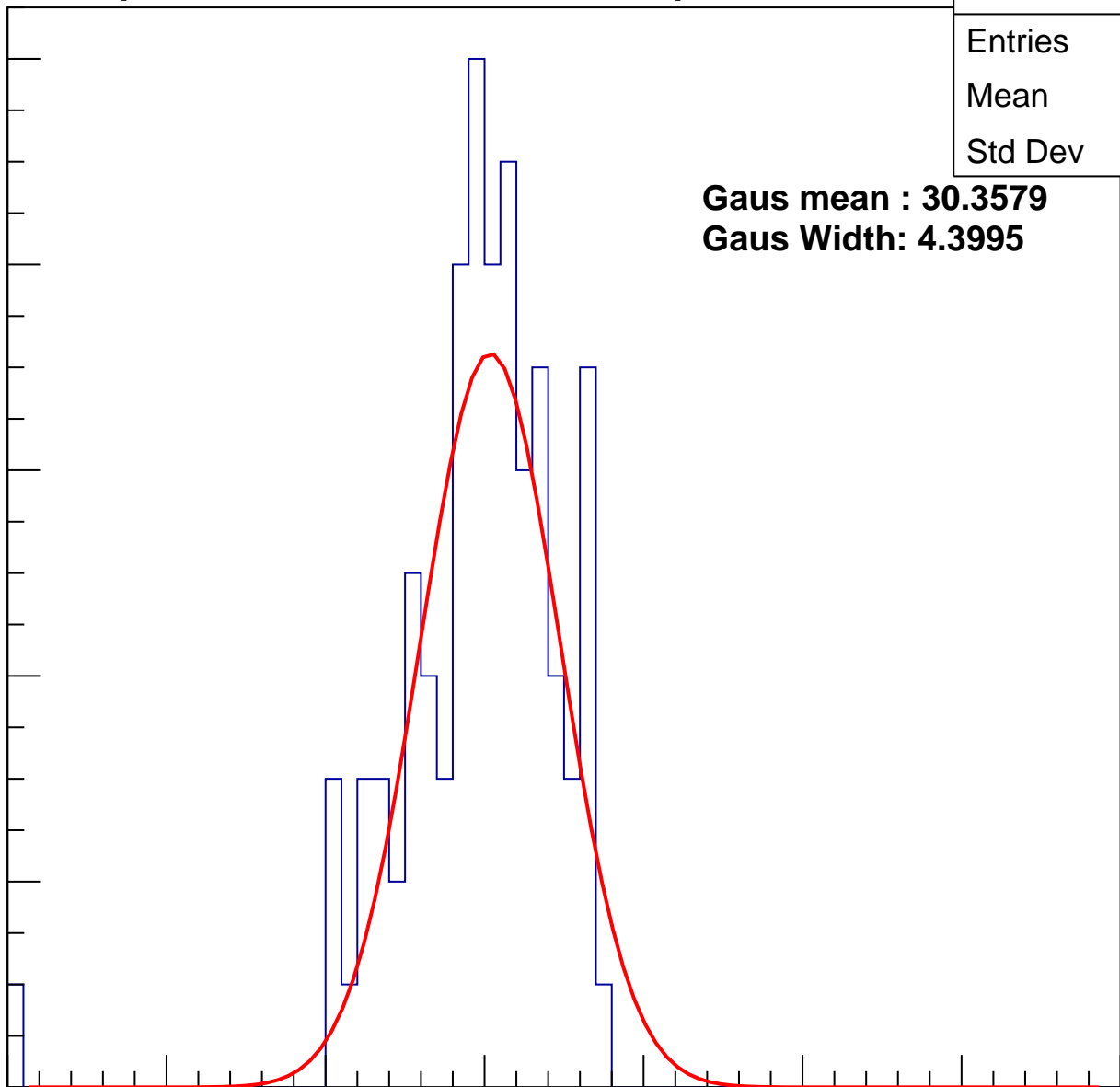
**Gaus Width: 4.3995**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



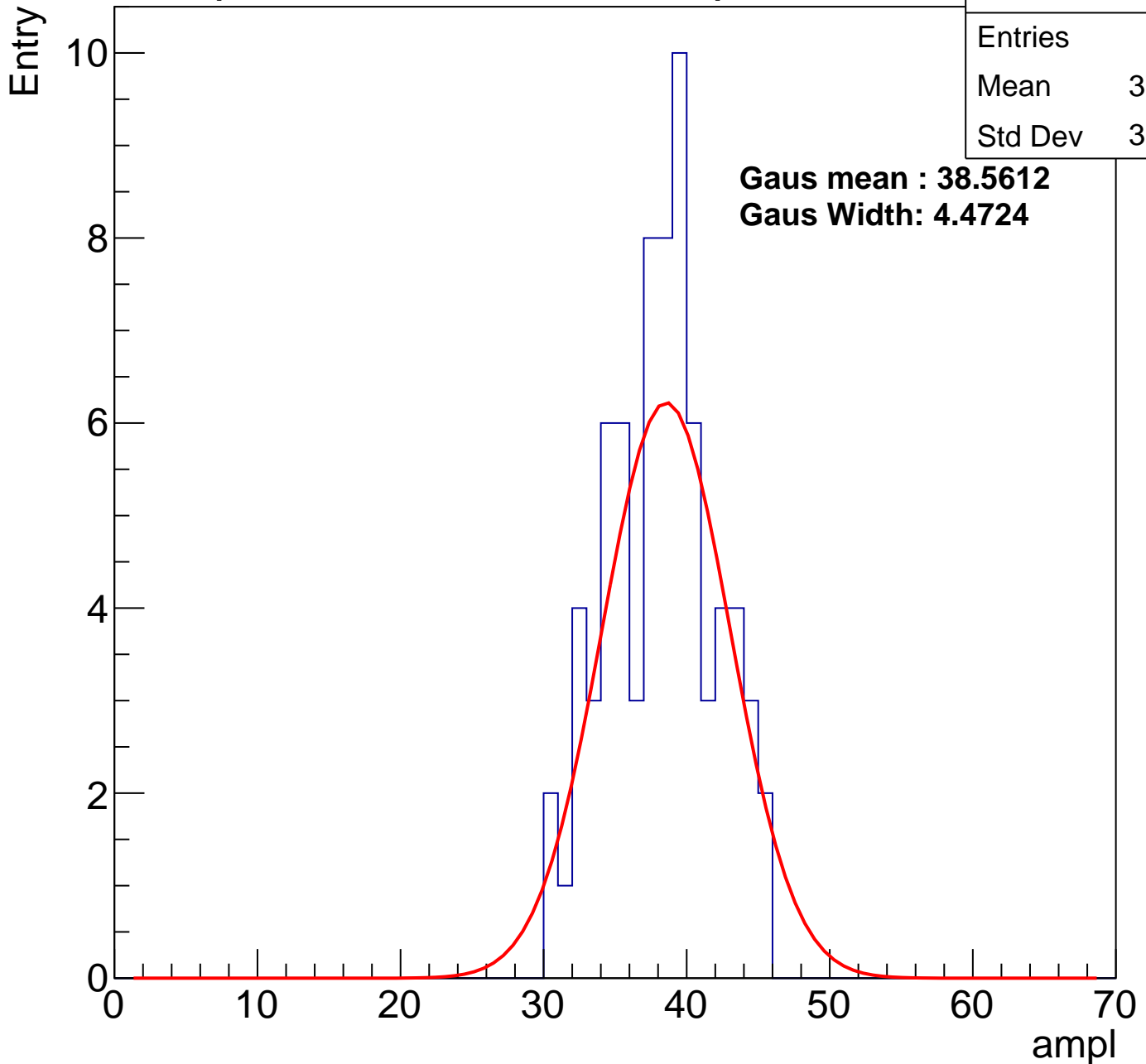
# B0L000S, U10-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	37.74
Std Dev	3.686

**Gaus mean : 38.5612**

**Gaus Width: 4.4724**



# B0L000S, U10-ch81, adc2

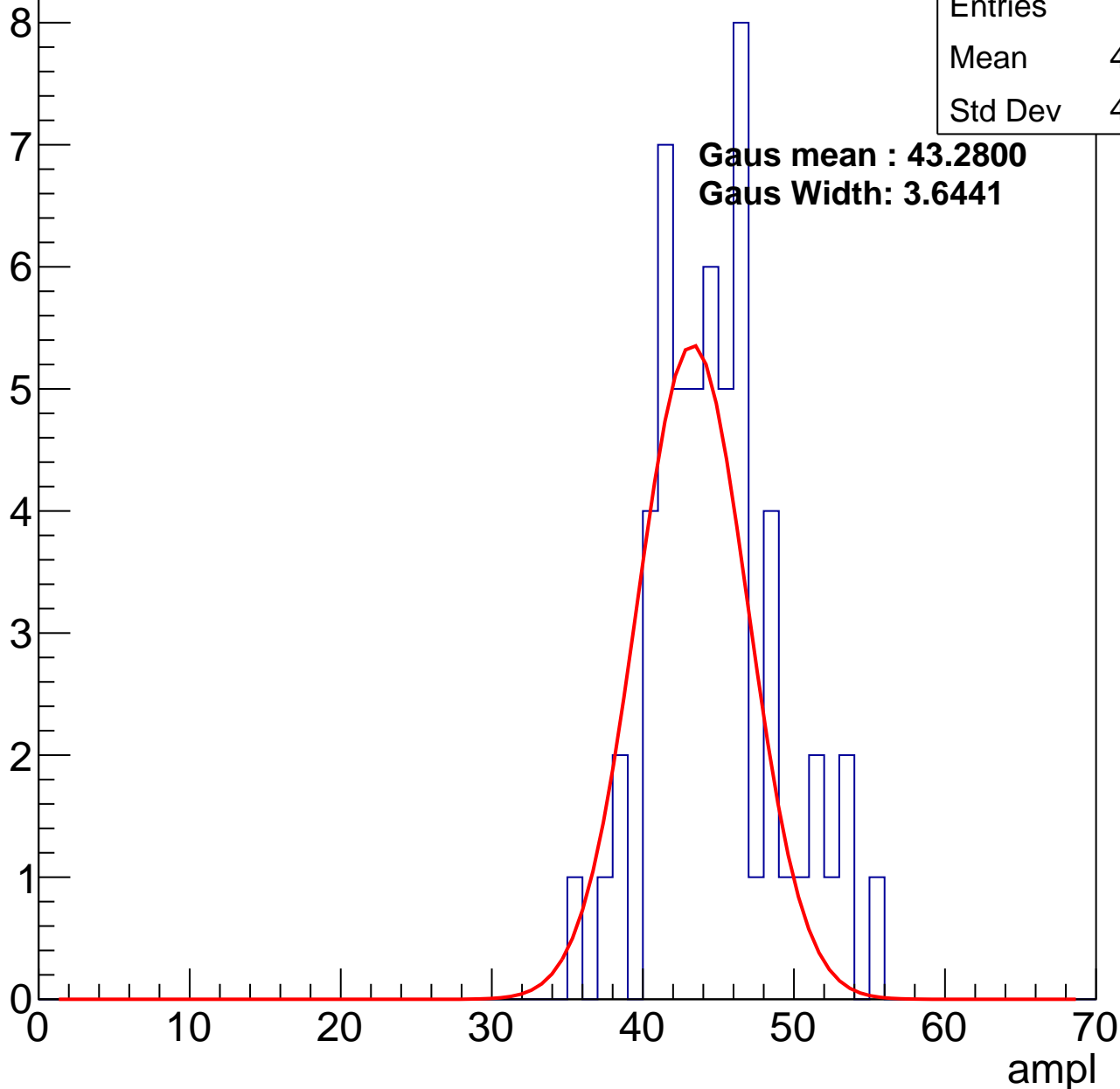
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	44.39
Std Dev	4.107

**Gaus mean : 43.2800**

**Gaus Width: 3.6441**

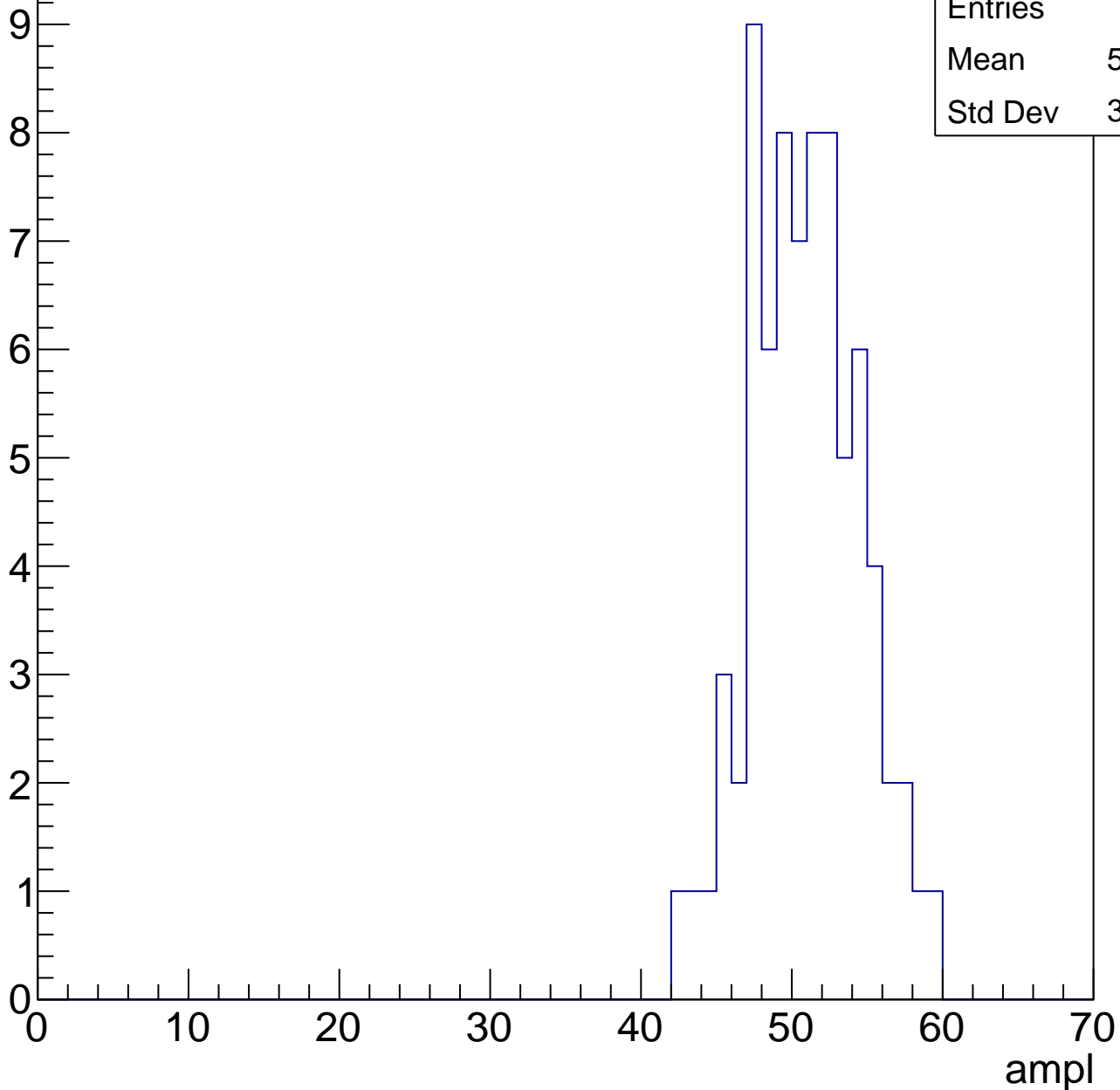


# B0L000S, U10-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

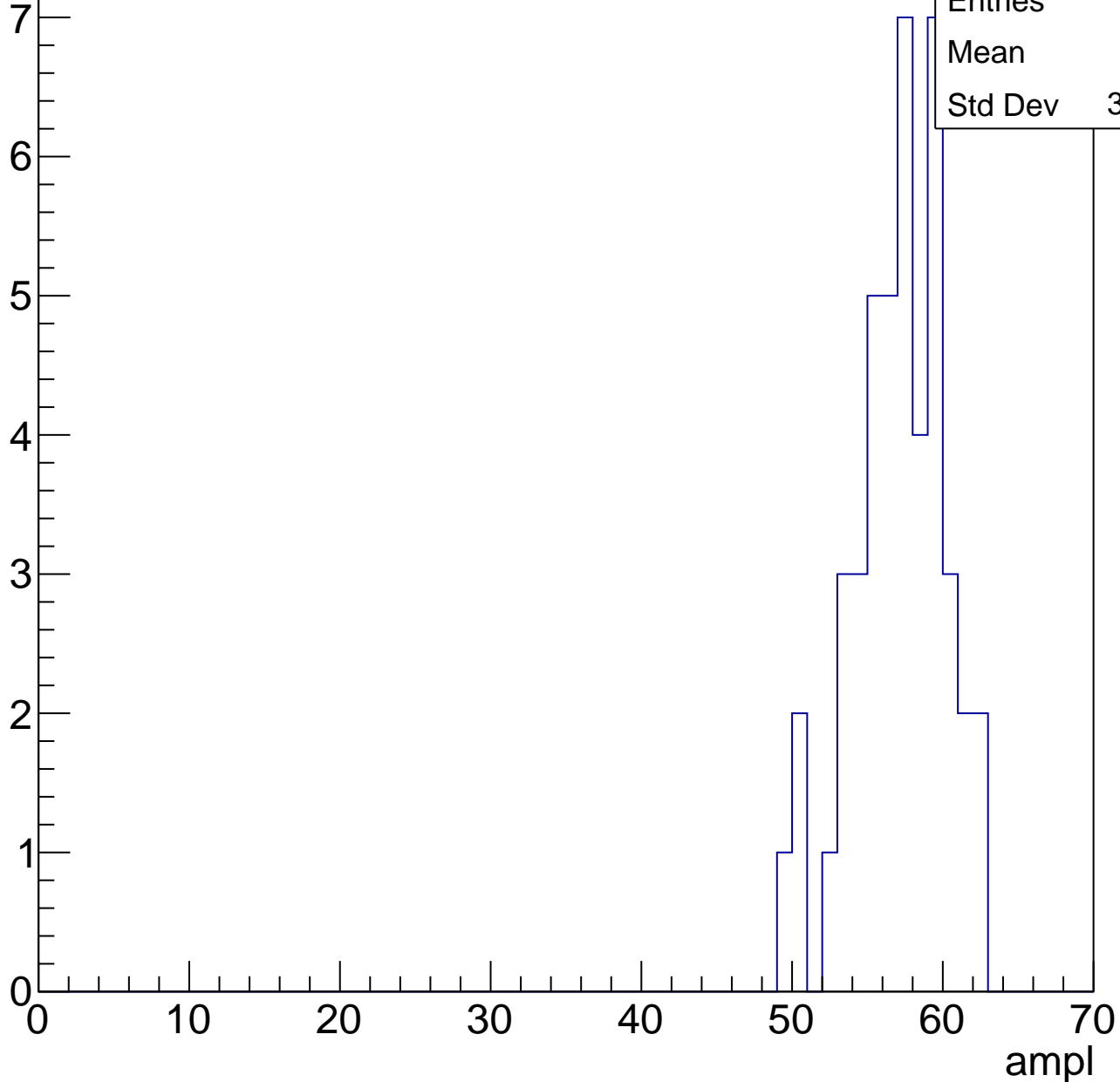
Entries	75
Mean	50.47
Std Dev	3.557



# B0L000S, U10-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

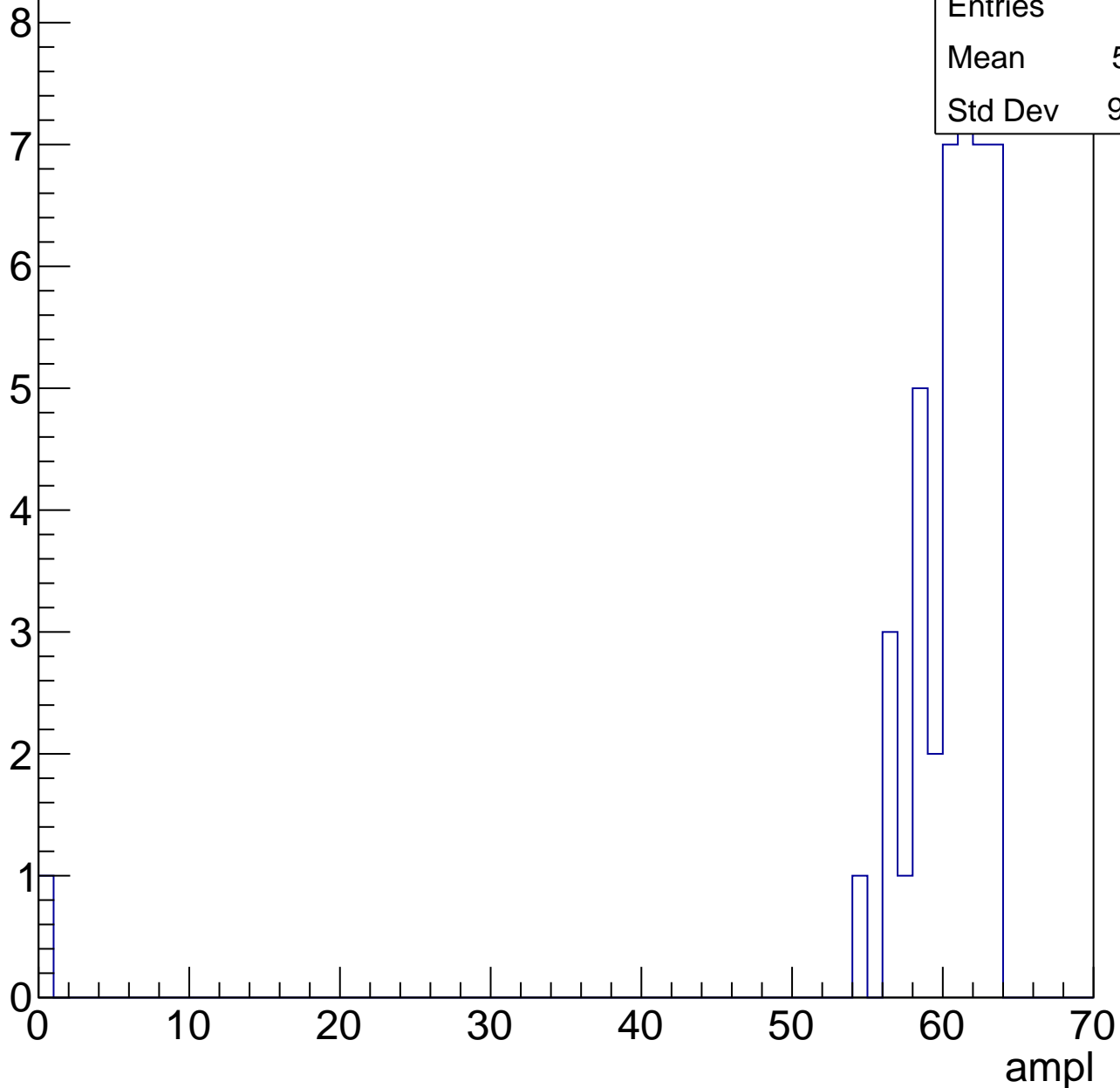


# B0L000S, U10-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.81
Std Dev	9.455



# B0L000S, U10-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch82, adc0

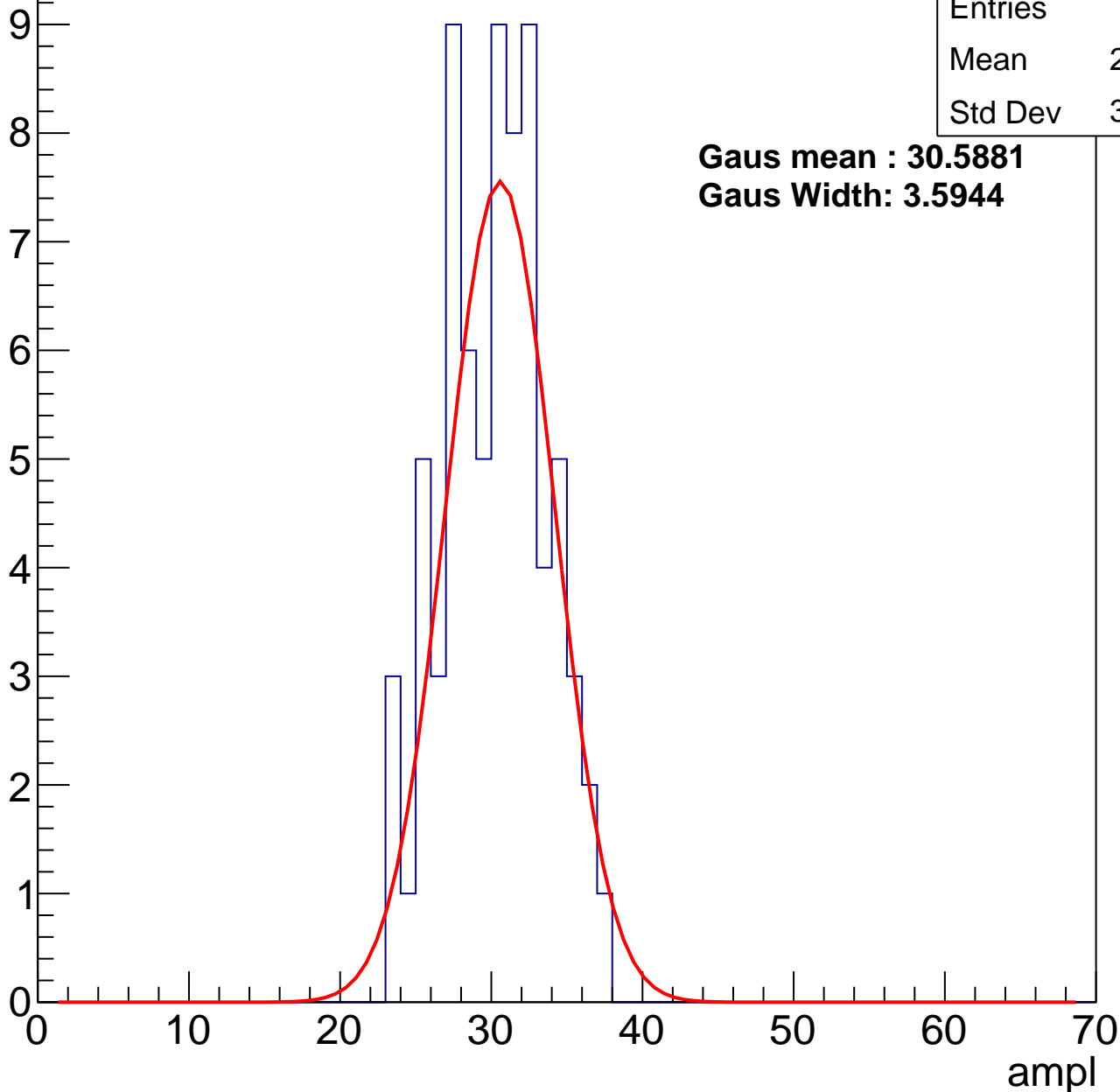
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.78
Std Dev	3.348

**Gaus mean : 30.5881**

**Gaus Width: 3.5944**



# B0L000S, U10-ch82, adc1

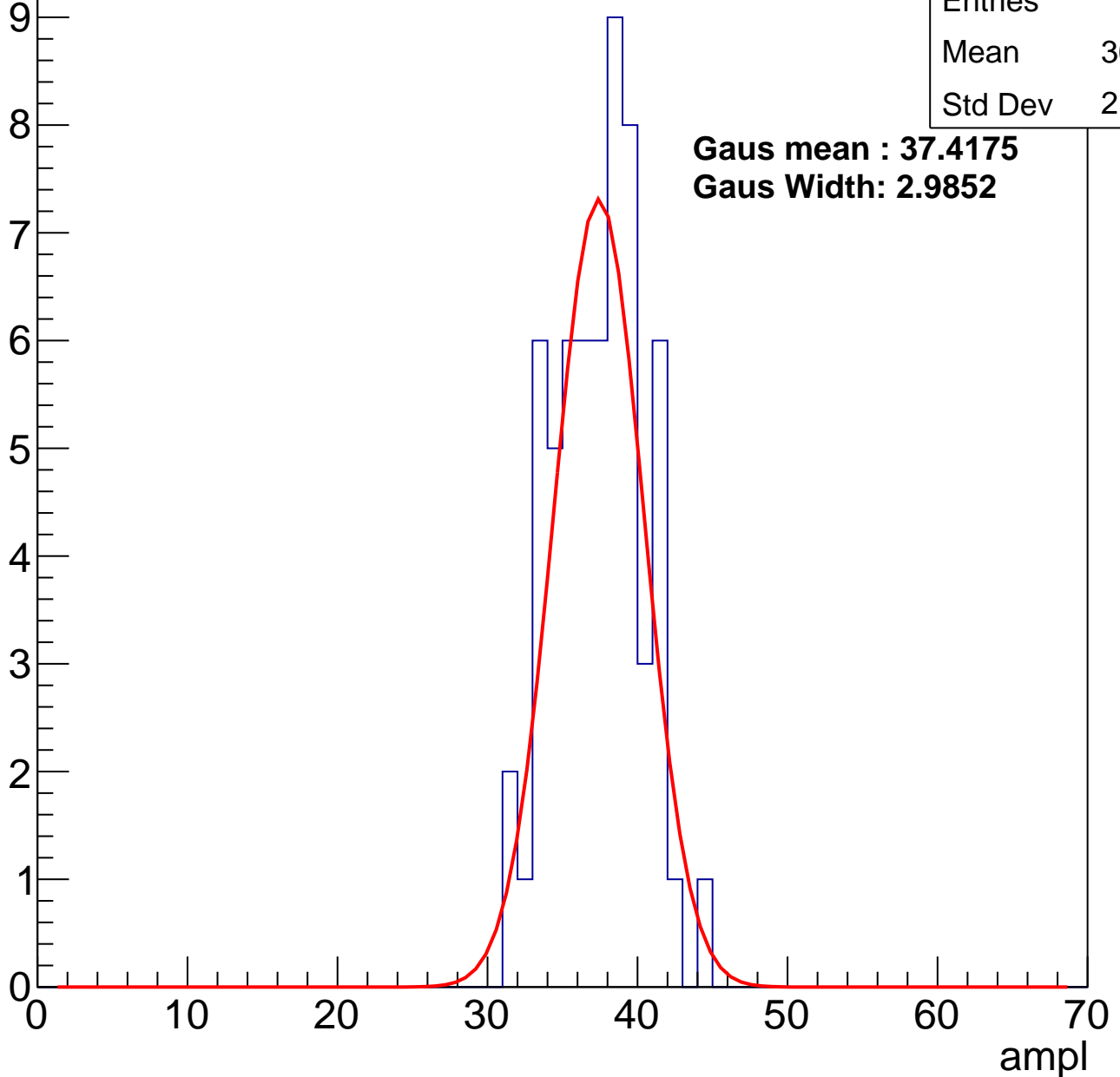
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.93
Std Dev	2.903

**Gaus mean : 37.4175**

**Gaus Width: 2.9852**



# B0L000S, U10-ch82, adc2

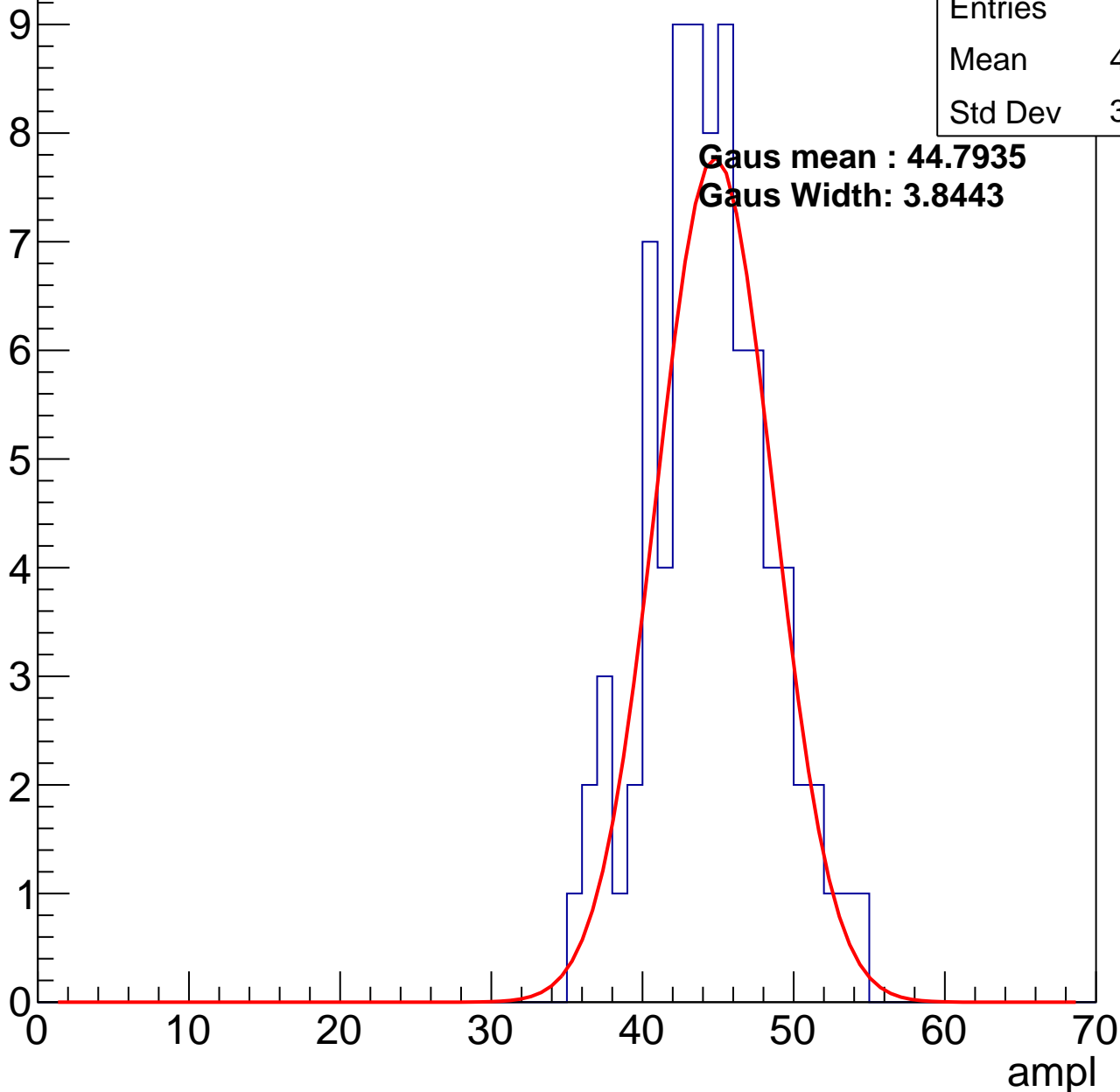
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	43.99
Std Dev	3.983

**Gaus mean : 44.7935**

**Gaus Width: 3.8443**

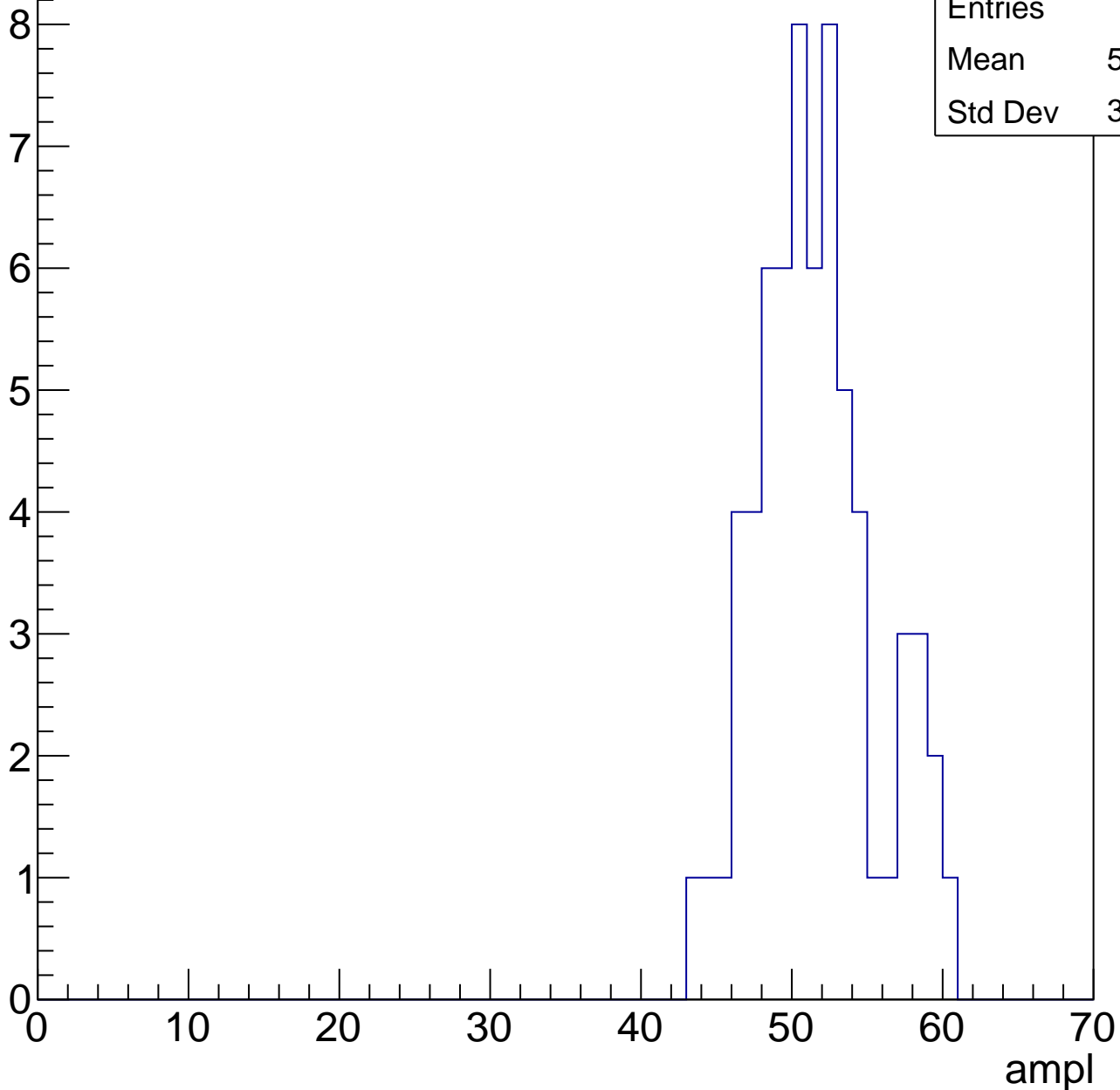


# B0L000S, U10-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

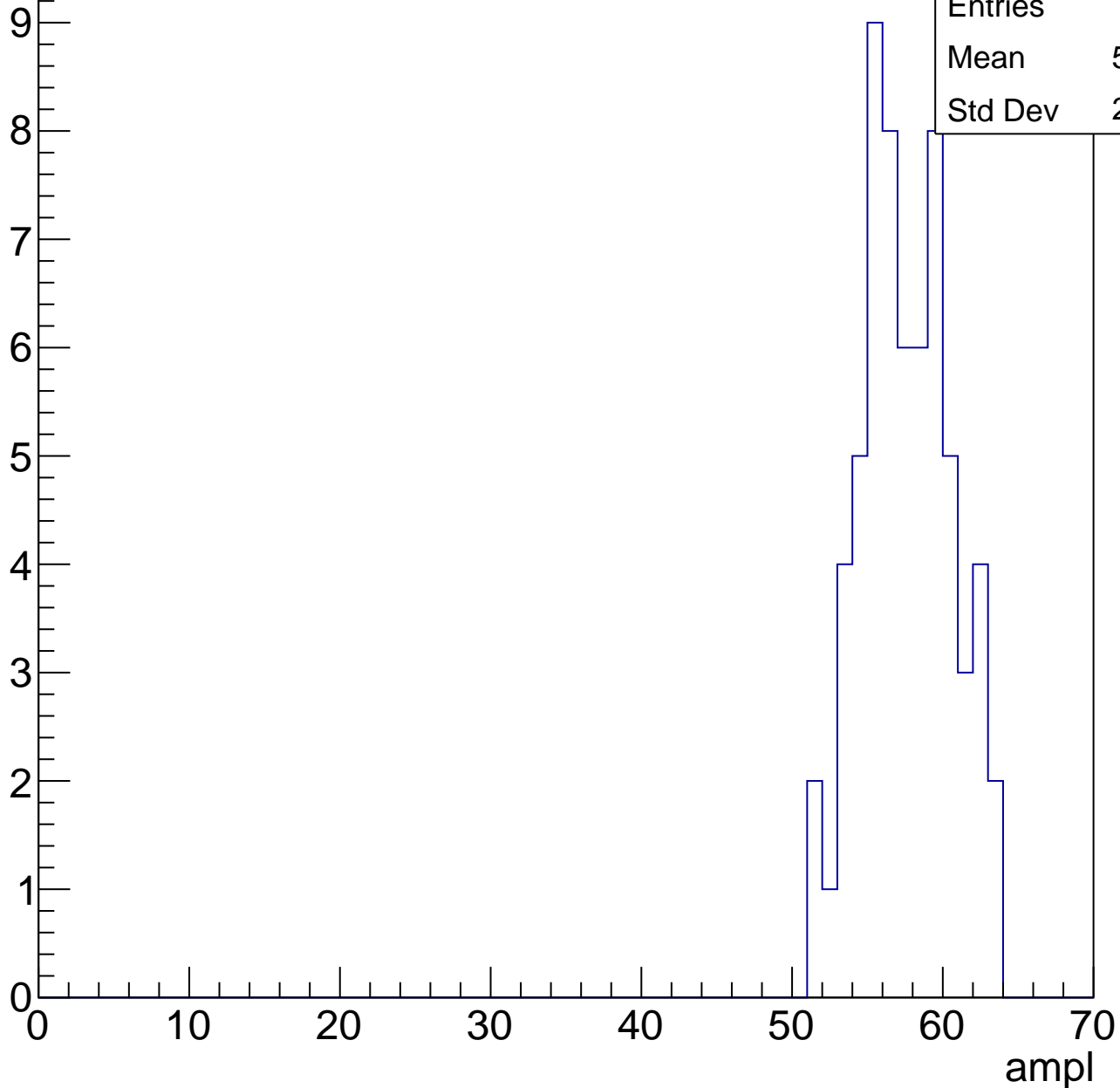
Entries	65
Mean	51.12
Std Dev	3.857



# B0L000S, U10-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

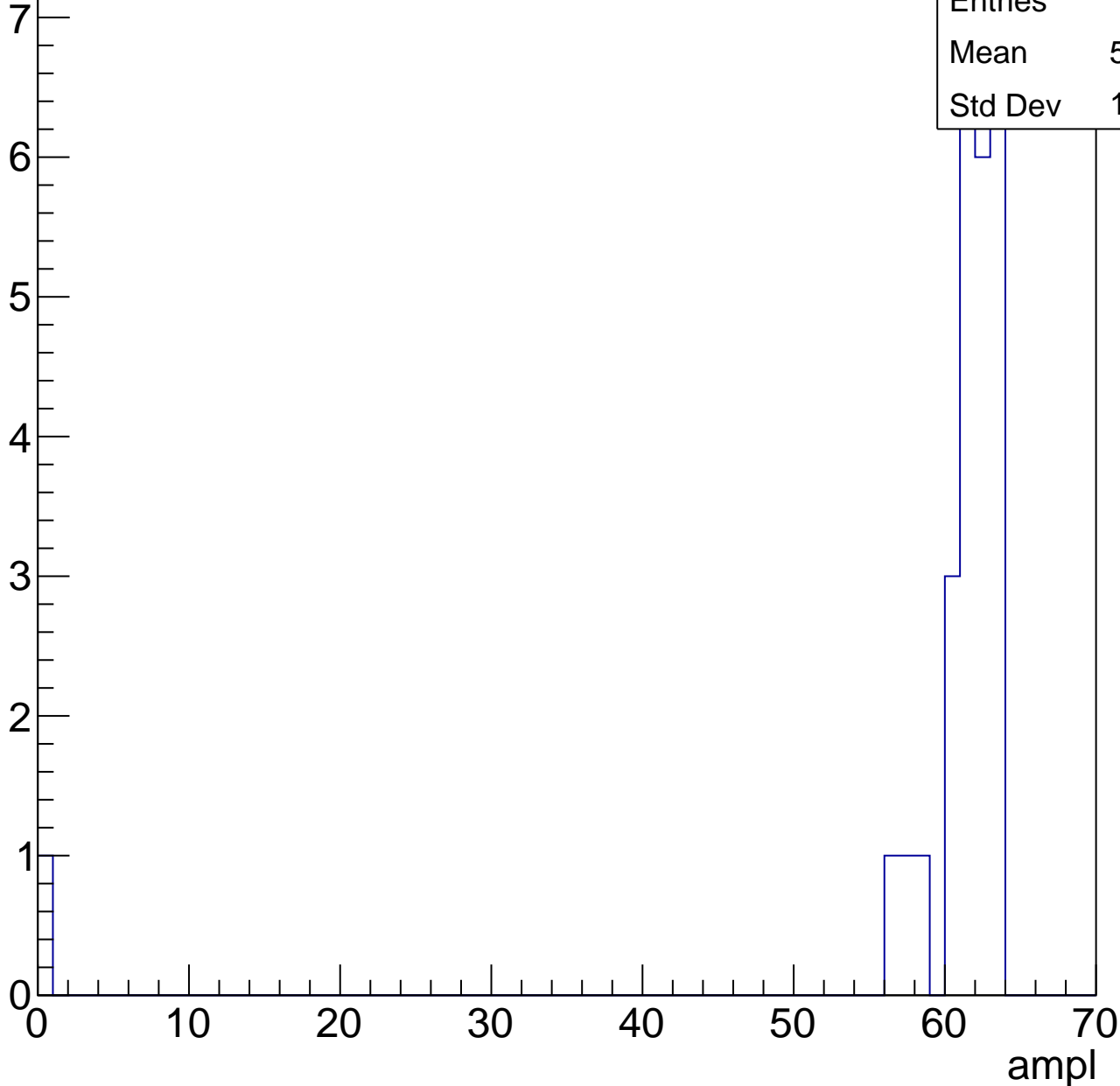


# B0L000S, U10-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	58.93
Std Dev	11.69



# B0L000S, U10-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch83, adc0

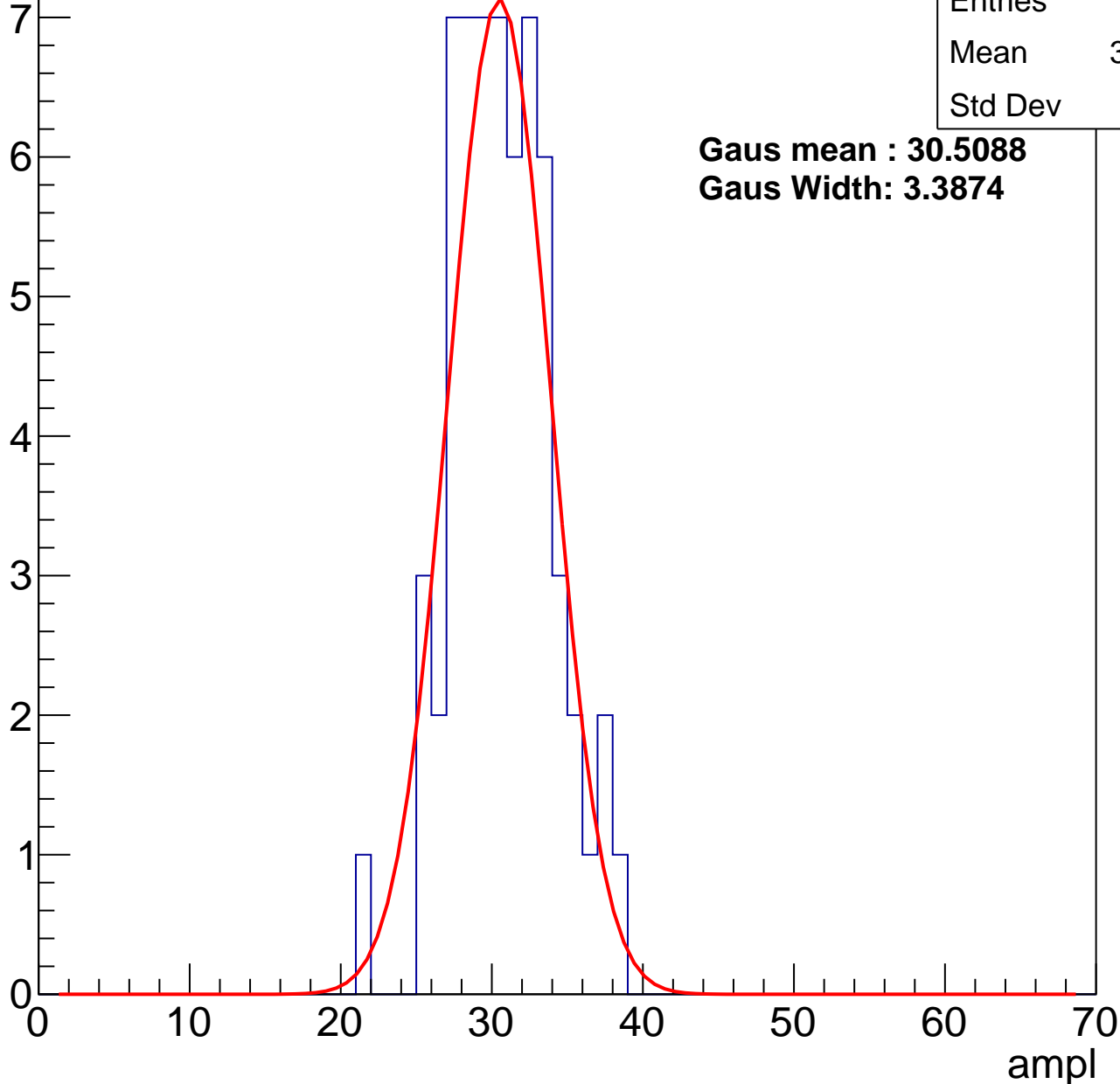
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.23
Std Dev	3.28

**Gaus mean : 30.5088**

**Gaus Width: 3.3874**



# B0L000S, U10-ch83, adc1

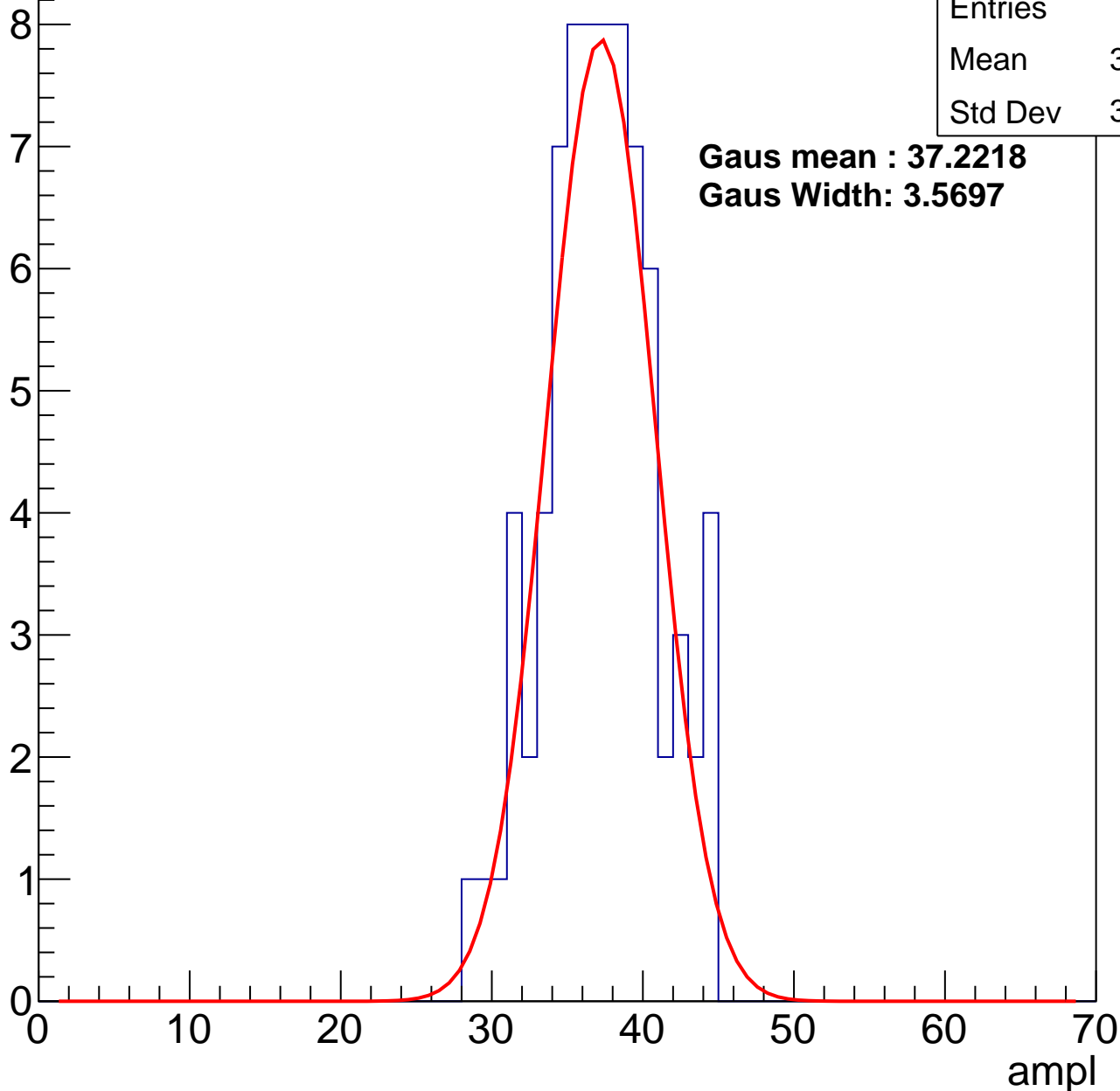
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.79
Std Dev	3.657

**Gaus mean : 37.2218**

**Gaus Width: 3.5697**



# B0L000S, U10-ch83, adc2

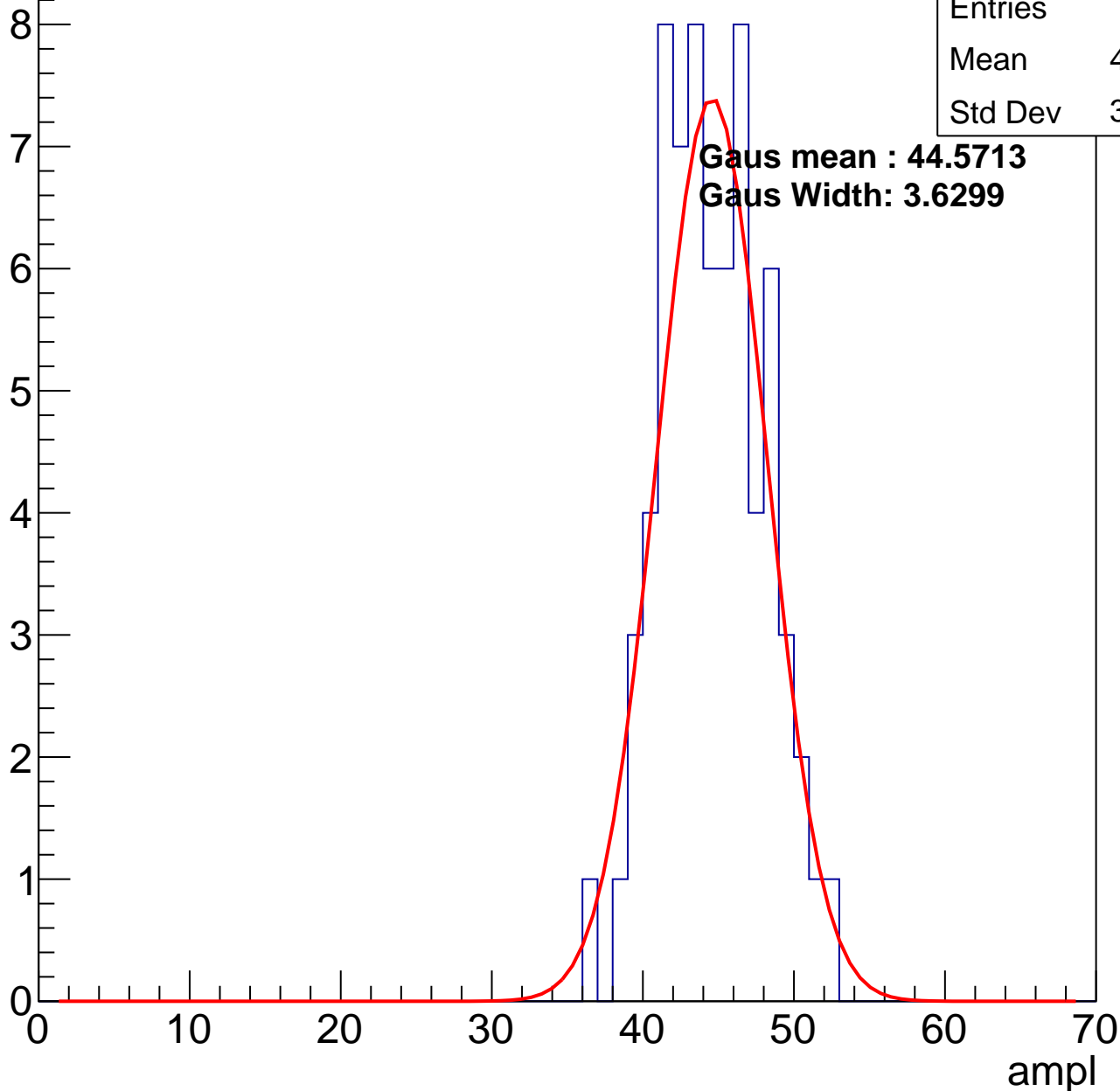
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.13
Std Dev	3.353

**Gaus mean : 44.5713**

**Gaus Width: 3.6299**

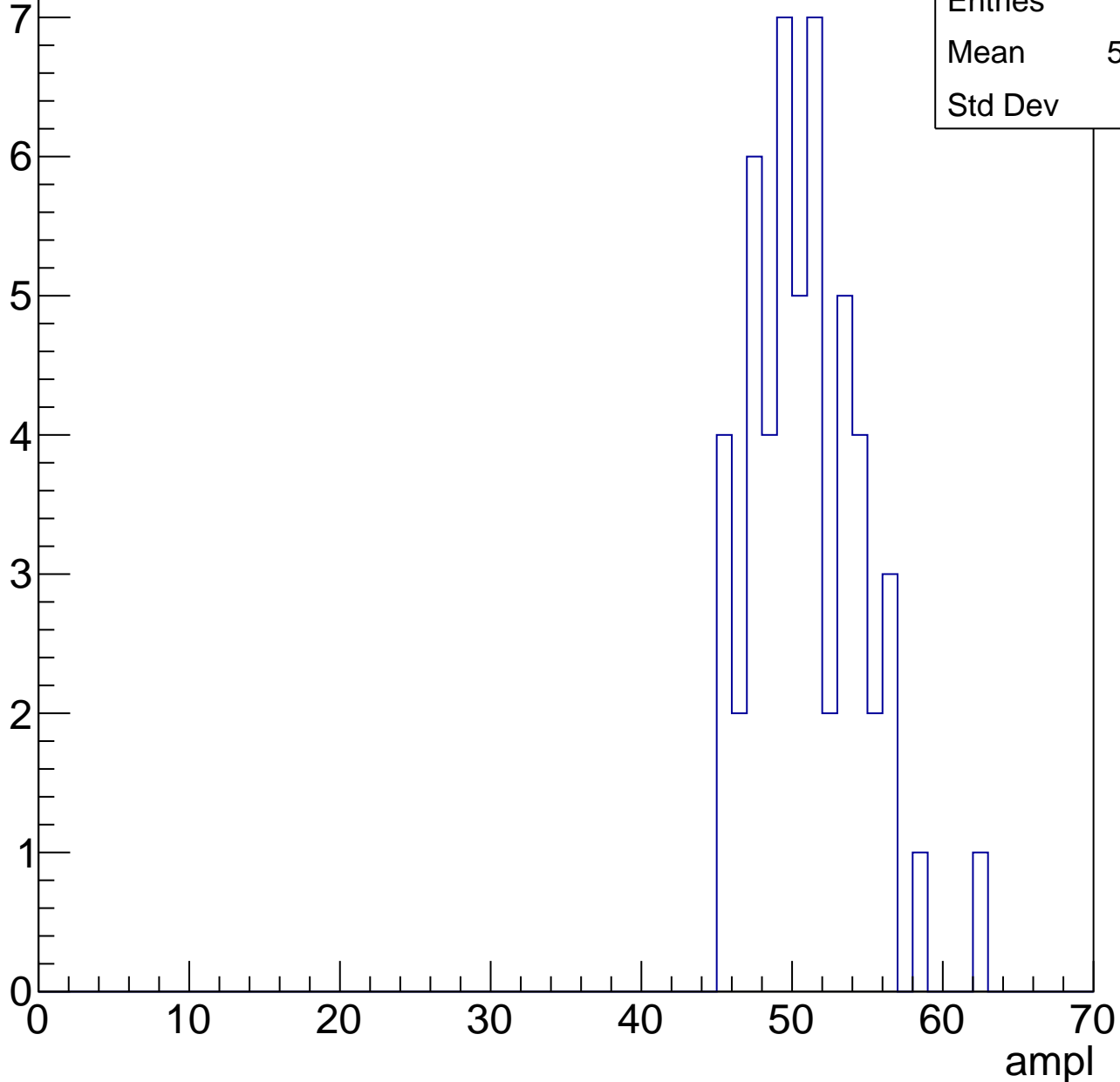


# B0L000S, U10-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	50.55
Std Dev	3.59

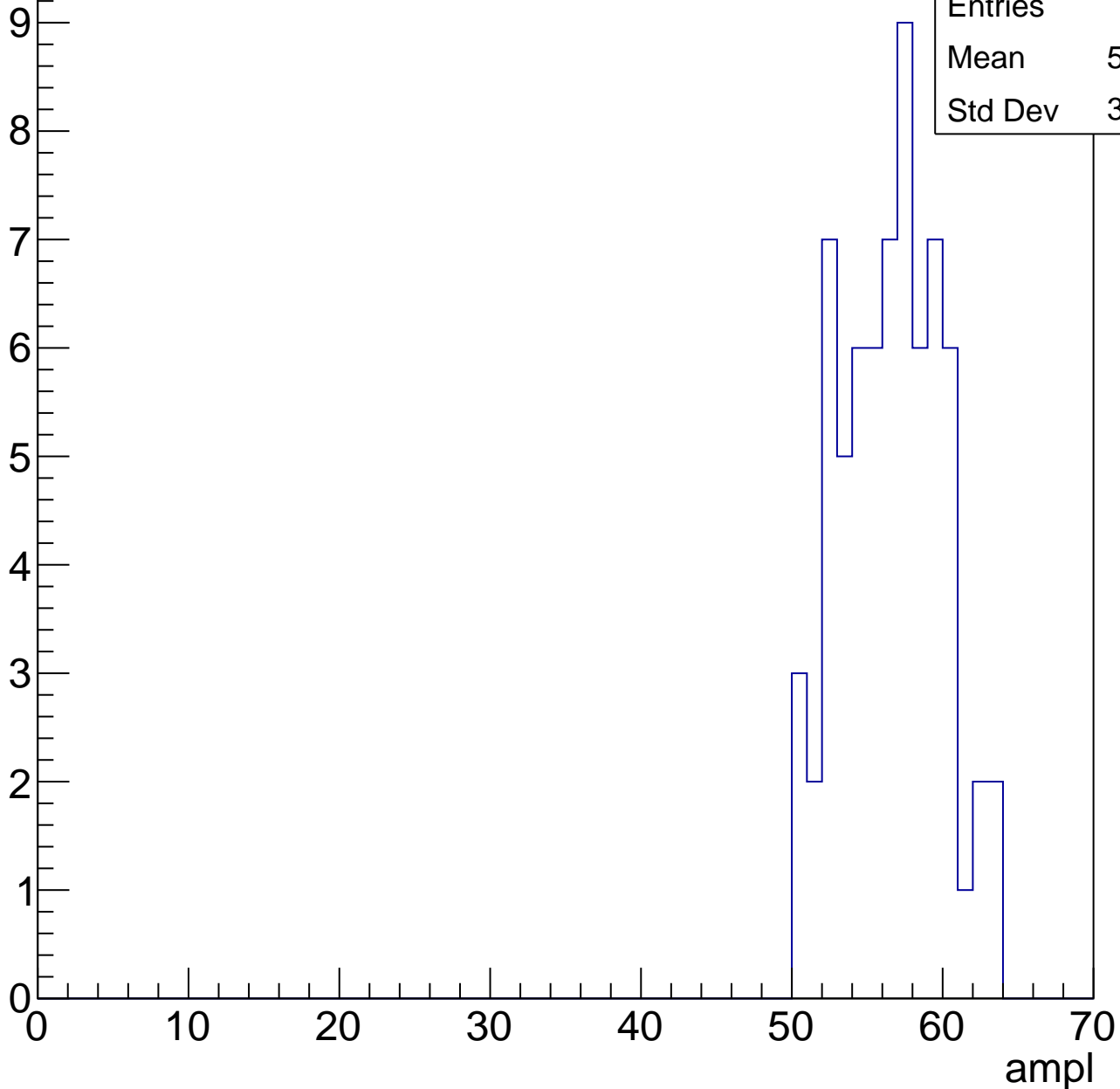


# B0L000S, U10-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	56.12
Std Dev	3.255

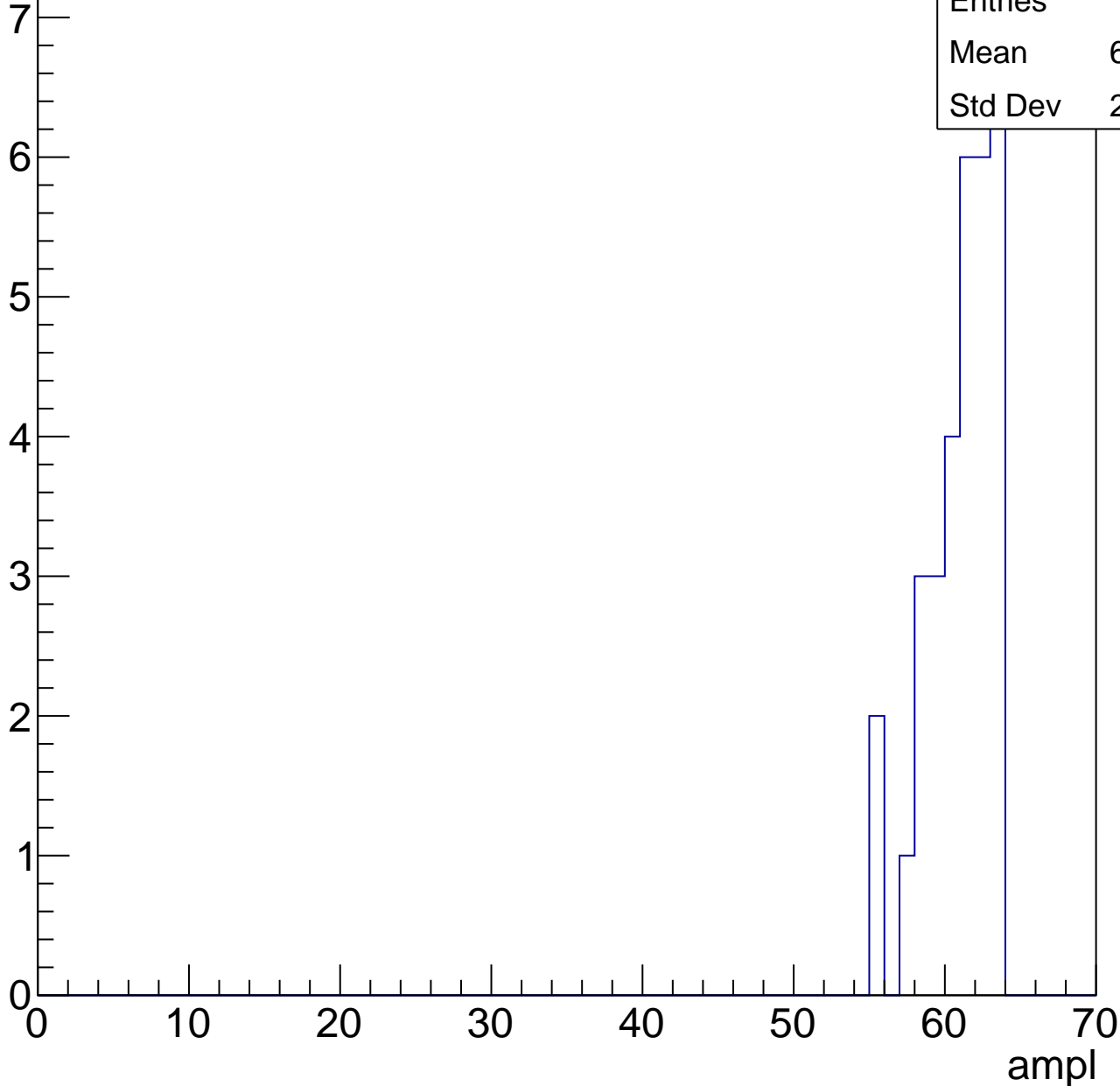


# B0L000S, U10-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

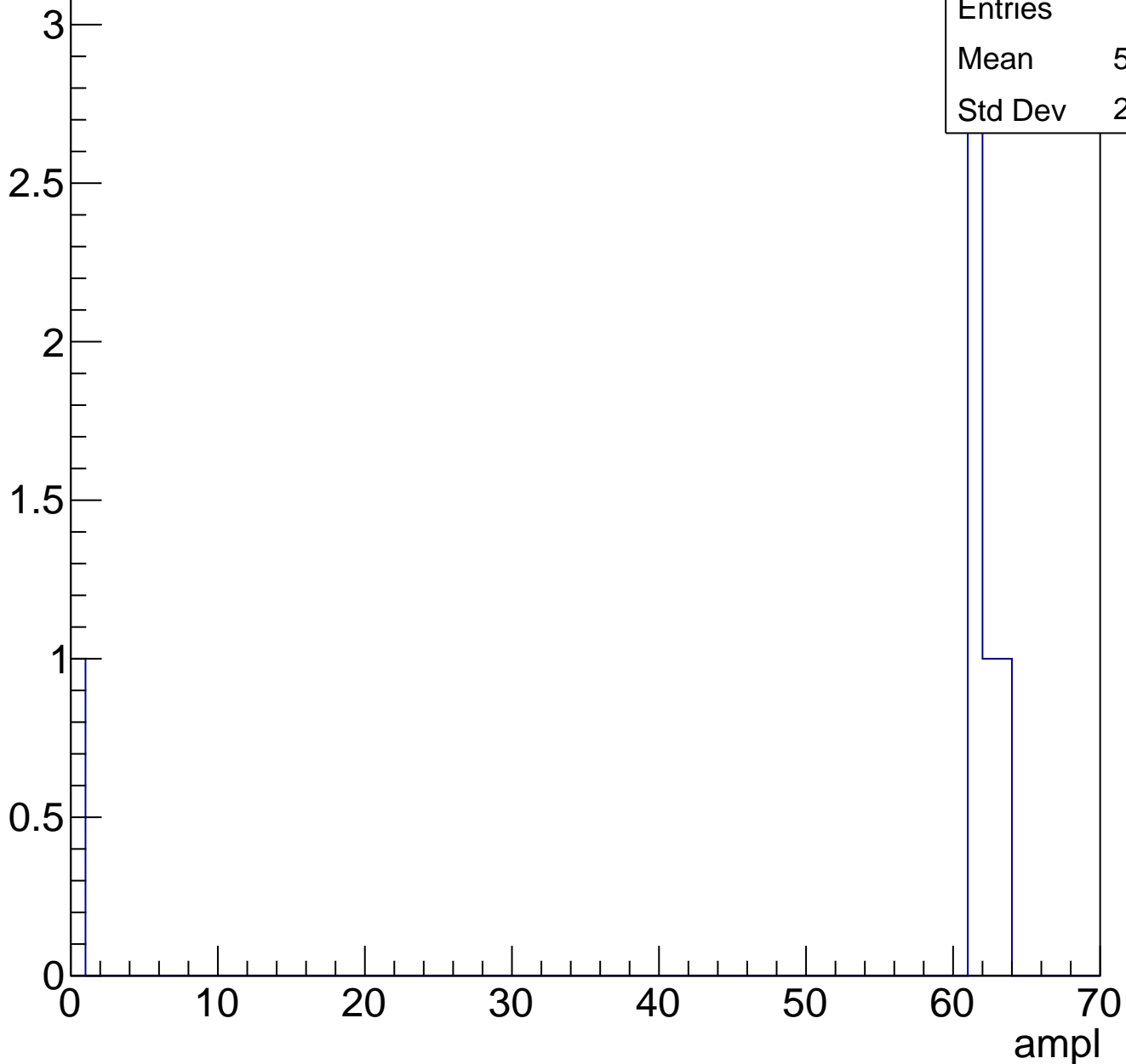
Entries	32
Mean	60.53
Std Dev	2.222



# B0L000S, U10-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch84, adc0

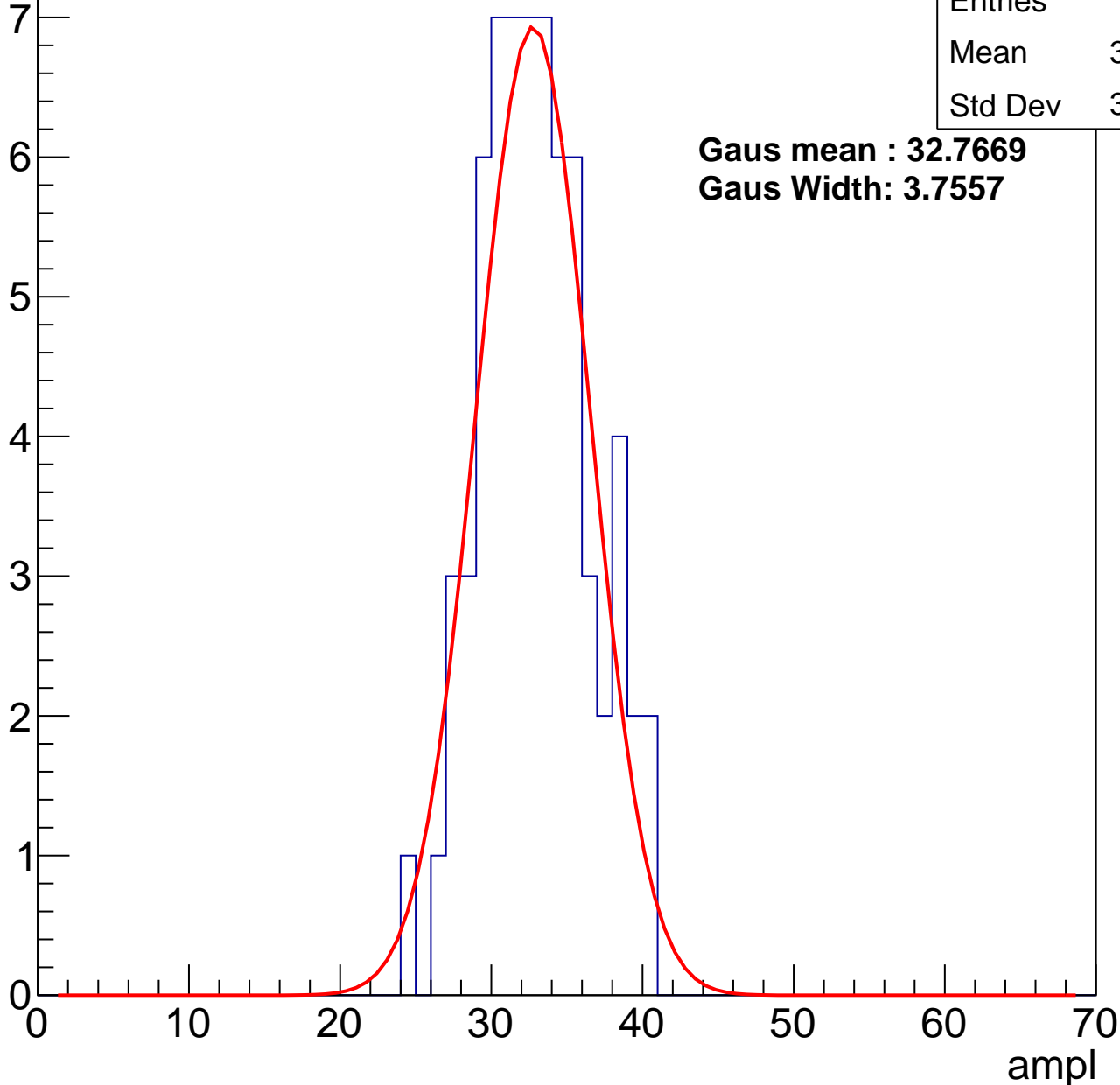
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	32.49
Std Dev	3.559

**Gaus mean : 32.7669**

**Gaus Width: 3.7557**



# B0L000S, U10-ch84, adc1

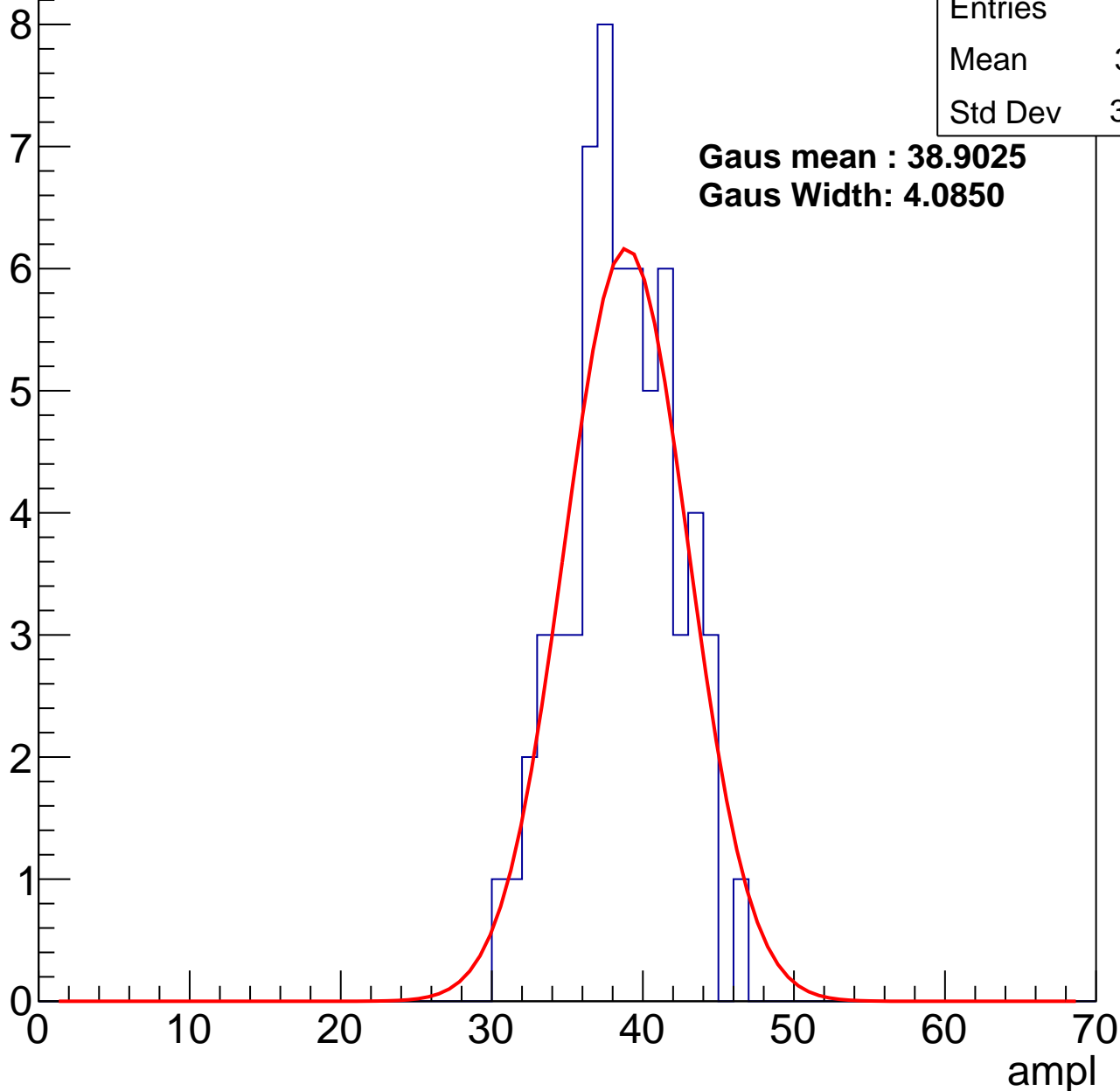
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.11
Std Dev	3.525

**Gaus mean : 38.9025**

**Gaus Width: 4.0850**



# B0L000S, U10-ch84, adc2

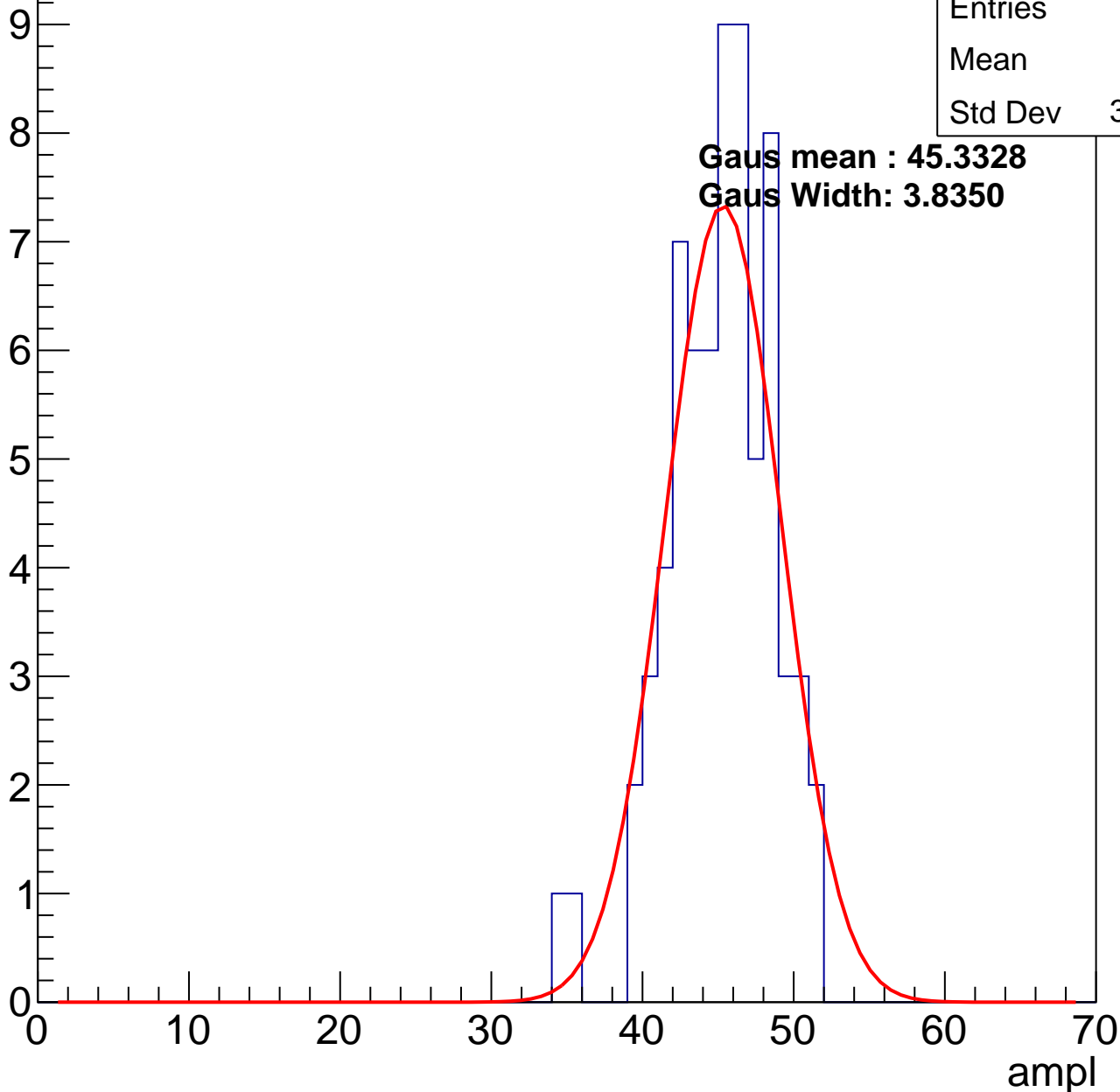
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.7
Std Dev	3.436

**Gaus mean : 45.3328**

**Gaus Width: 3.8350**

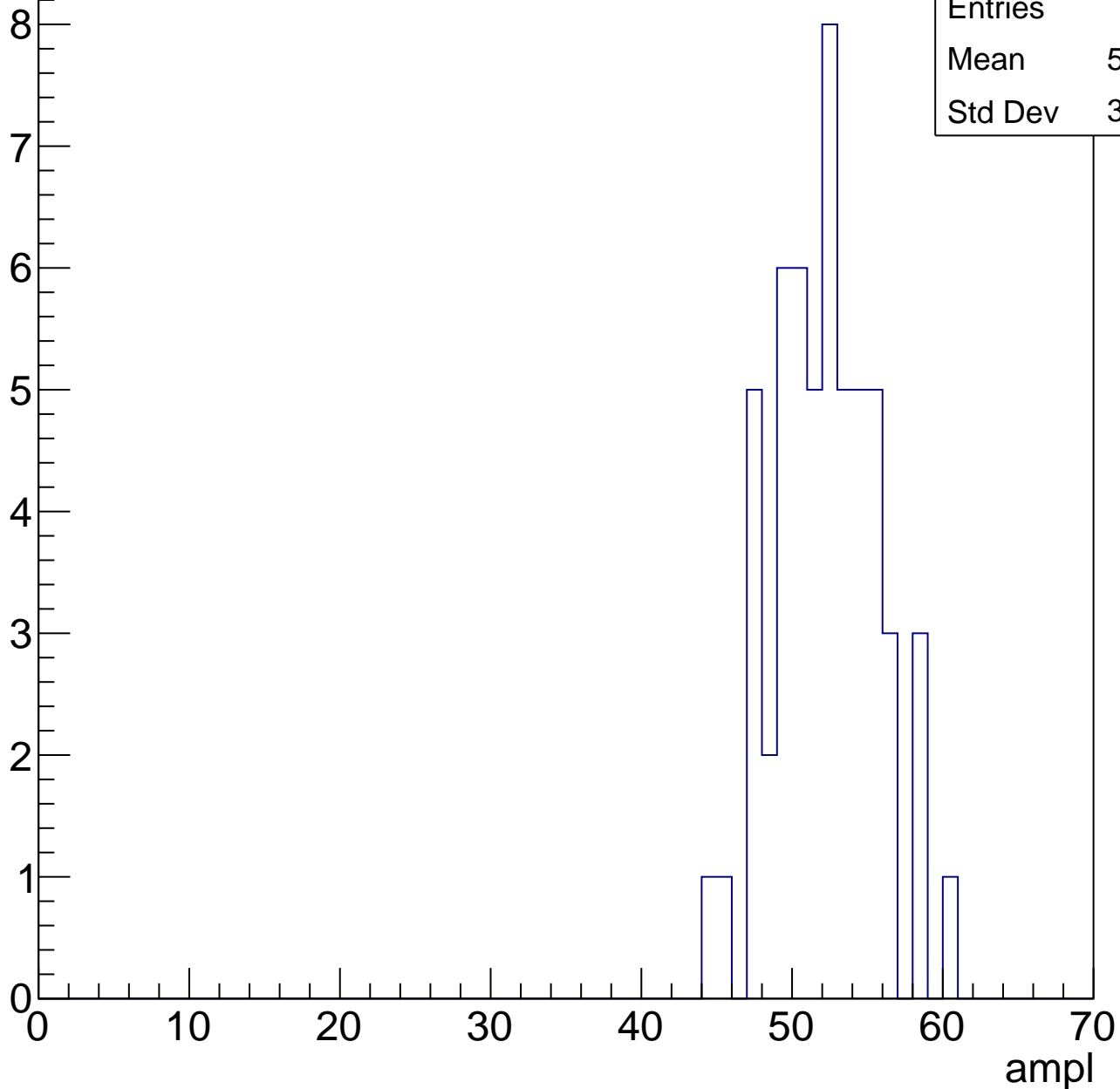


# B0L000S, U10-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

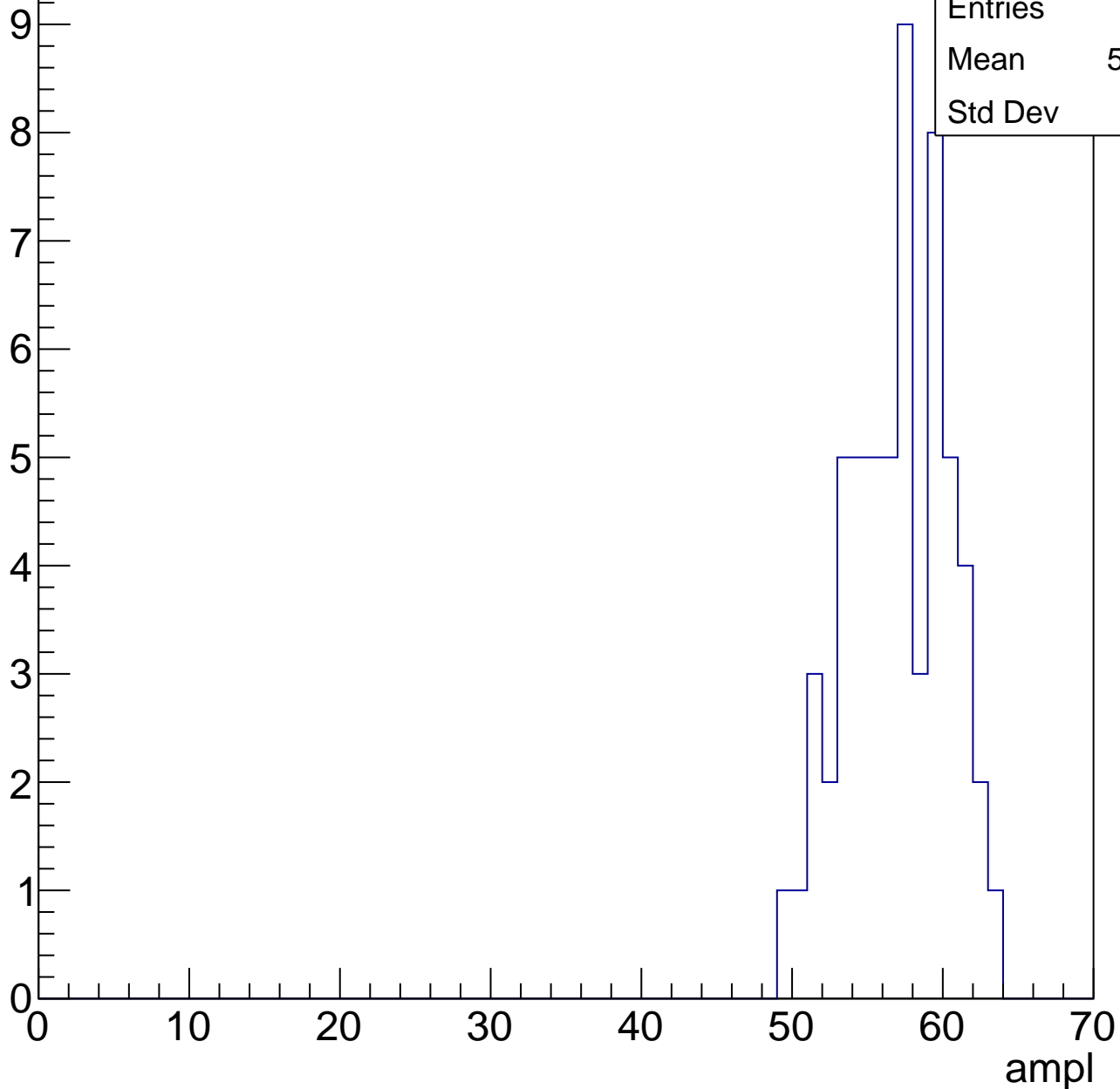
Entries	56
Mean	51.73
Std Dev	3.378



# B0L000S, U10-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

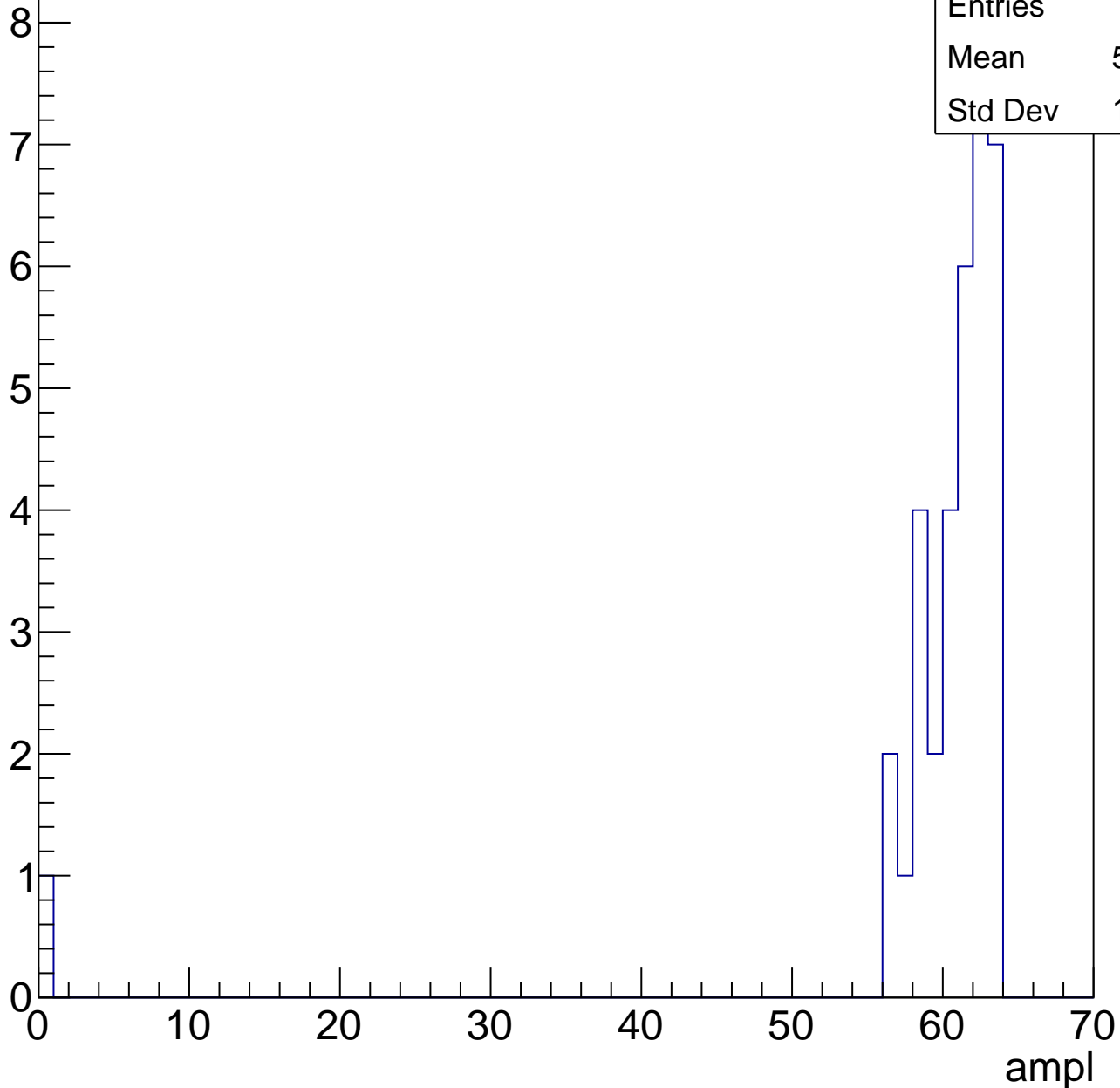


# B0L000S, U10-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.91
Std Dev	10.31



# B0L000S, U10-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch85, adc0

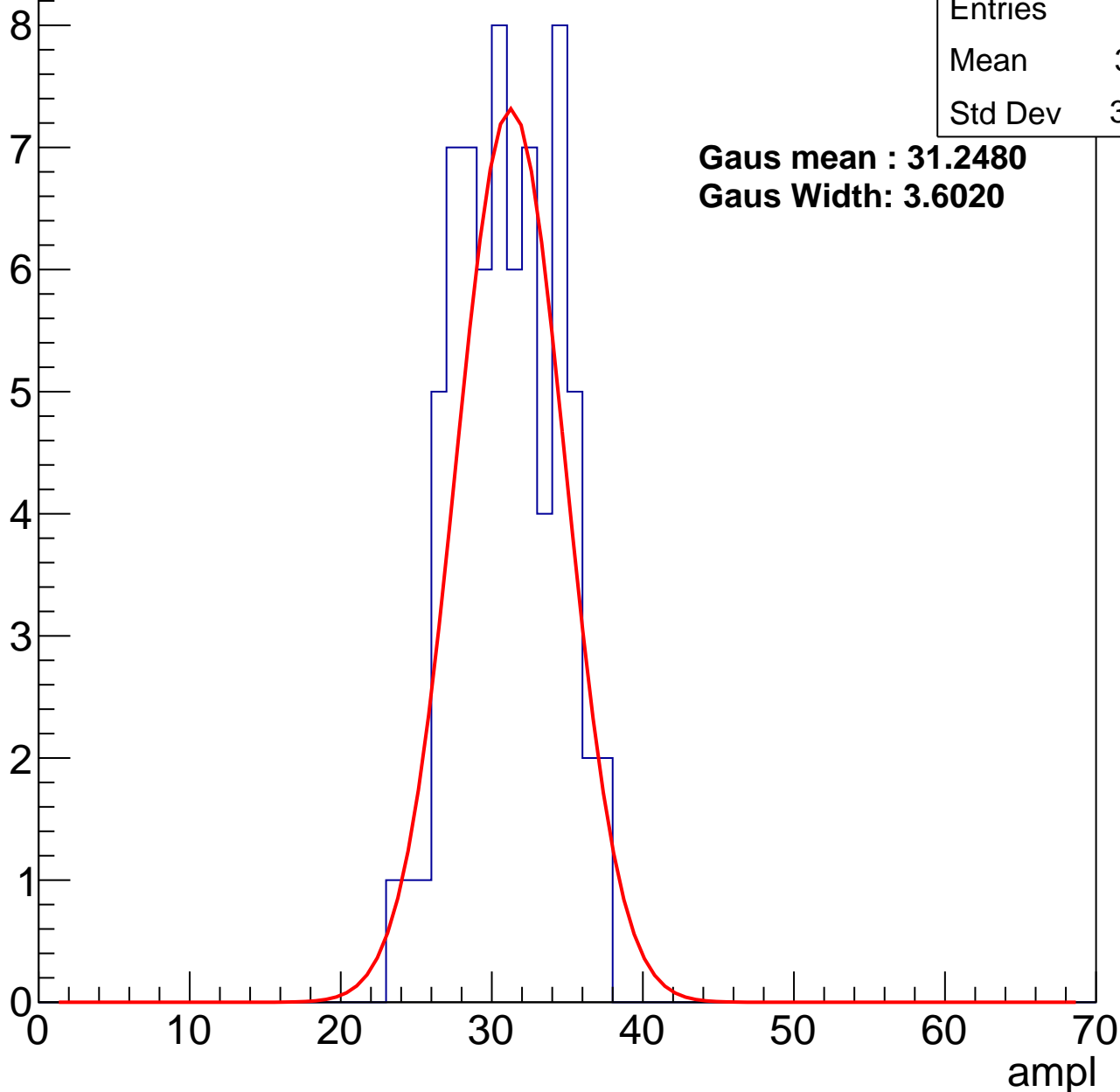
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.51
Std Dev	3.298

**Gaus mean : 31.2480**

**Gaus Width: 3.6020**



# B0L000S, U10-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	37.45
Std Dev	3.508

**Gaus mean : 37.7276**

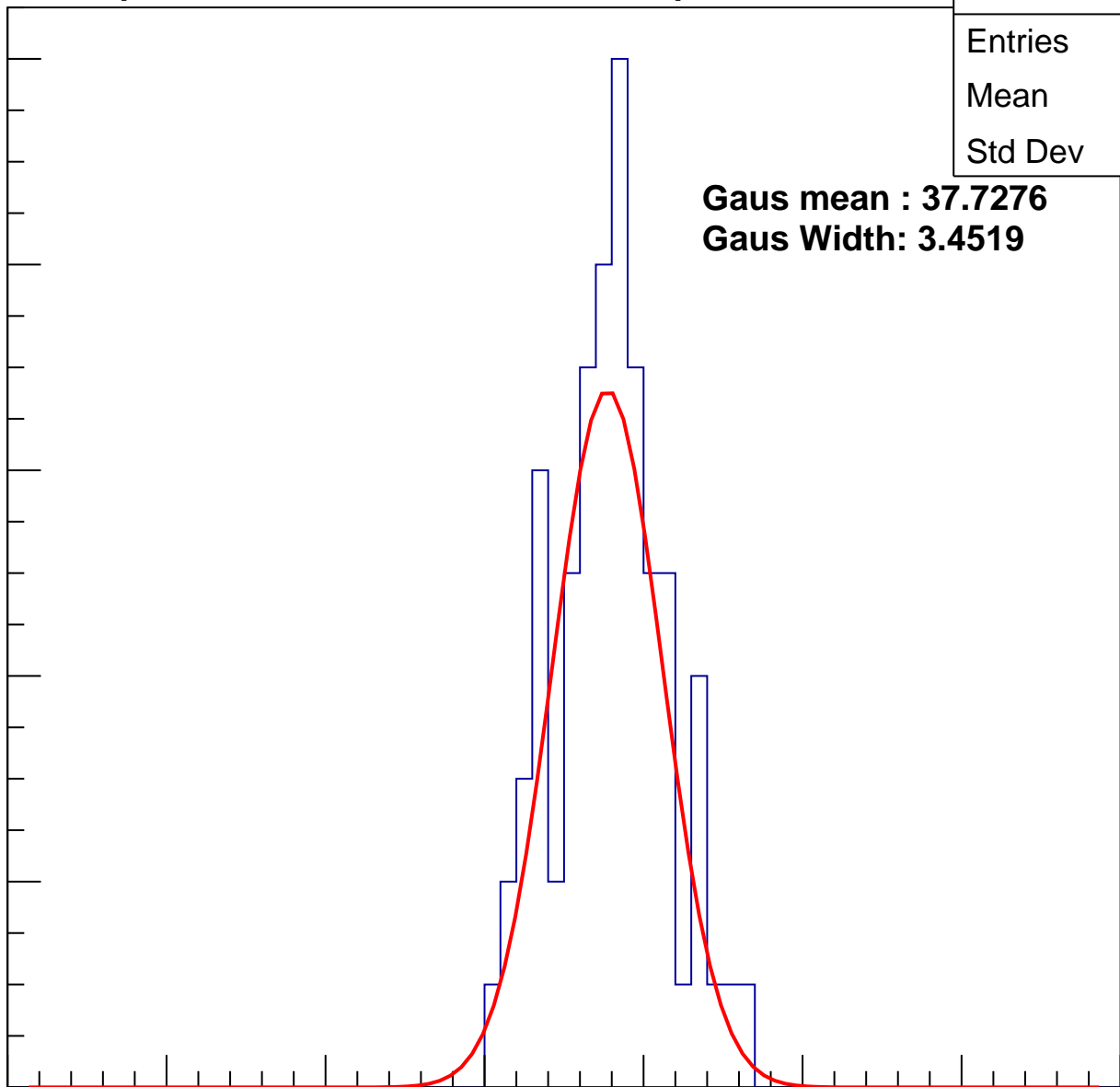
**Gaus Width: 3.4519**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch85, adc2

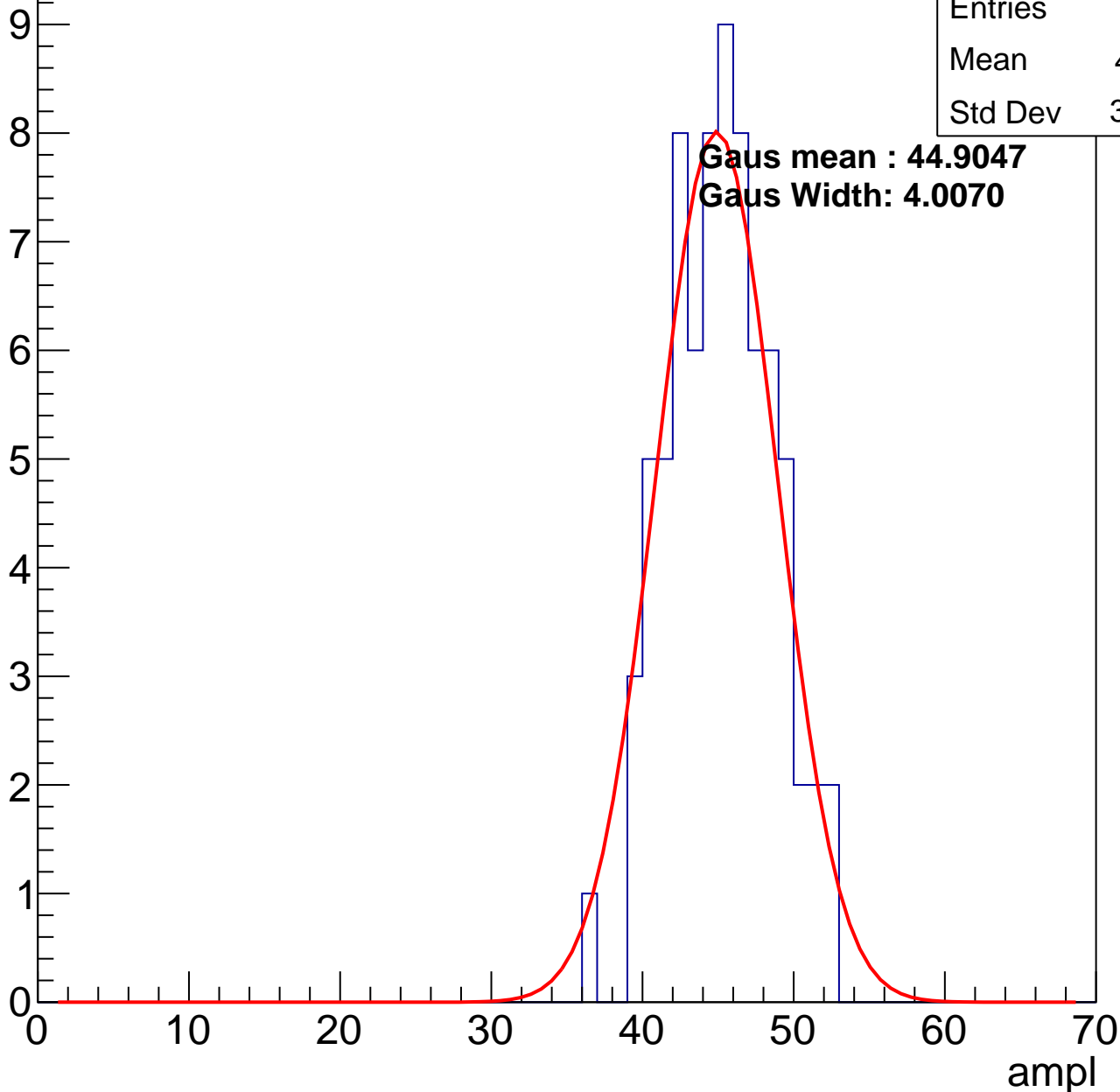
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	44.71
Std Dev	3.398

**Gaus mean : 44.9047**

**Gaus Width: 4.0070**

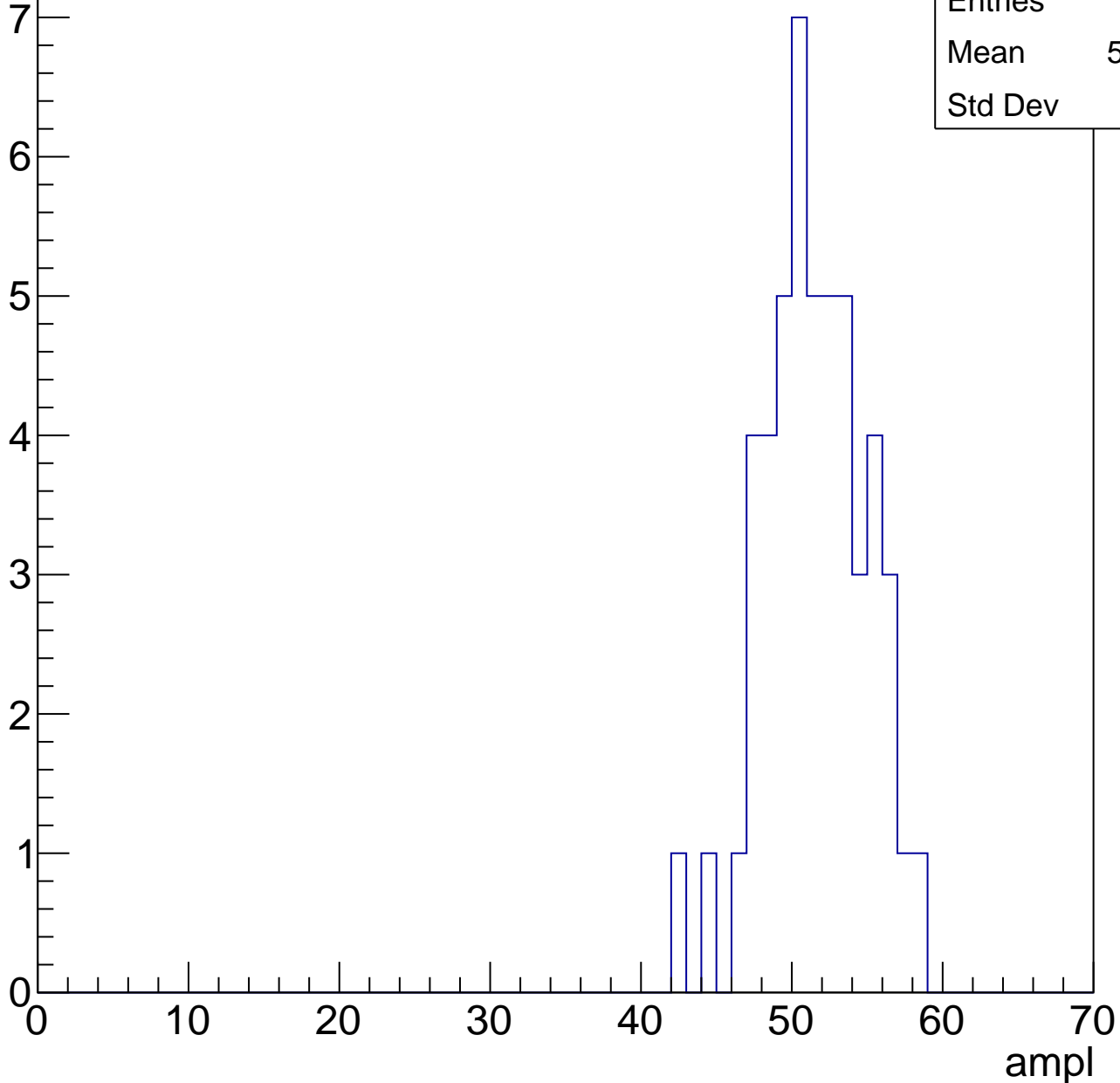


# B0L000S, U10-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	51.04
Std Dev	3.34

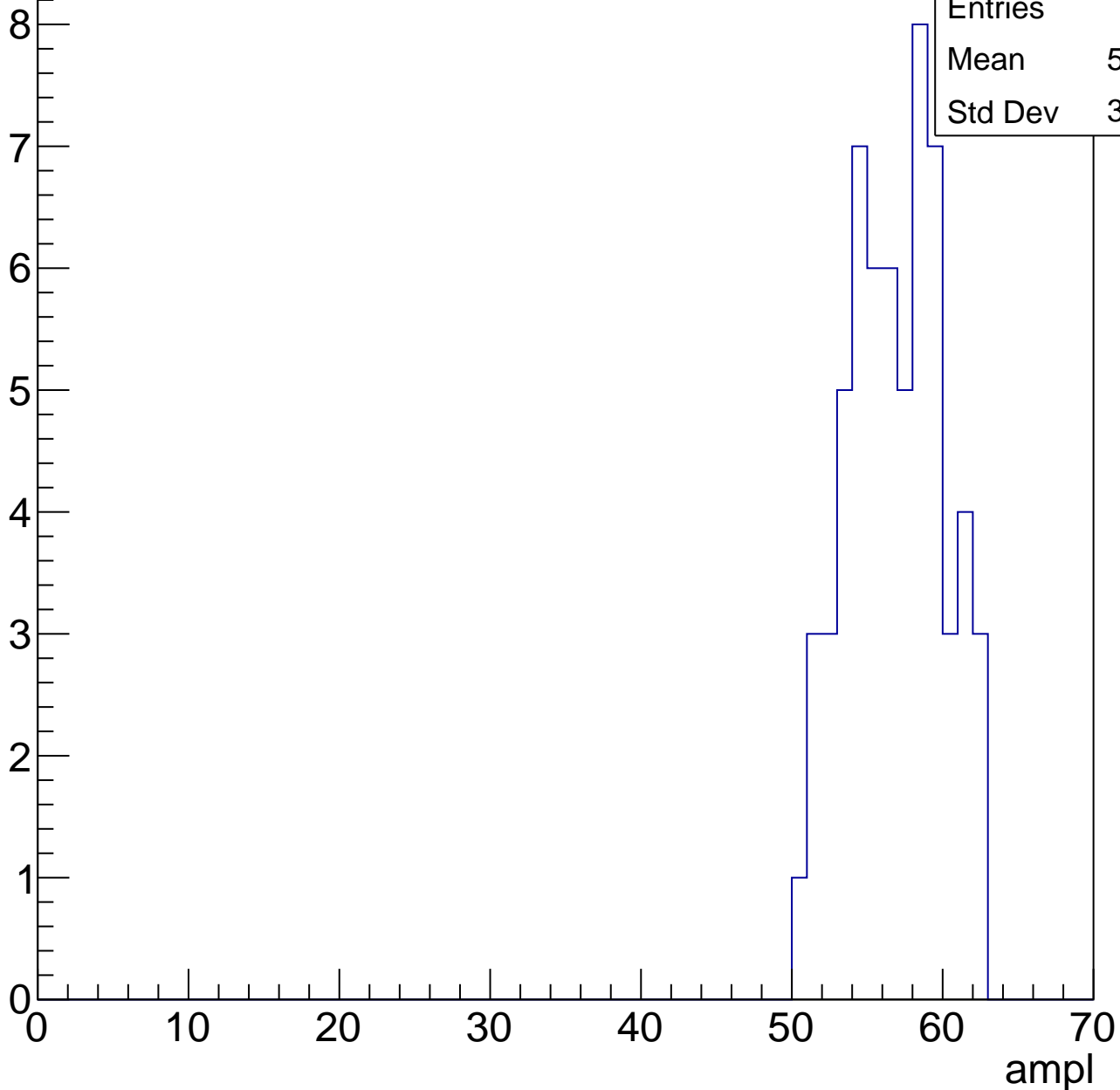


# B0L000S, U10-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.39
Std Dev	3.096

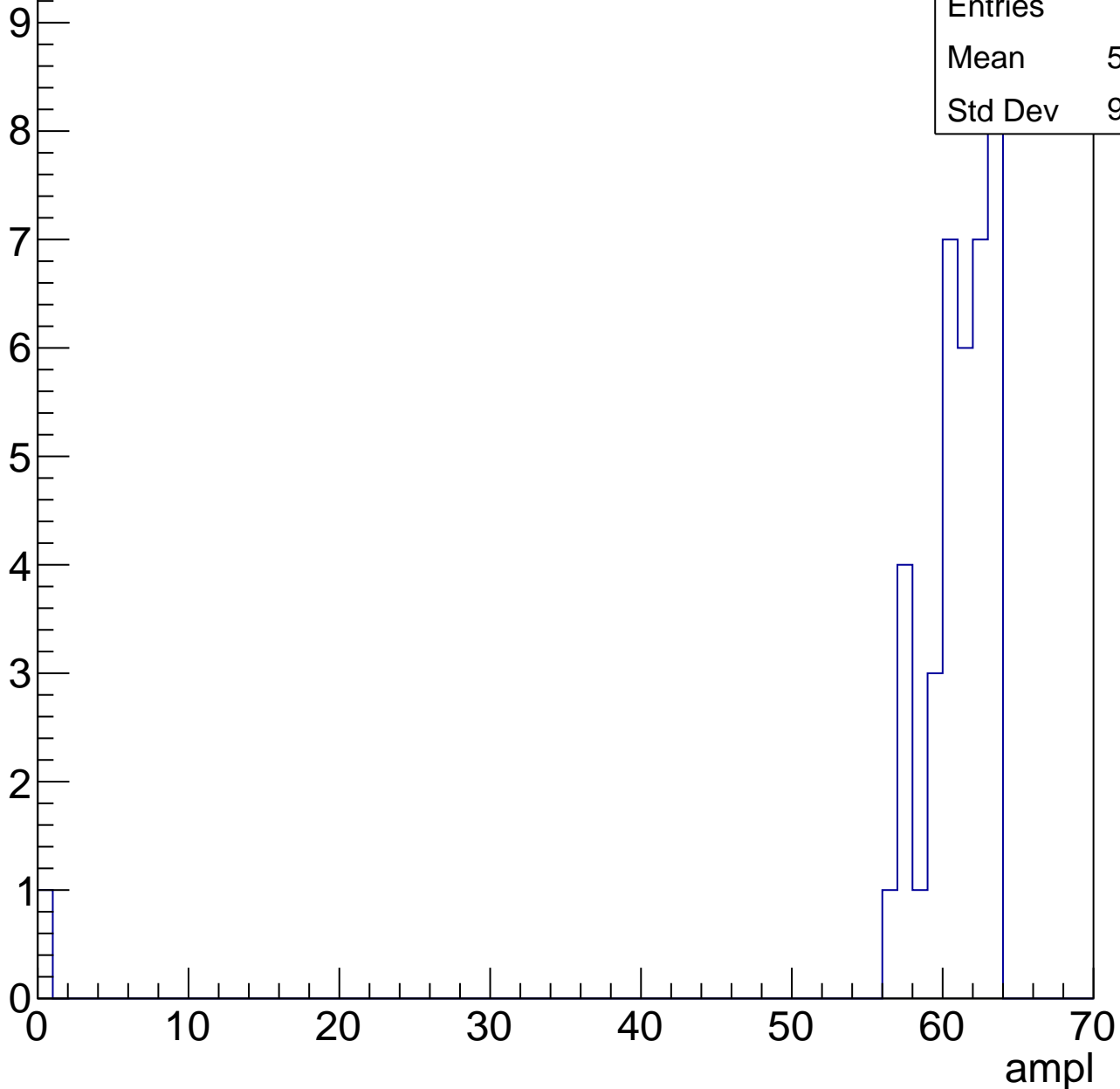


# B0L000S, U10-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	59.13
Std Dev	9.798



# B0L000S, U10-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch86, adc0

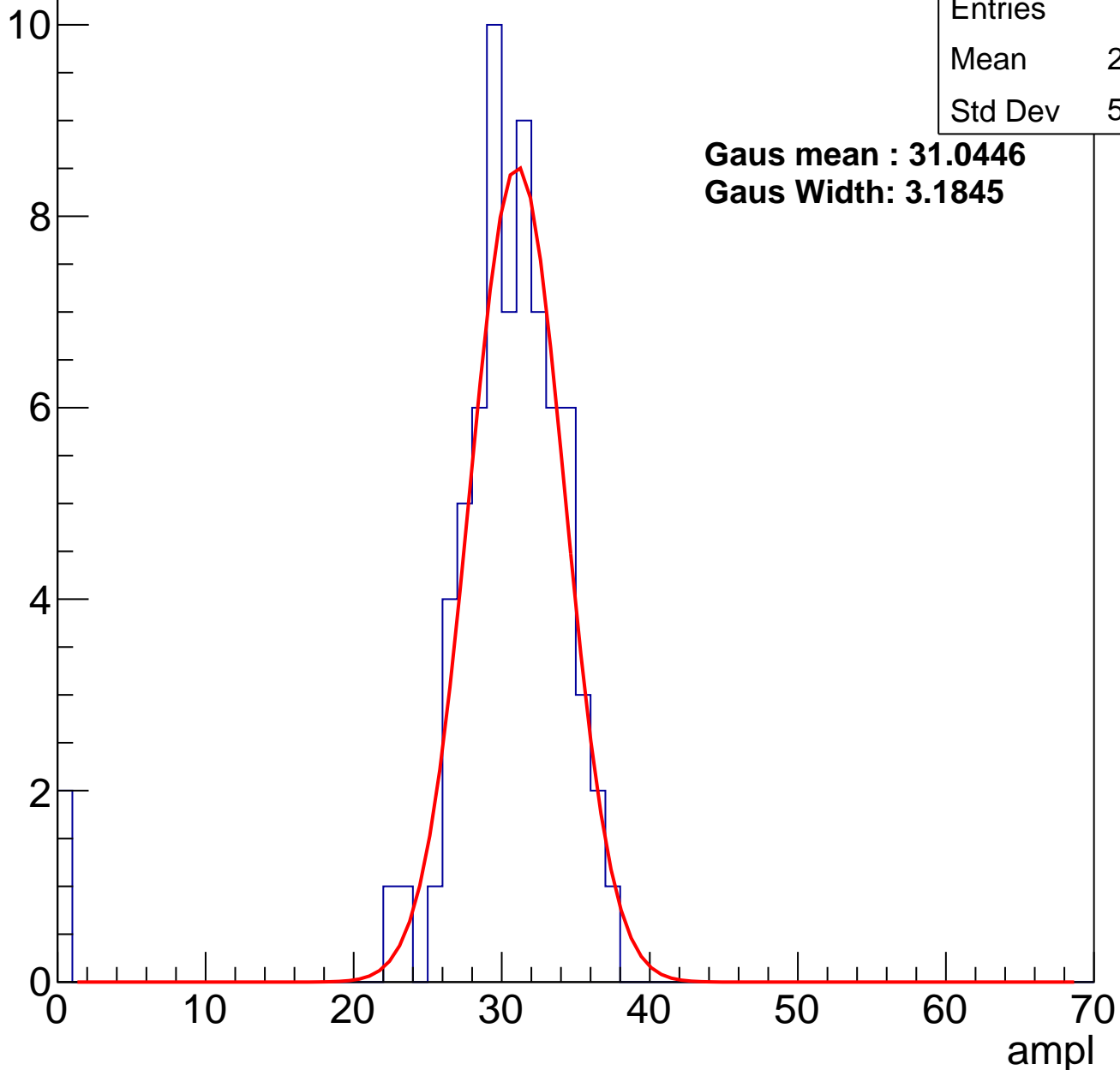
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	29.52
Std Dev	5.872

**Gaus mean : 31.0446**

**Gaus Width: 3.1845**

Entry



# B0L000S, U10-ch86, adc1

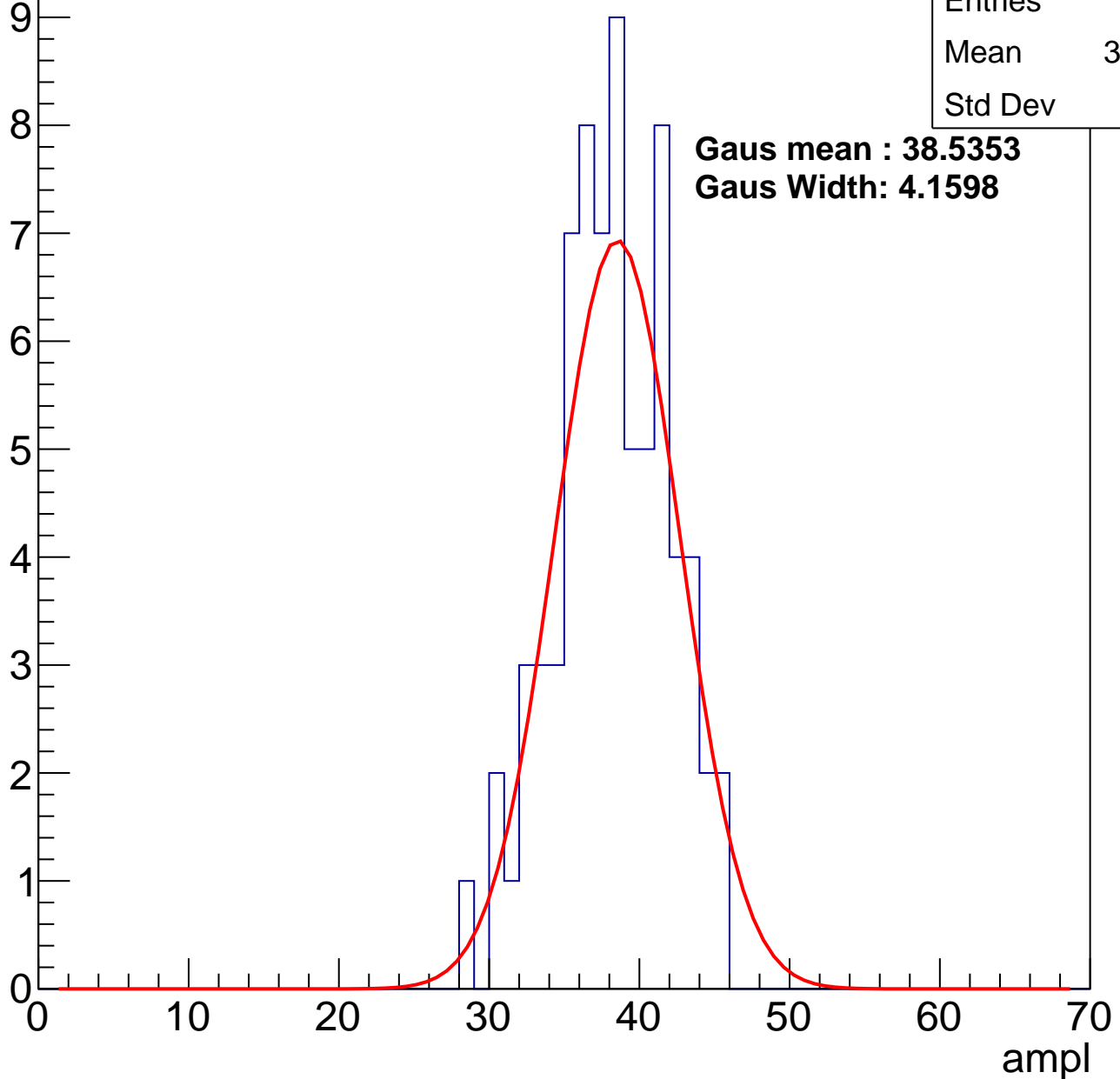
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.72
Std Dev	3.74

**Gaus mean : 38.5353**

**Gaus Width: 4.1598**



# B0L000S, U10-ch86, adc2

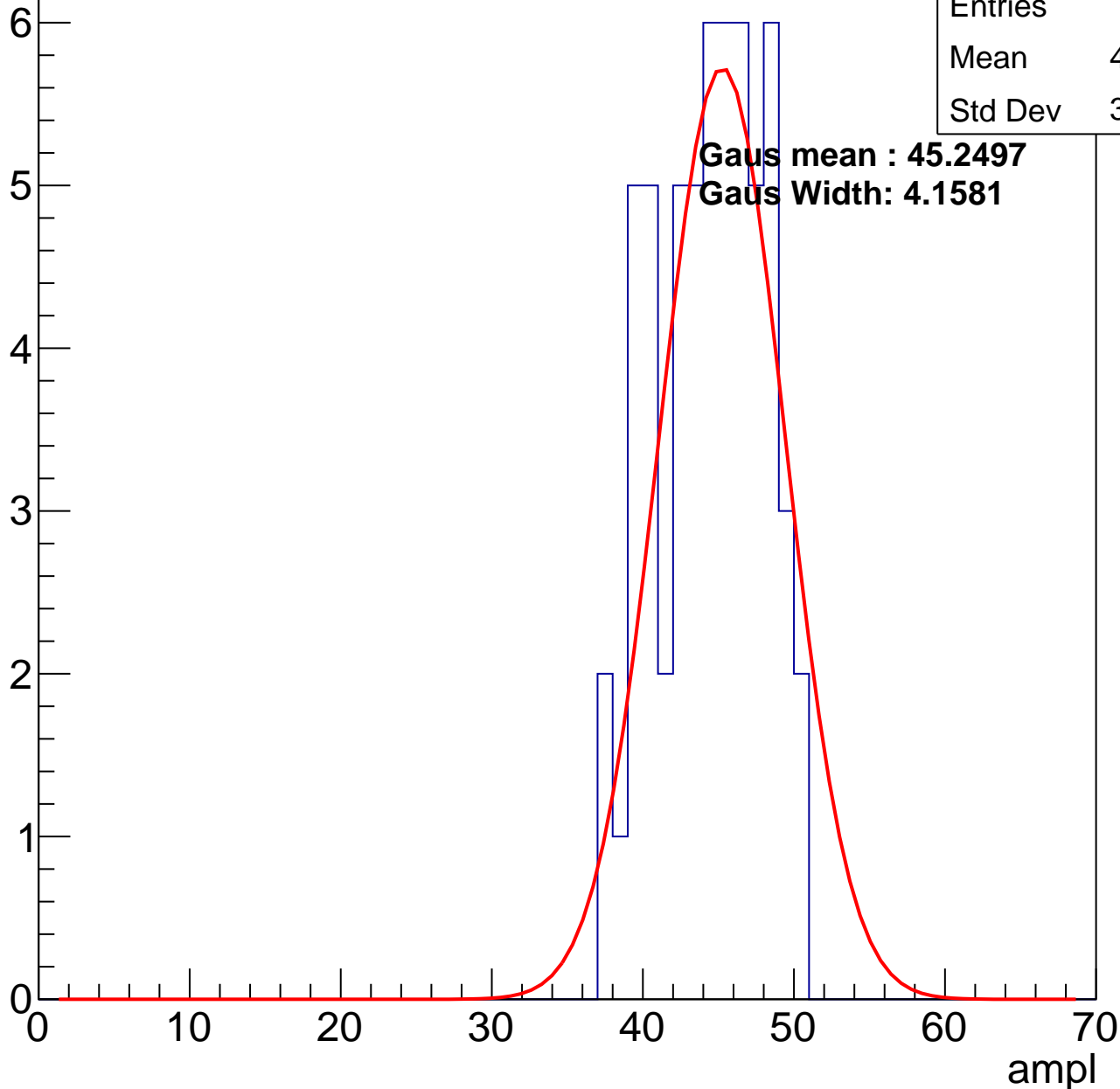
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	43.97
Std Dev	3.444

**Gaus mean : 45.2497**

**Gaus Width: 4.1581**

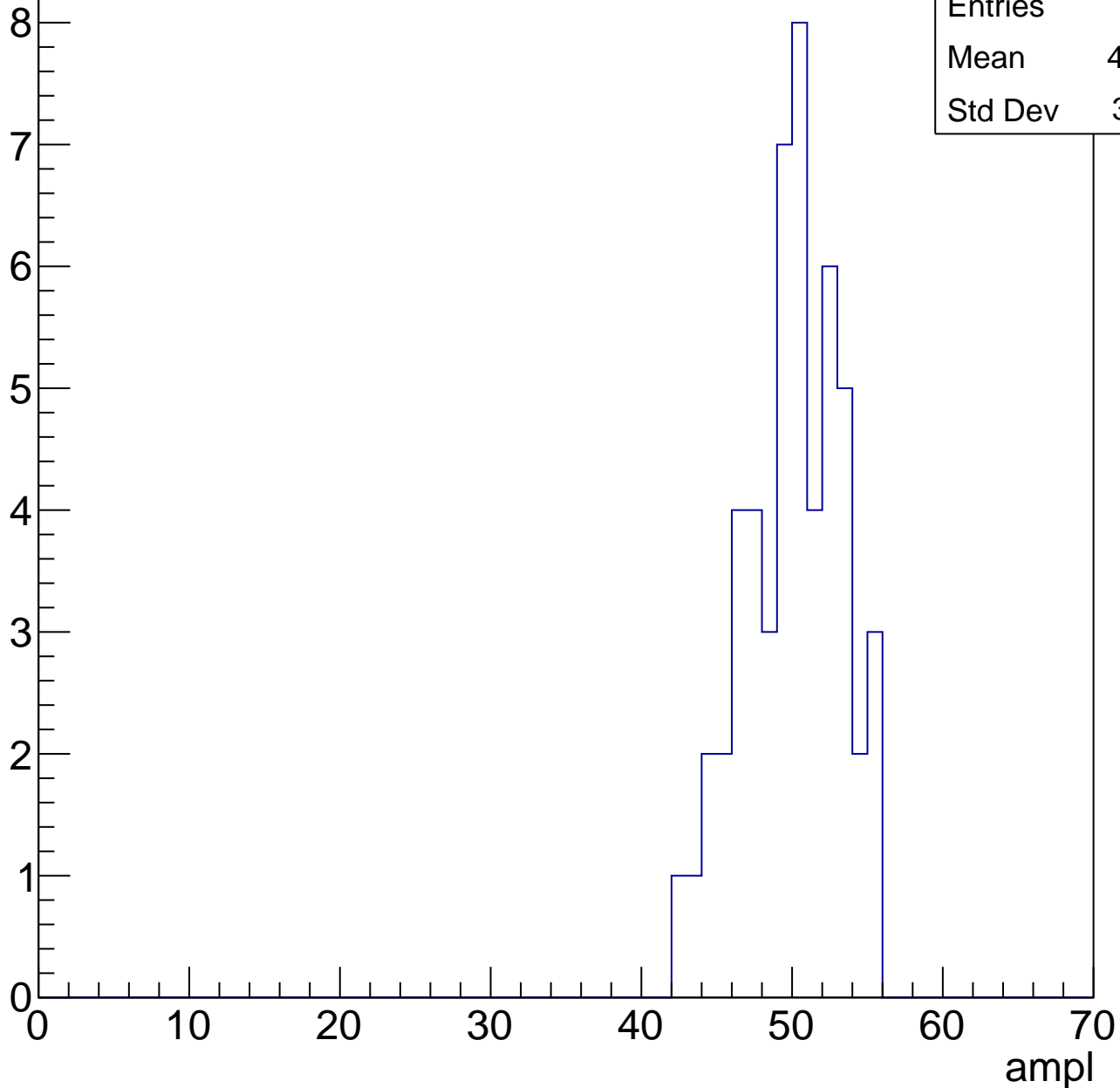


# B0L000S, U10-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	49.54
Std Dev	3.171



# B0L000S, U10-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

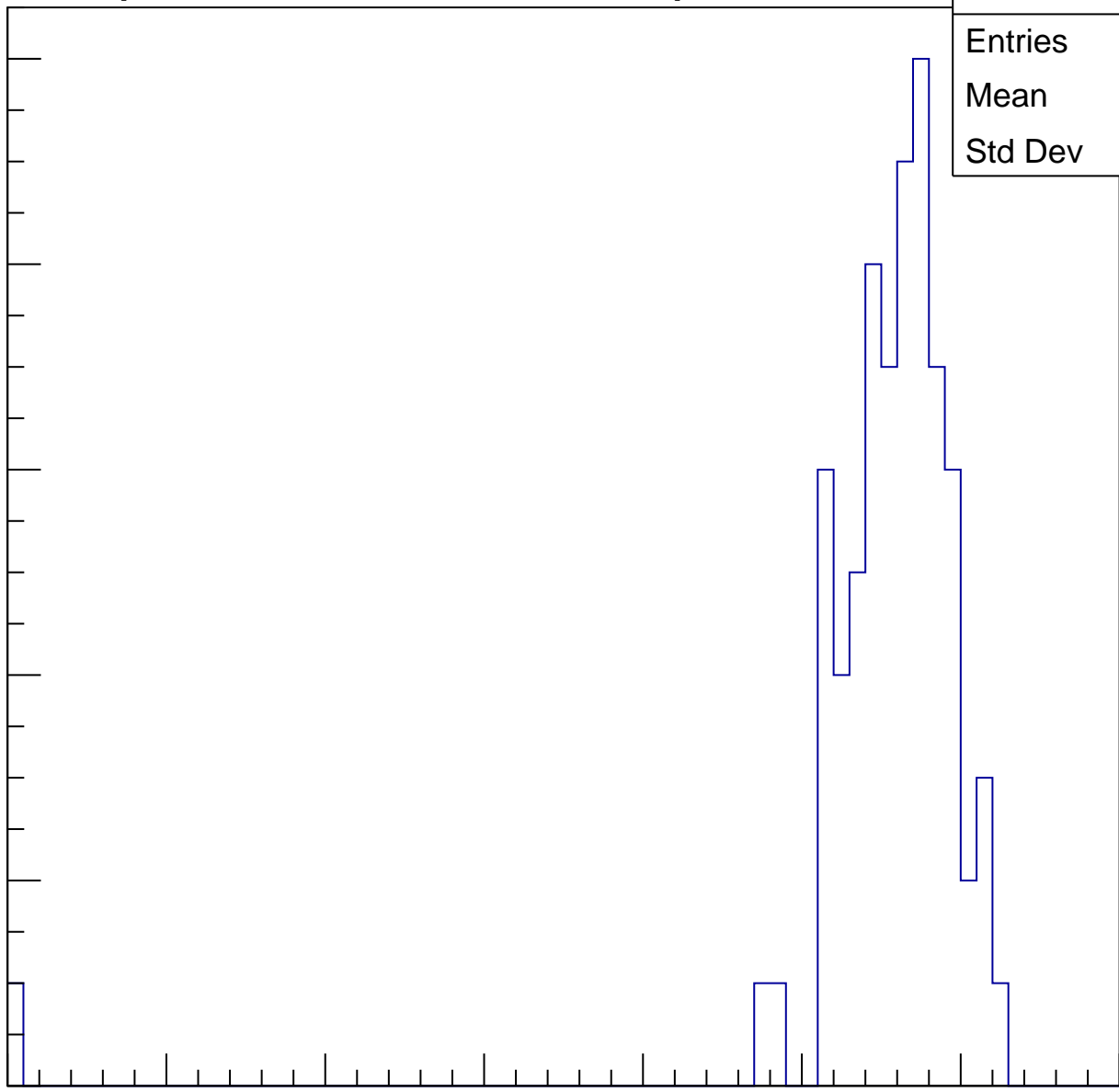
Entries	71
Mean	54.79
Std Dev	7.228

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

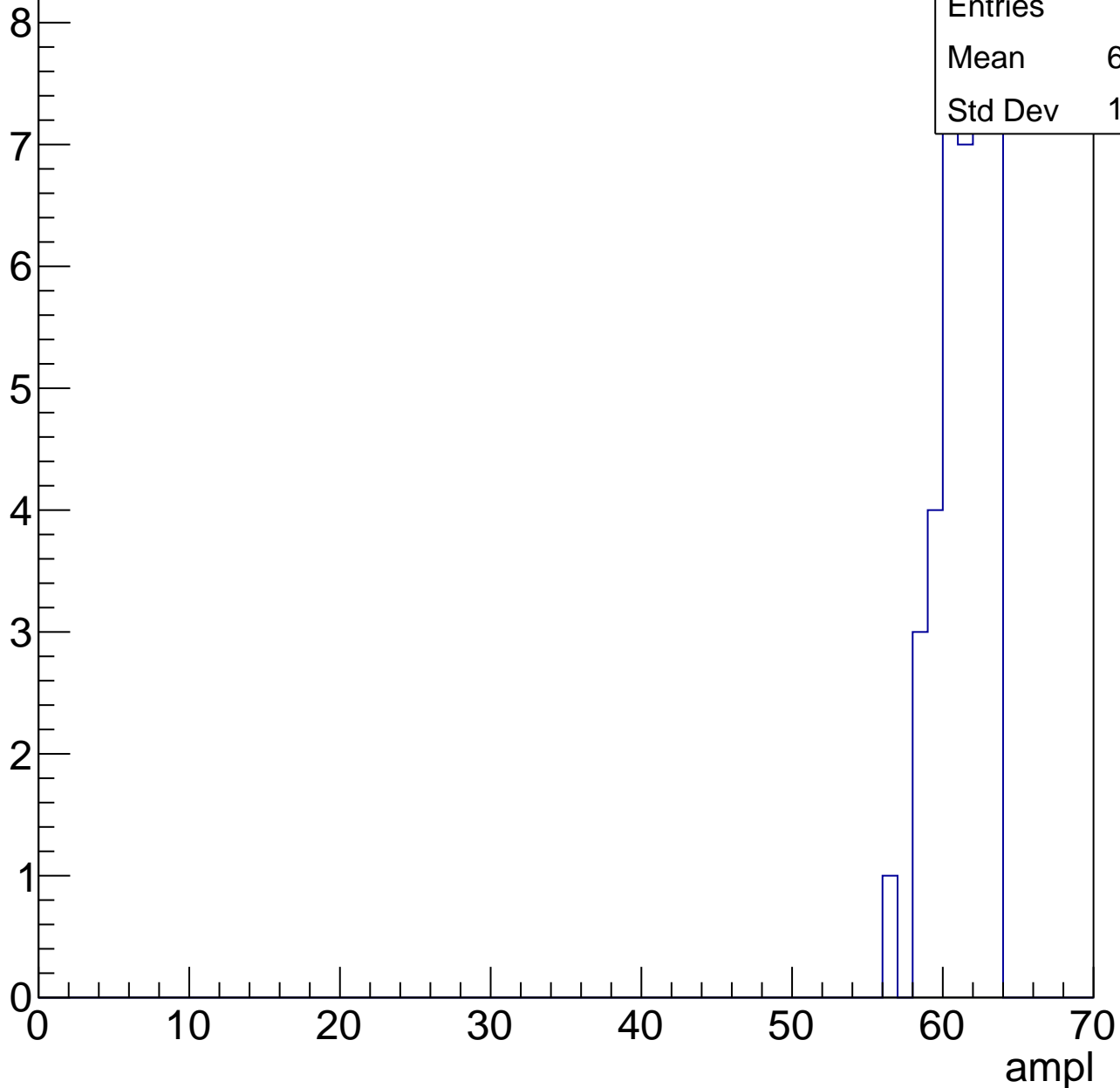
ampl



# B0L000S, U10-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch87, adc0

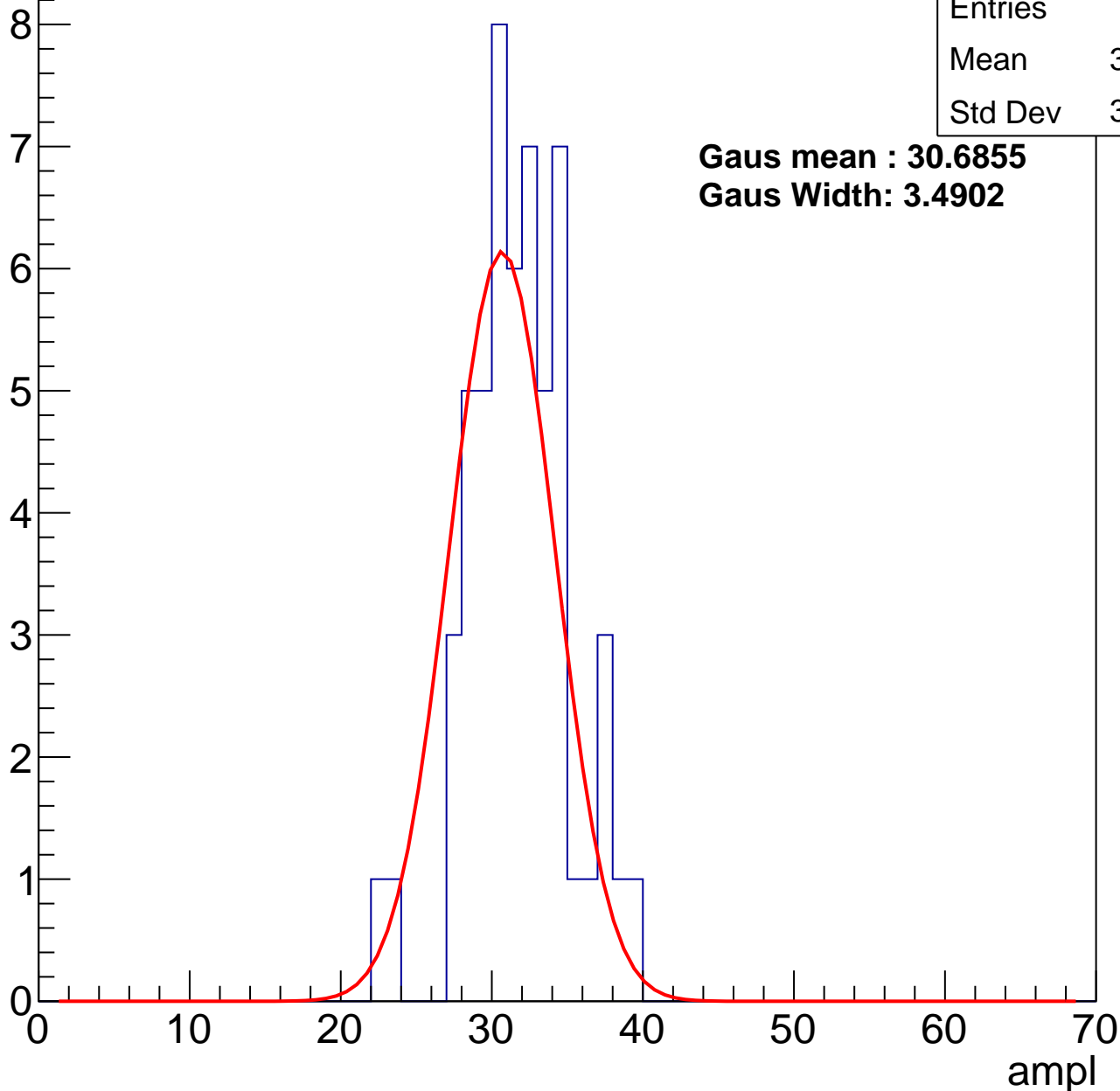
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	31.33
Std Dev	3.336

**Gaus mean : 30.6855**

**Gaus Width: 3.4902**



# B0L000S, U10-ch87, adc1

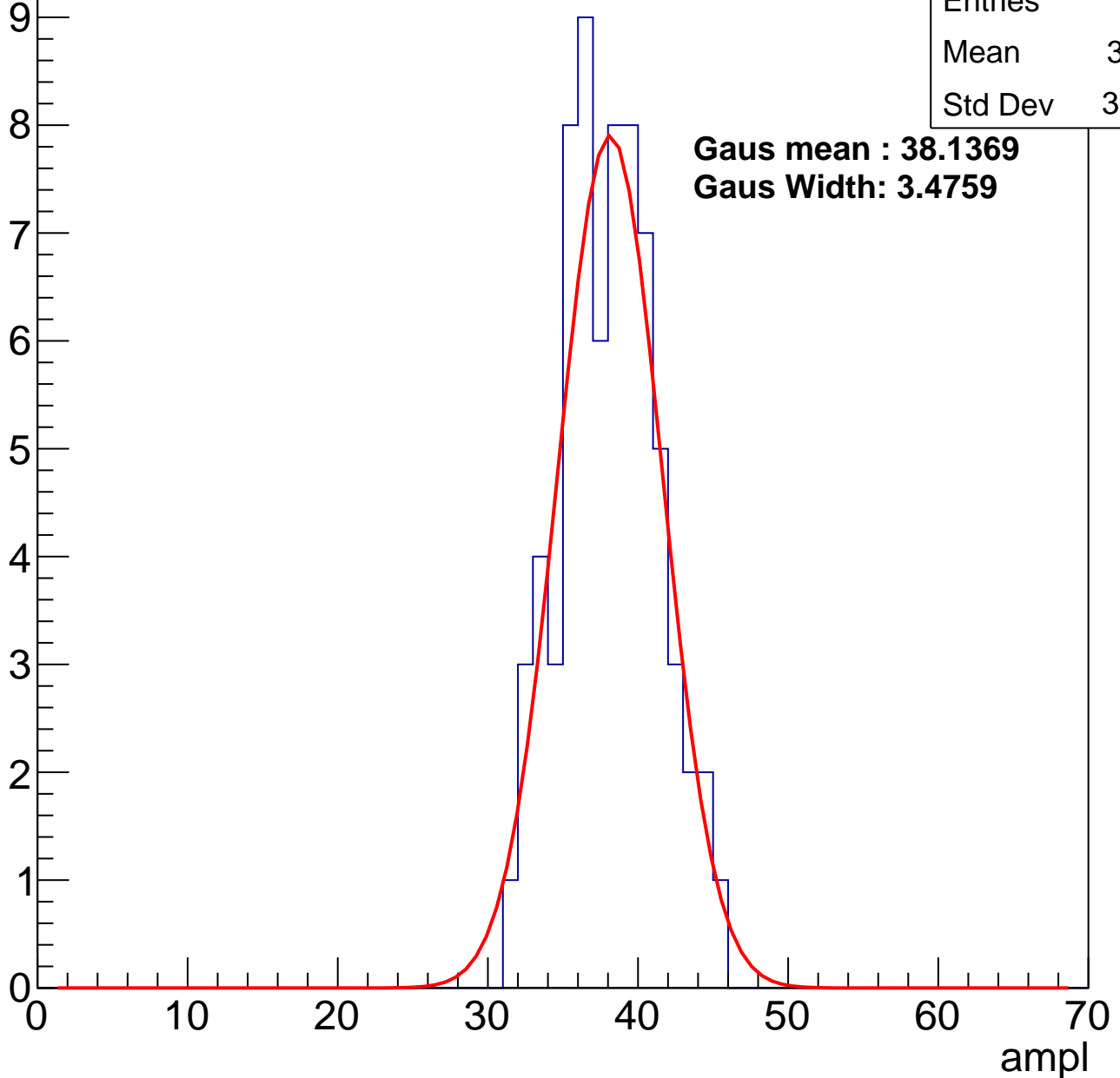
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.61
Std Dev	3.182

**Gaus mean : 38.1369**

**Gaus Width: 3.4759**



# B0L000S, U10-ch87, adc2

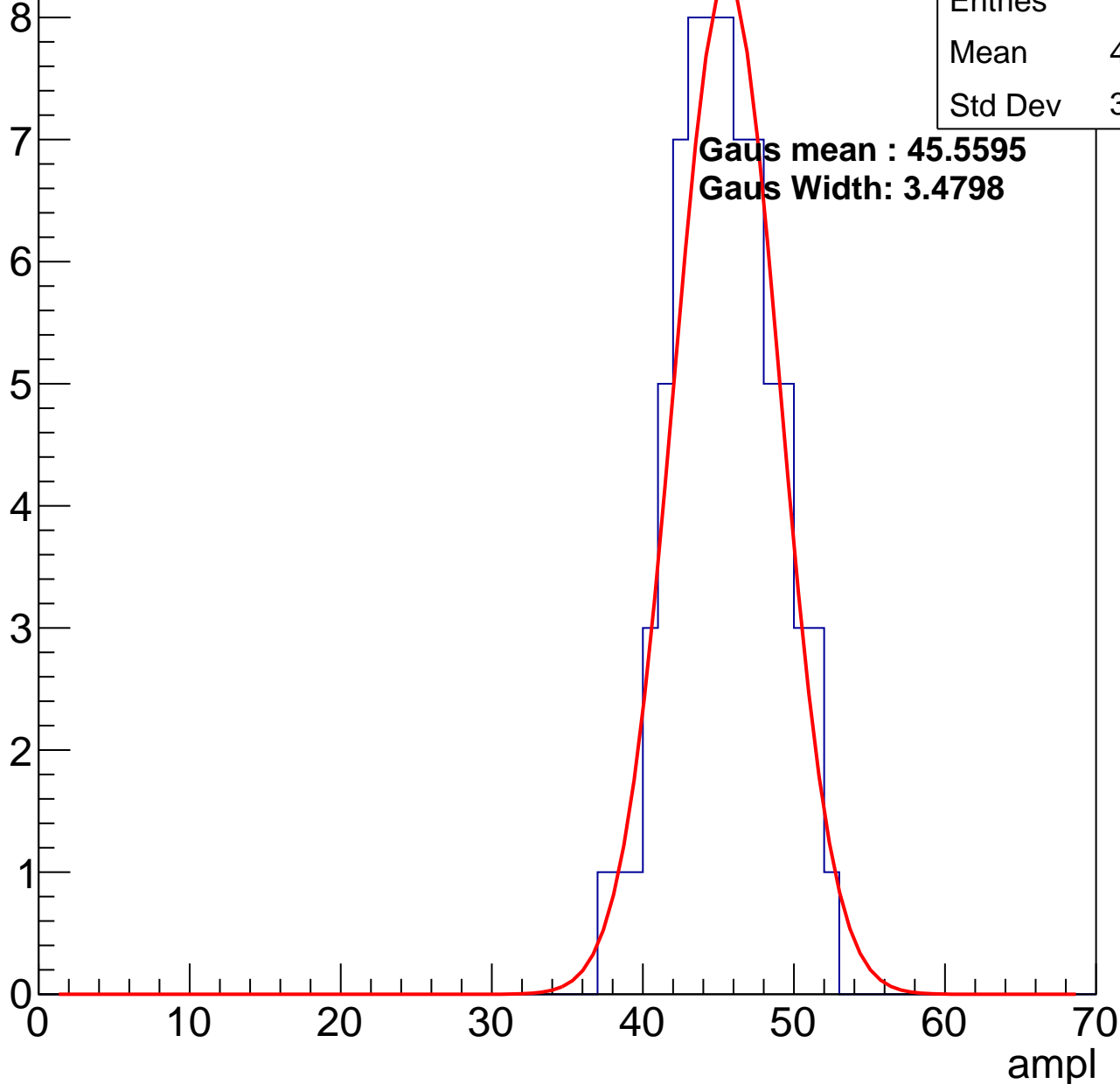
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	44.93
Std Dev	3.308

**Gaus mean : 45.5595**

**Gaus Width: 3.4798**

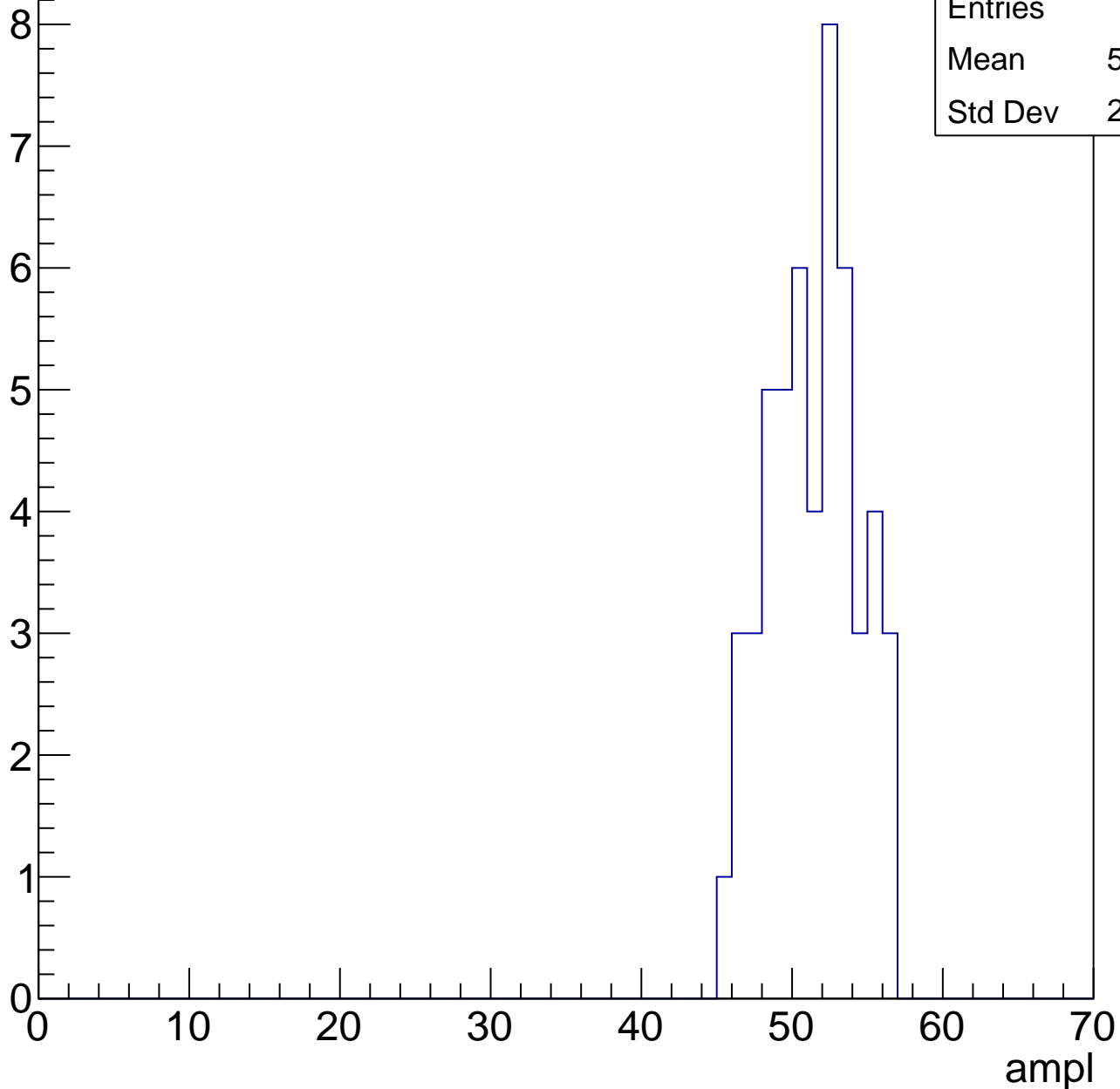


# B0L000S, U10-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

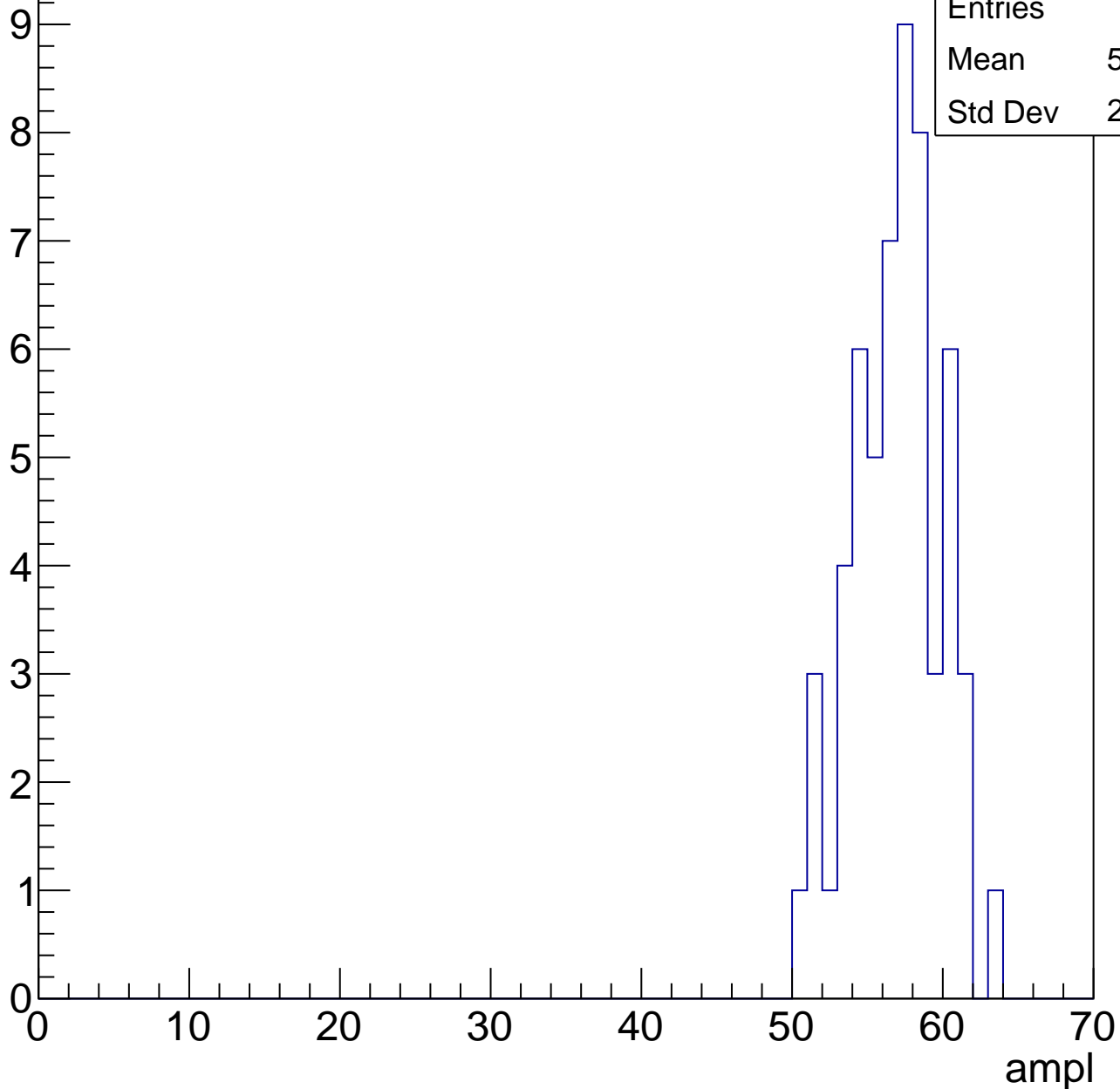
Entries	51
Mean	50.92
Std Dev	2.896



# B0L000S, U10-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

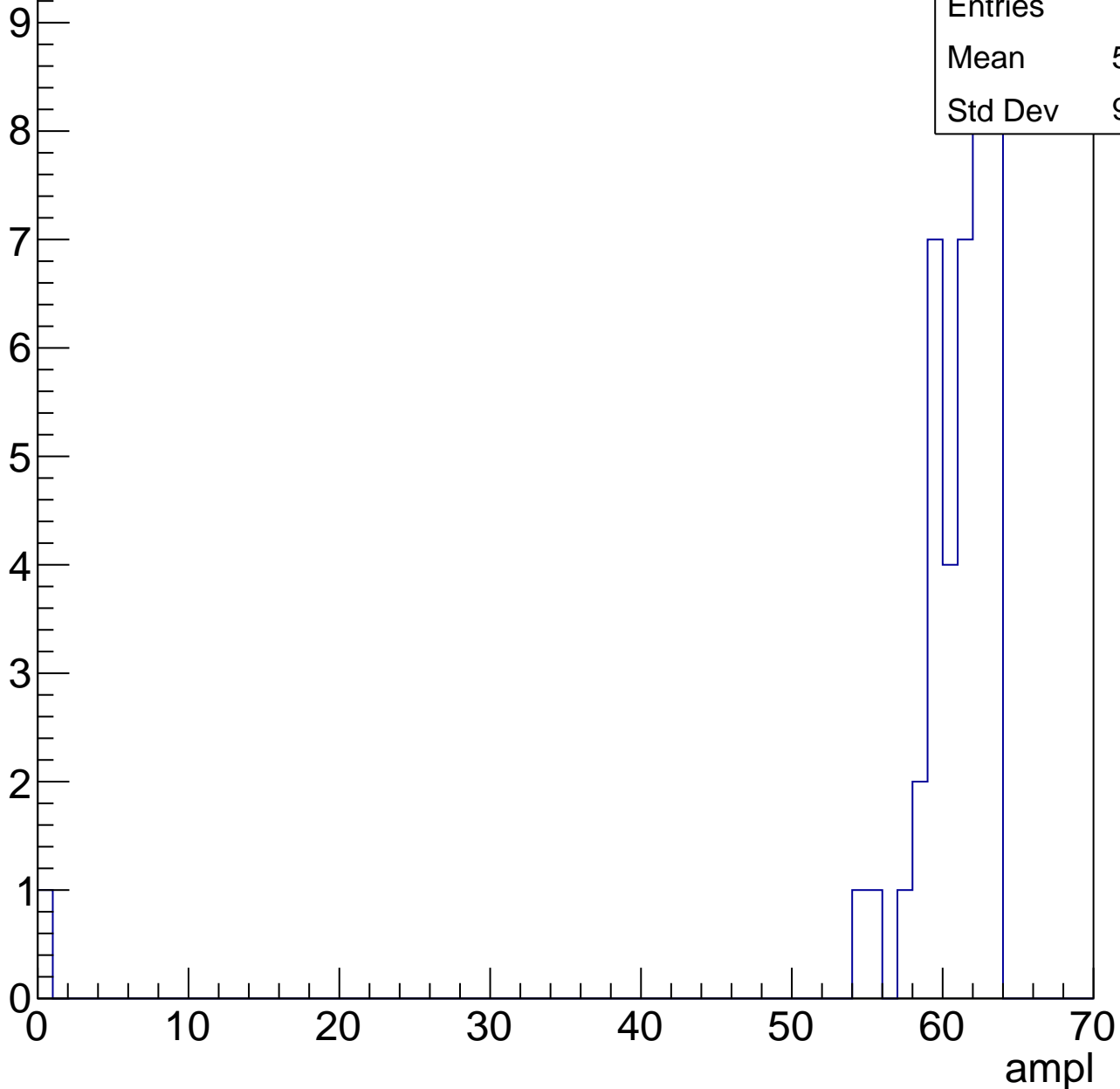


# B0L000S, U10-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	59.21
Std Dev	9.491



# B0L000S, U10-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch88, adc0

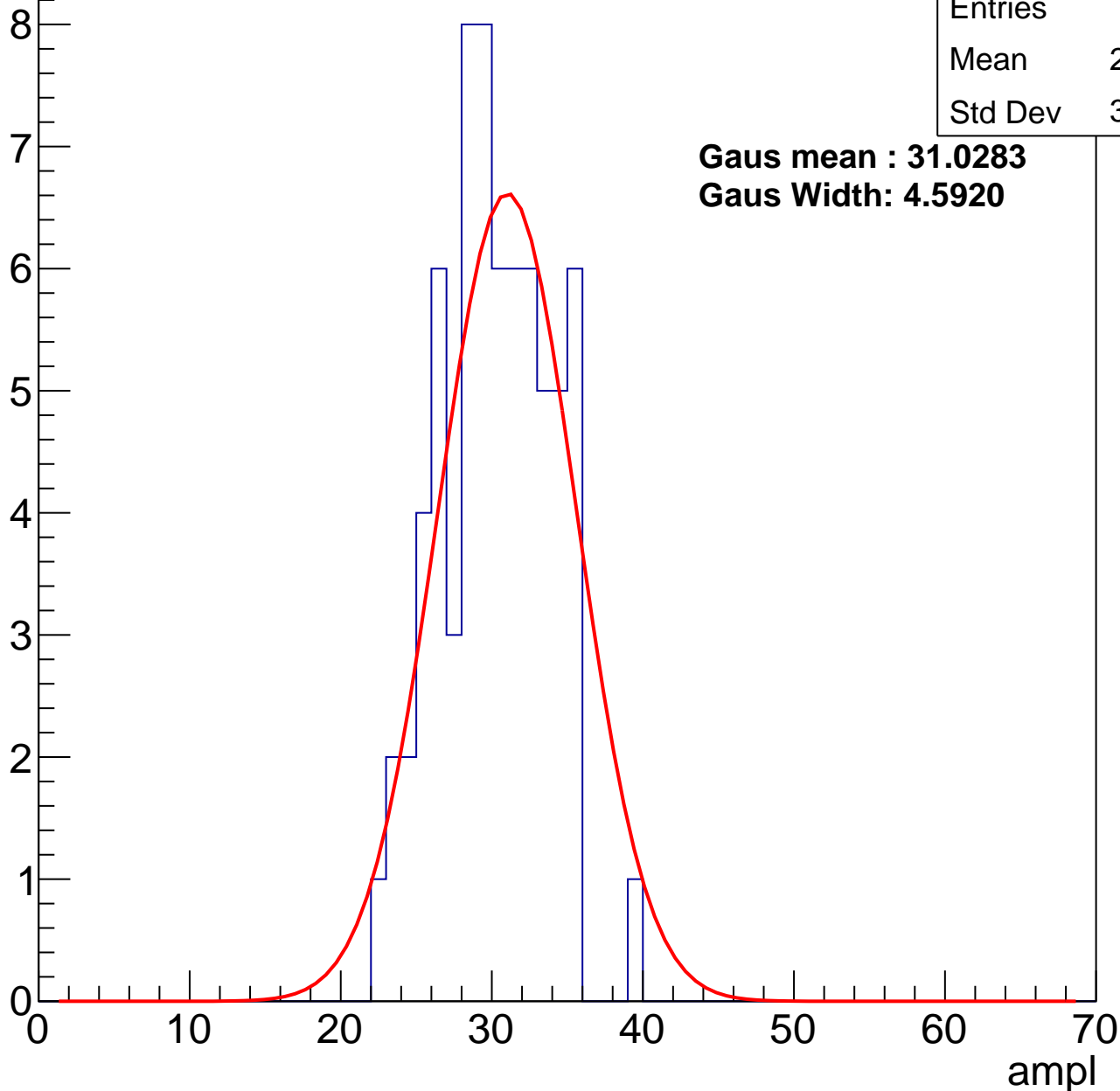
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.72
Std Dev	3.567

**Gaus mean : 31.0283**

**Gaus Width: 4.5920**



# B0L000S, U10-ch88, adc1

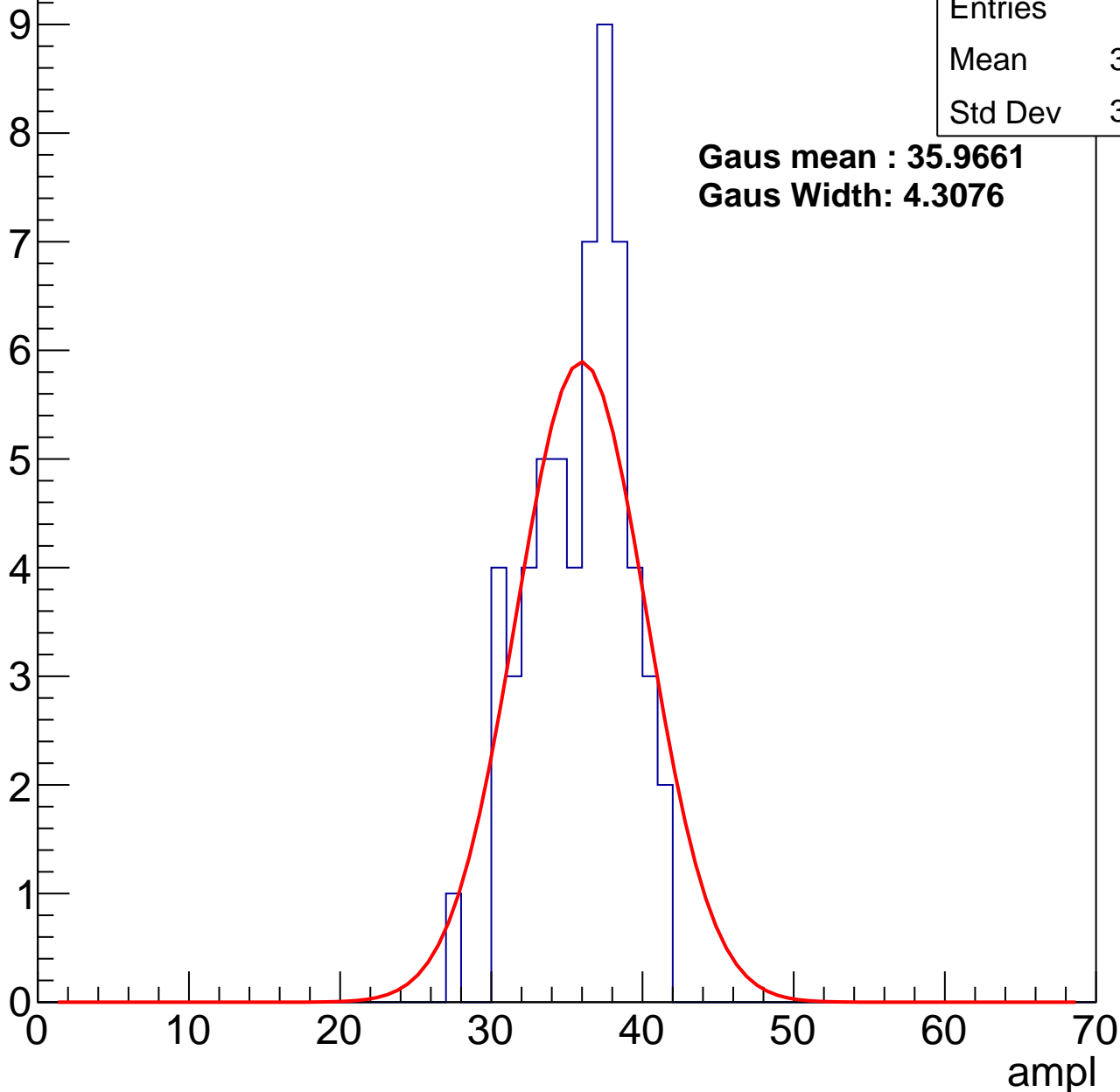
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.38
Std Dev	3.167

**Gaus mean : 35.9661**

**Gaus Width: 4.3076**



# B0L000S, U10-ch88, adc2

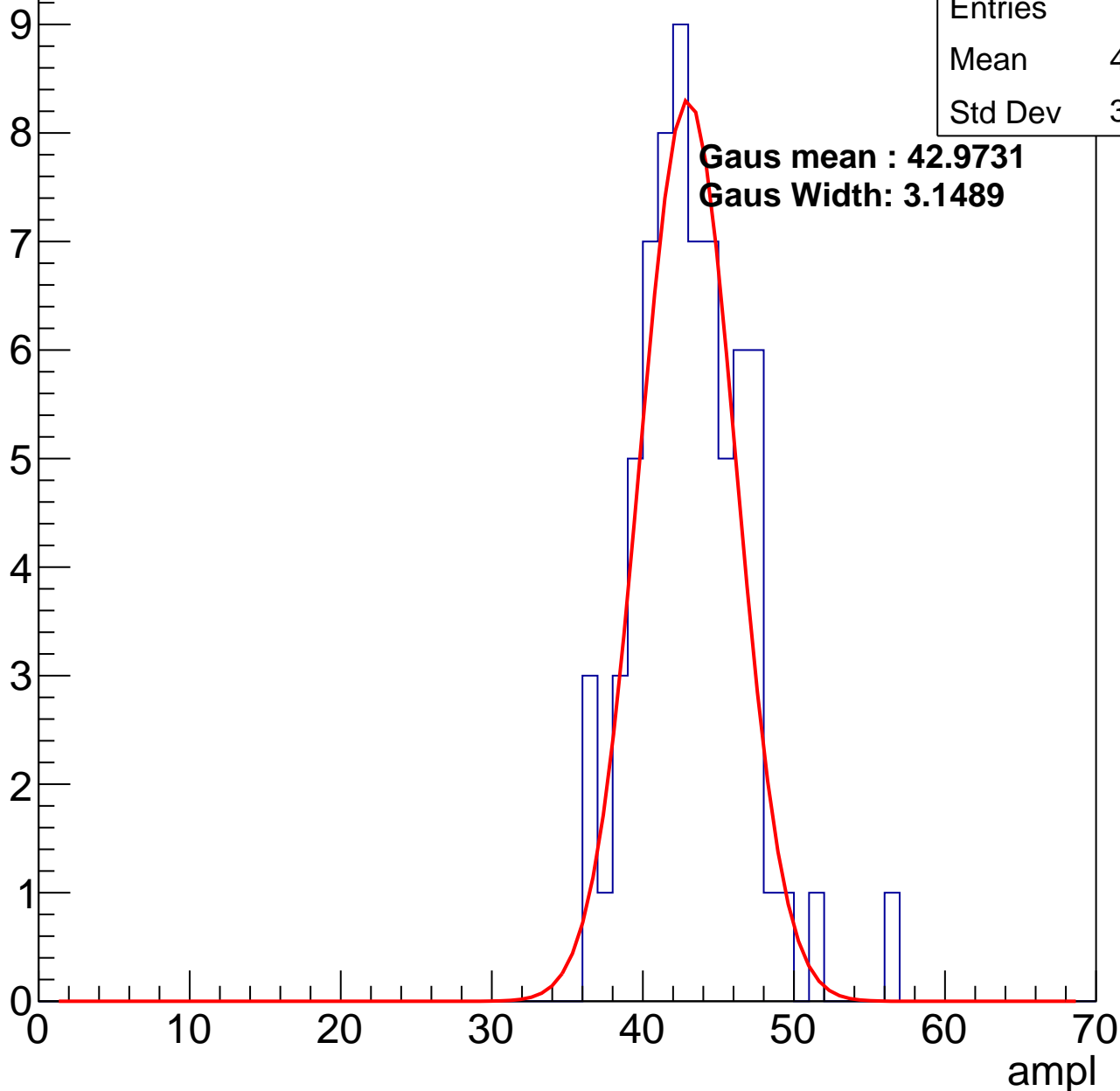
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	42.76
Std Dev	3.578

**Gaus mean : 42.9731**

**Gaus Width: 3.1489**



# B0L000S, U10-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

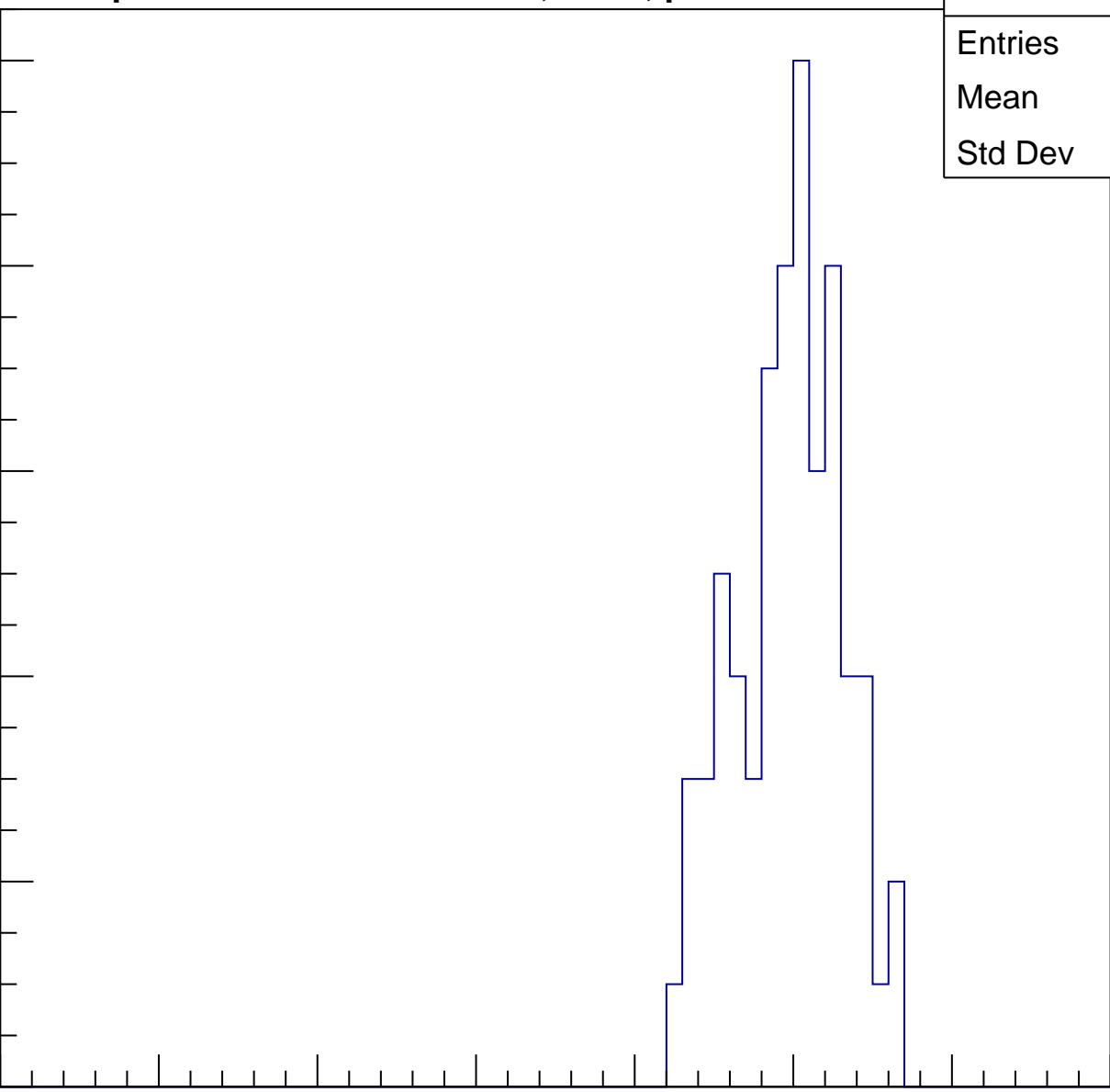
Entries	69
Mean	49.25
Std Dev	3.334

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

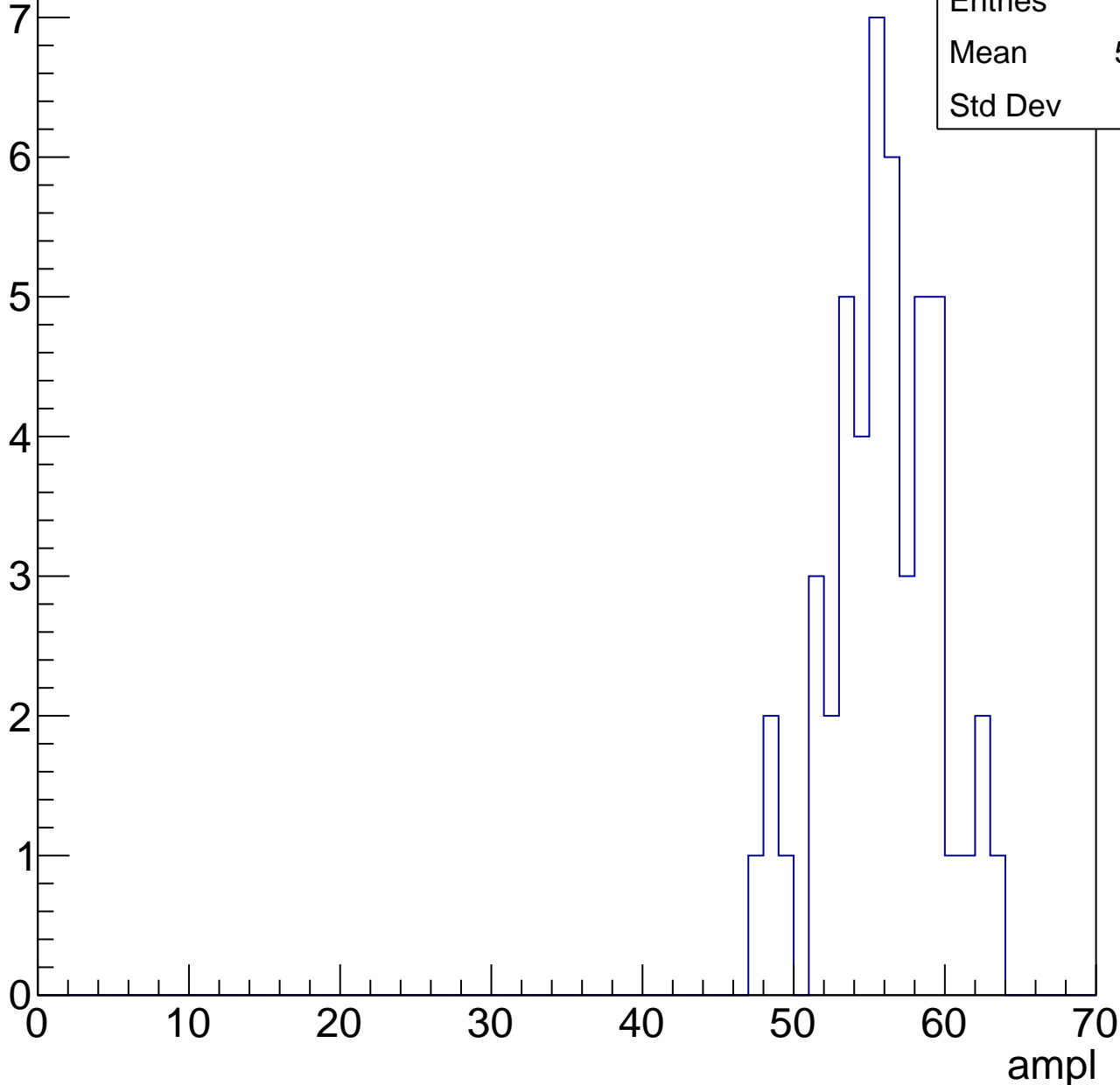


# B0L000S, U10-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.41
Std Dev	3.63



# B0L000S, U10-ch88, adc5

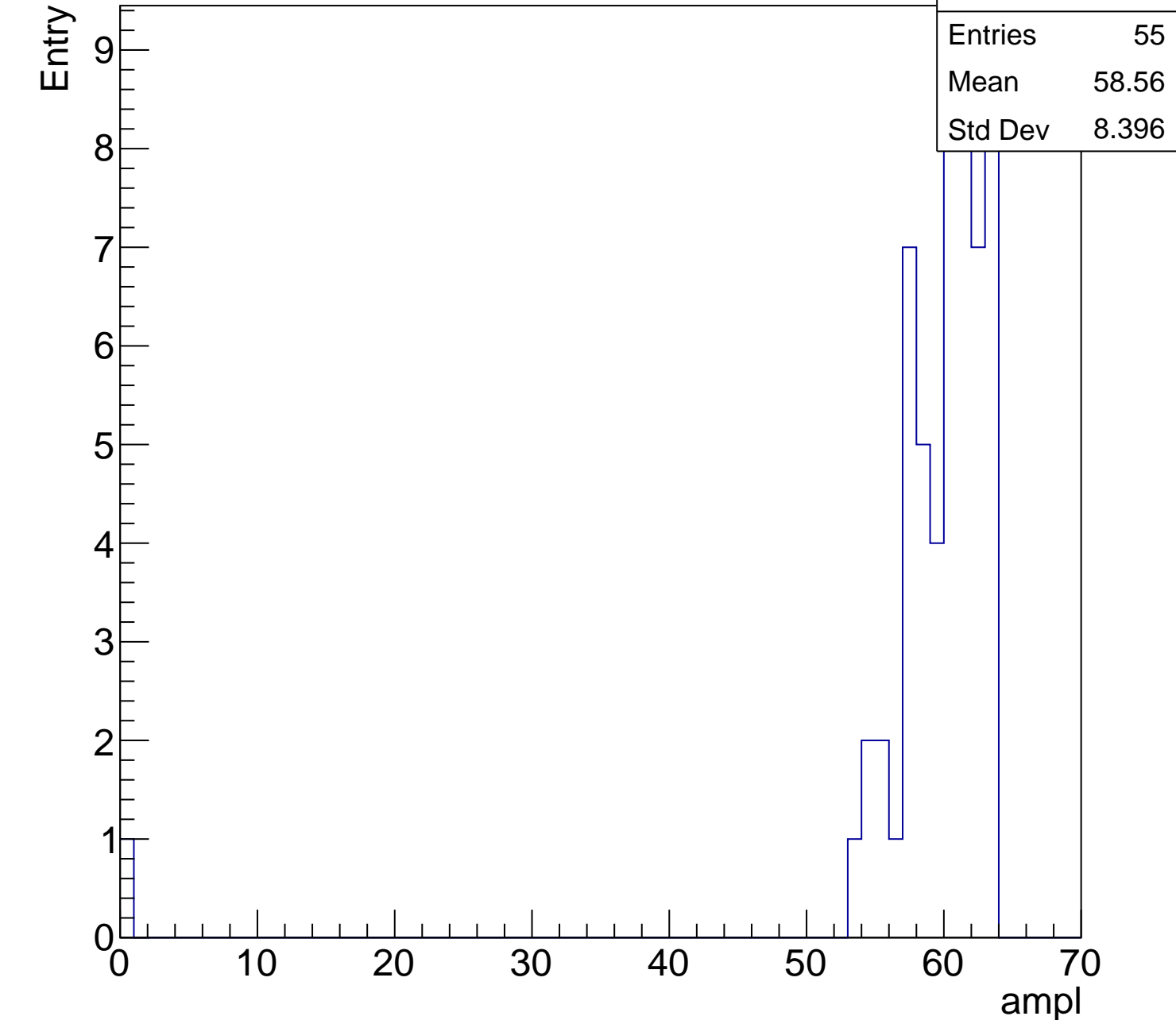
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.56
Std Dev	8.396

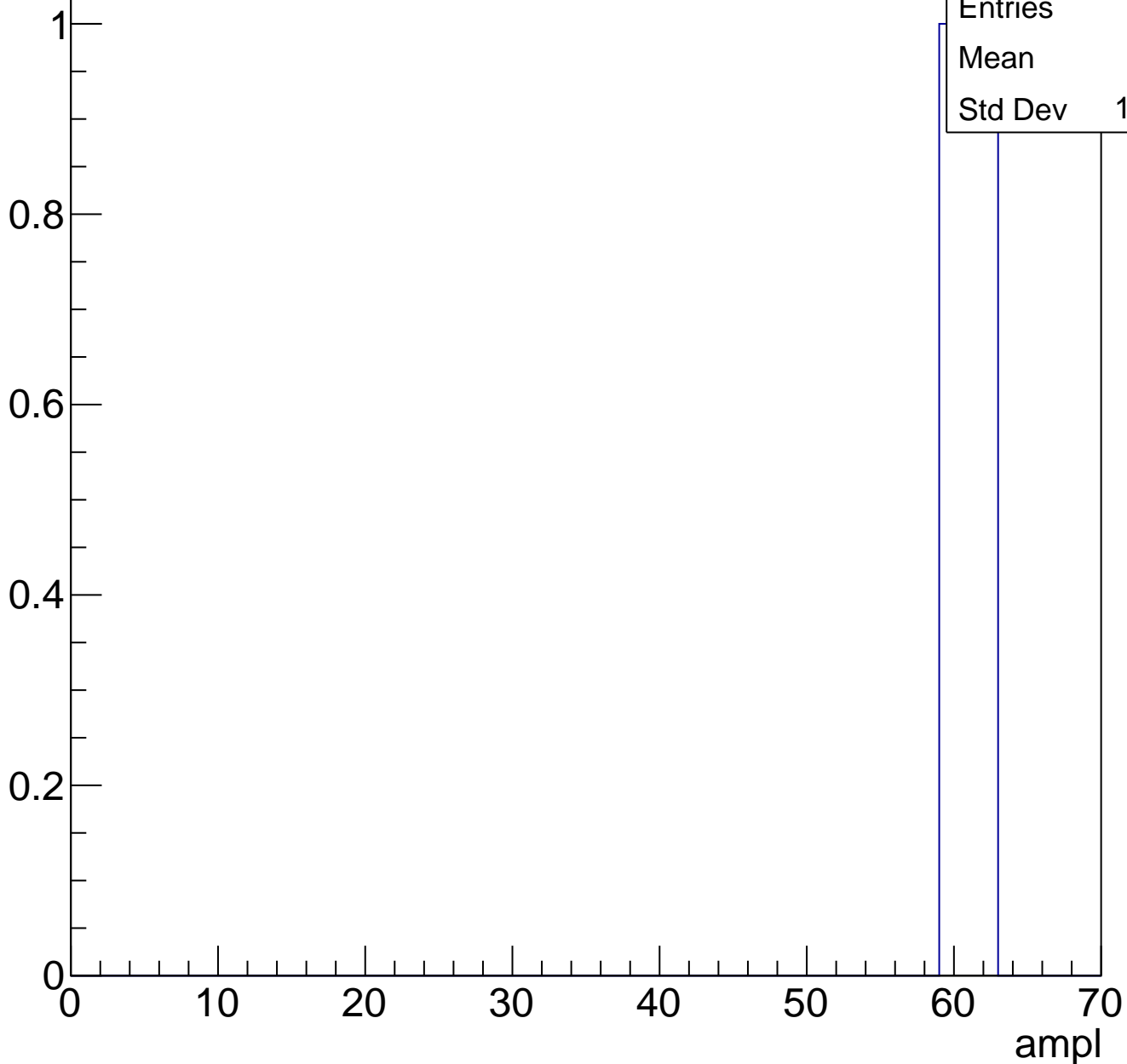
ampl



# B0L000S, U10-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U10-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	90
Mean	29.77
Std Dev	5.8

**Gaus mean : 31.0280**

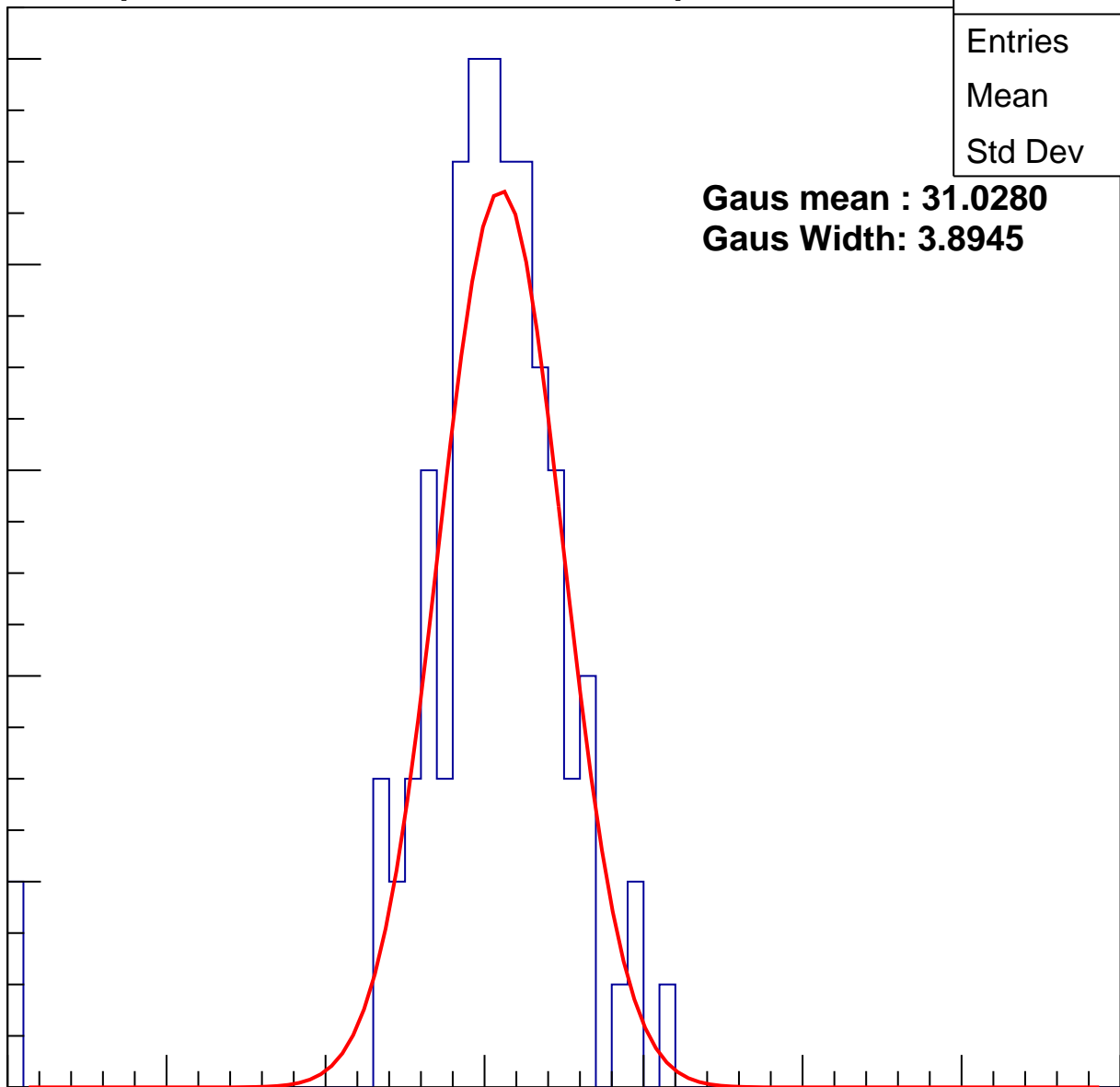
**Gaus Width: 3.8945**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch89, adc1

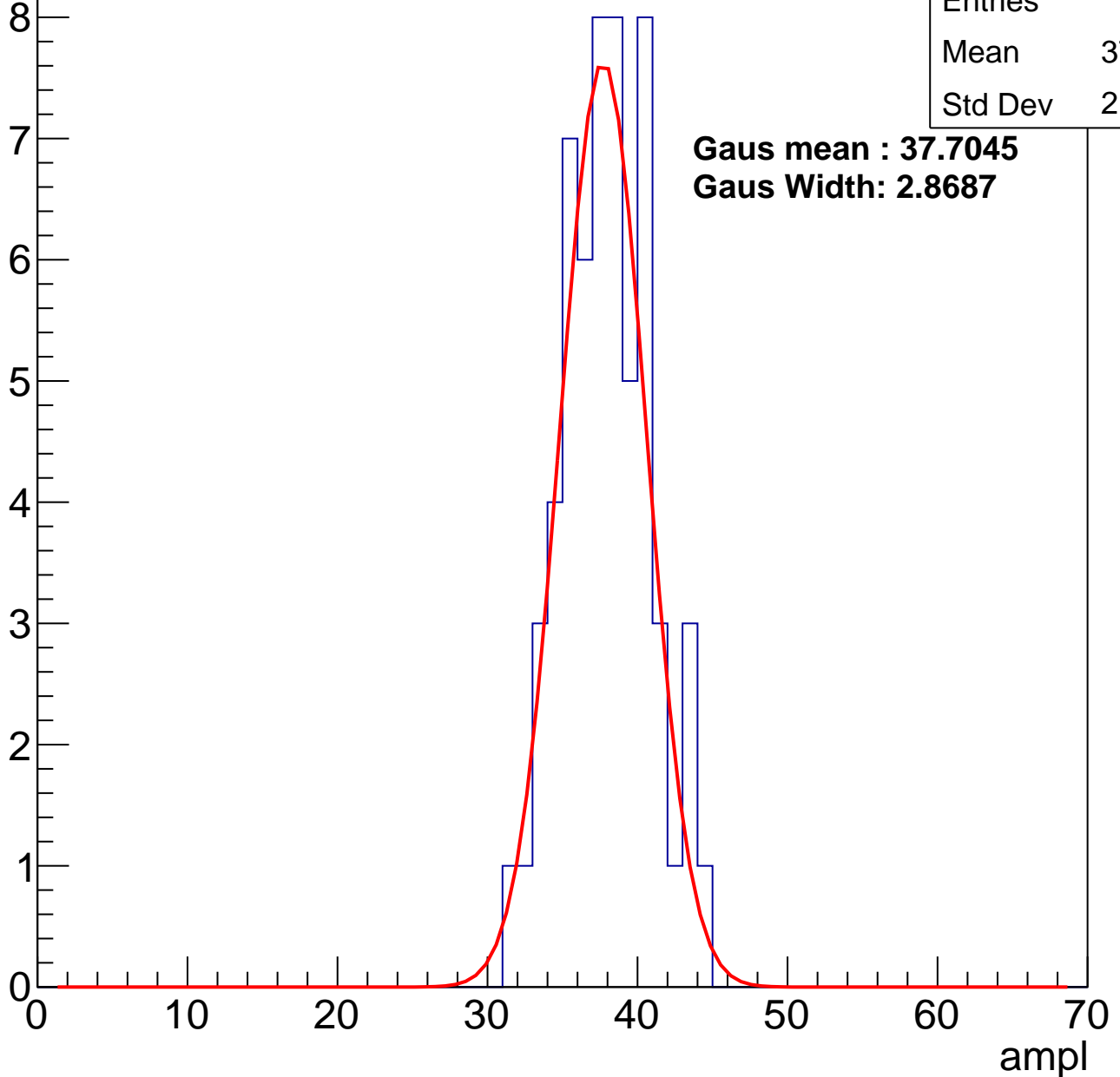
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.49
Std Dev	2.896

**Gaus mean : 37.7045**

**Gaus Width: 2.8687**



# B0L000S, U10-ch89, adc2

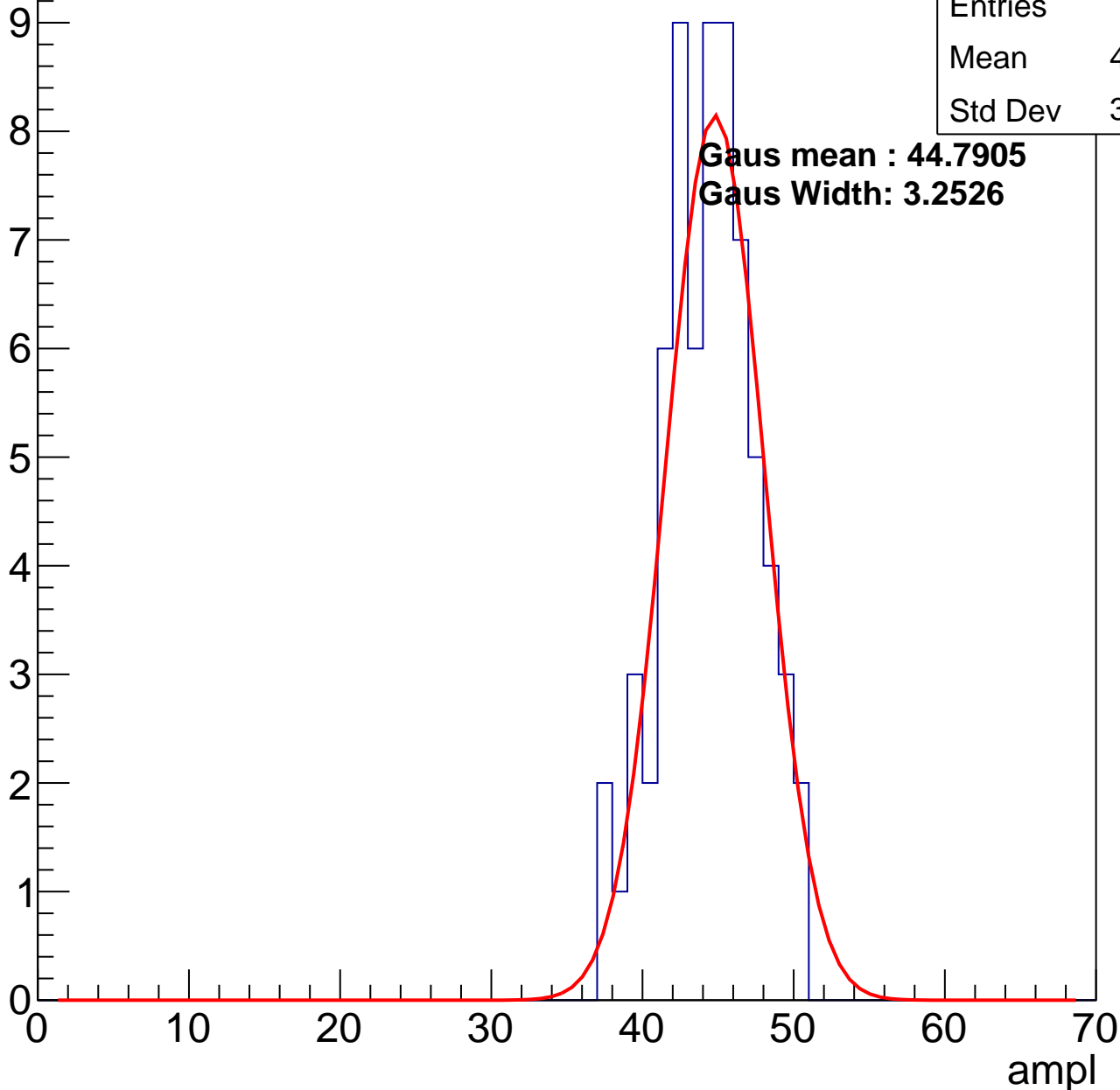
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.94
Std Dev	3.043

**Gaus mean : 44.7905**

**Gaus Width: 3.2526**

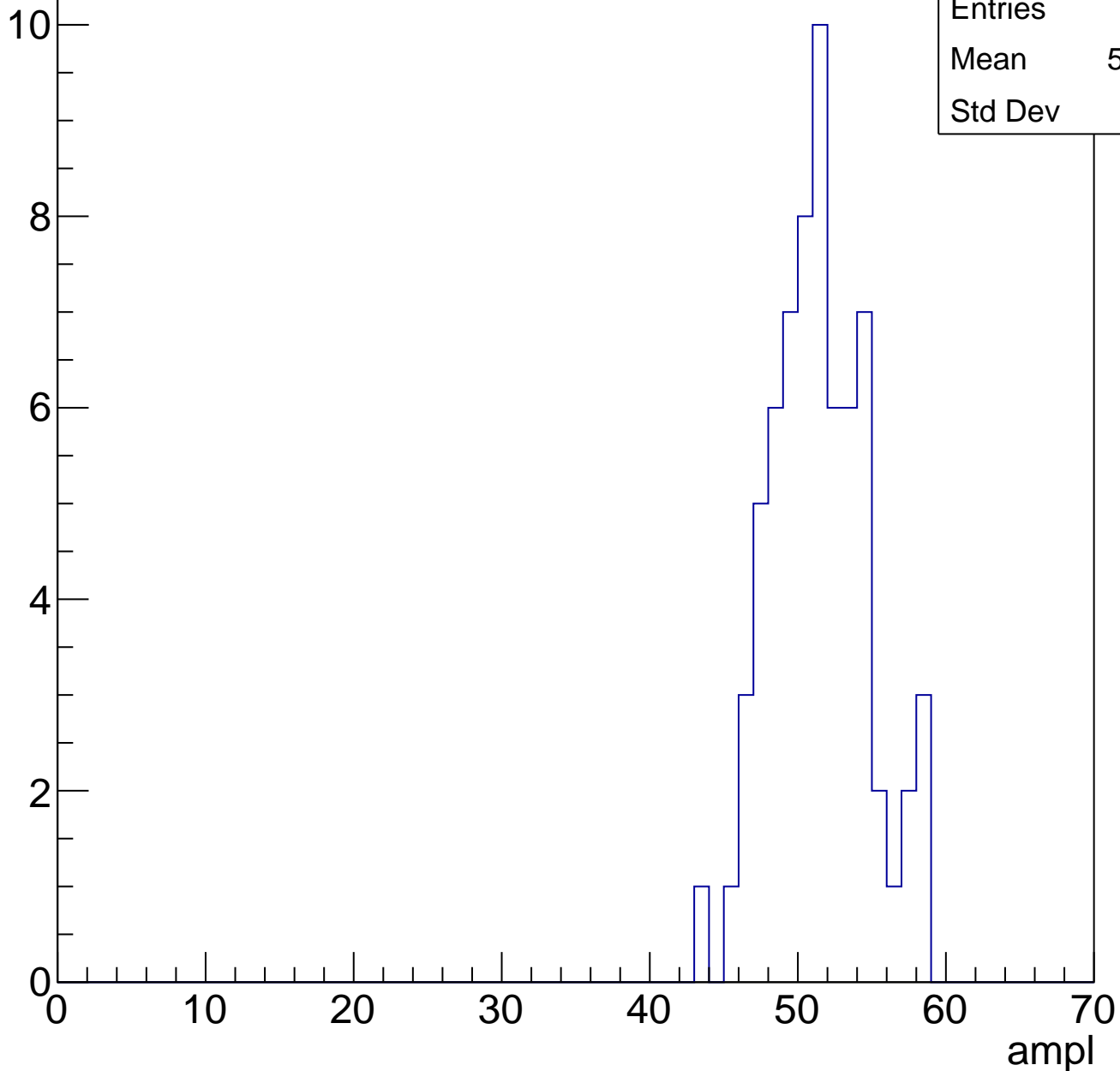


# B0L000S, U10-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	50.94
Std Dev	3.24

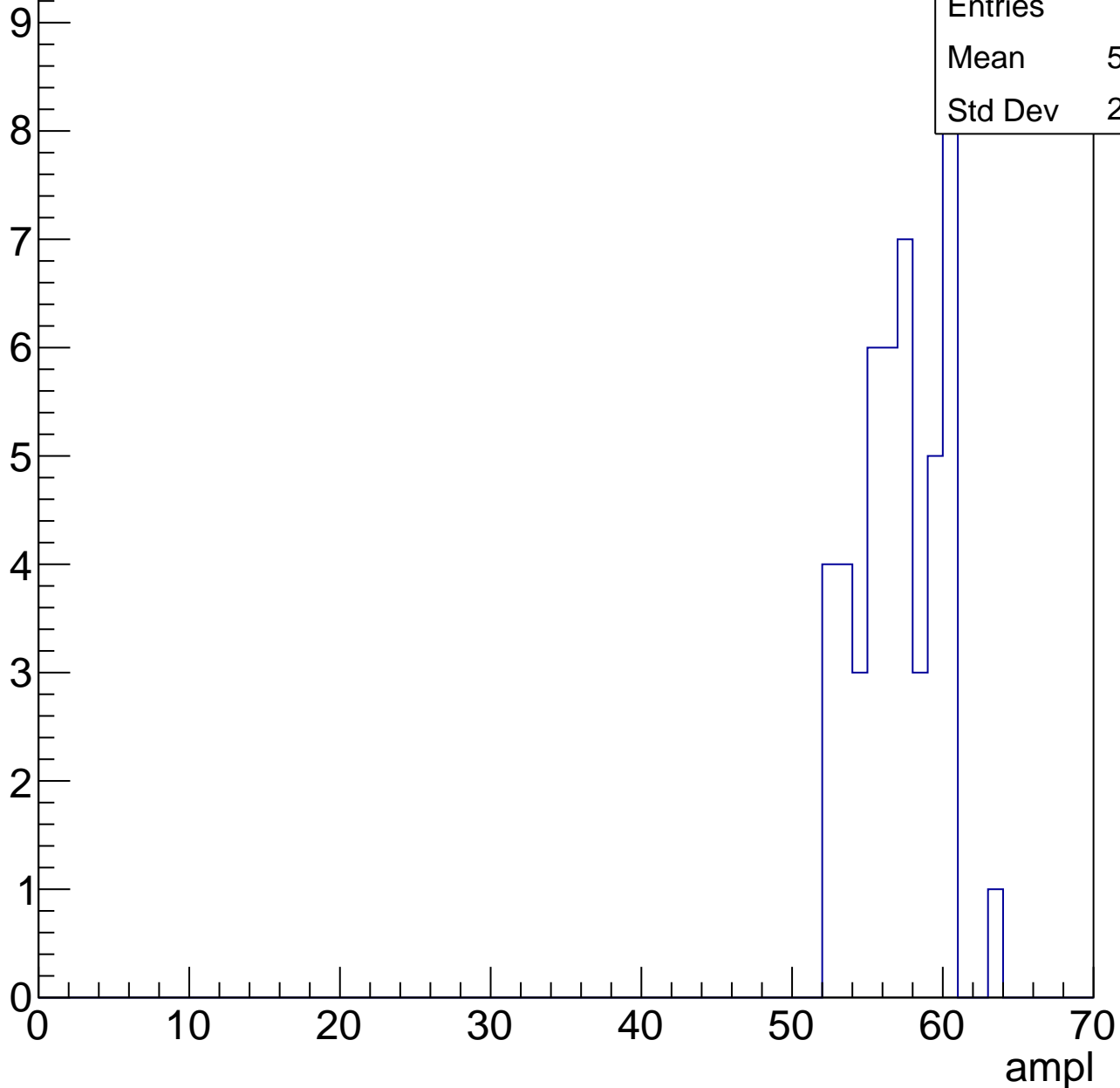
Entry



# B0L000S, U10-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

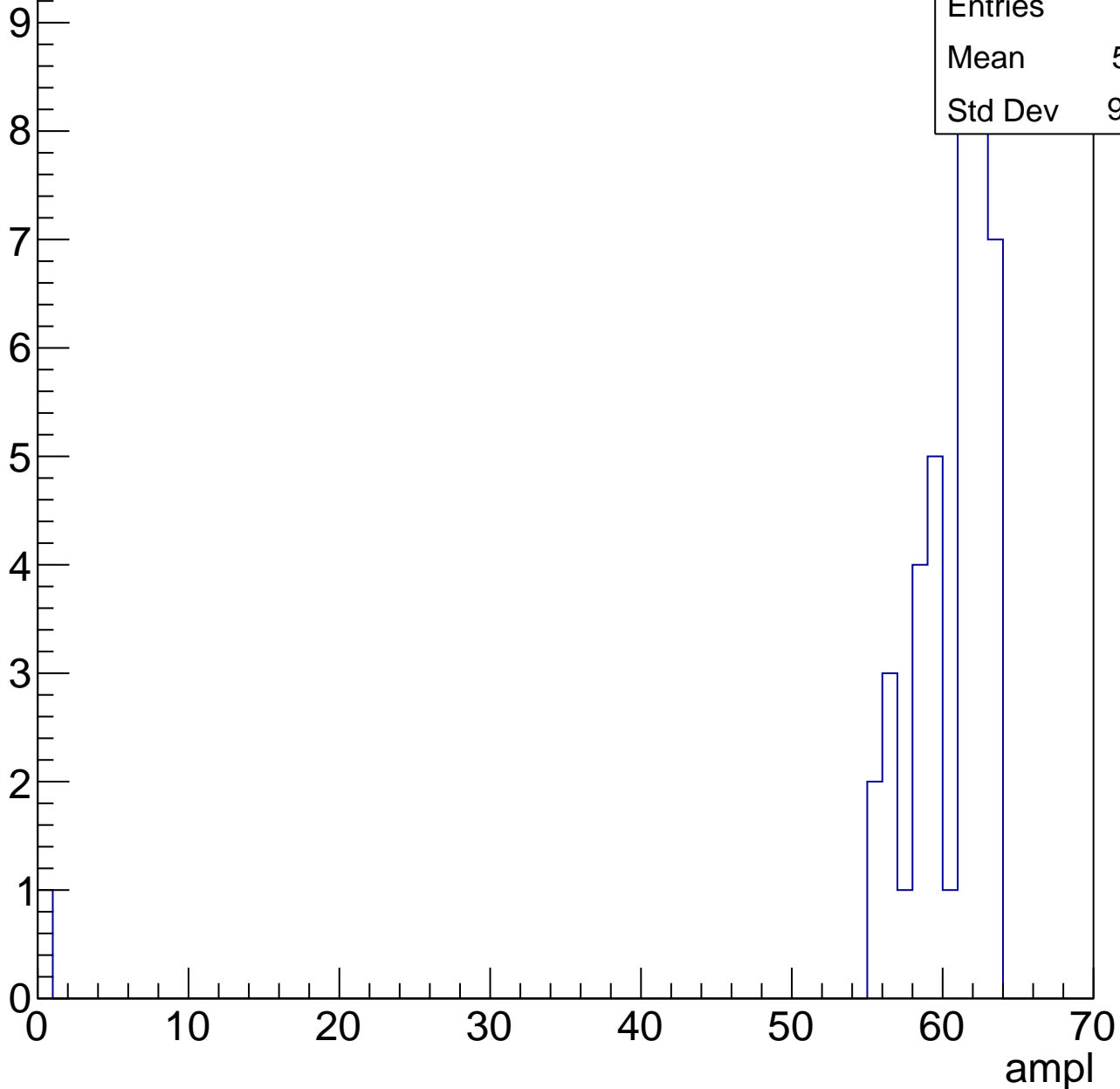


# B0L000S, U10-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.81
Std Dev	9.482



# B0L000S, U10-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B0L000S, U10-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.53
Std Dev	4.775

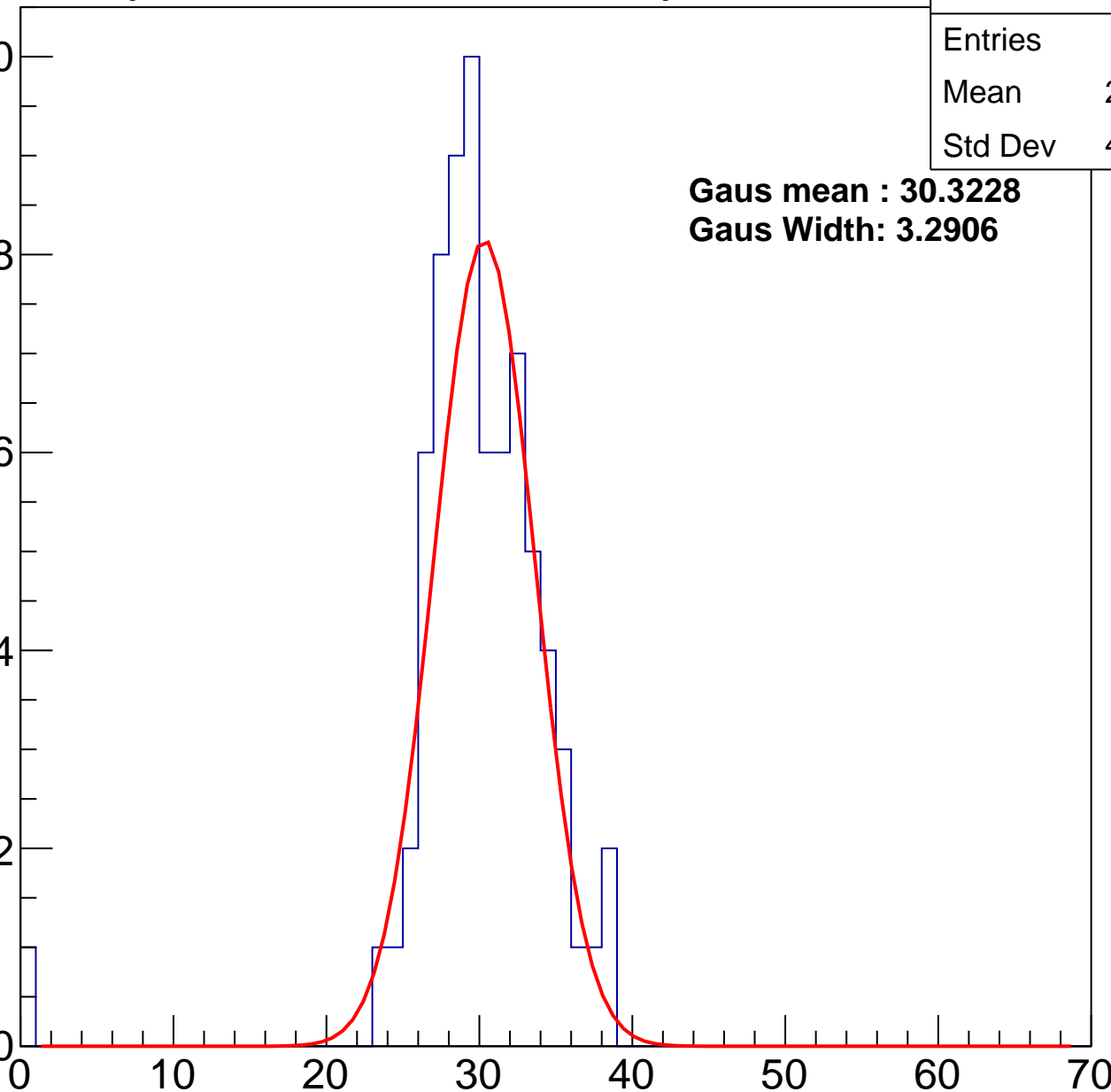
**Gaus mean : 30.3228**

**Gaus Width: 3.2906**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U10-ch90, adc1

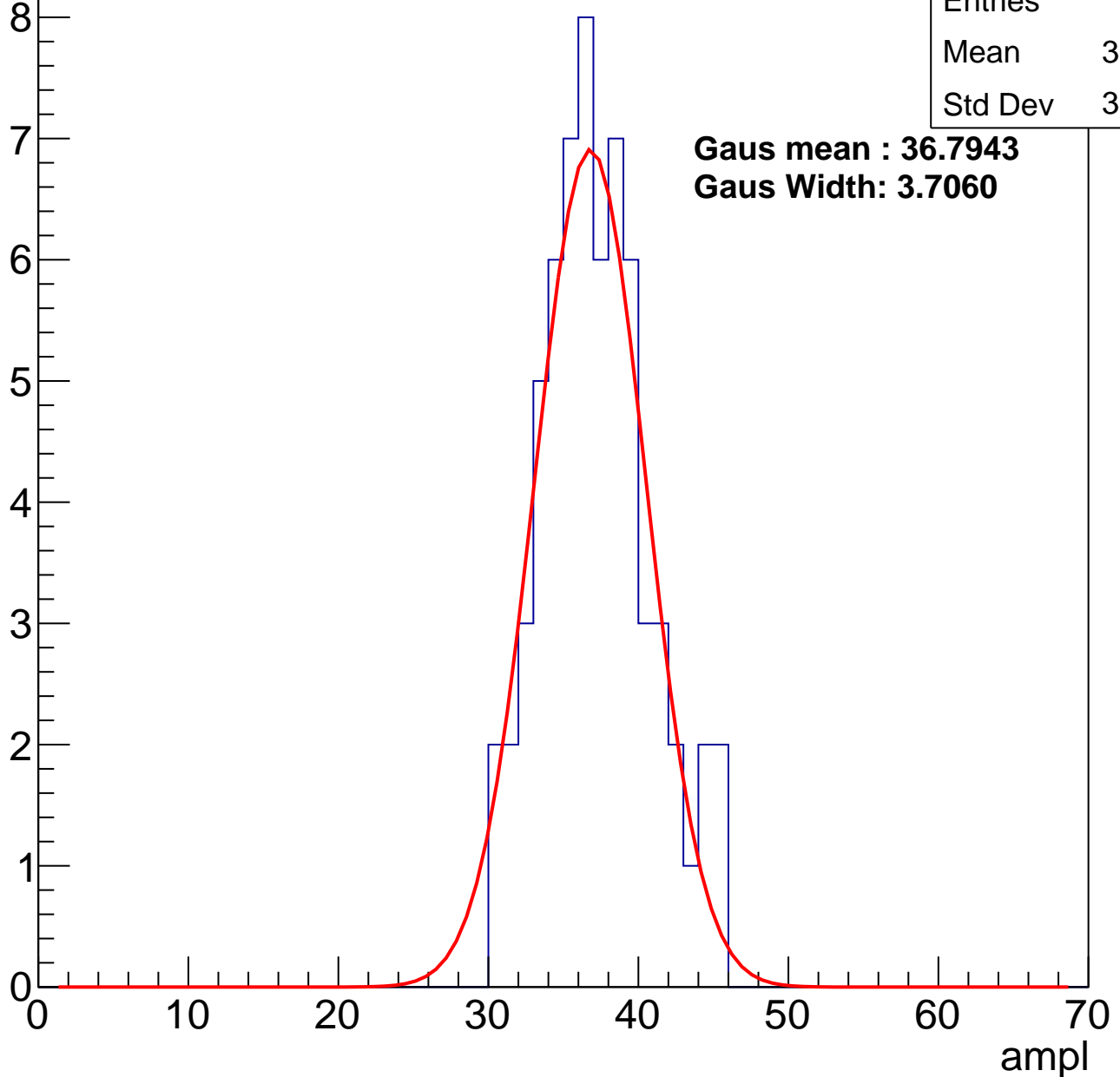
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.77
Std Dev	3.572

**Gaus mean : 36.7943**

**Gaus Width: 3.7060**



# B0L000S, U10-ch90, adc2

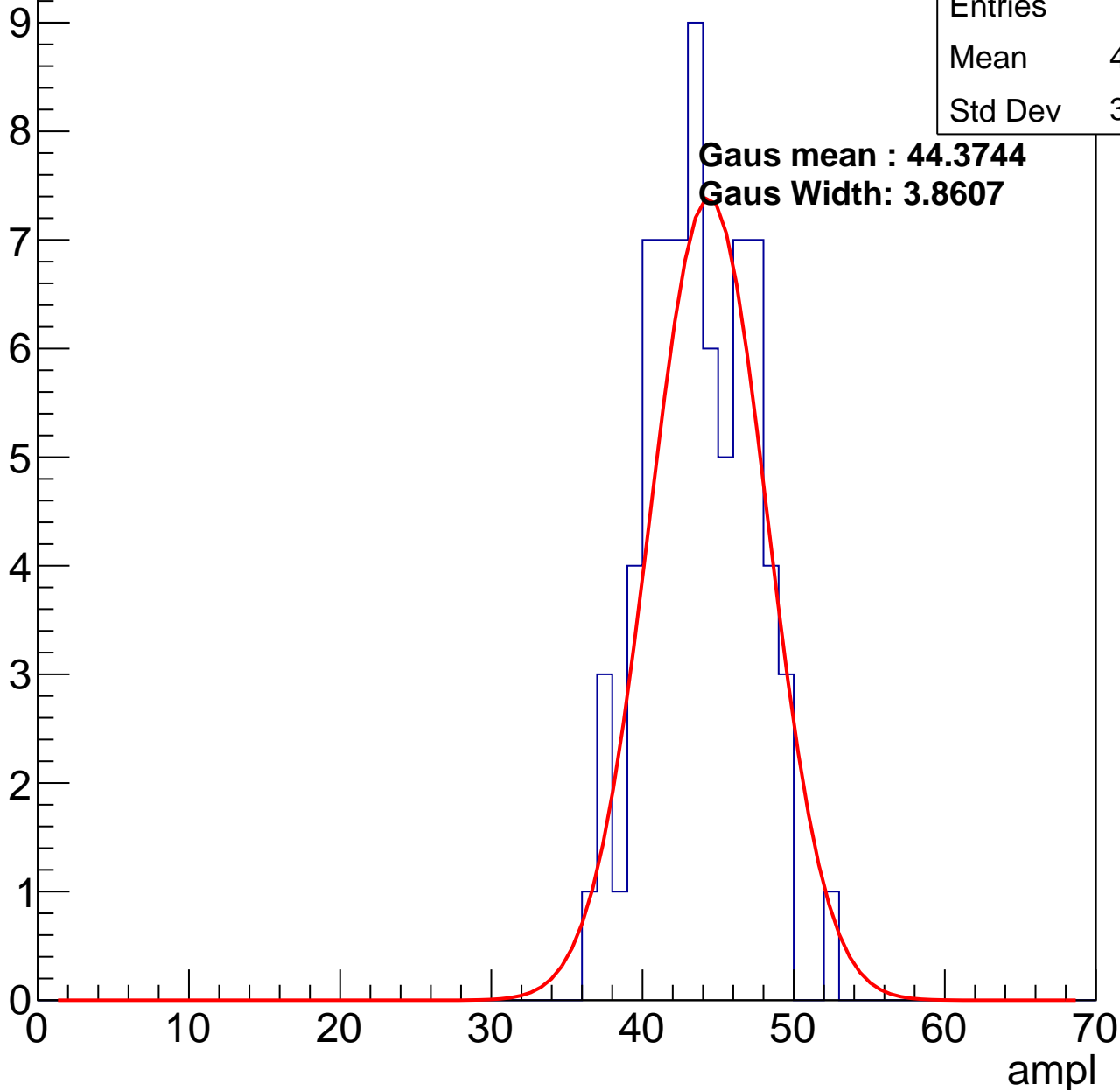
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	43.33
Std Dev	3.387

**Gaus mean : 44.3744**

**Gaus Width: 3.8607**

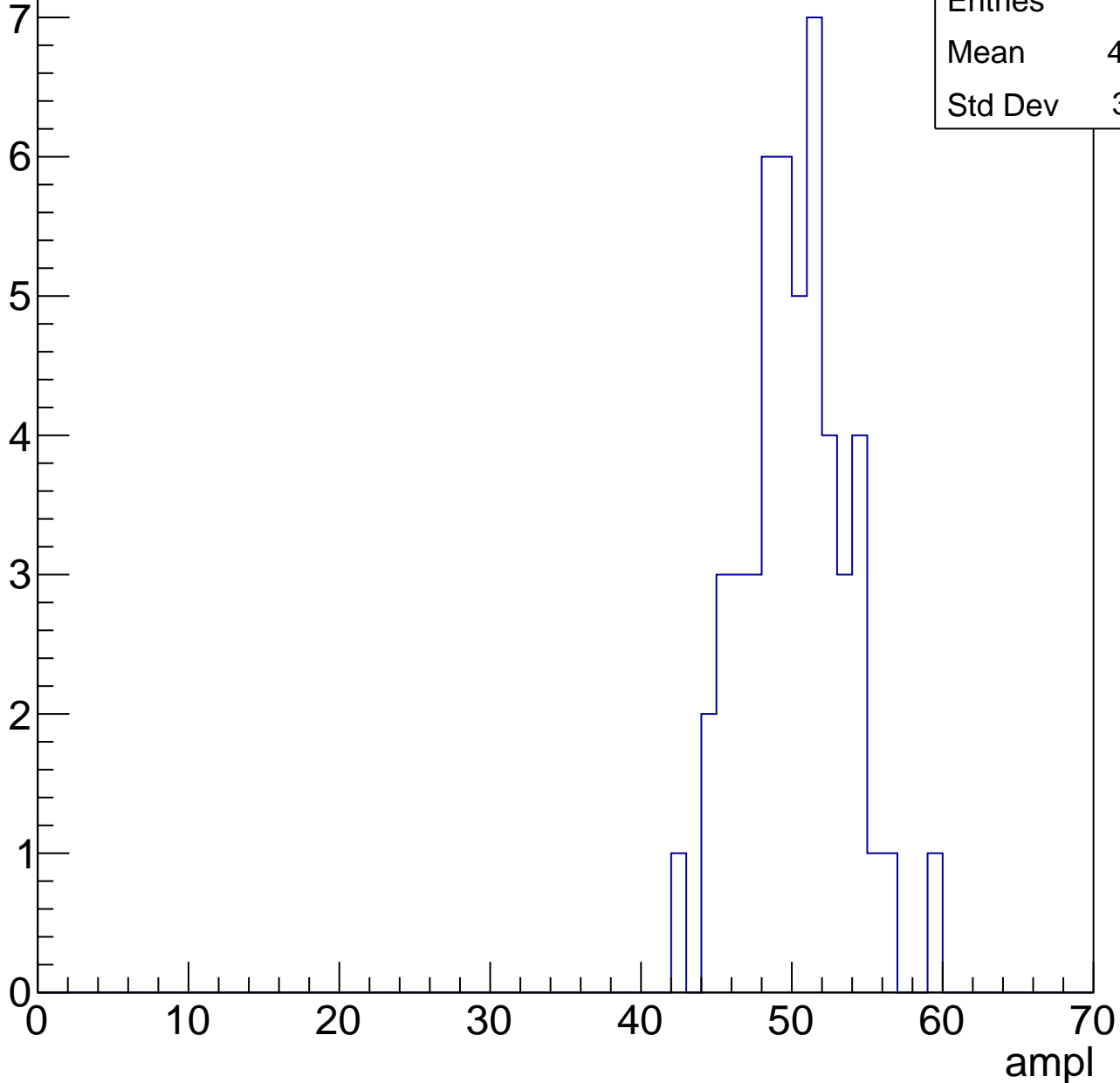


# B0L000S, U10-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	49.72
Std Dev	3.371

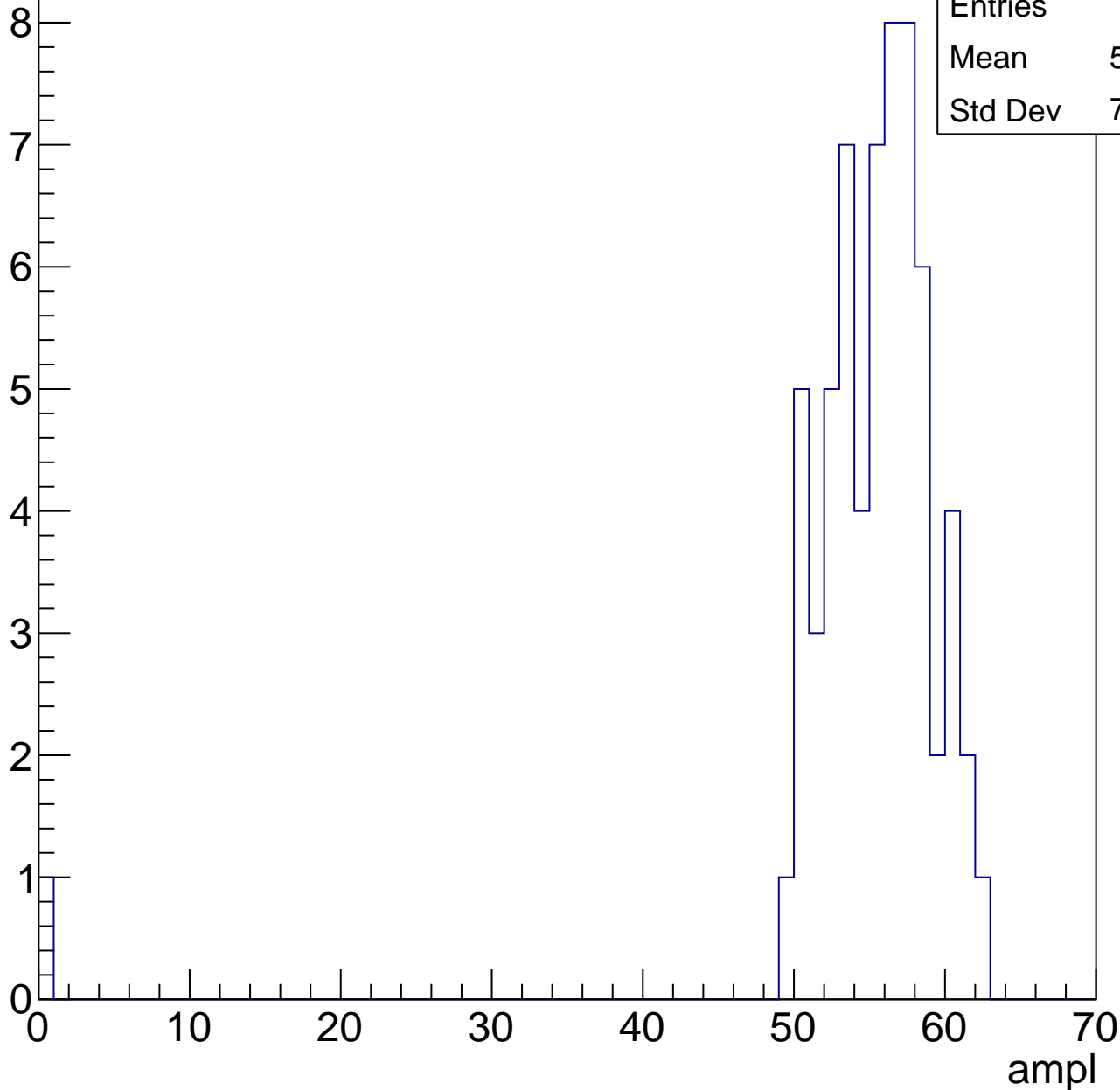


# B0L000S, U10-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.34
Std Dev	7.532



# B0L000S, U10-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.96
Std Dev	2.493

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

# B0L000S, U10-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L000S, U10-ch91, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	30.29
Std Dev	5.02

**Gaus mean : 31.1510**

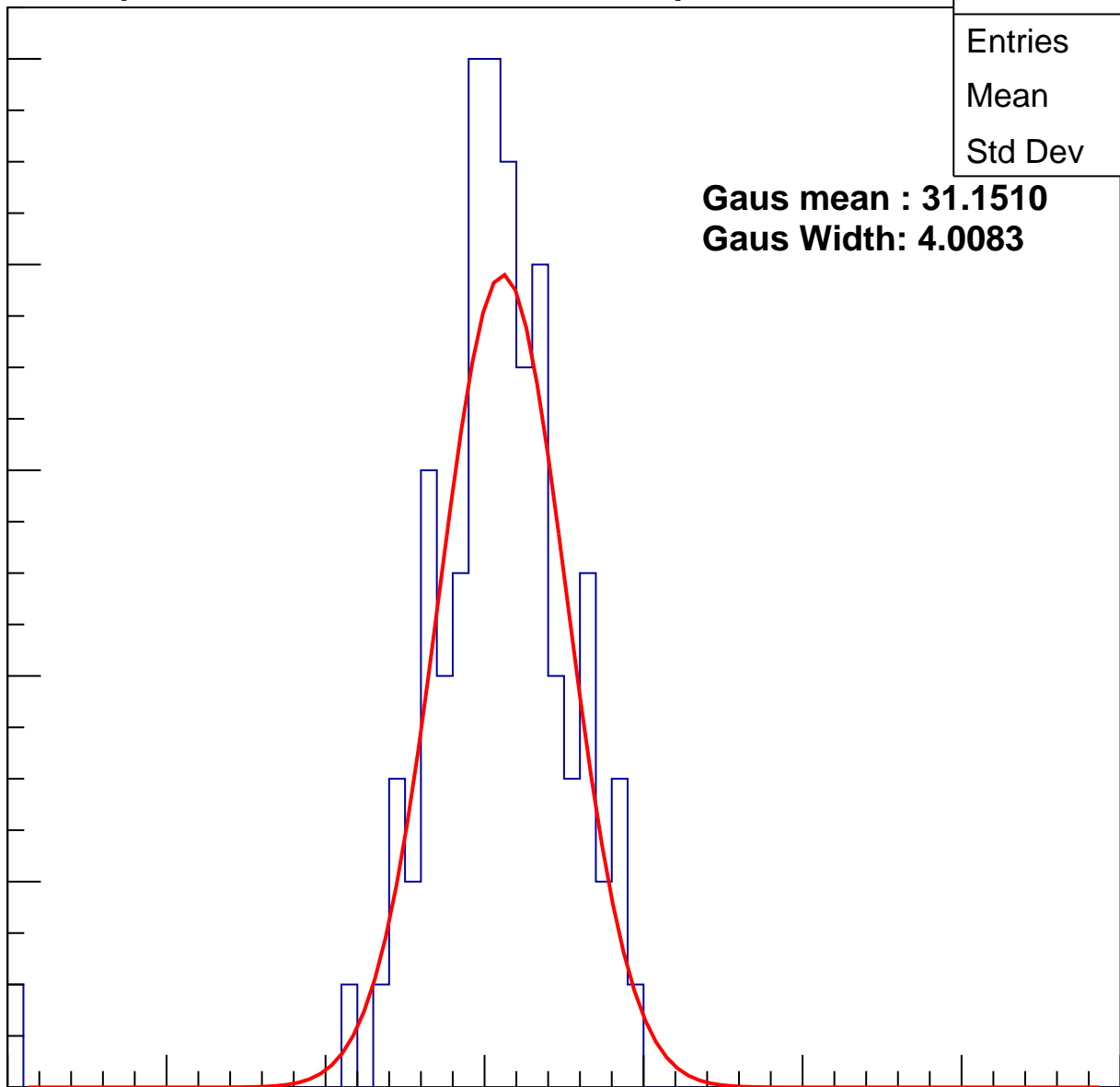
**Gaus Width: 4.0083**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch91, adc1

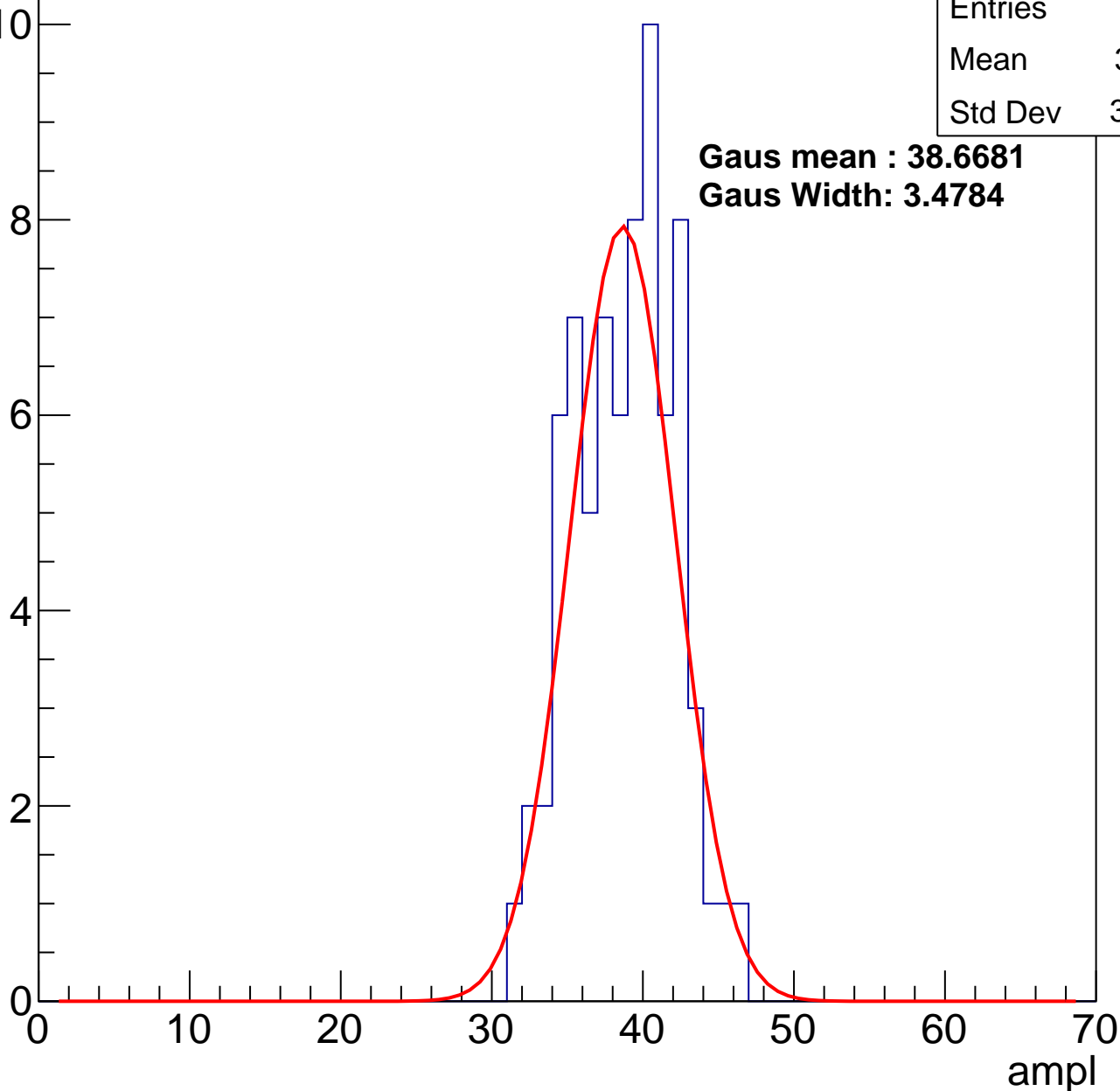
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	38.31
Std Dev	3.296

**Gaus mean : 38.6681**

**Gaus Width: 3.4784**



# B0L000S, U10-ch91, adc2

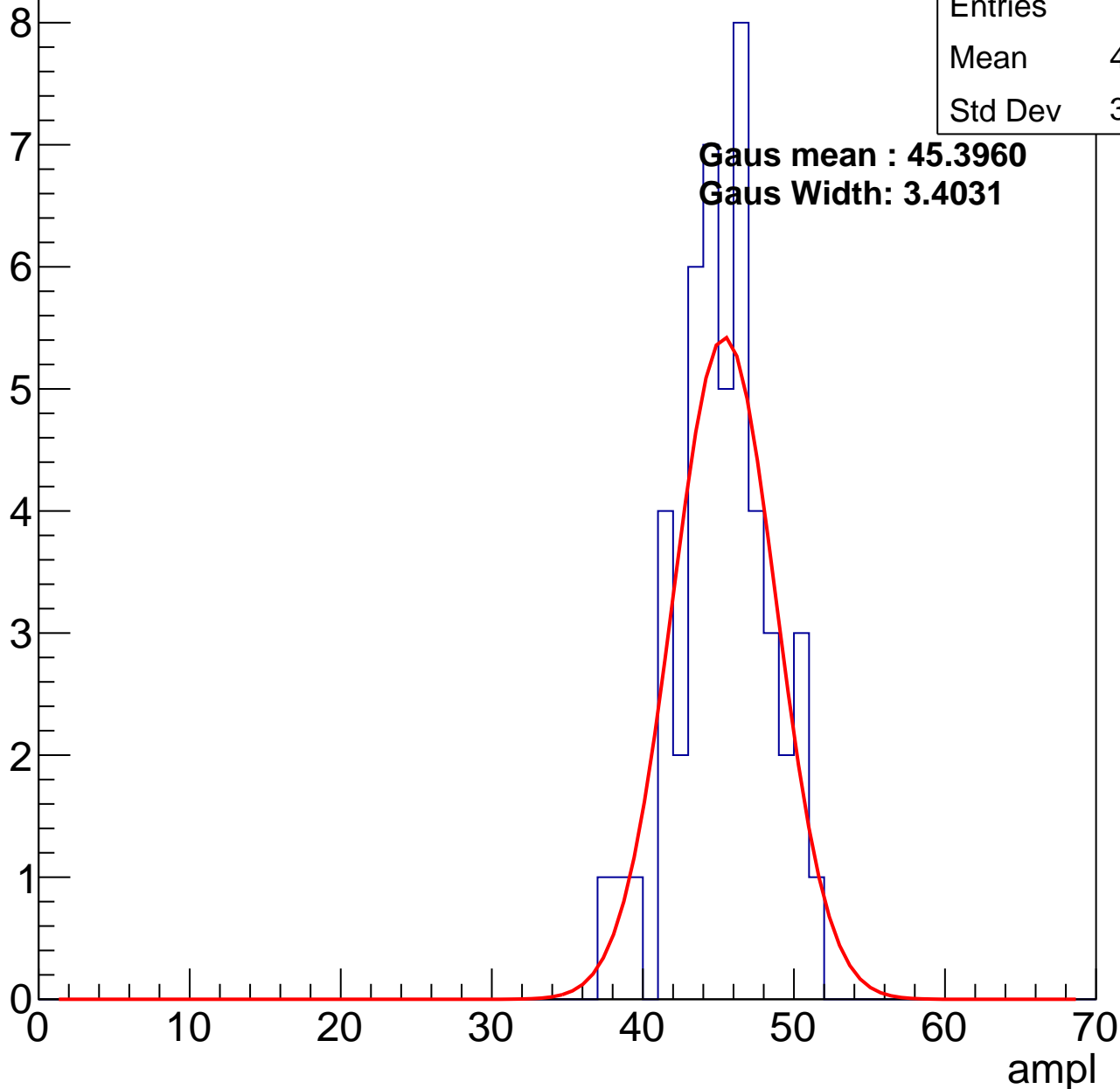
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	44.83
Std Dev	3.078

**Gaus mean : 45.3960**

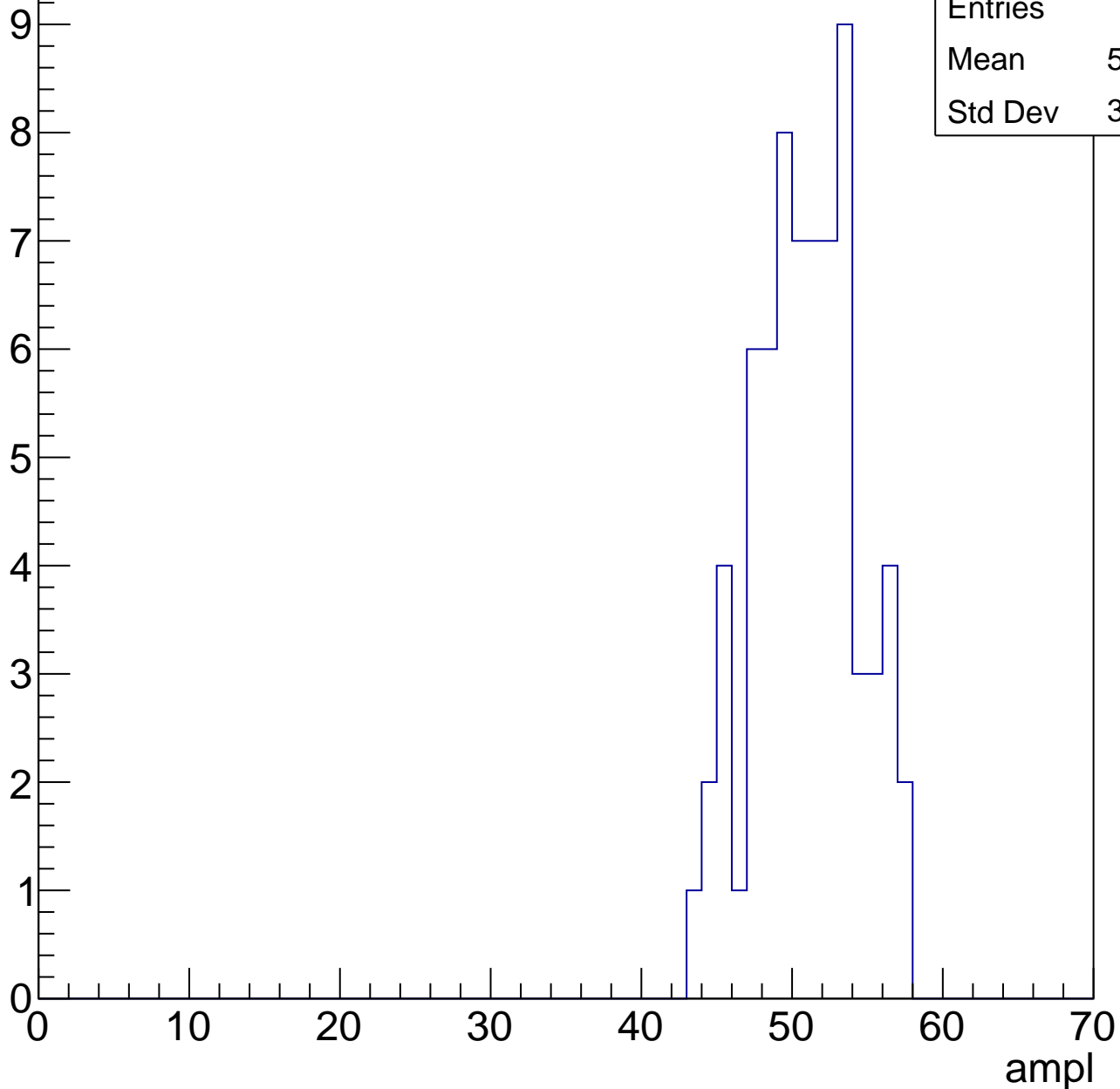
**Gaus Width: 3.4031**



# B0L000S, U10-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

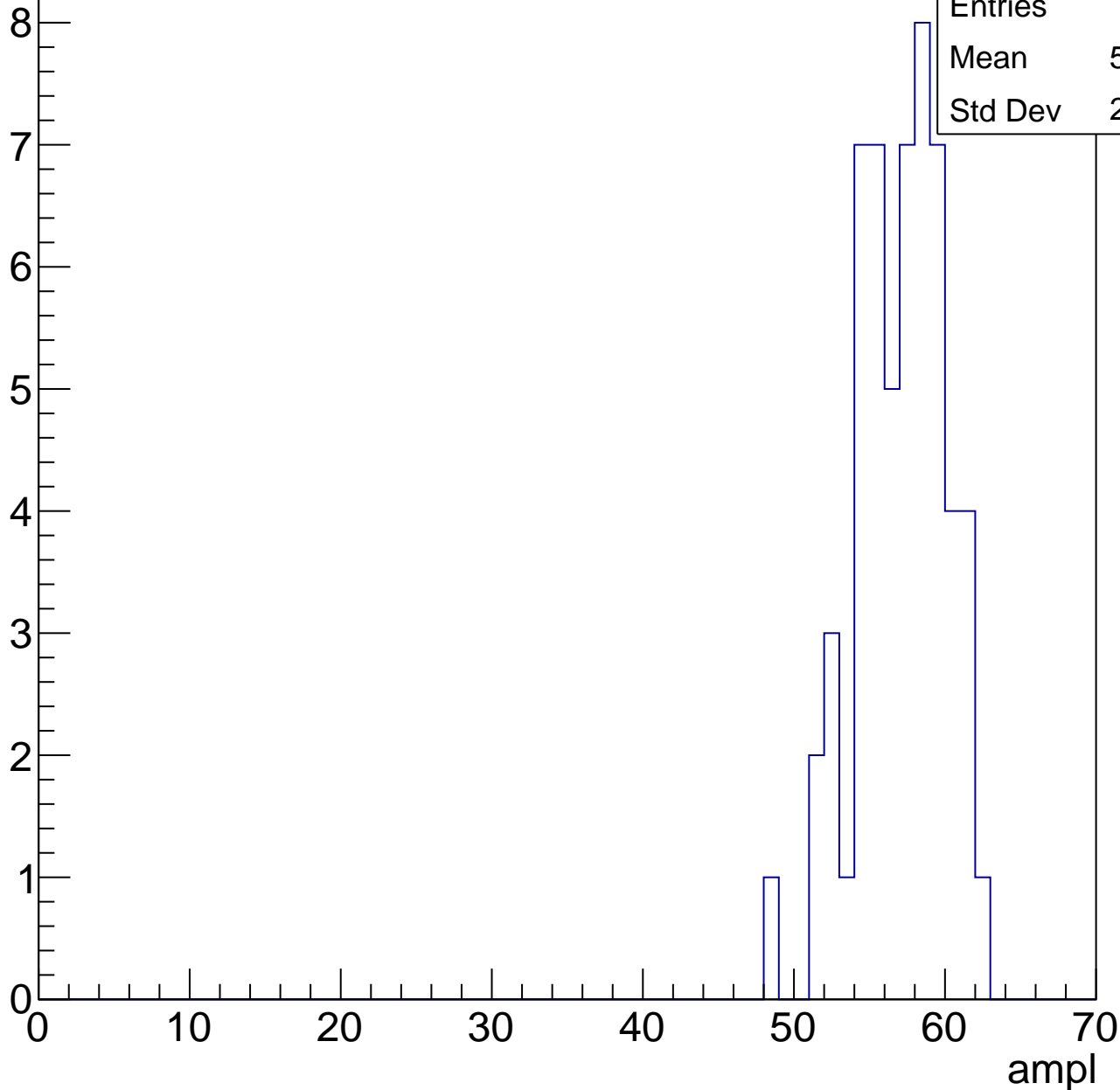


# B0L000S, U10-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.56
Std Dev	2.932

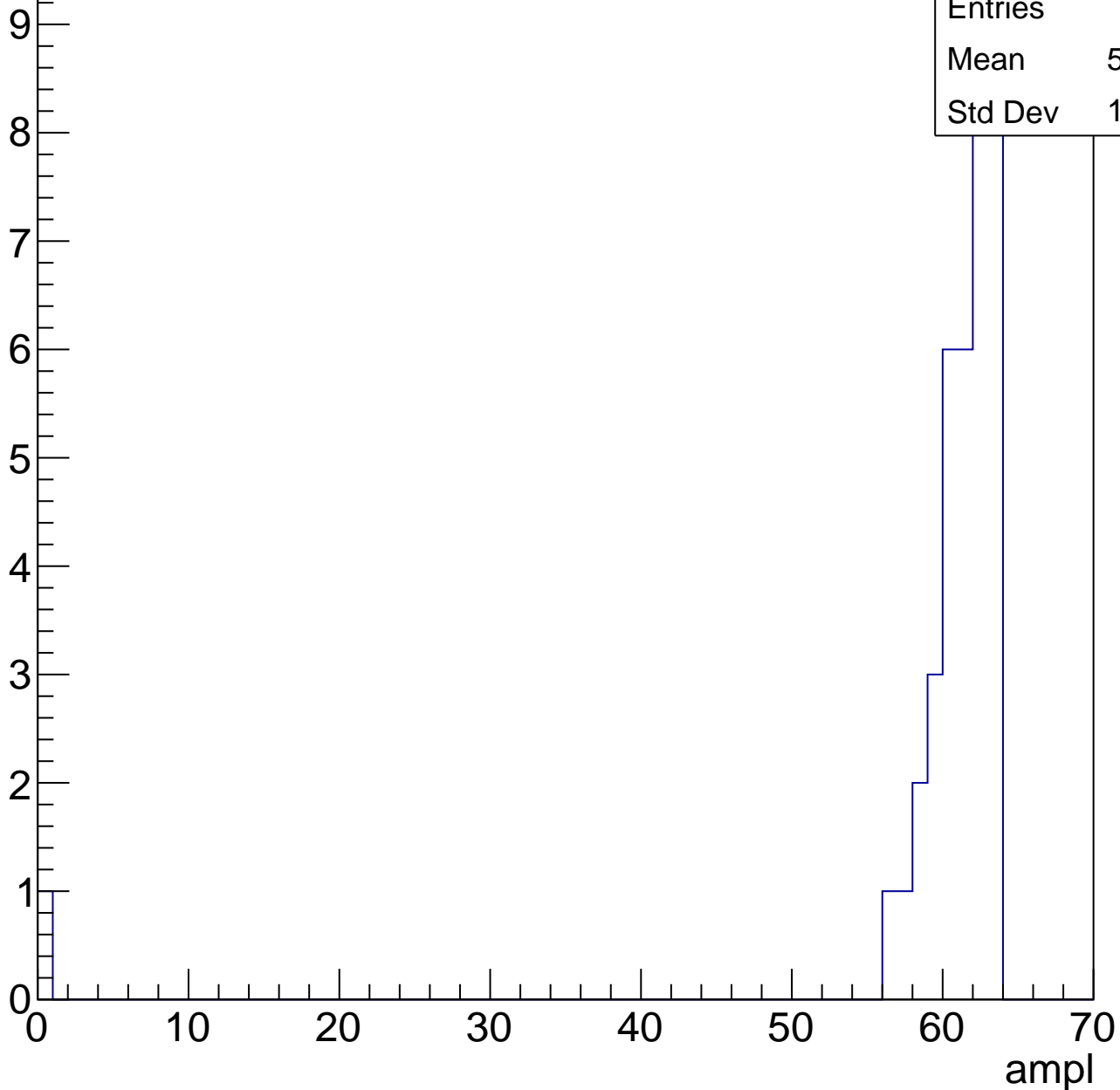


# B0L000S, U10-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

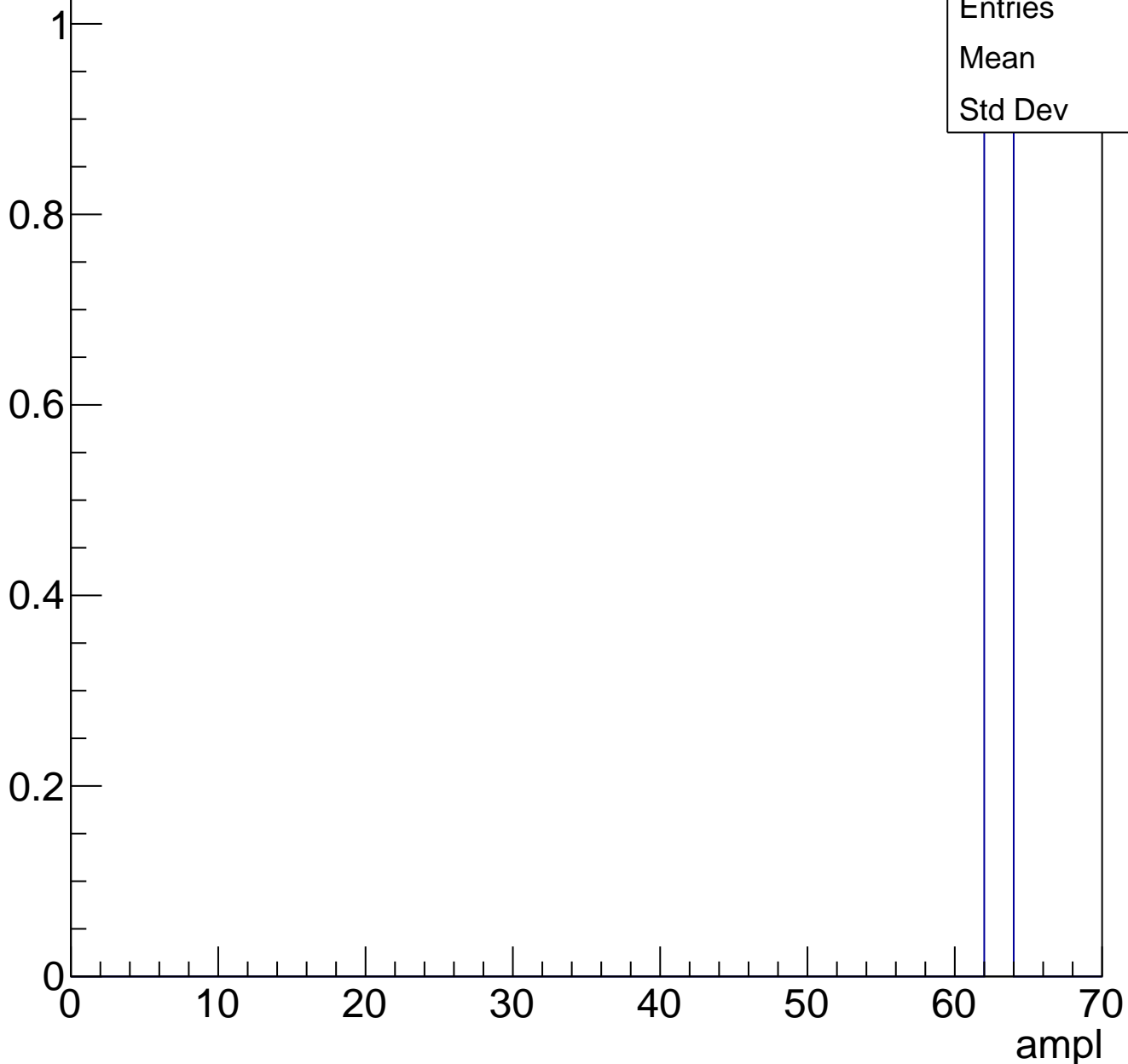
Entries	37
Mean	59.32
Std Dev	10.05



# B0L000S, U10-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch92, adc0

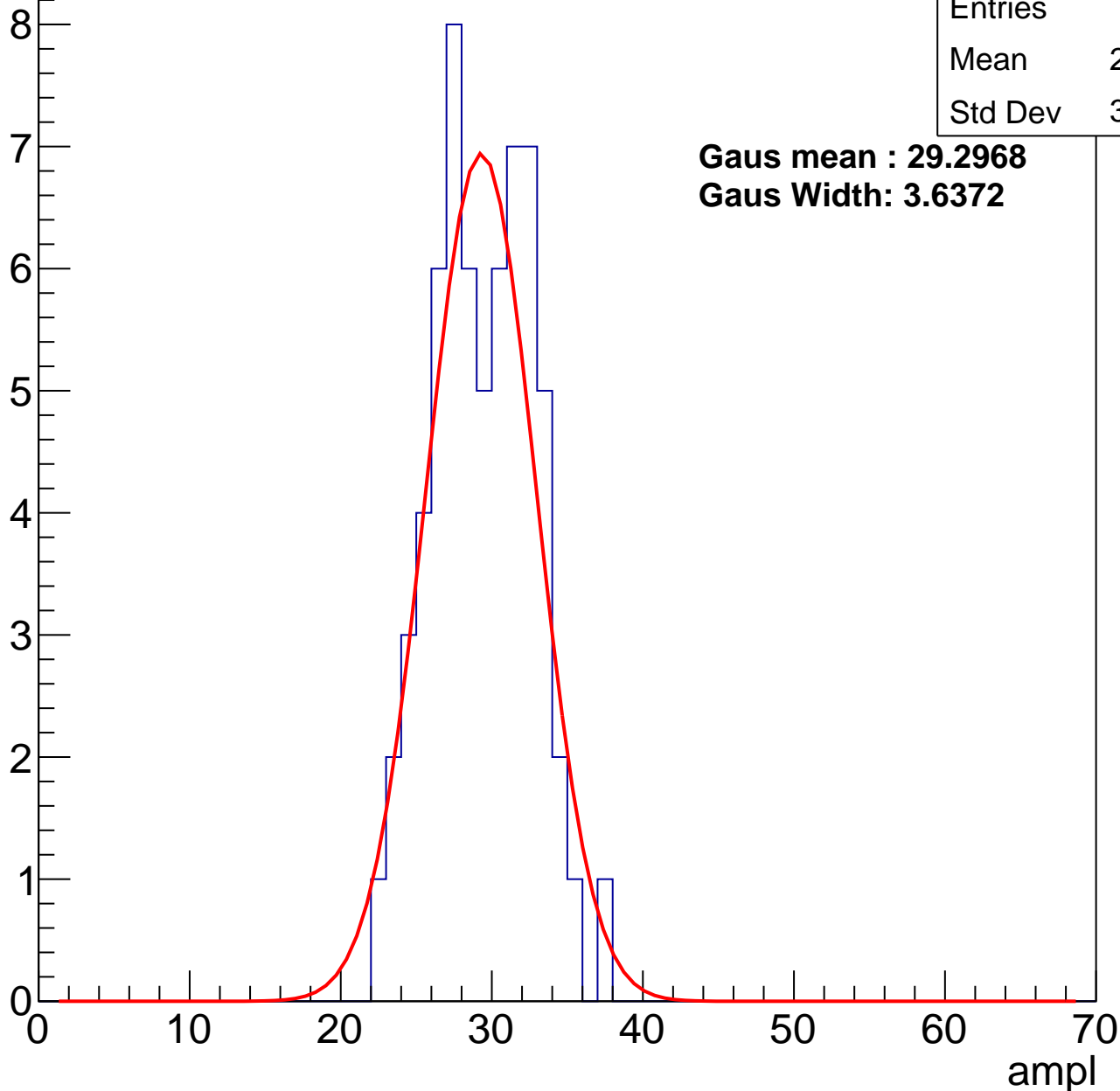
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.92
Std Dev	3.256

**Gaus mean : 29.2968**

**Gaus Width: 3.6372**



# B0L000S, U10-ch92, adc1

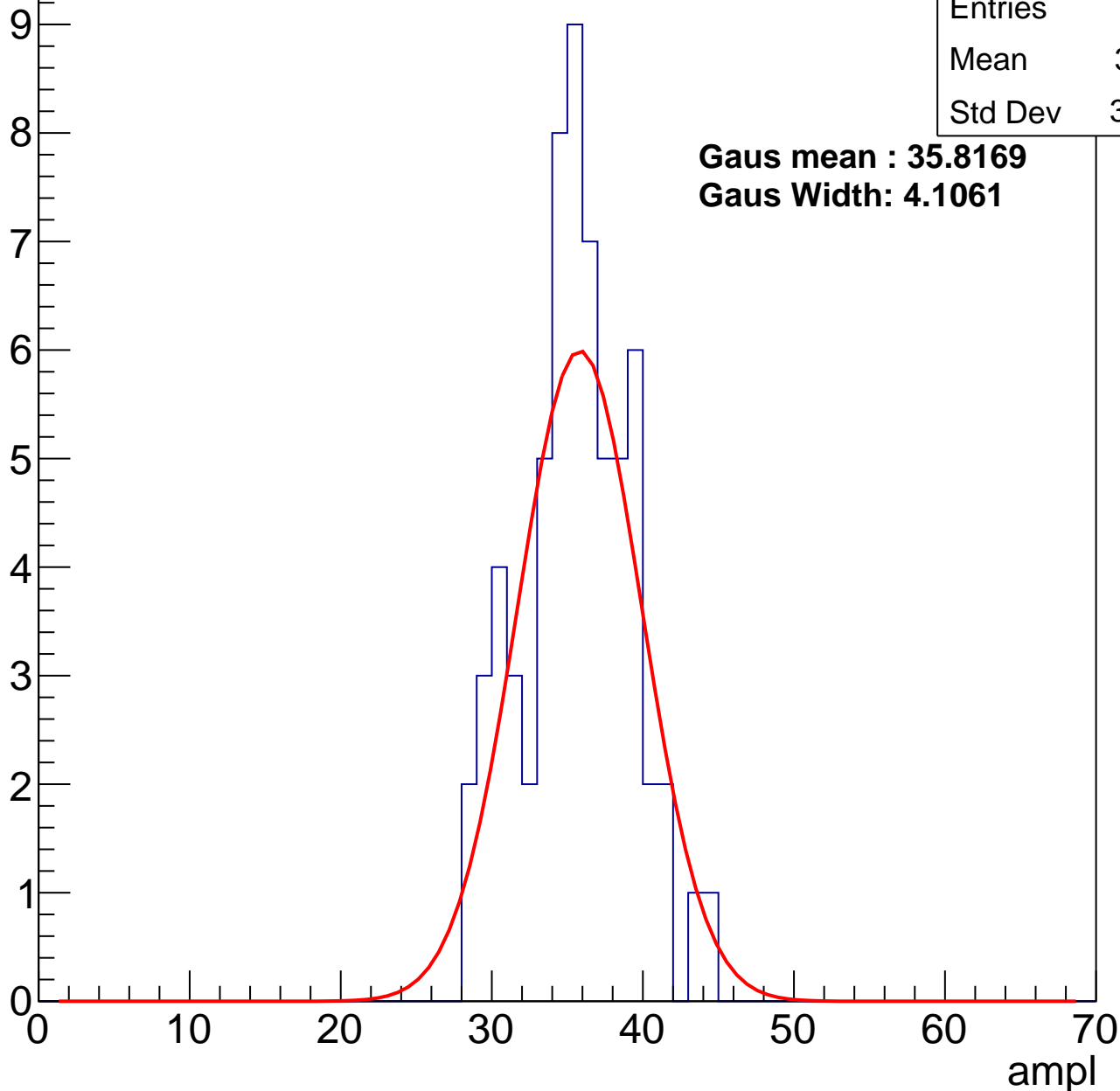
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.11
Std Dev	3.583

**Gaus mean : 35.8169**

**Gaus Width: 4.1061**



# B0L000S, U10-ch92, adc2

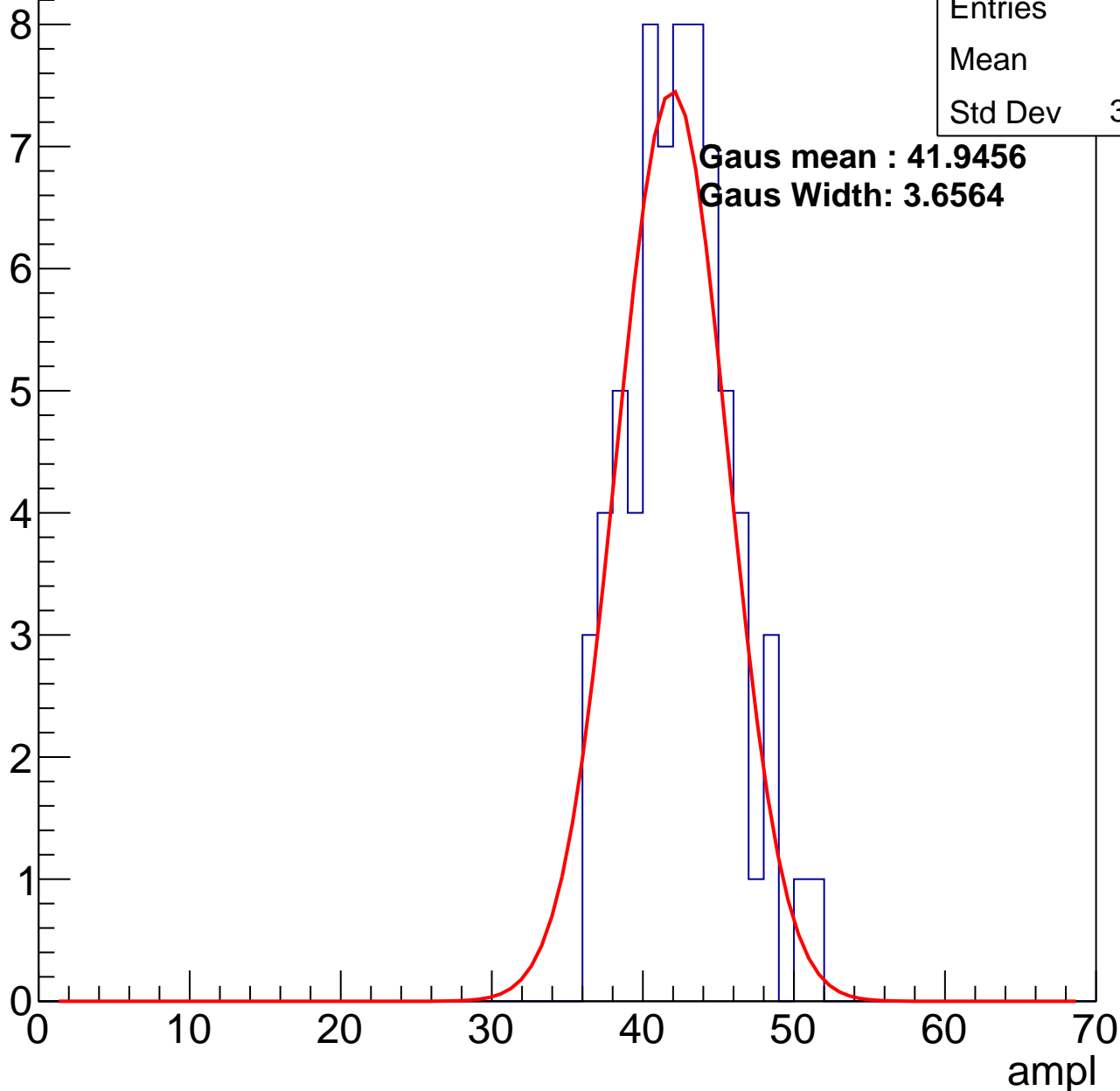
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42
Std Dev	3.375

**Gaus mean : 41.9456**

**Gaus Width: 3.6564**



# B0L000S, U10-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	58
Mean	48.09
Std Dev	2.92

Entry

10

8

6

4

2

0

0

10

20

30

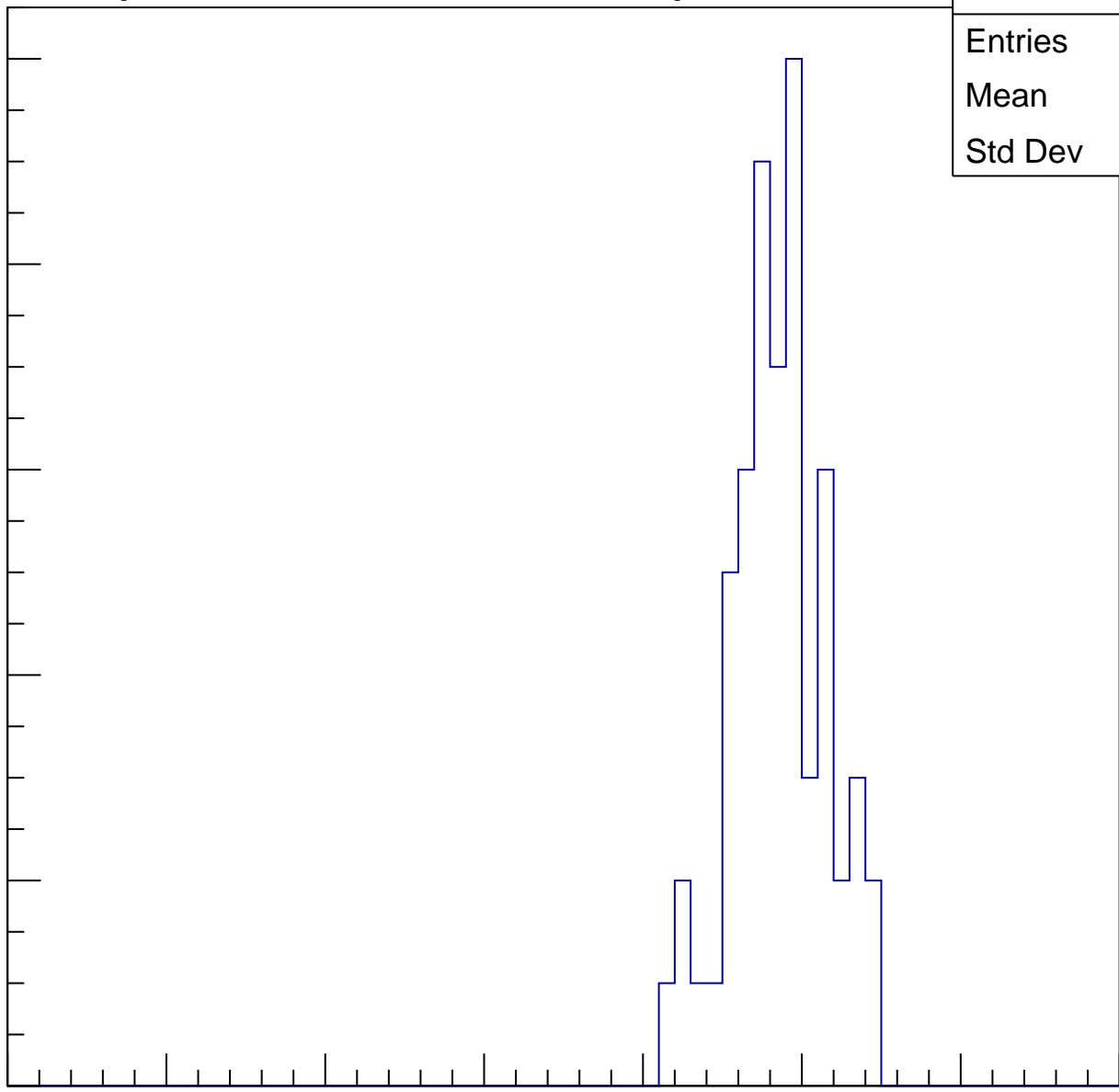
40

50

60

70

ampl

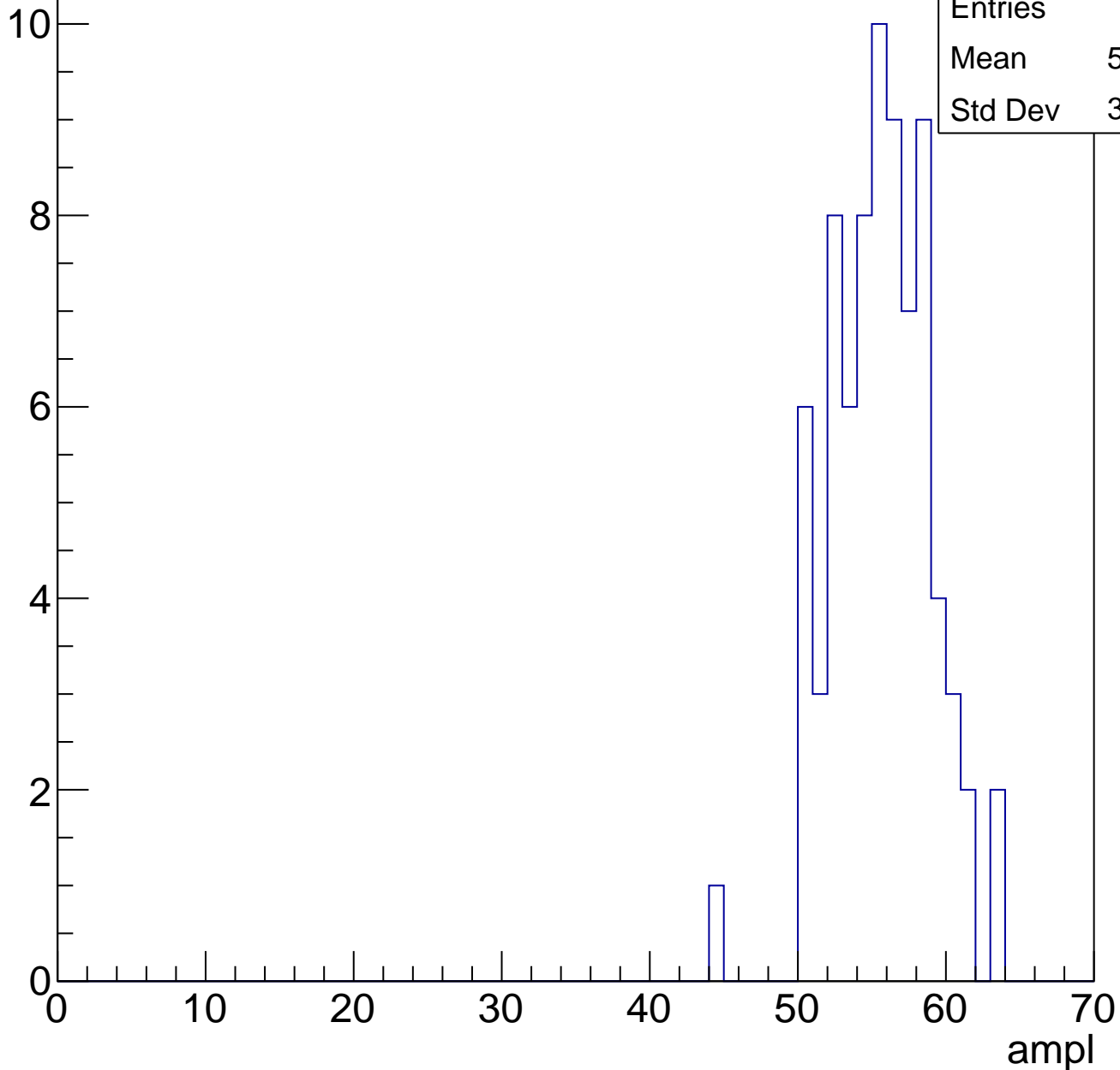


# B0L000S, U10-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	55.15
Std Dev	3.355

Entry

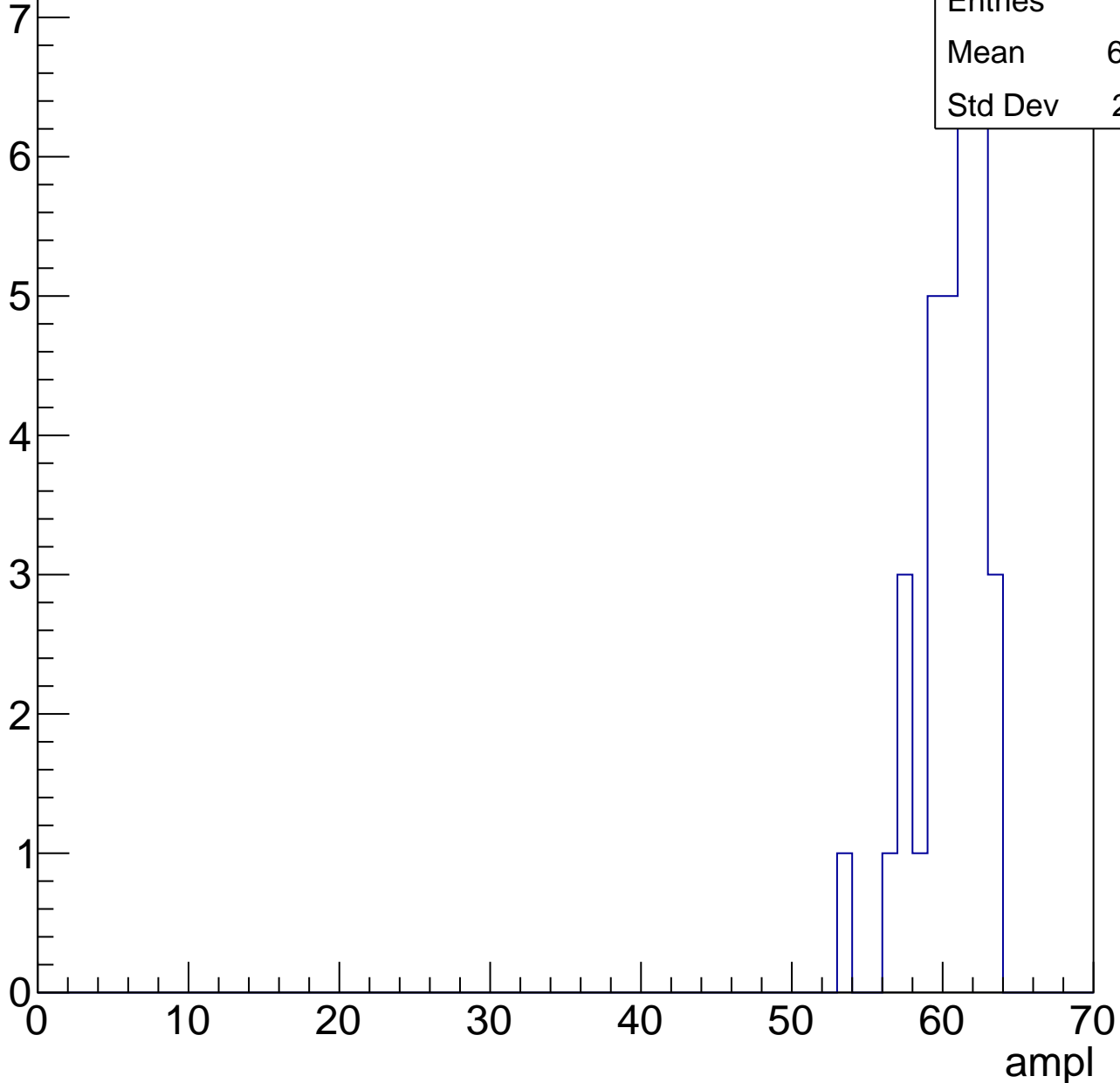


# B0L000S, U10-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	60.09
Std Dev	2.221

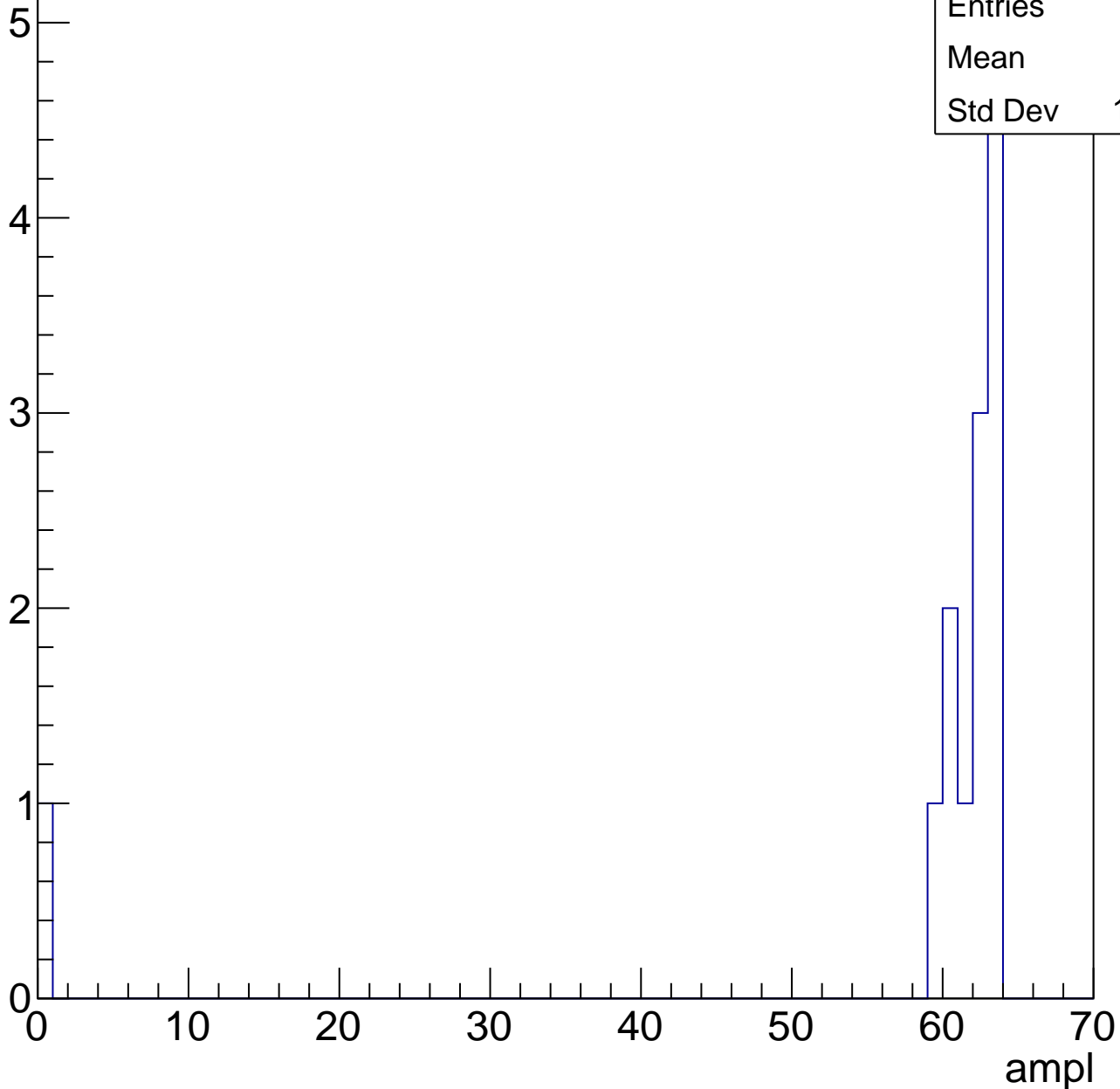


# B0L000S, U10-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	57
Std Dev	16.51





# B0L000S, U10-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch93, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	29.59
Std Dev	3.681

**Gaus mean : 29.9874**

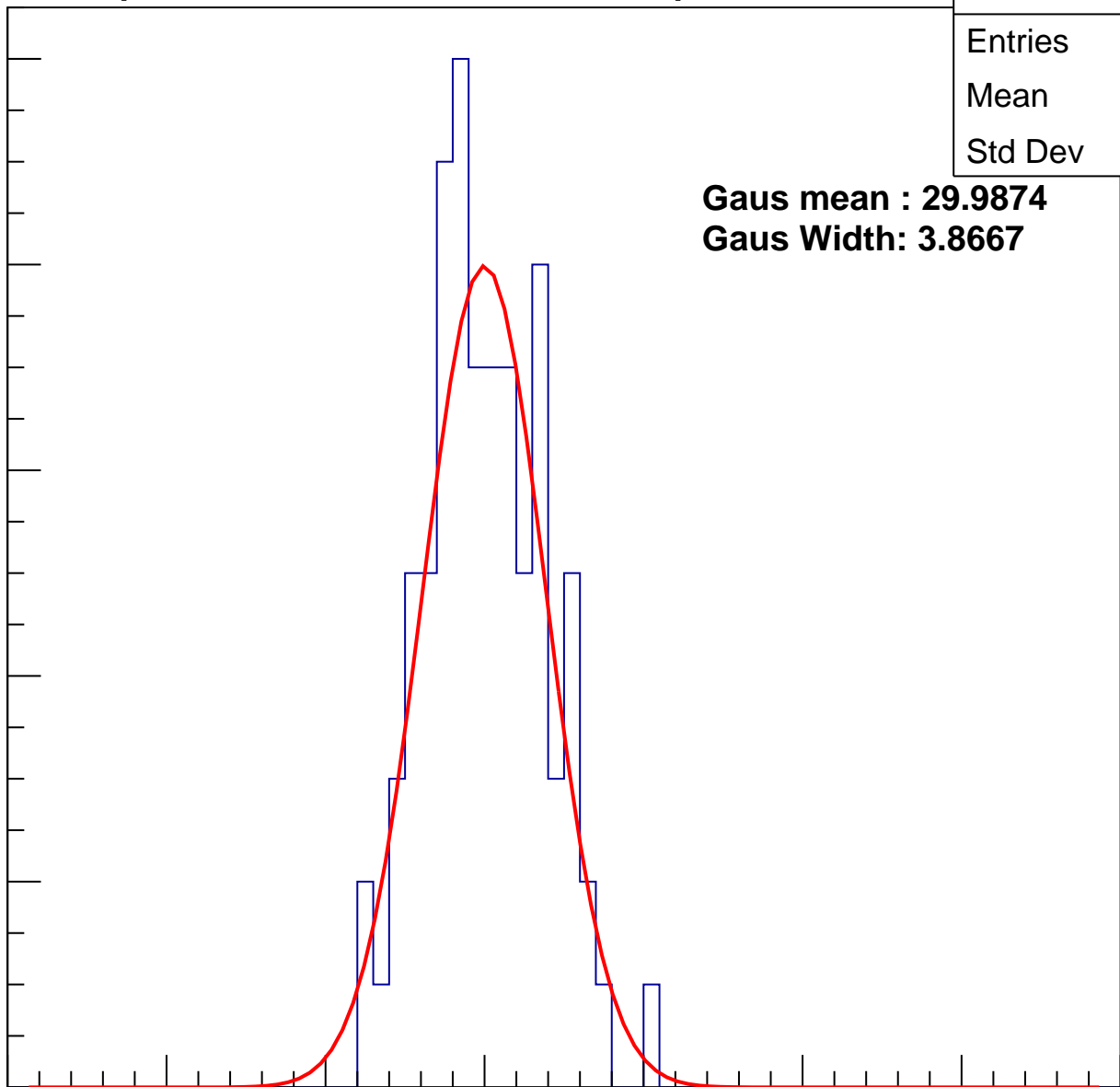
**Gaus Width: 3.8667**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



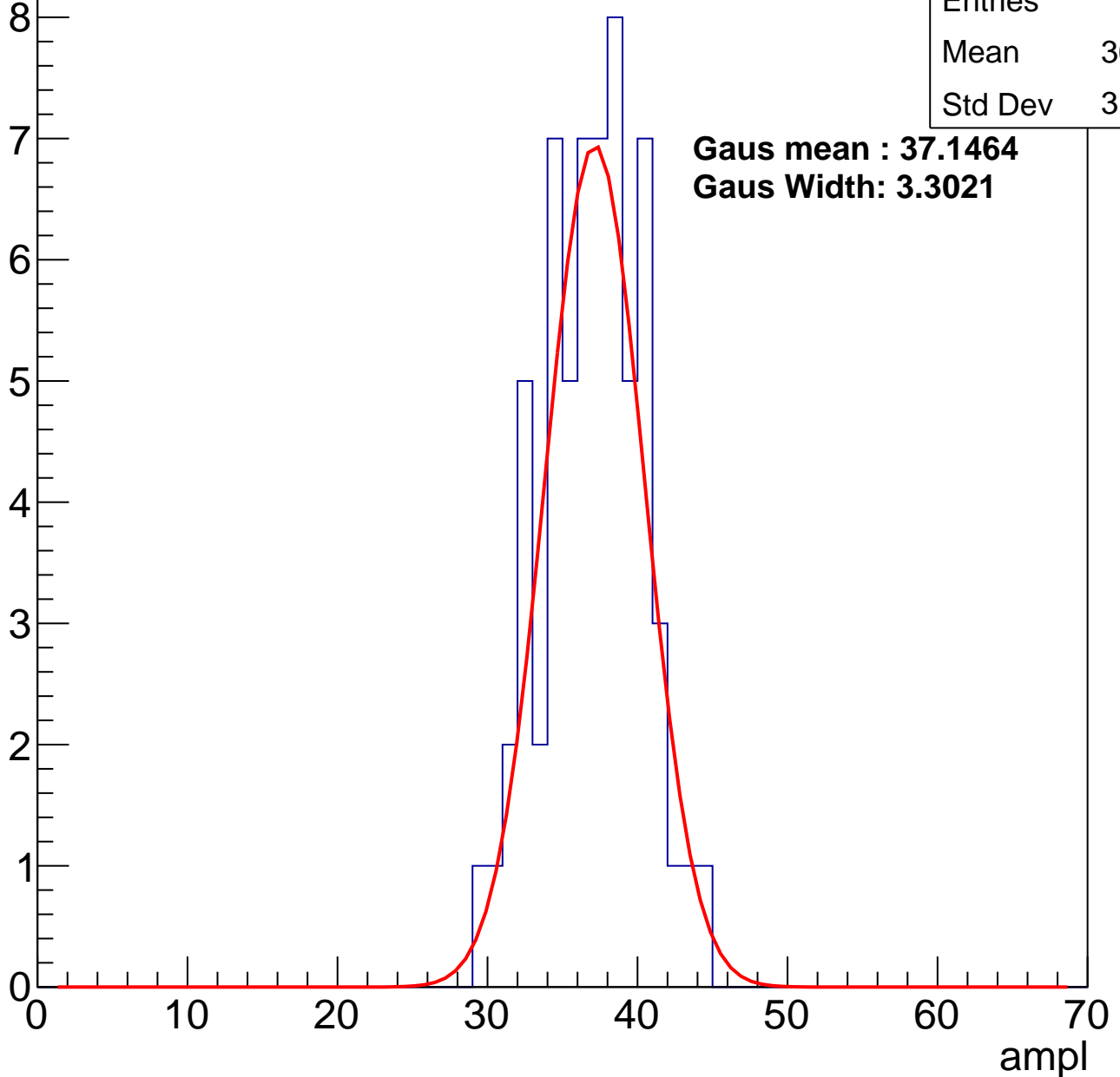
# B0L000S, U10-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.54
Std Dev	3.255

**Gaus mean : 37.1464**  
**Gaus Width: 3.3021**



# B0L000S, U10-ch93, adc2

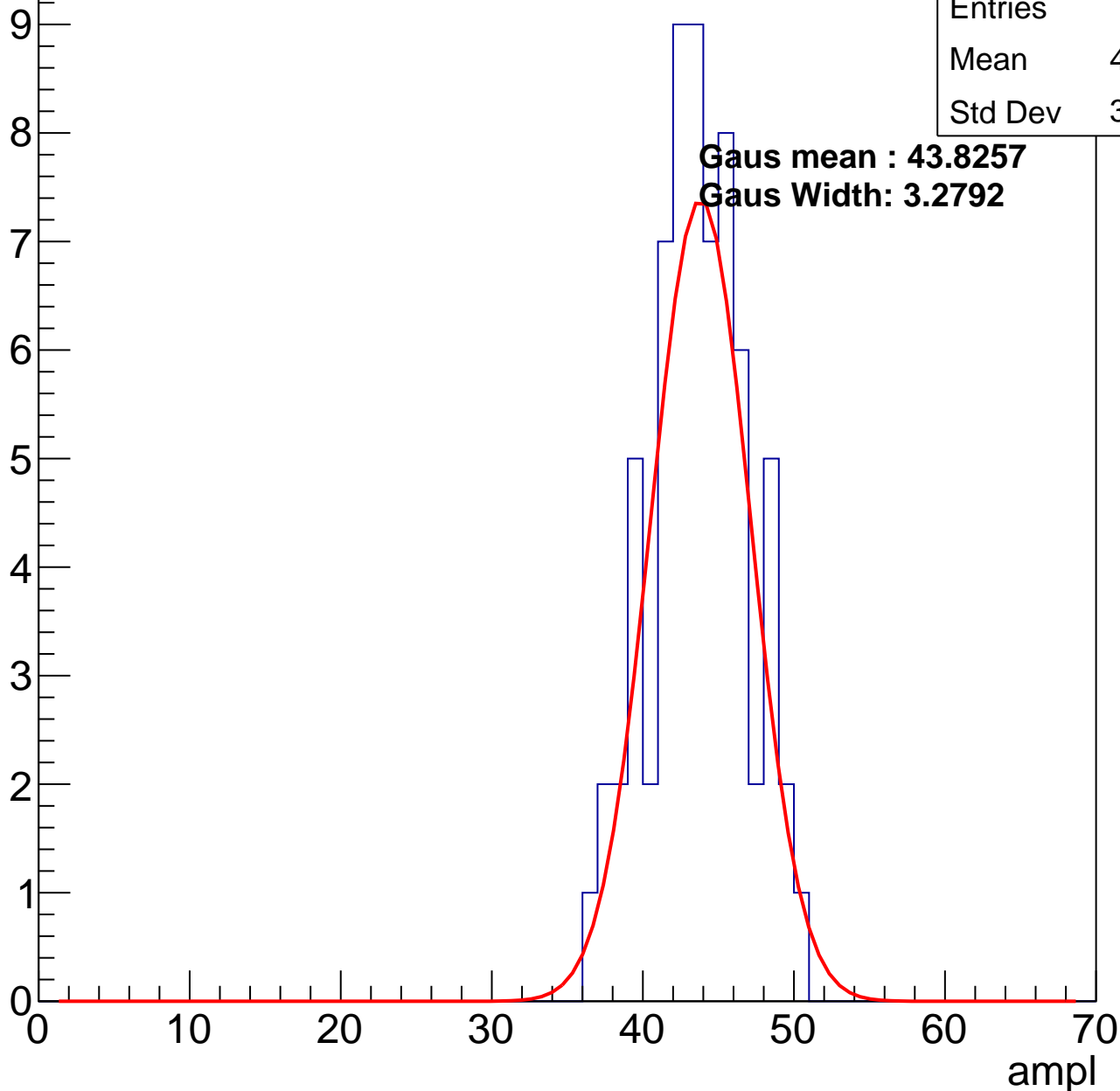
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.22
Std Dev	3.148

**Gaus mean : 43.8257**

**Gaus Width: 3.2792**

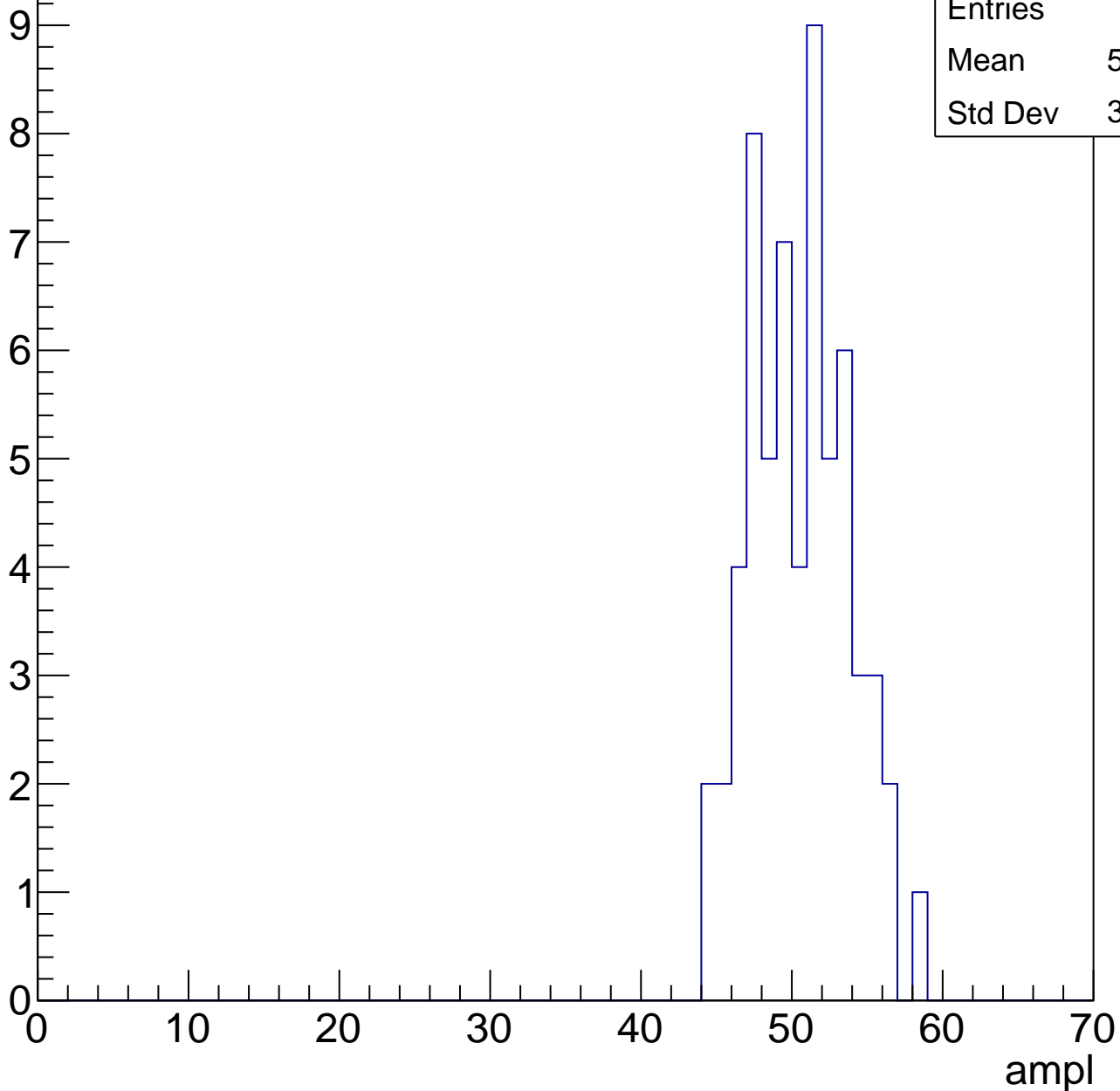


# B0L000S, U10-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

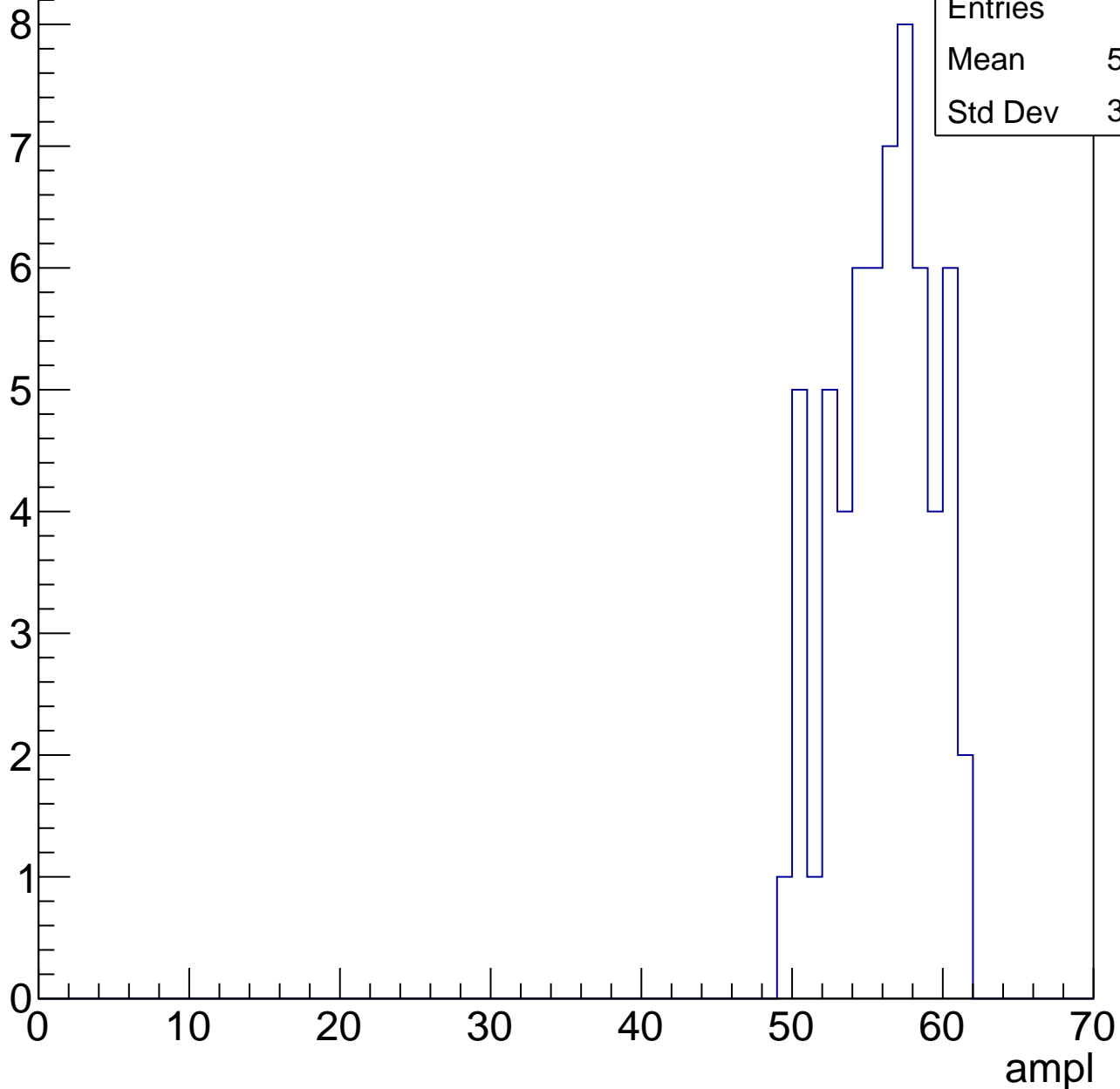
Entries	61
Mean	50.08
Std Dev	3.205



# B0L000S, U10-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



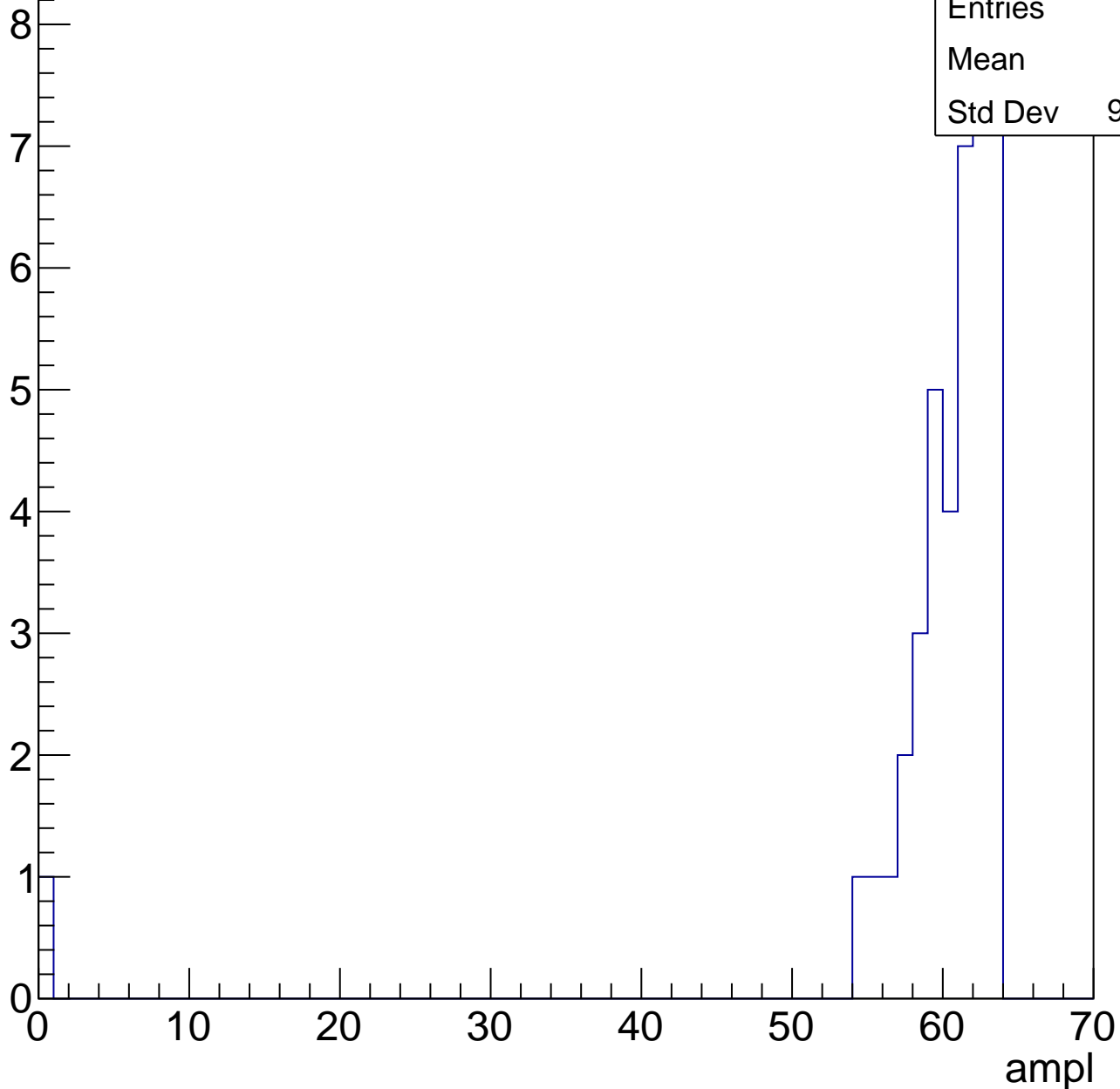
Entries	61
Mean	55.57
Std Dev	3.144

# B0L000S, U10-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

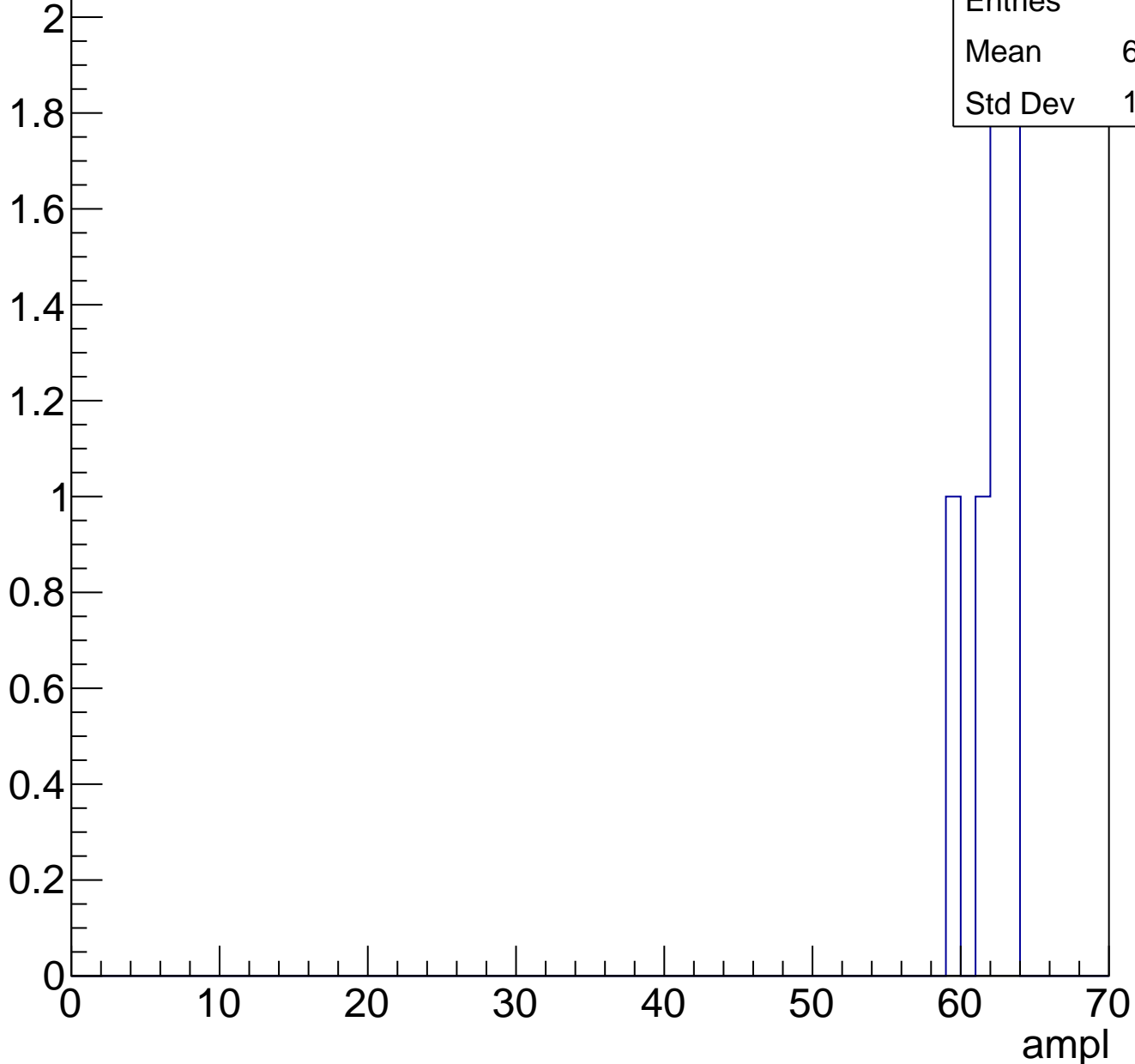
Entries	41
Mean	58.9
Std Dev	9.594



# B0L000S, U10-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch94, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.57
Std Dev	4.817

**Gaus mean : 30.5135**

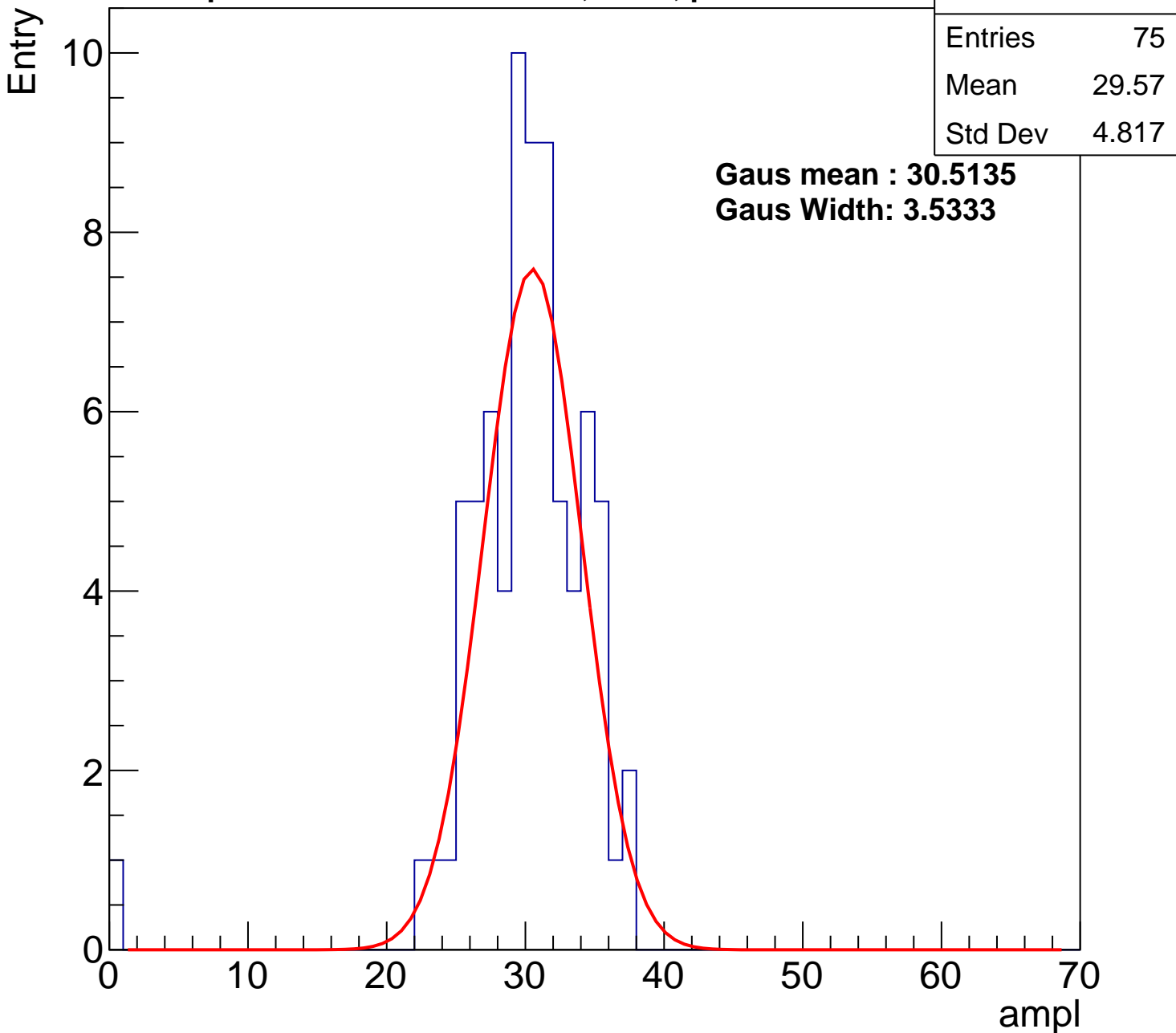
**Gaus Width: 3.5333**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch94, adc1

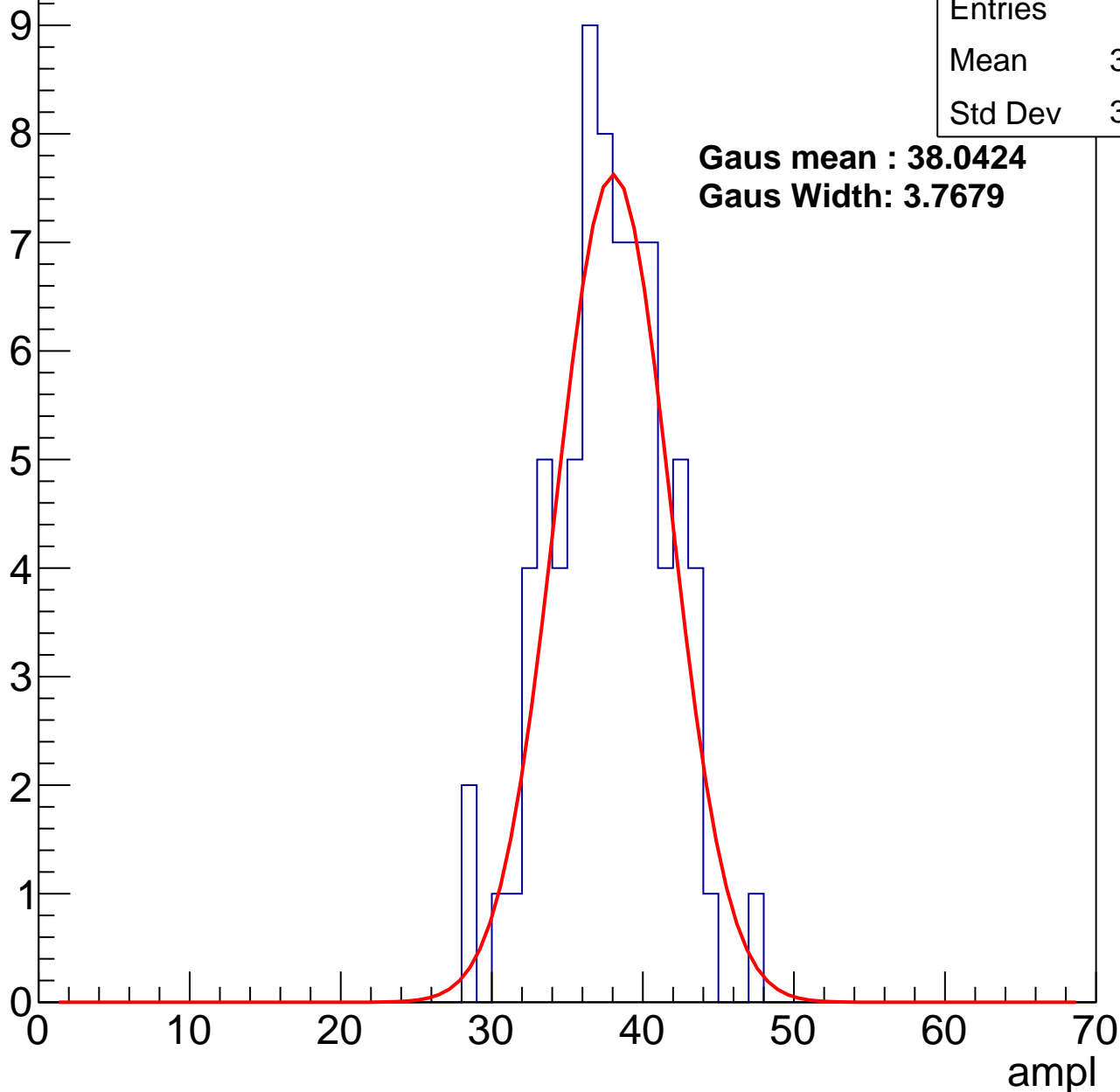
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.29
Std Dev	3.769

**Gaus mean : 38.0424**

**Gaus Width: 3.7679**



# B0L000S, U10-ch94, adc2

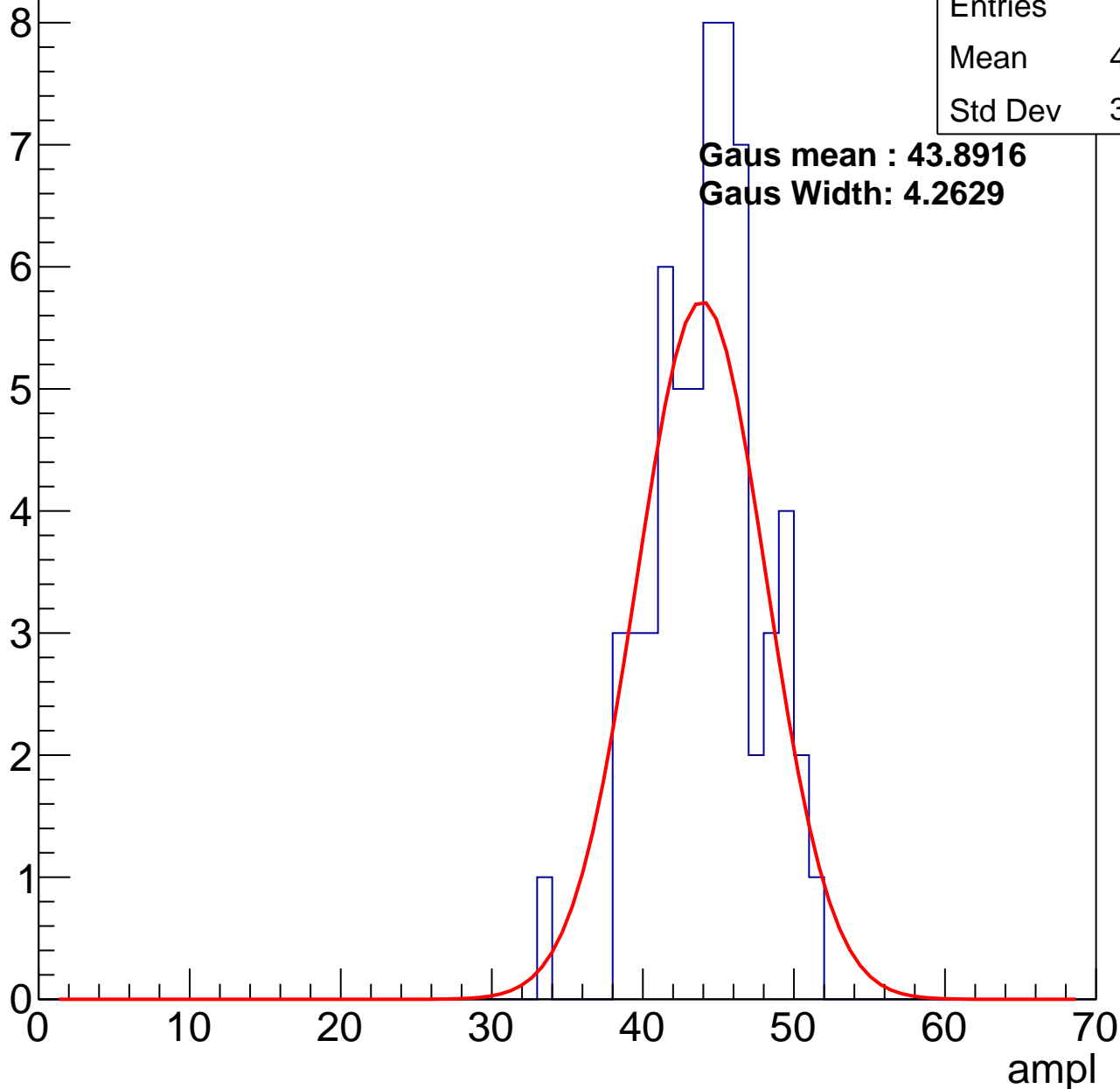
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.84
Std Dev	3.512

**Gaus mean : 43.8916**

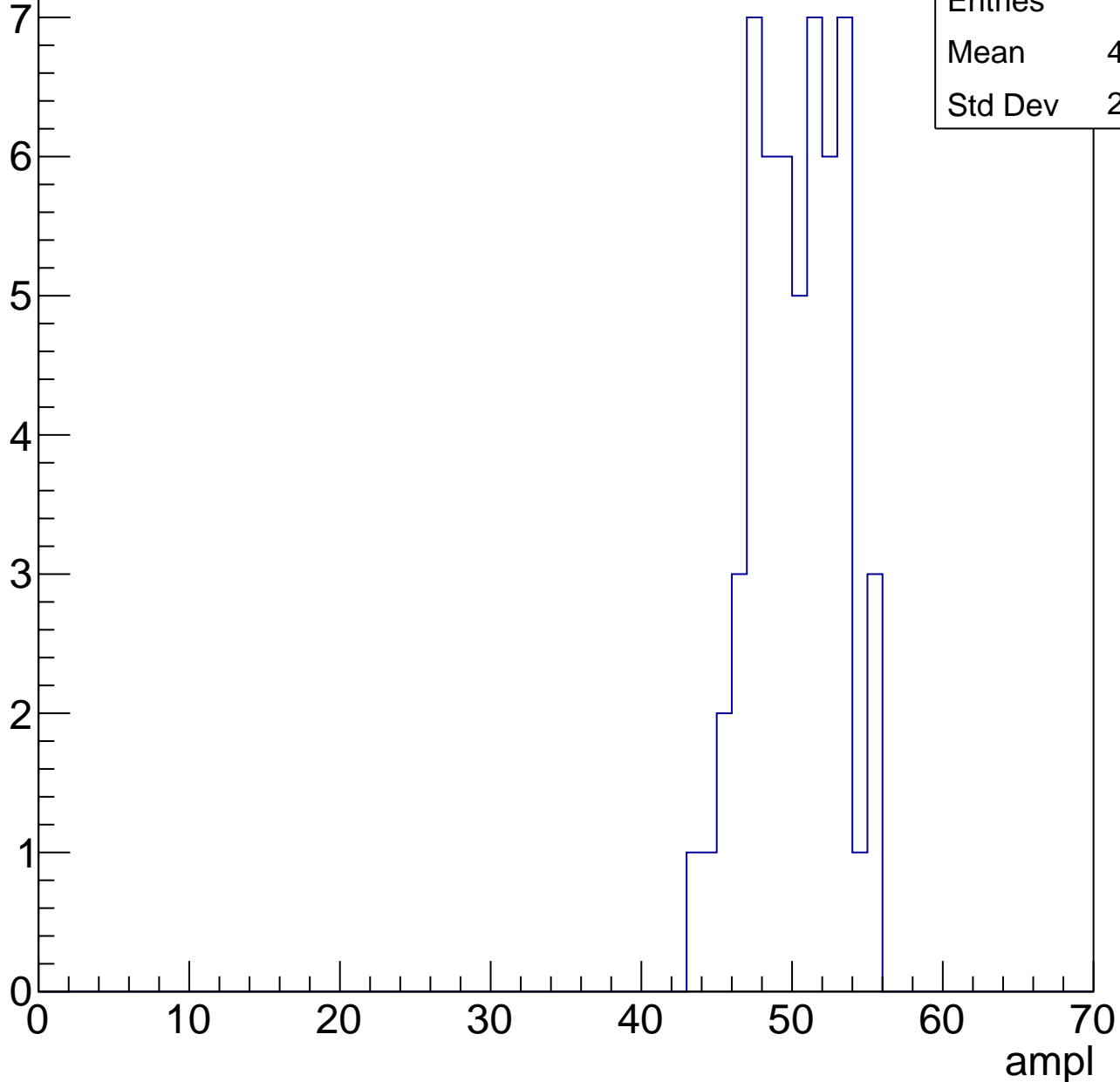
**Gaus Width: 4.2629**



# B0L000S, U10-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

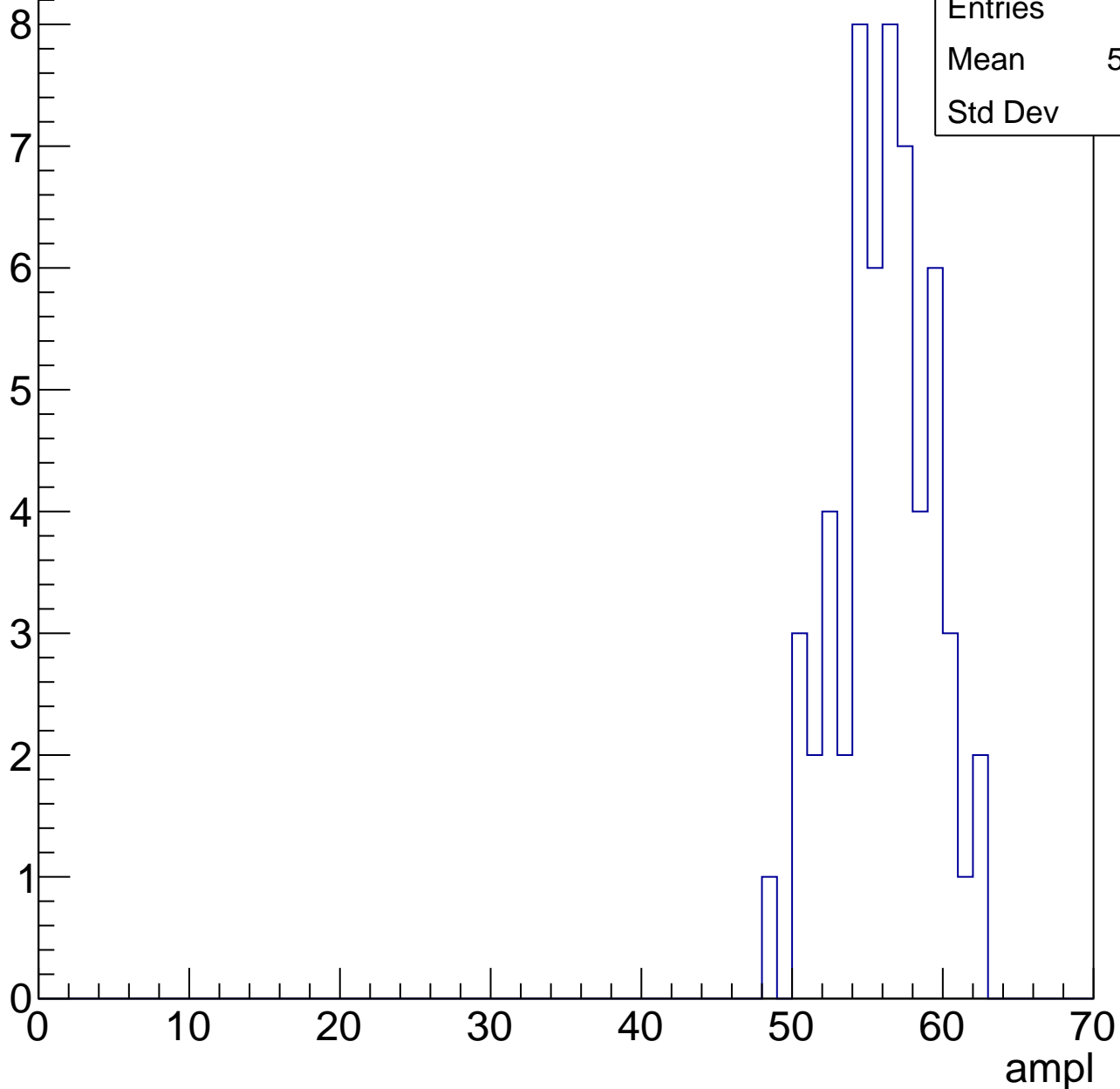


# B0L000S, U10-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

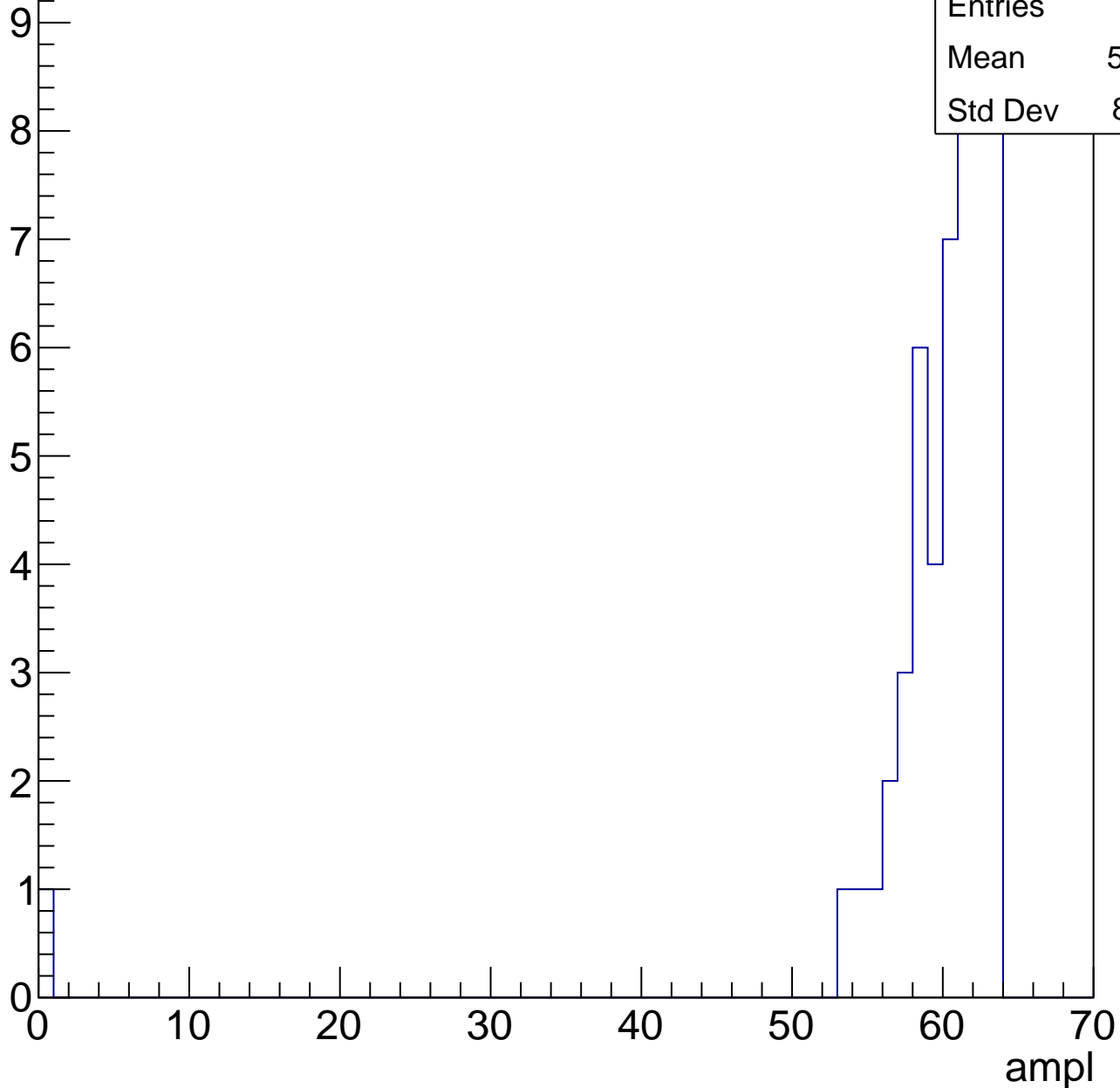
Entries	57
Mean	55.68
Std Dev	3.13



# B0L000S, U10-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L000S, U10-ch95, adc0

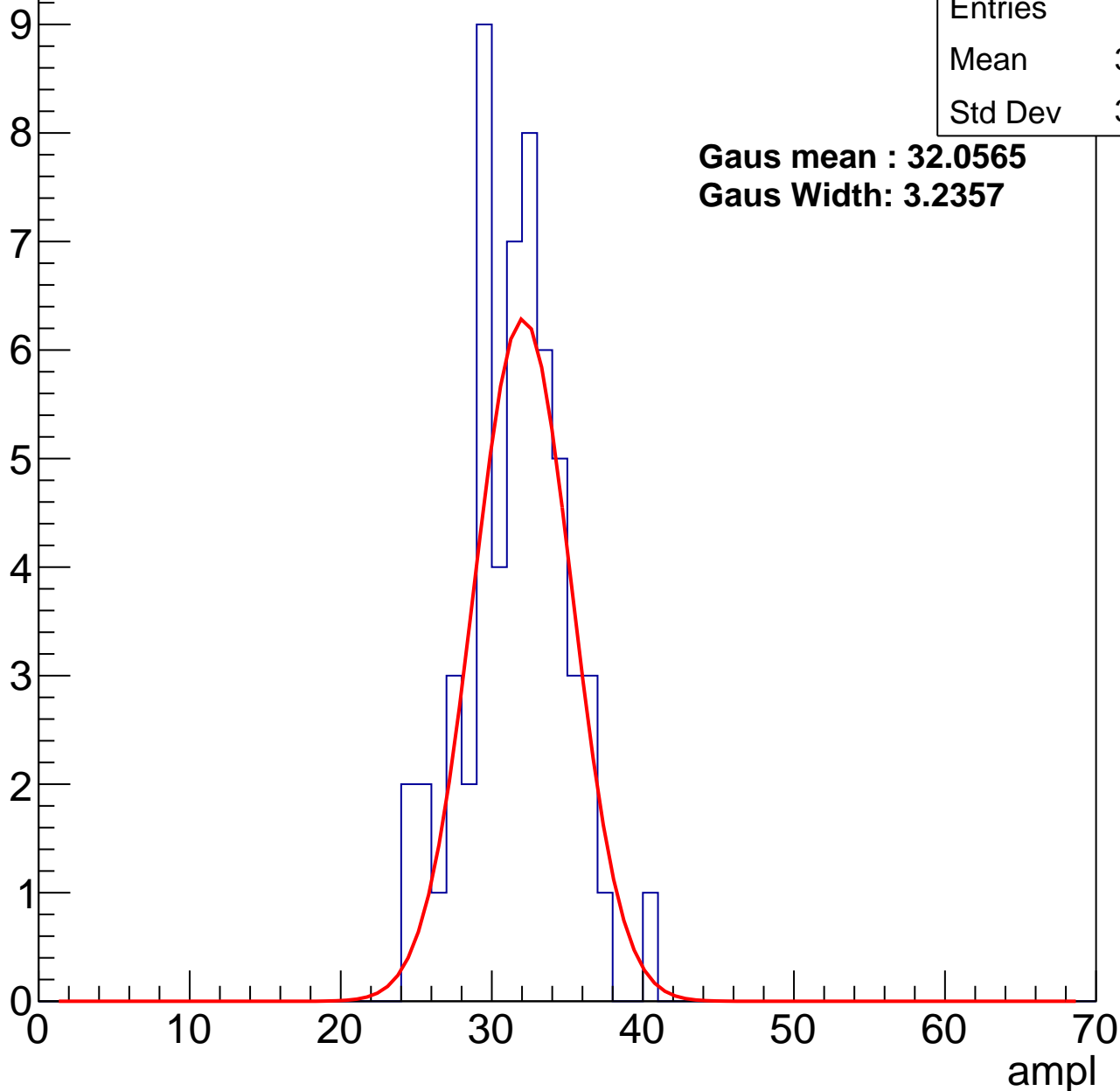
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	31.11
Std Dev	3.291

**Gaus mean : 32.0565**

**Gaus Width: 3.2357**



# B0L000S, U10-ch95, adc1

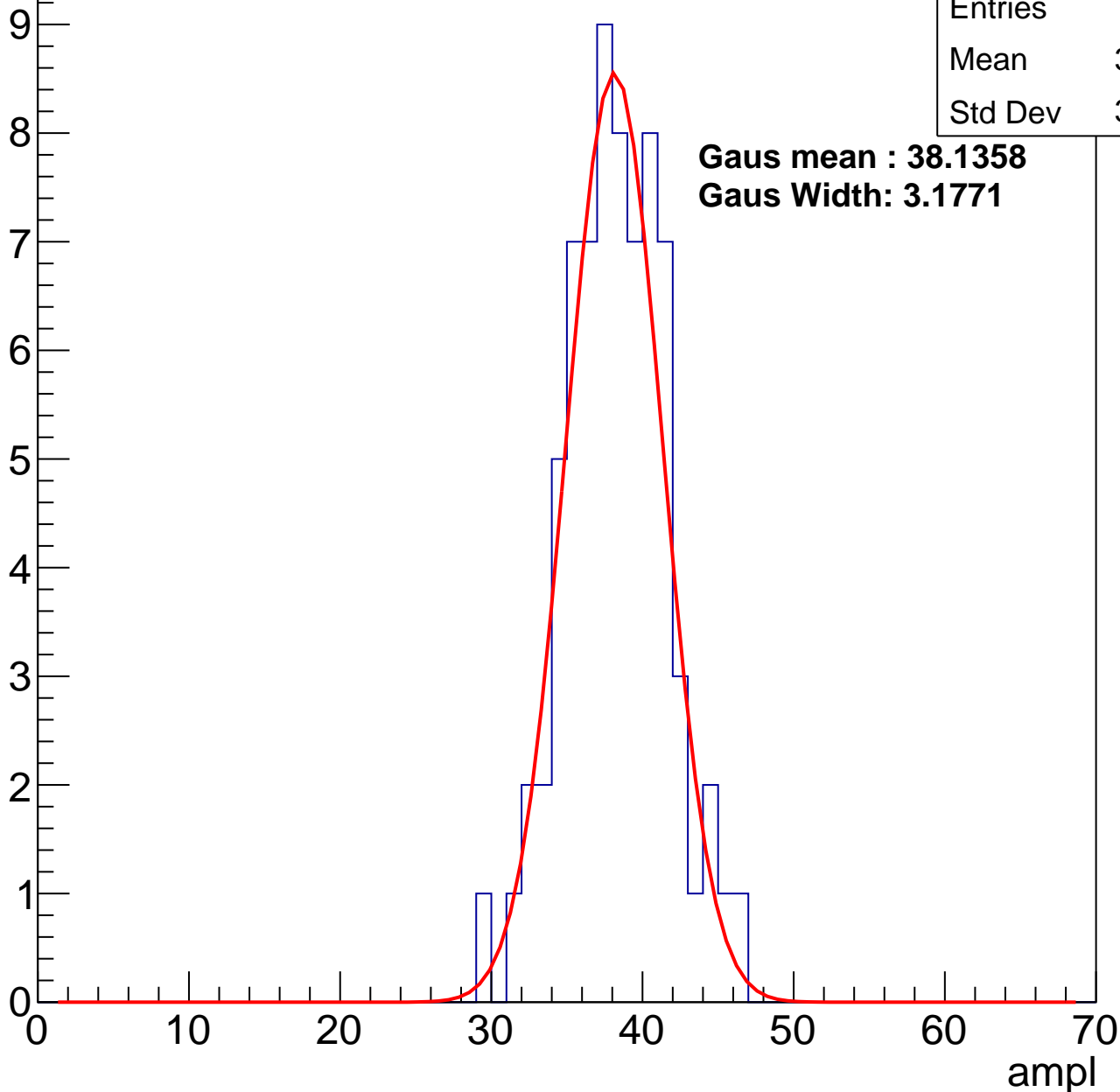
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	37.81
Std Dev	3.311

**Gaus mean : 38.1358**

**Gaus Width: 3.1771**



# B0L000S, U10-ch95, adc2

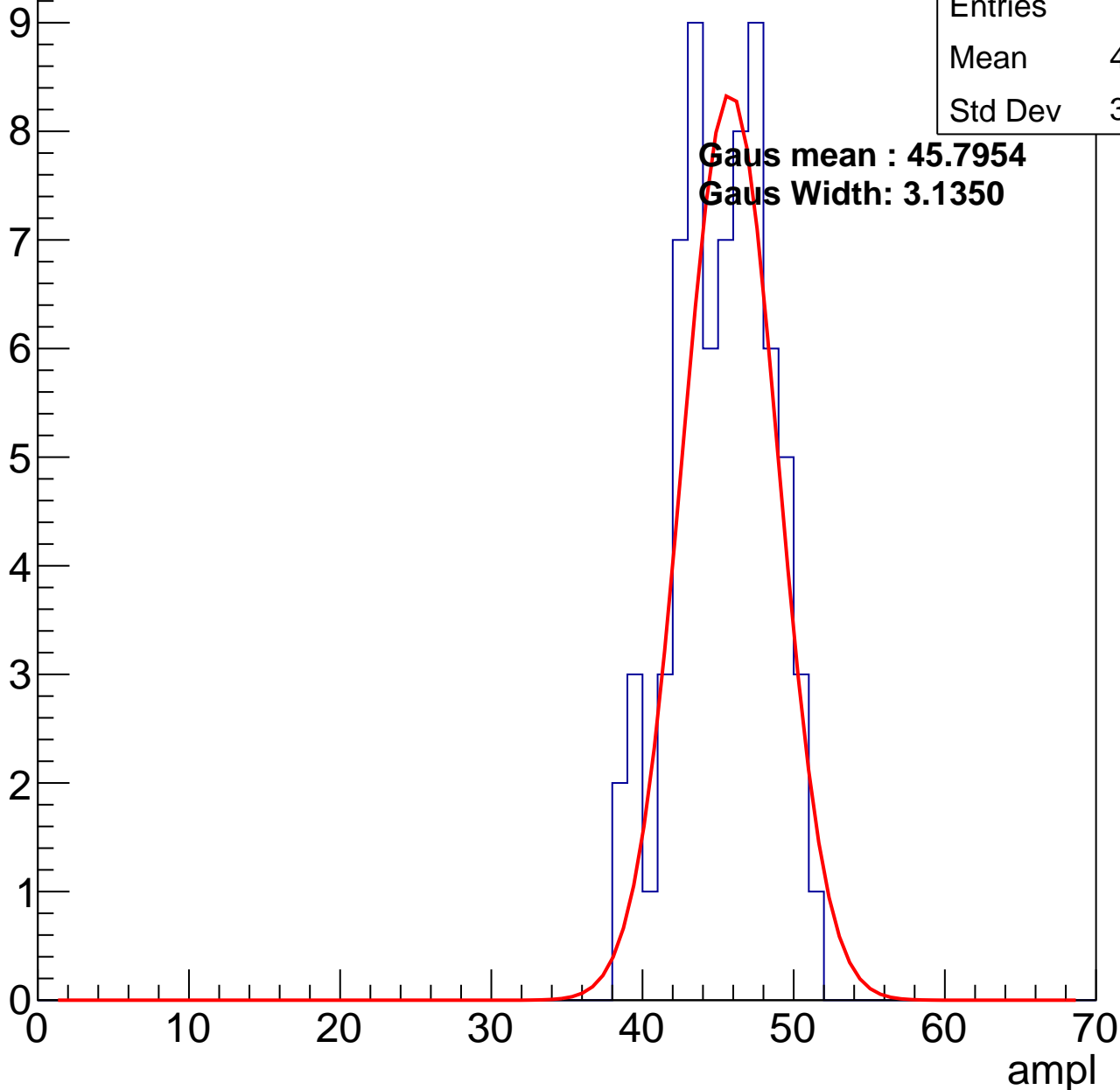
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	44.87
Std Dev	3.098

**Gaus mean : 45.7954**

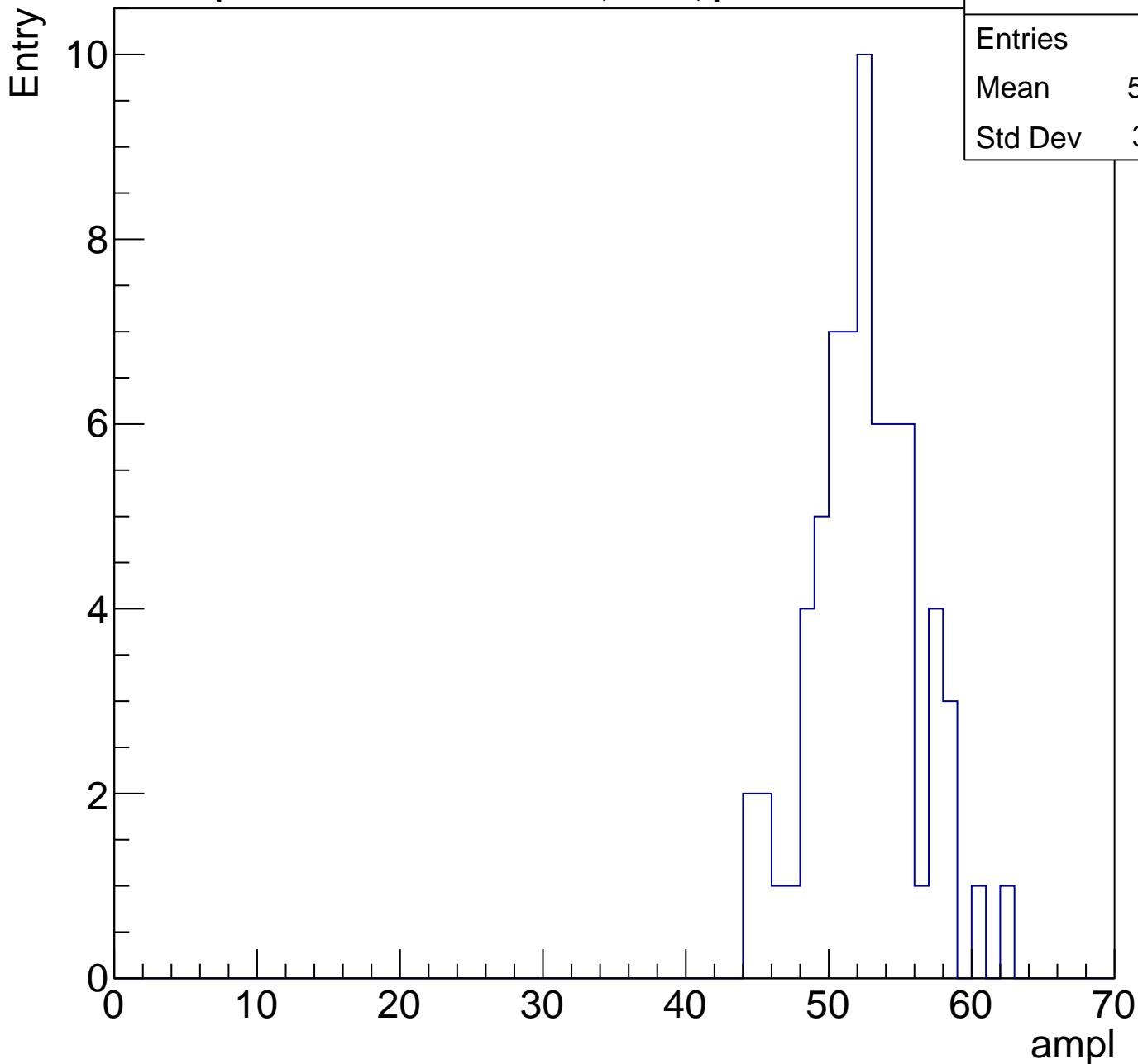
**Gaus Width: 3.1350**



# B0L000S, U10-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	52.04
Std Dev	3.671

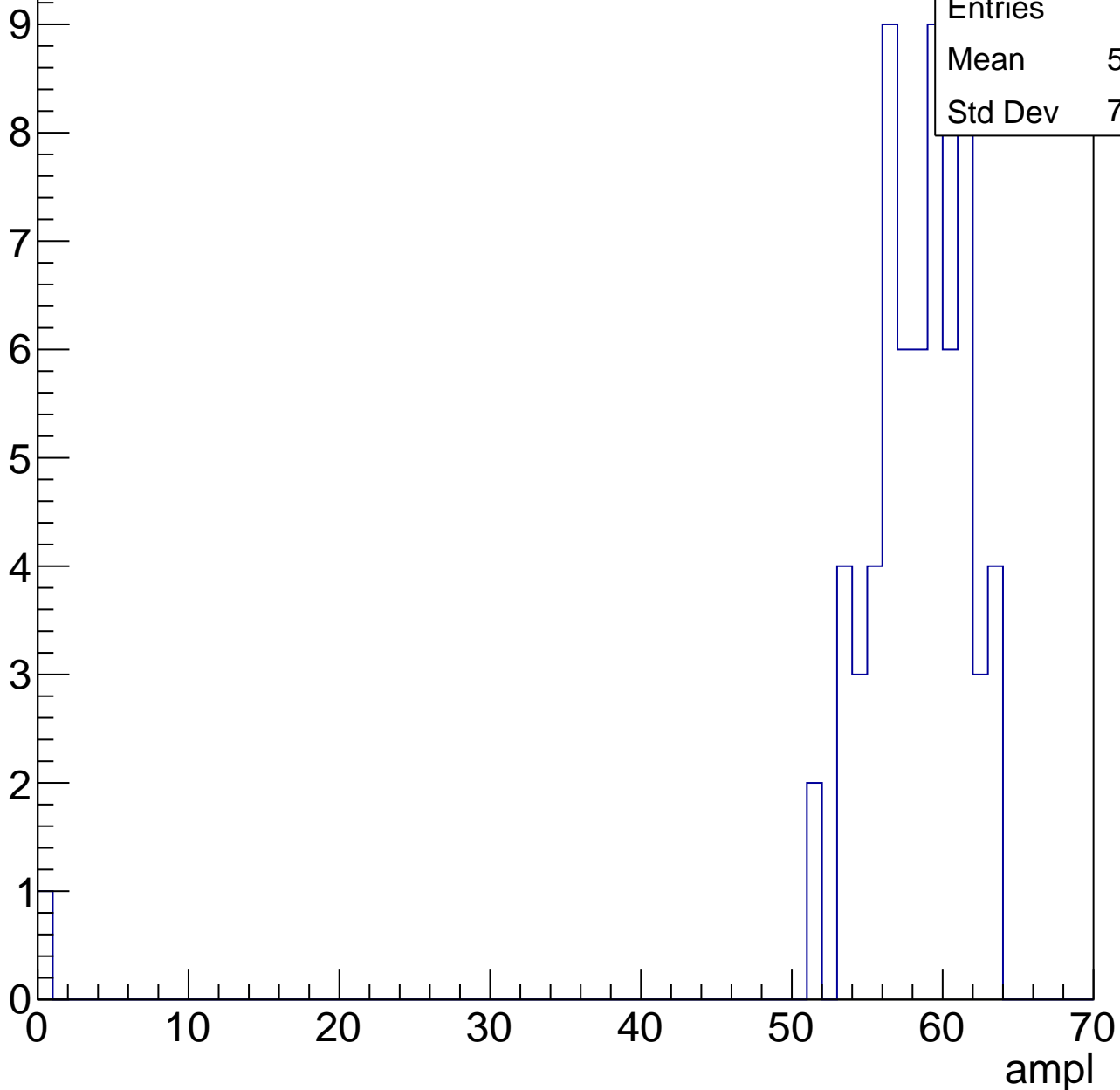


# B0L000S, U10-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	57.03
Std Dev	7.726

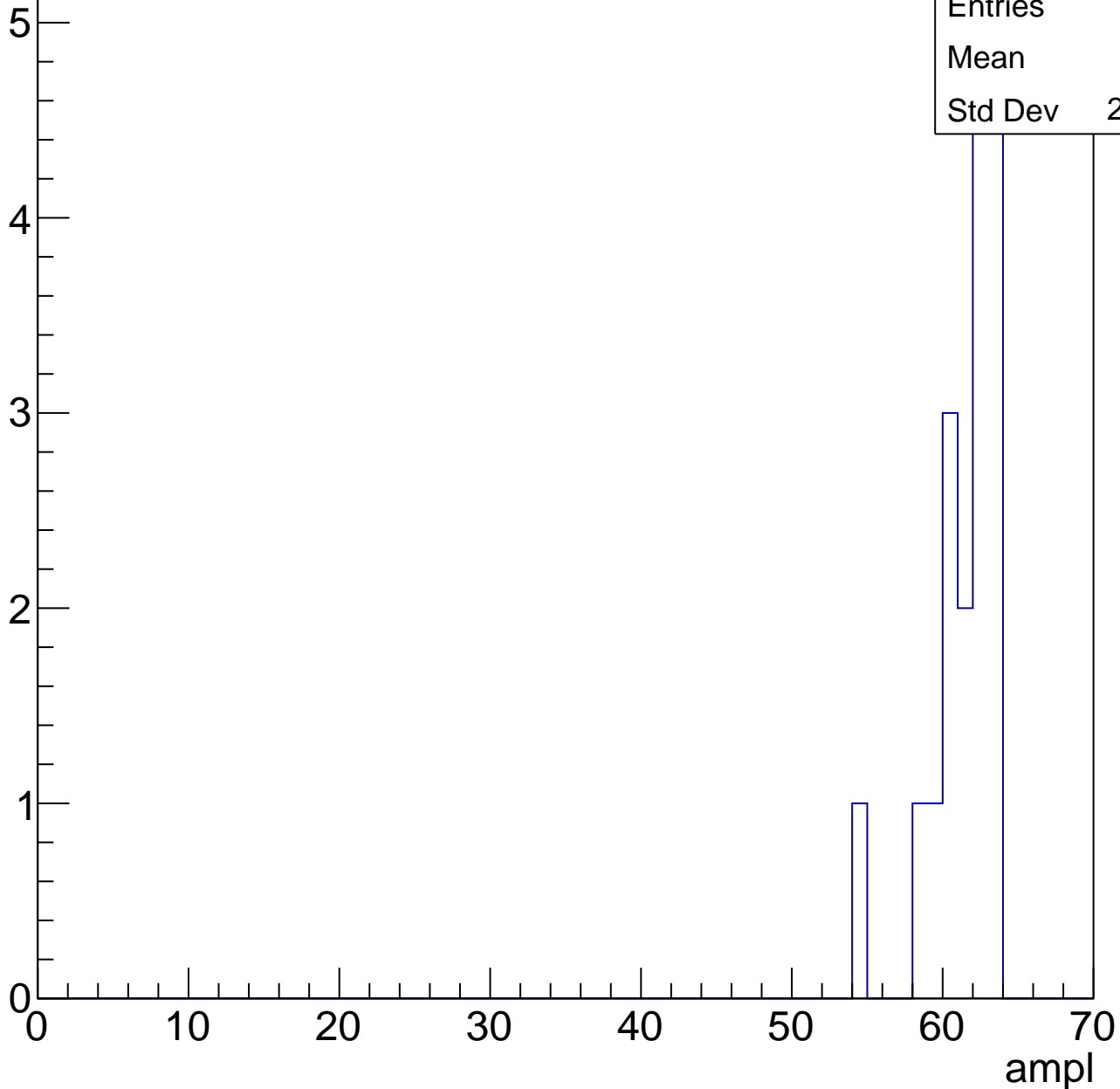


# B0L000S, U10-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	61
Std Dev	2.236



# B0L000S, U10-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch96, adc0

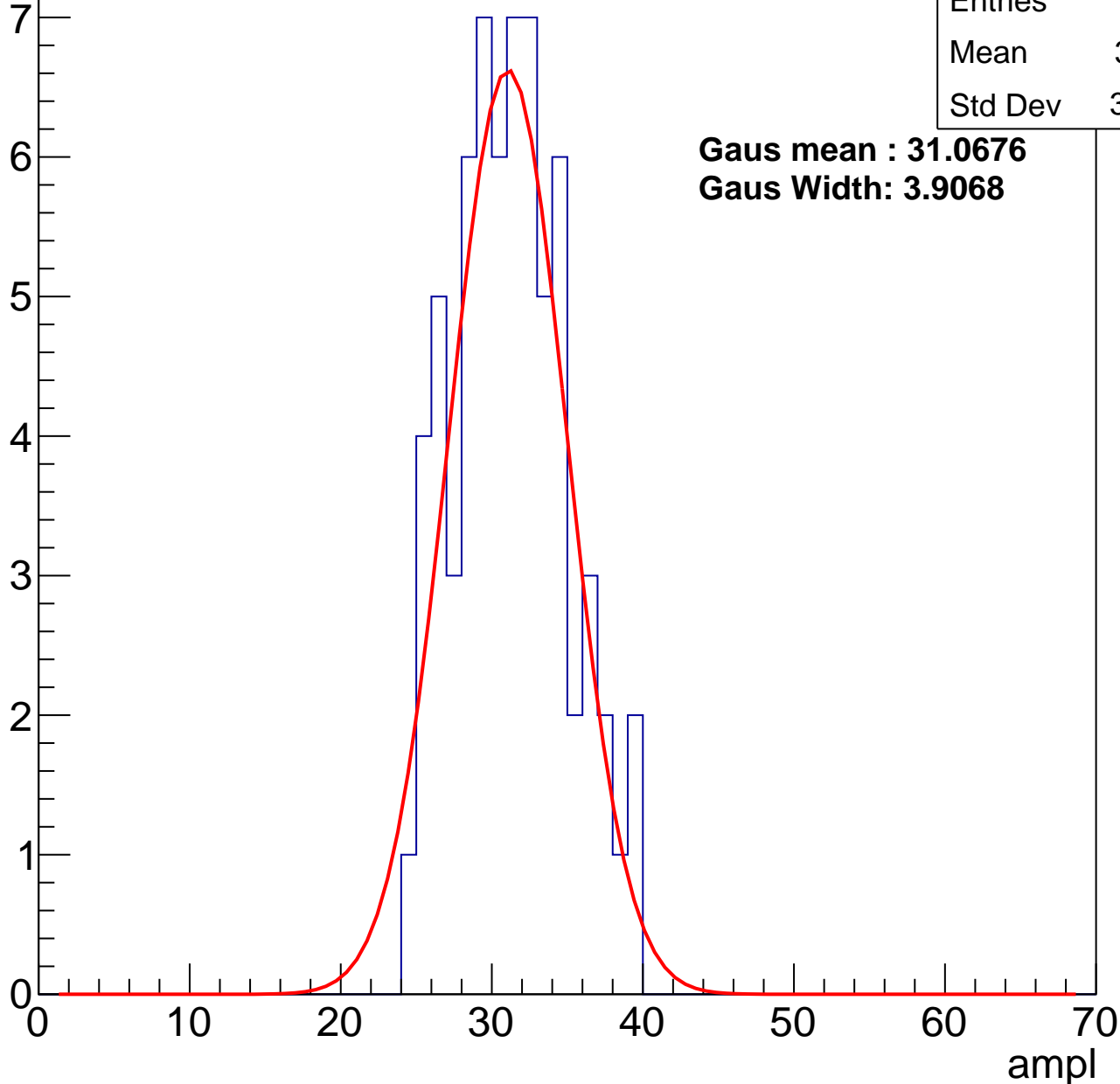
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.81
Std Dev	3.642

**Gaus mean : 31.0676**

**Gaus Width: 3.9068**



# B0L000S, U10-ch96, adc1

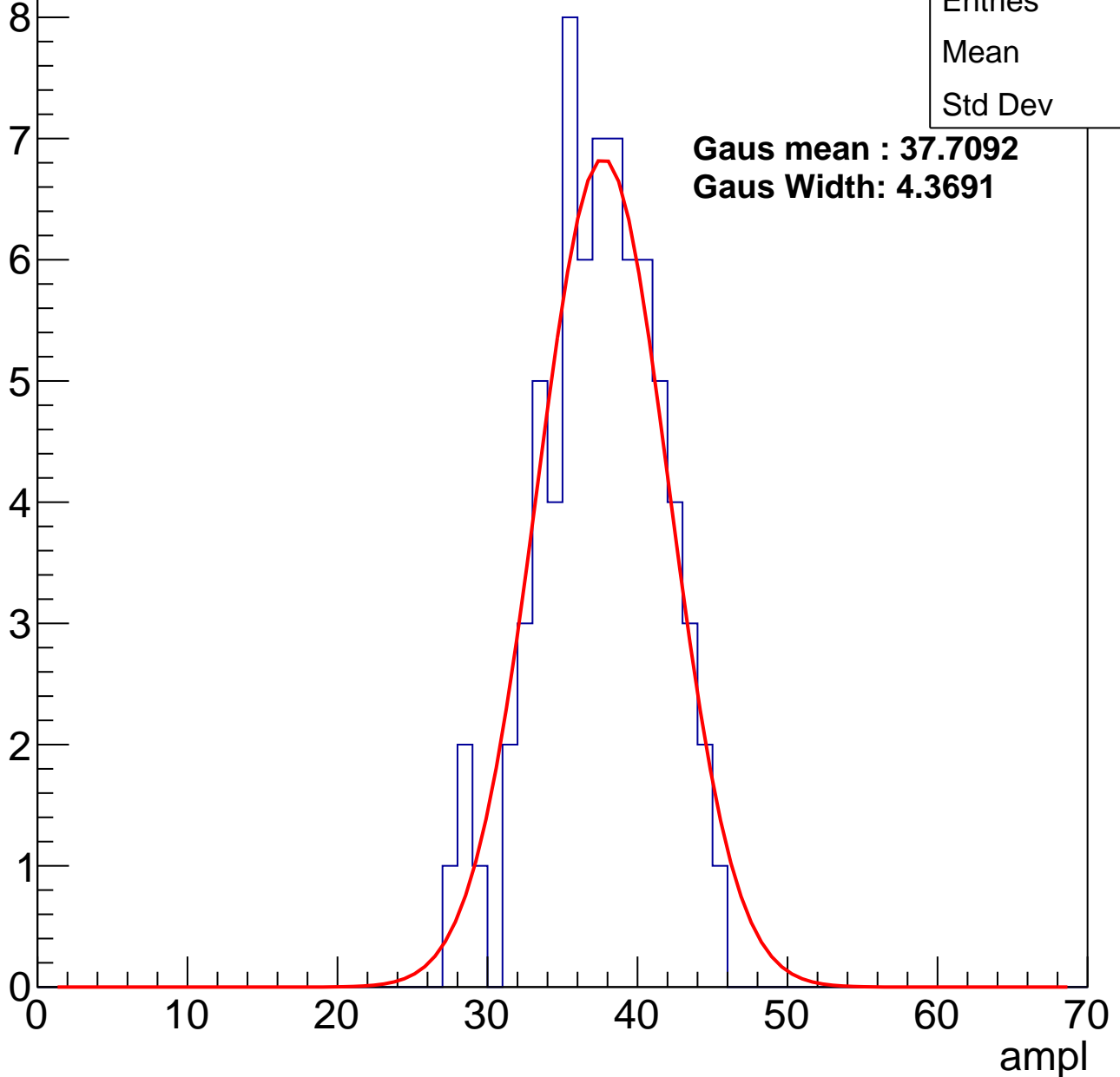
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	37
Std Dev	3.99

**Gaus mean : 37.7092**

**Gaus Width: 4.3691**



# B0L000S, U10-ch96, adc2

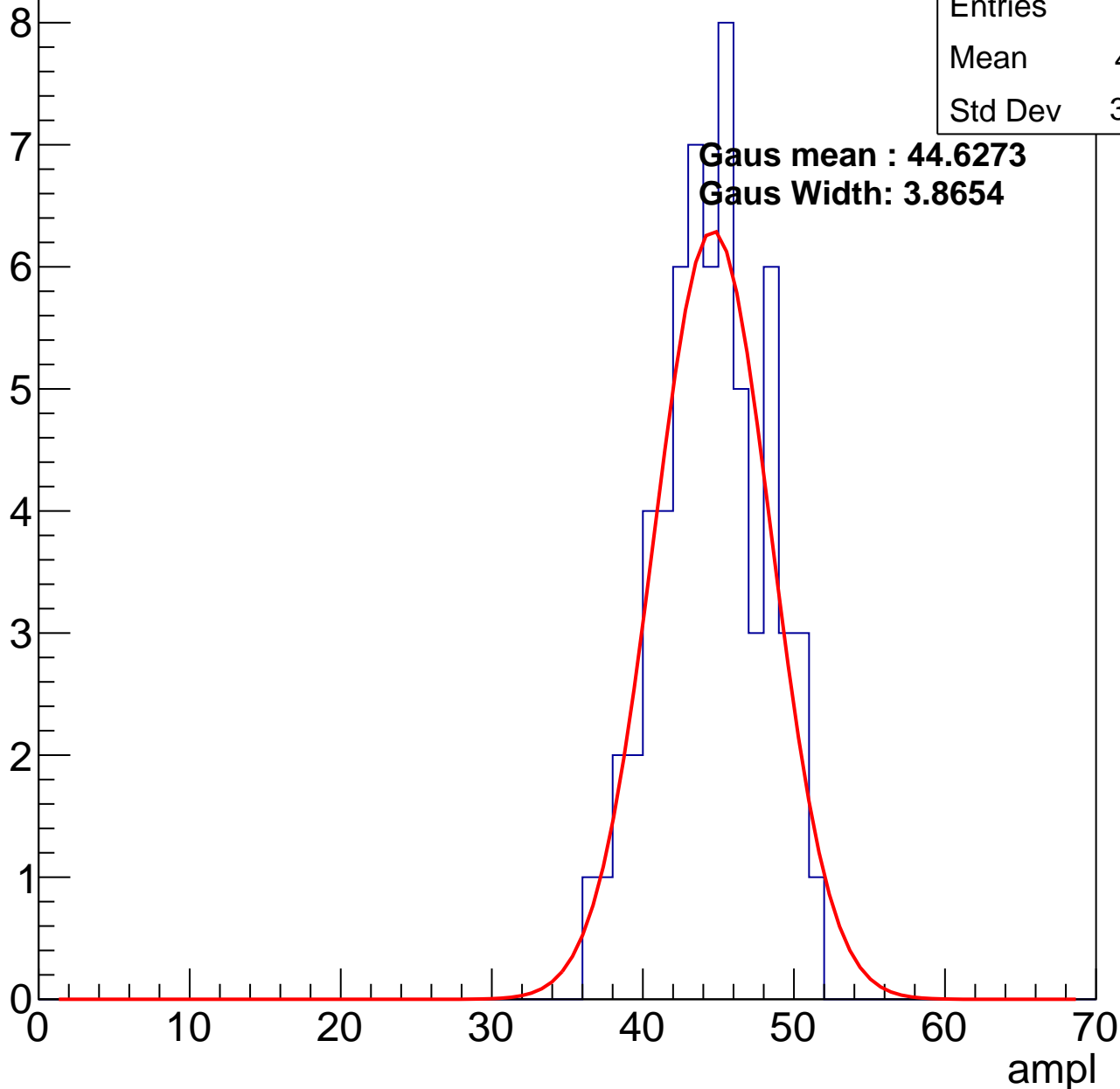
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.11
Std Dev	3.474

**Gaus mean : 44.6273**

**Gaus Width: 3.8654**

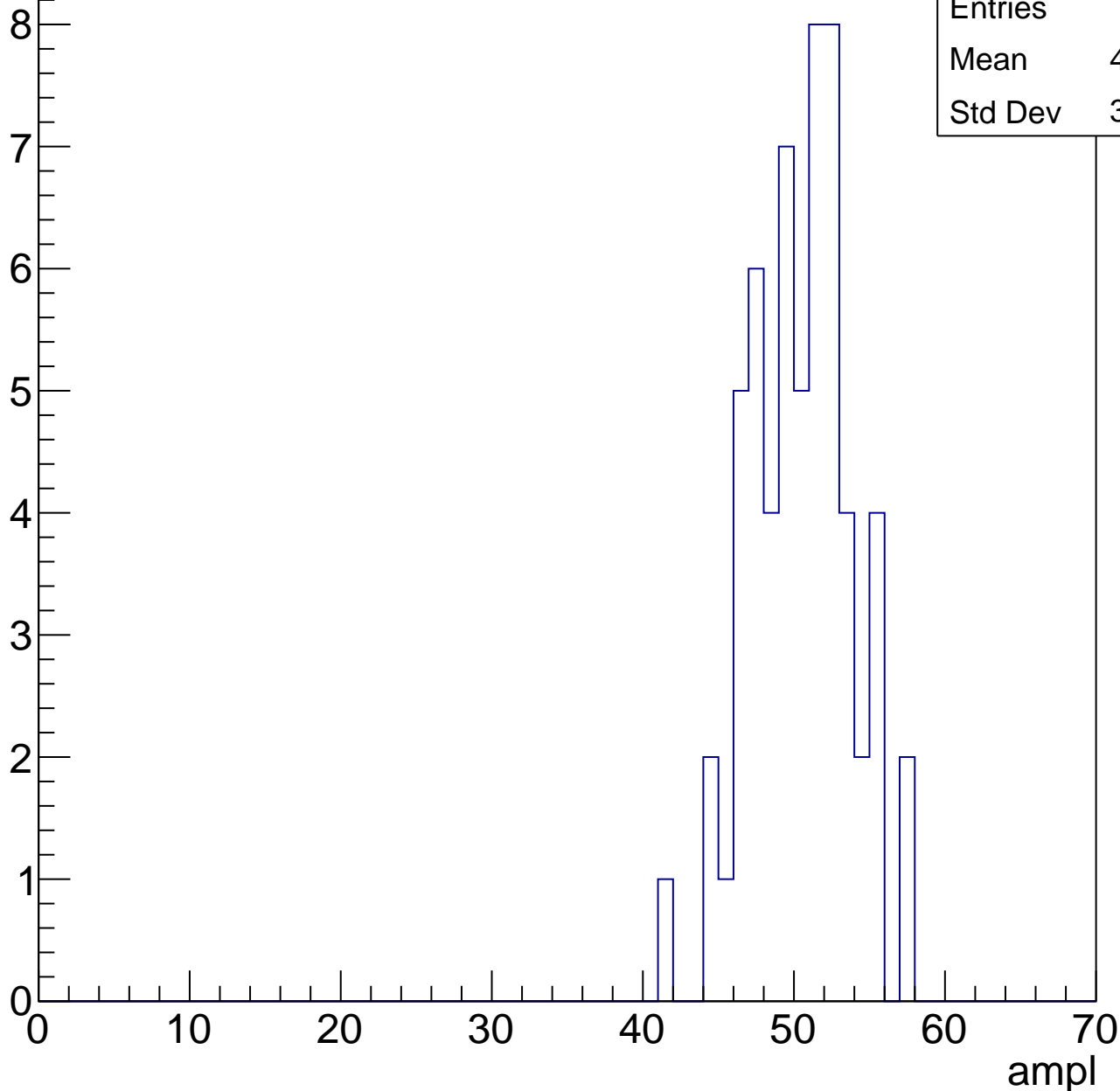


# B0L000S, U10-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.98
Std Dev	3.296

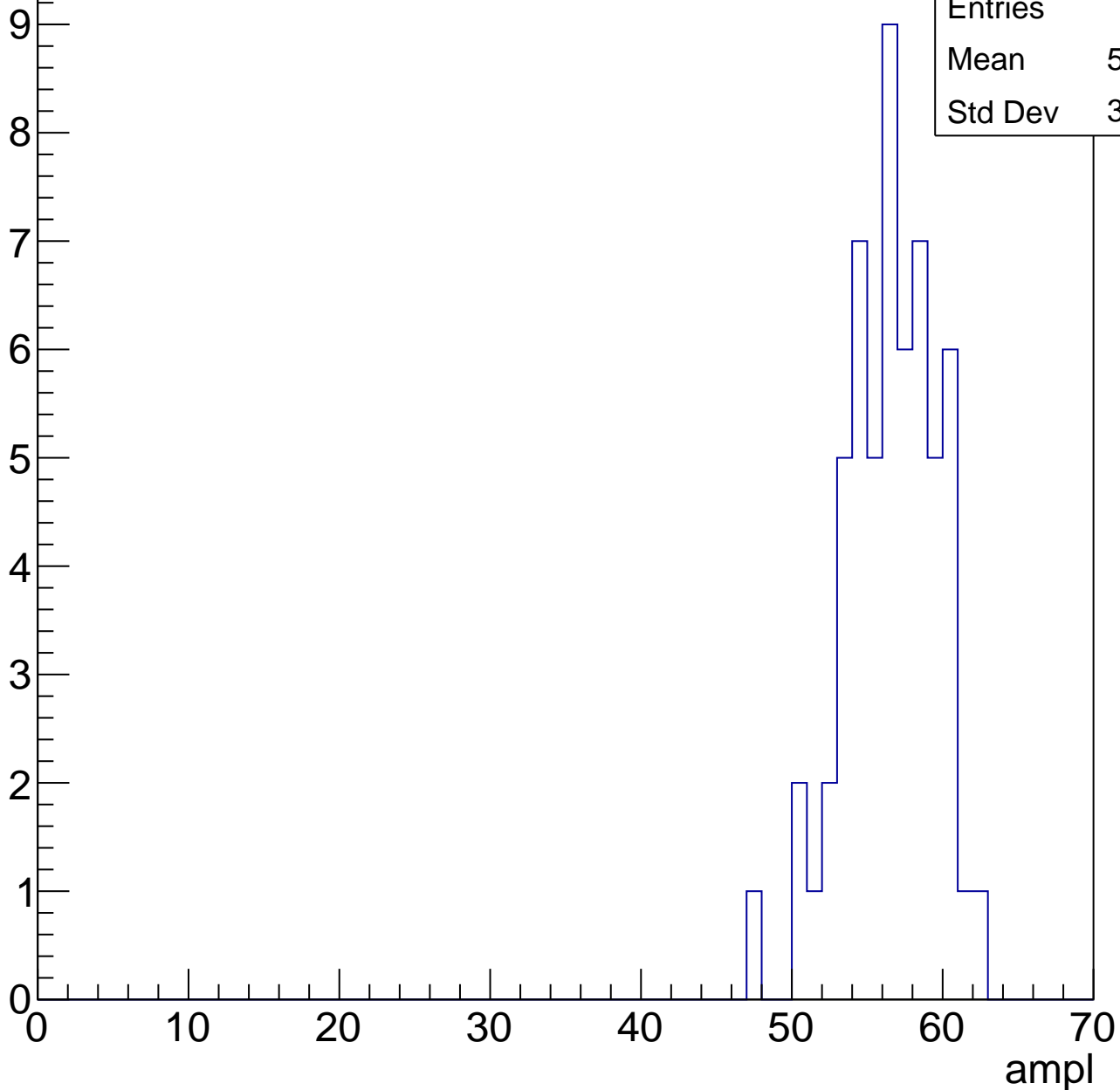


# B0L000S, U10-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	56.03
Std Dev	3.006

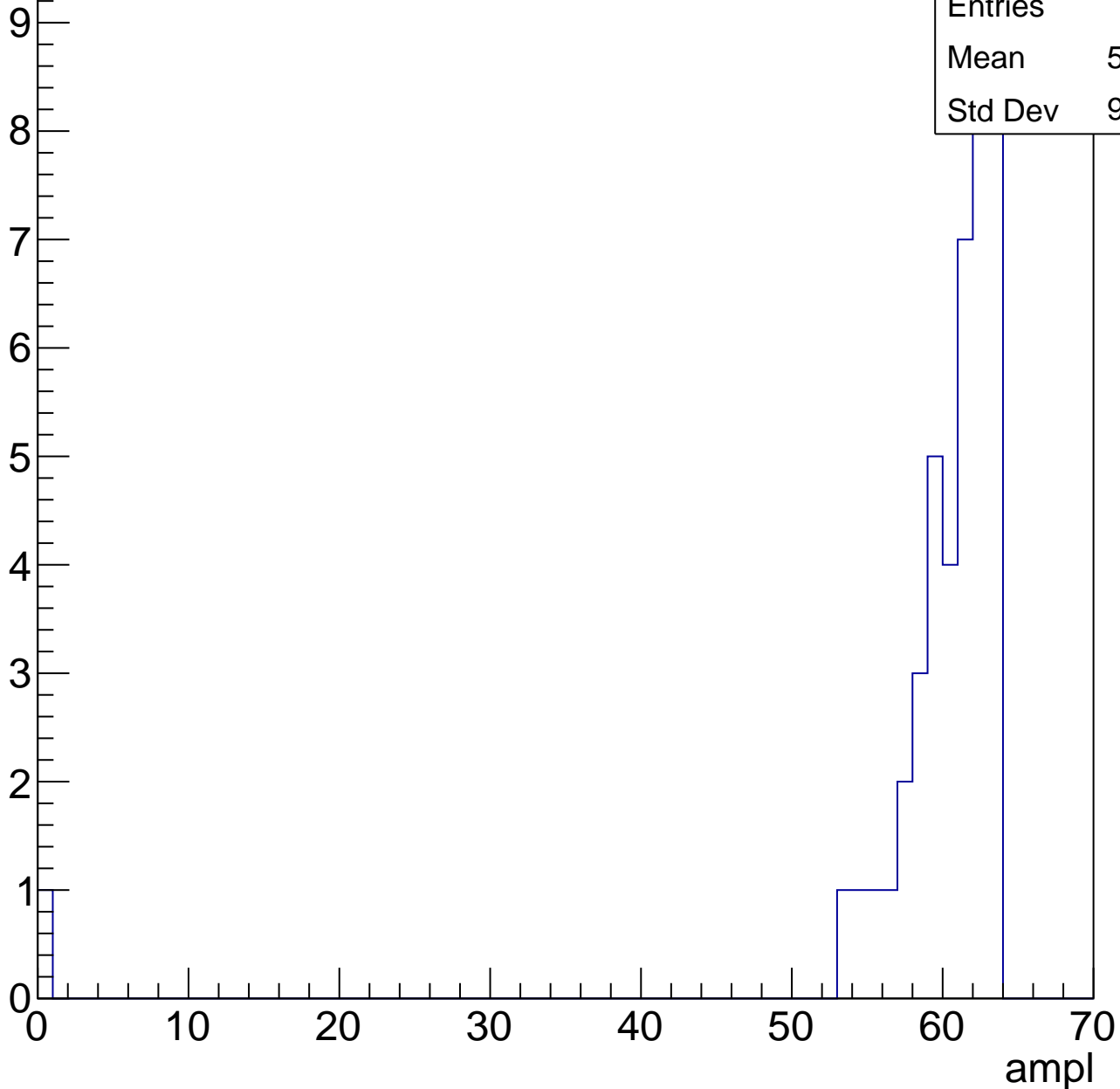


# B0L000S, U10-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

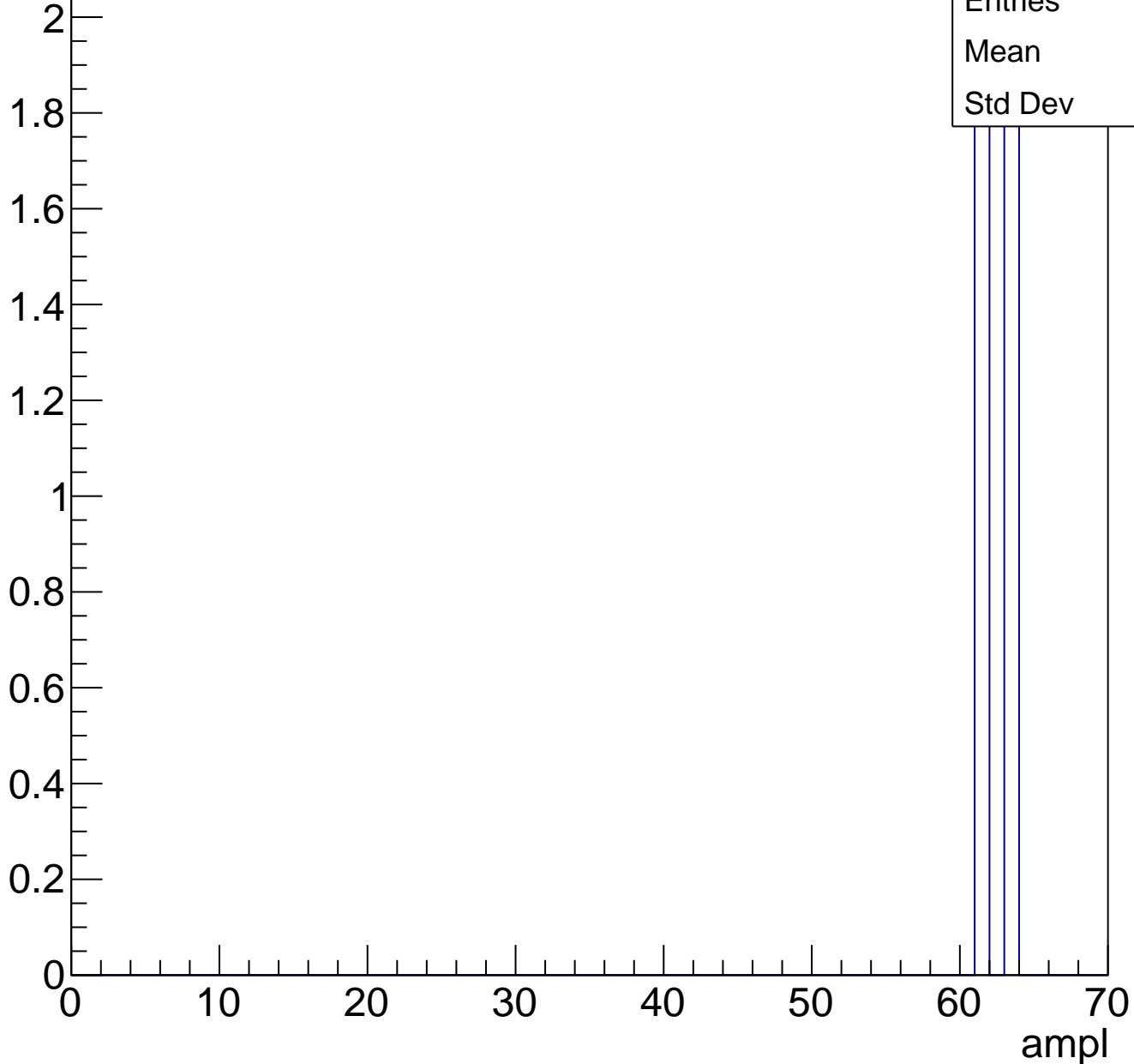
Entries	43
Mean	58.84
Std Dev	9.423



# B0L000S, U10-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch97, adc0

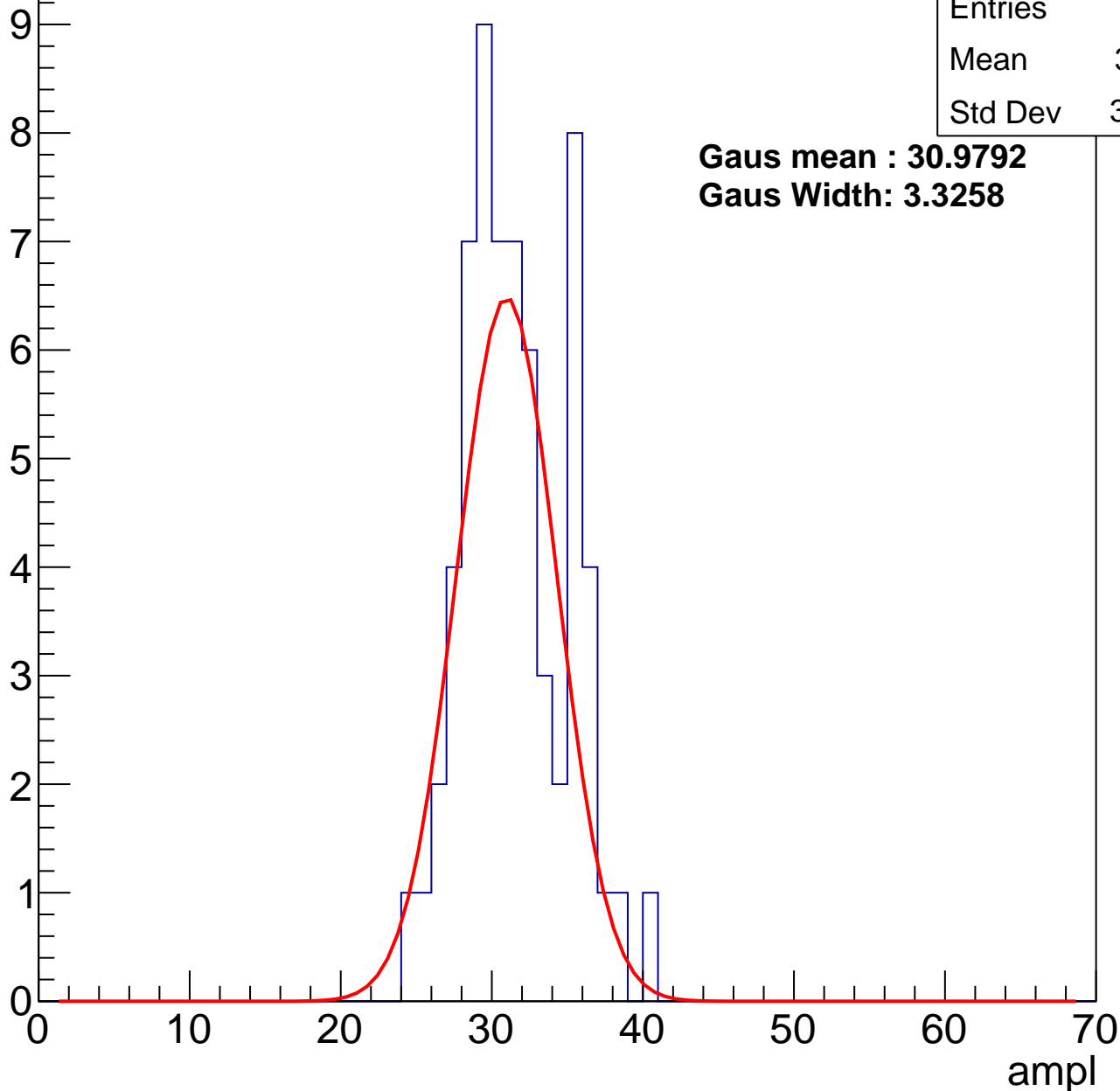
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.11
Std Dev	3.387

**Gaus mean : 30.9792**

**Gaus Width: 3.3258**



# B0L000S, U10-ch97, adc1

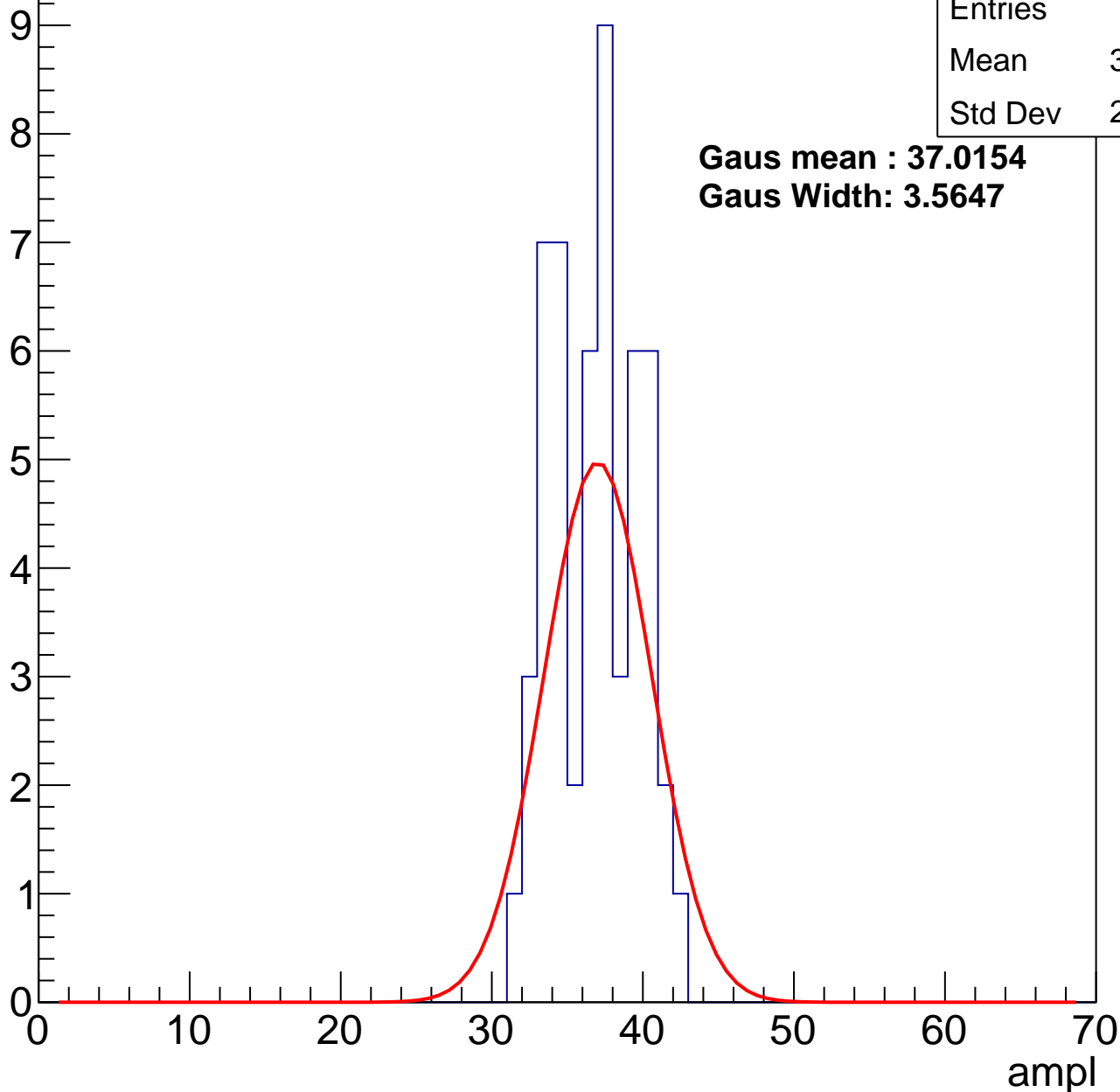
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.36
Std Dev	2.802

**Gaus mean : 37.0154**

**Gaus Width: 3.5647**



# B0L000S, U10-ch97, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	43.66
Std Dev	3.138

**Gaus mean : 44.5695**

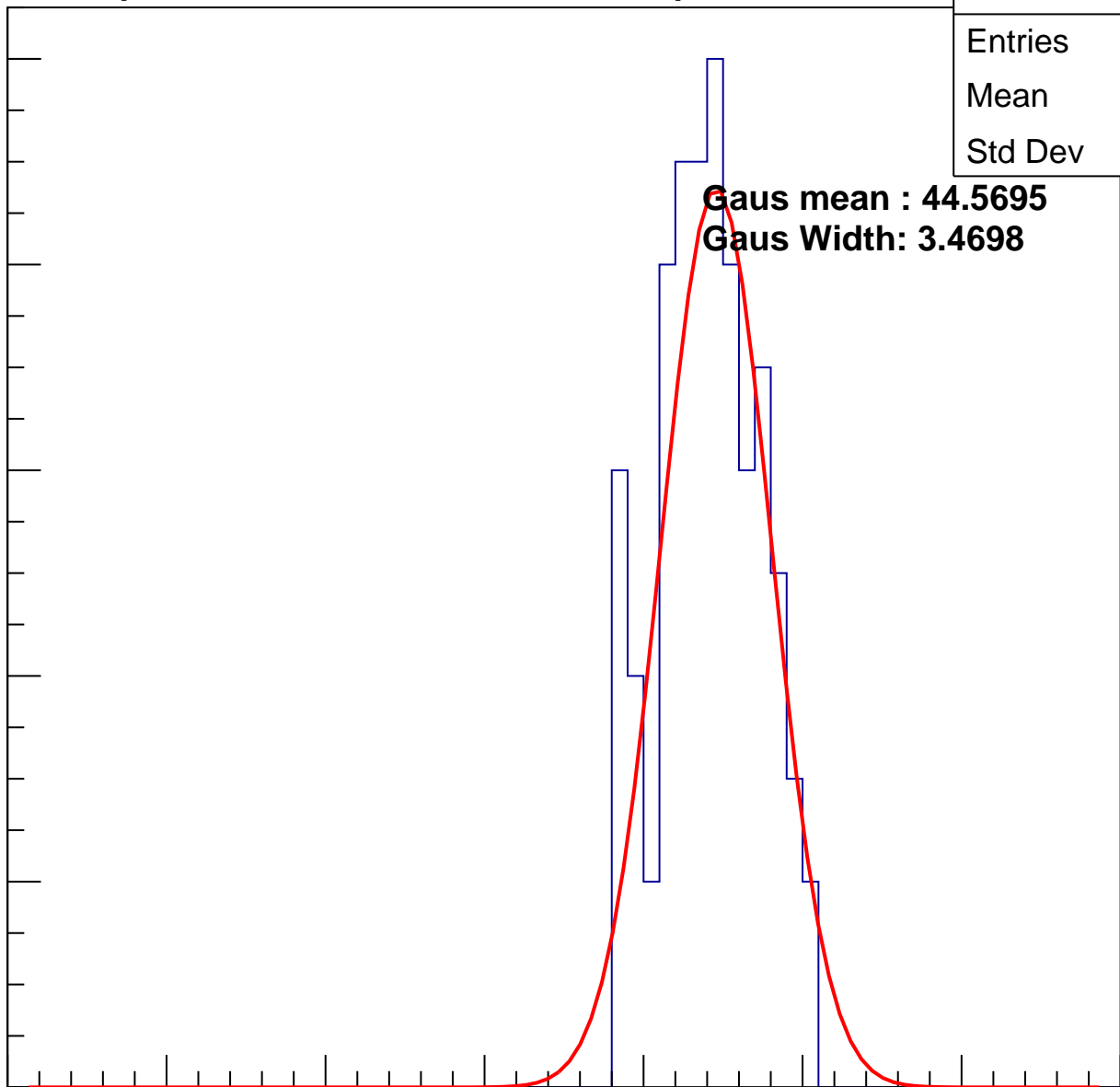
**Gaus Width: 3.4698**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

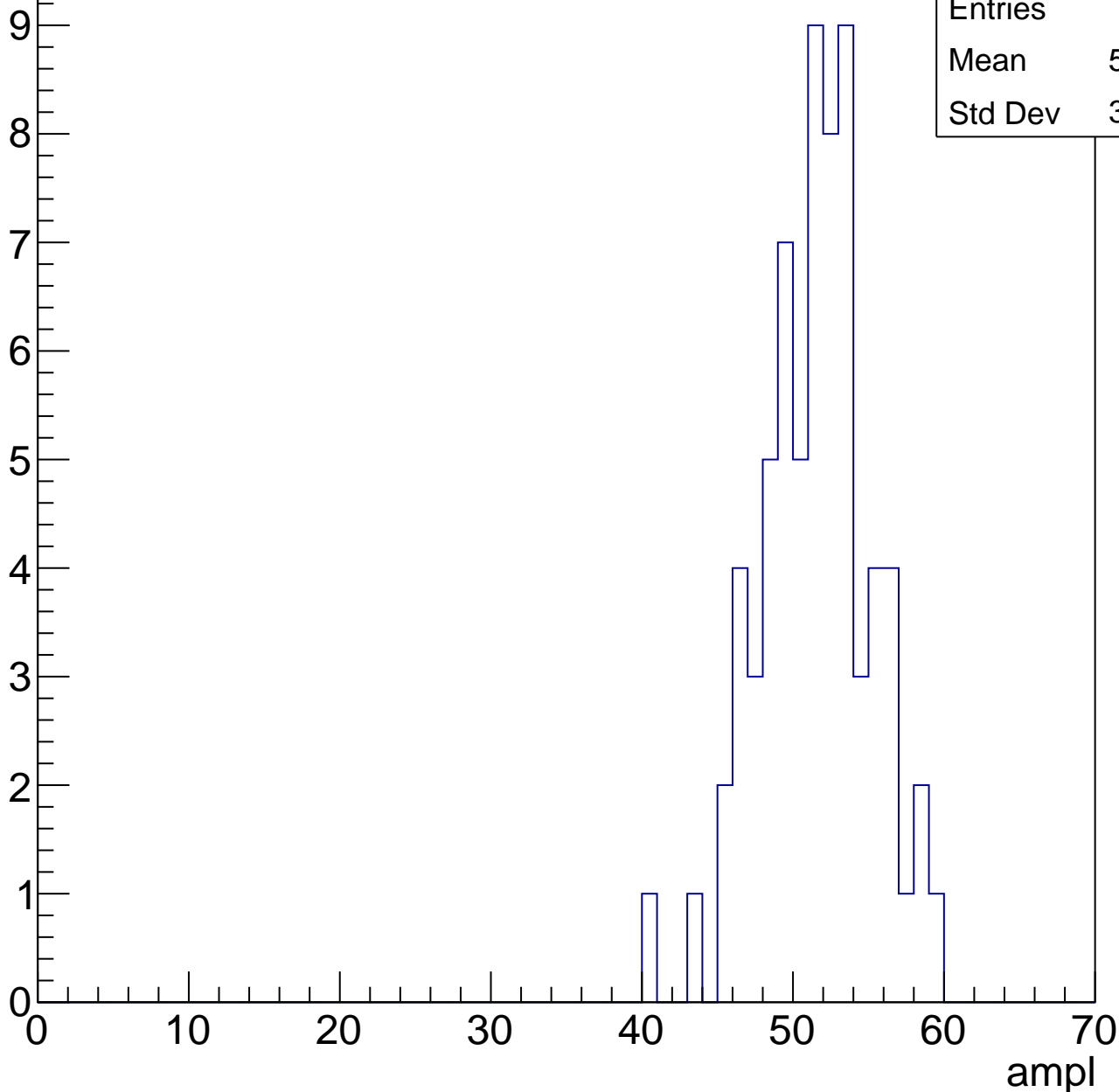


# B0L000S, U10-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	51.03
Std Dev	3.643

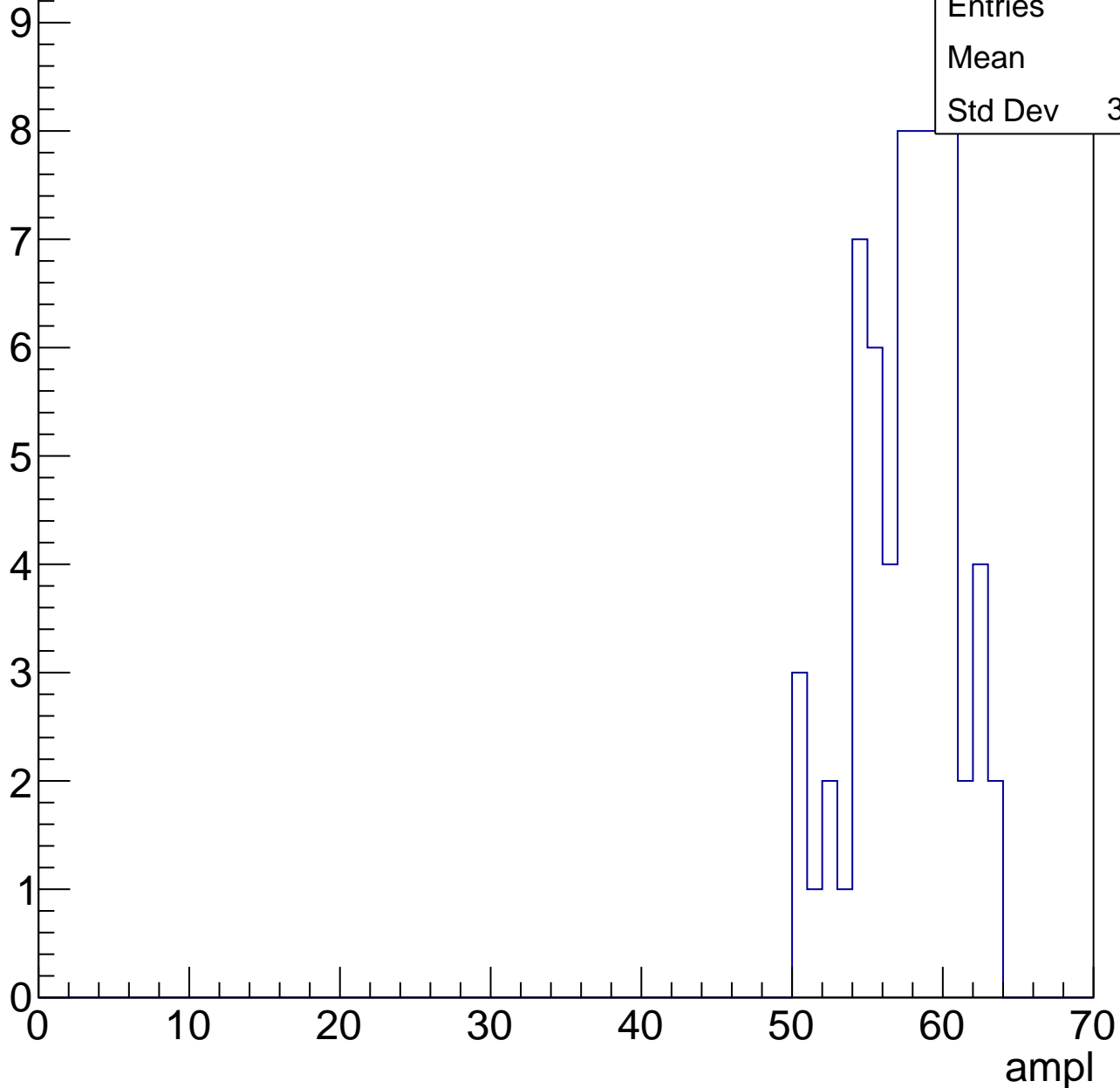


# B0L000S, U10-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	57.2
Std Dev	3.192

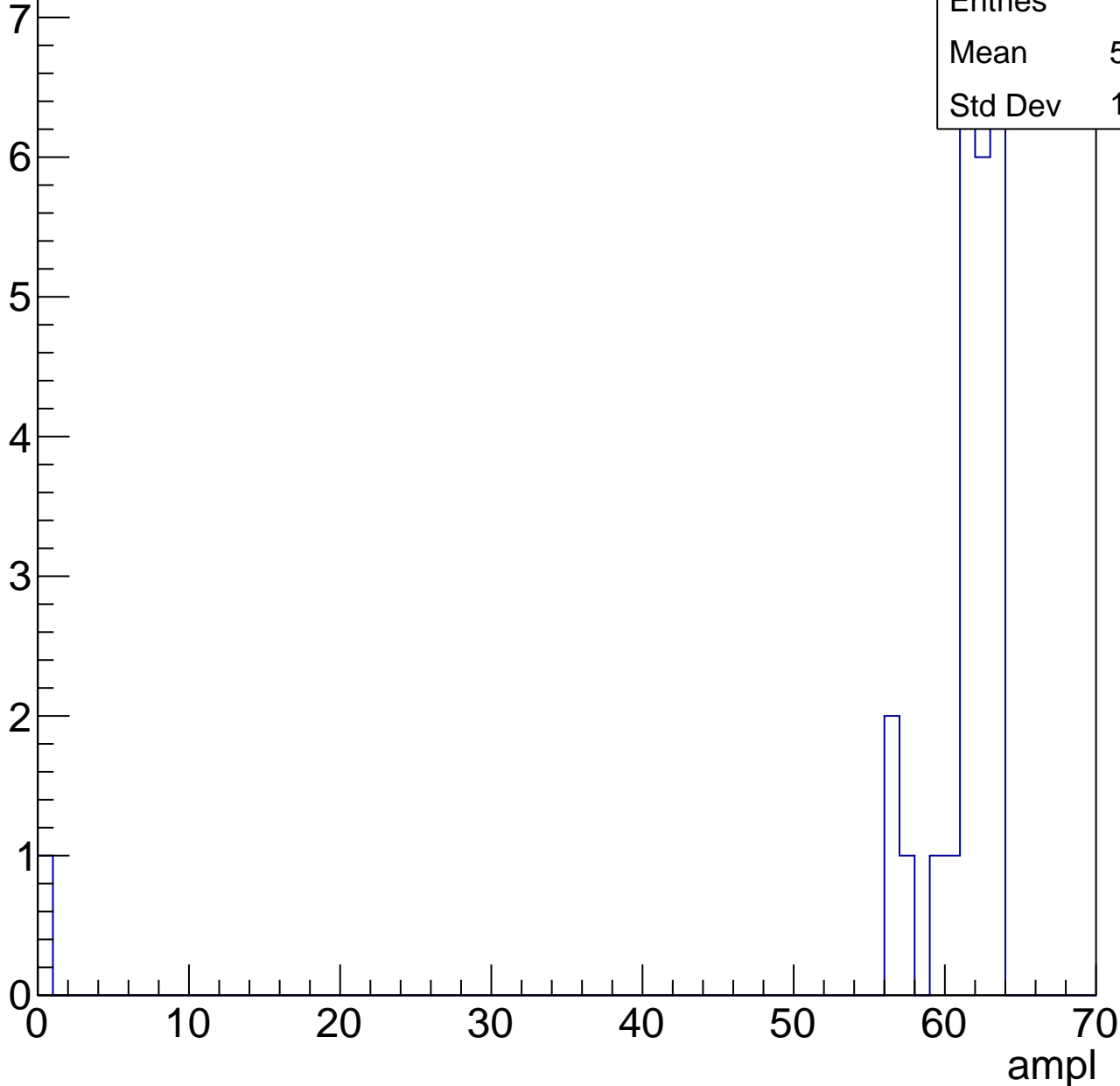


# B0L000S, U10-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.77
Std Dev	11.92



# B0L000S, U10-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	31.18
Std Dev	3.071

**Gaus mean : 31.6661**

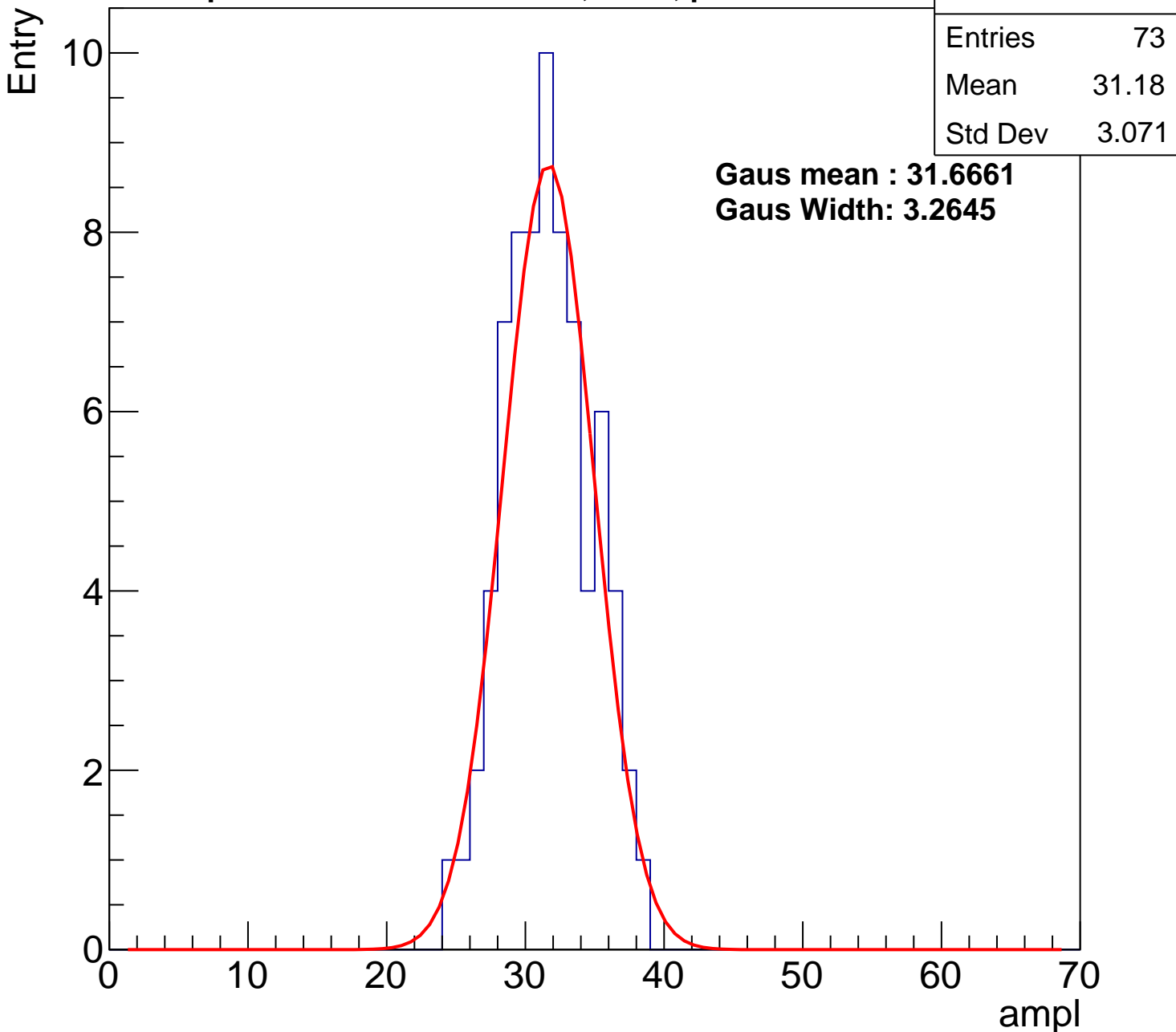
**Gaus Width: 3.2645**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch98, adc1

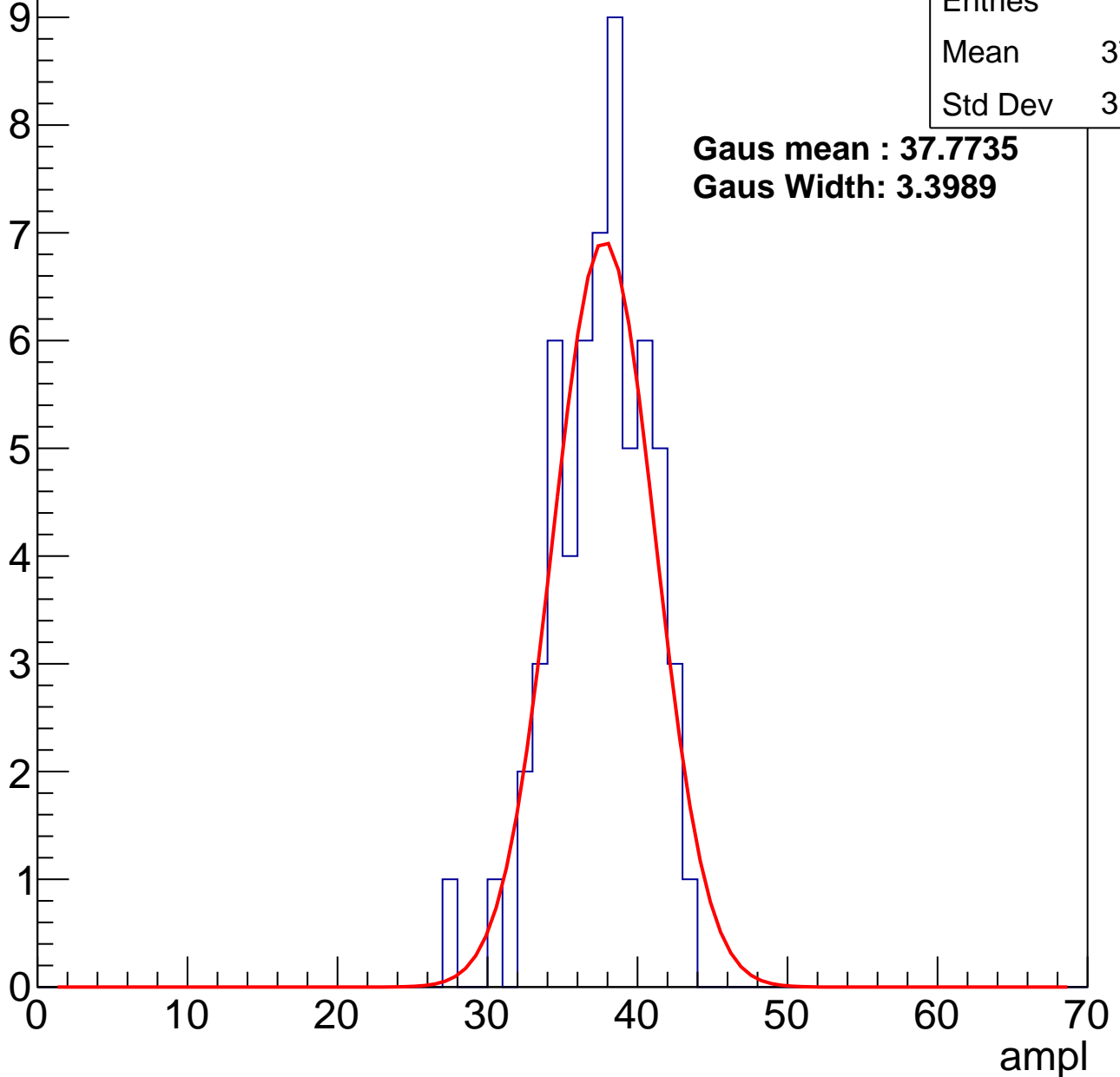
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.12
Std Dev	3.179

**Gaus mean : 37.7735**

**Gaus Width: 3.3989**



# B0L000S, U10-ch98, adc2

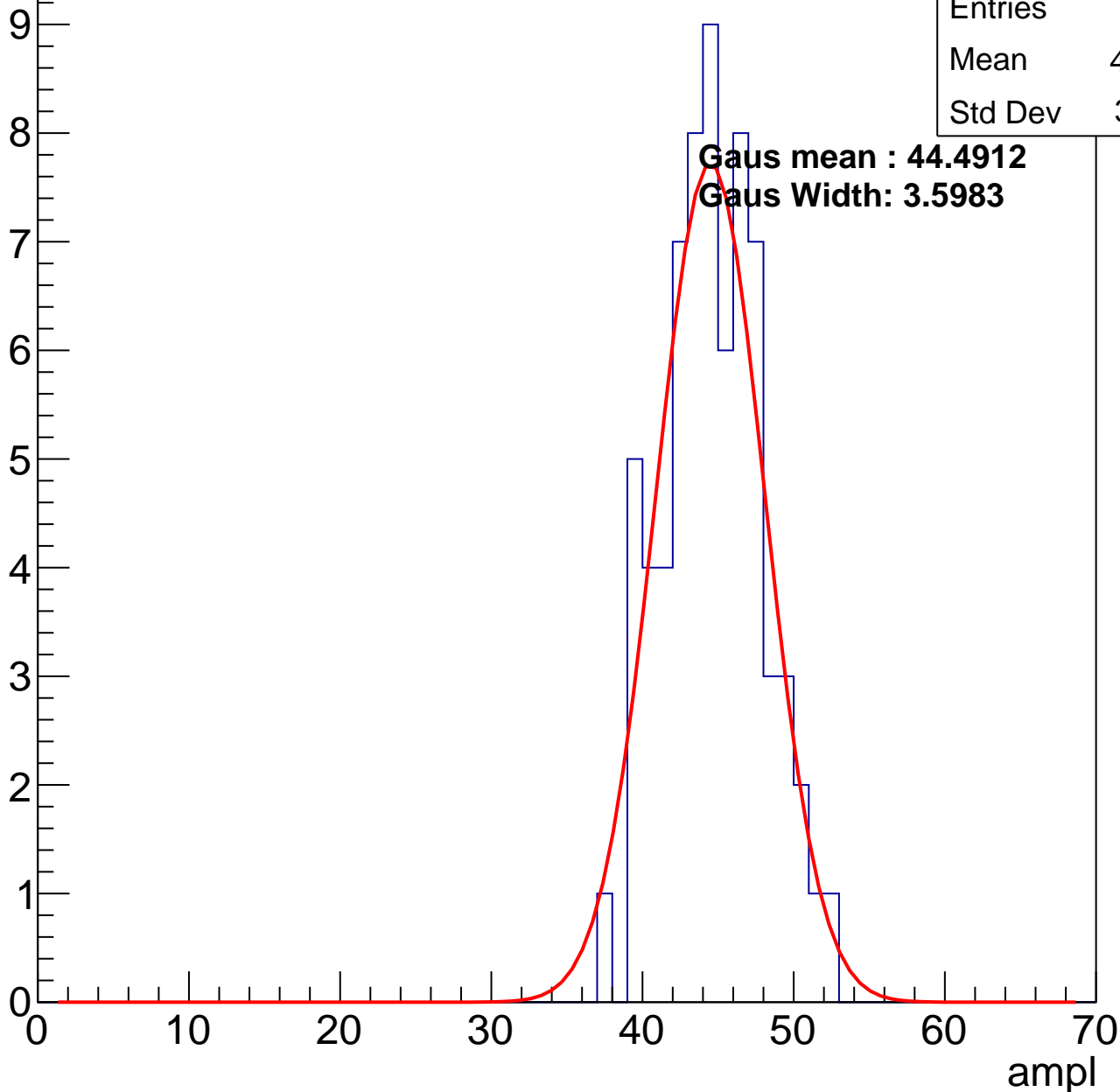
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.22
Std Dev	3.221

**Gaus mean : 44.4912**

**Gaus Width: 3.5983**

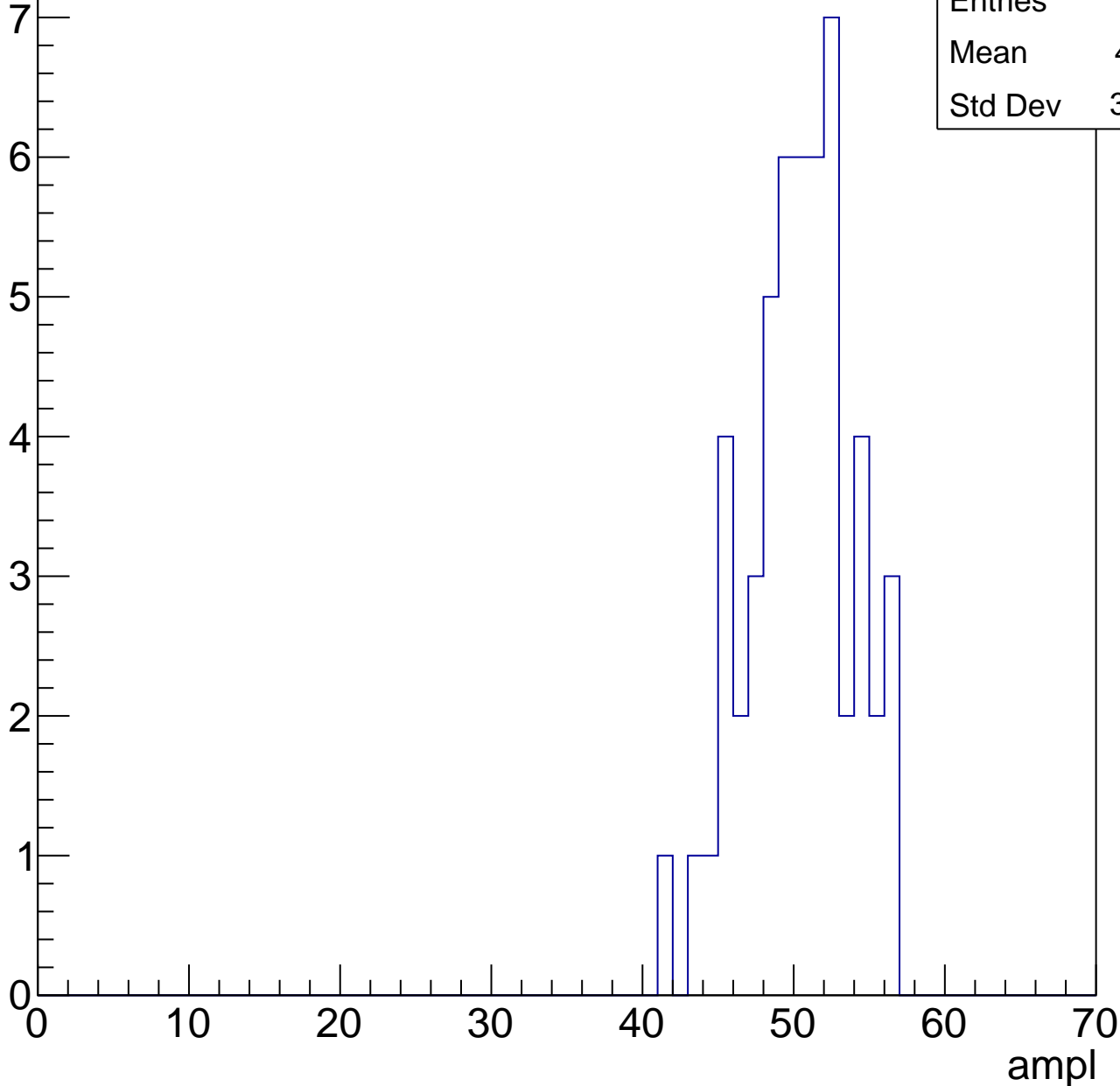


# B0L000S, U10-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	49.91
Std Dev	3.433

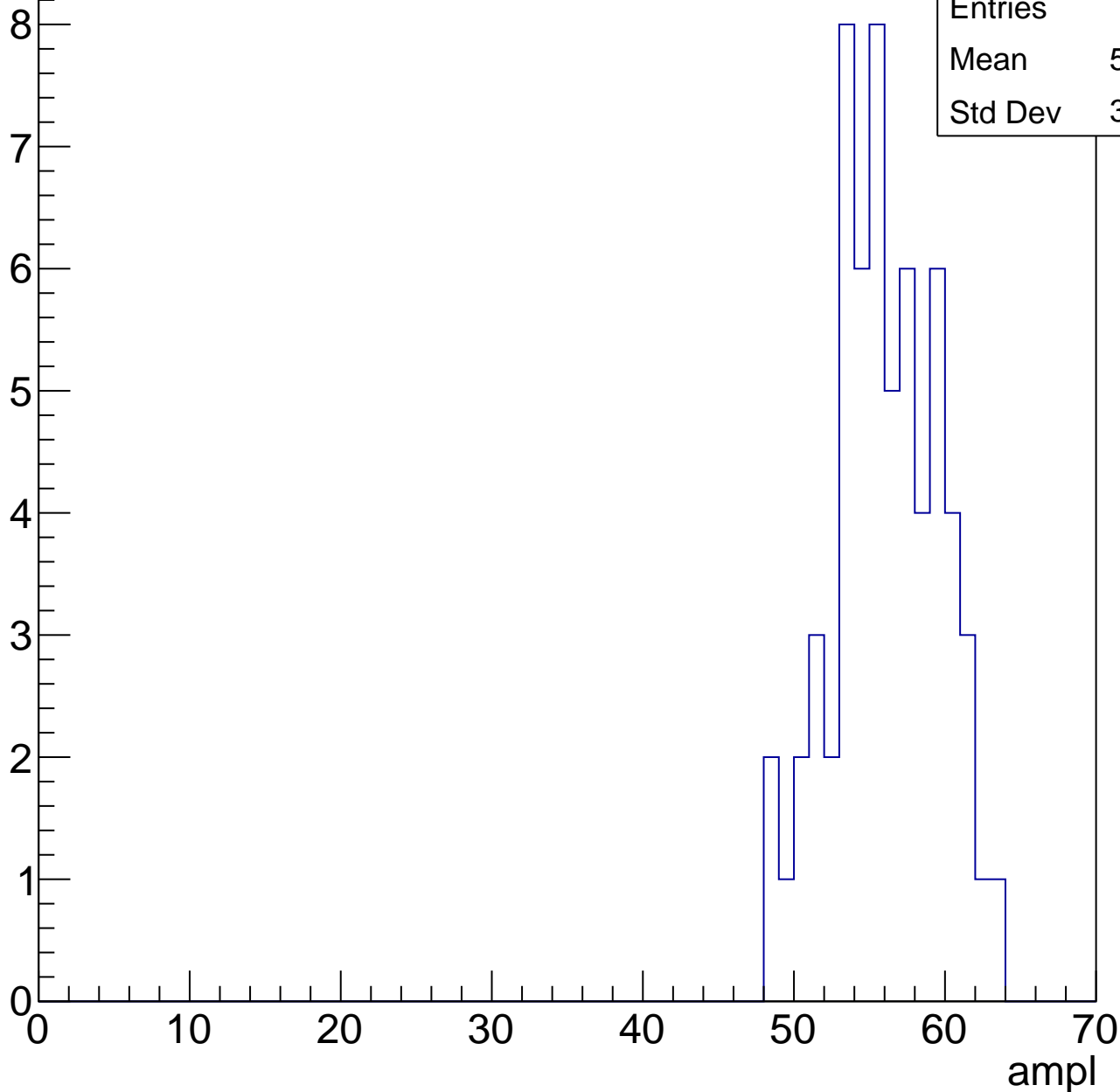


# B0L000S, U10-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.58
Std Dev	3.476

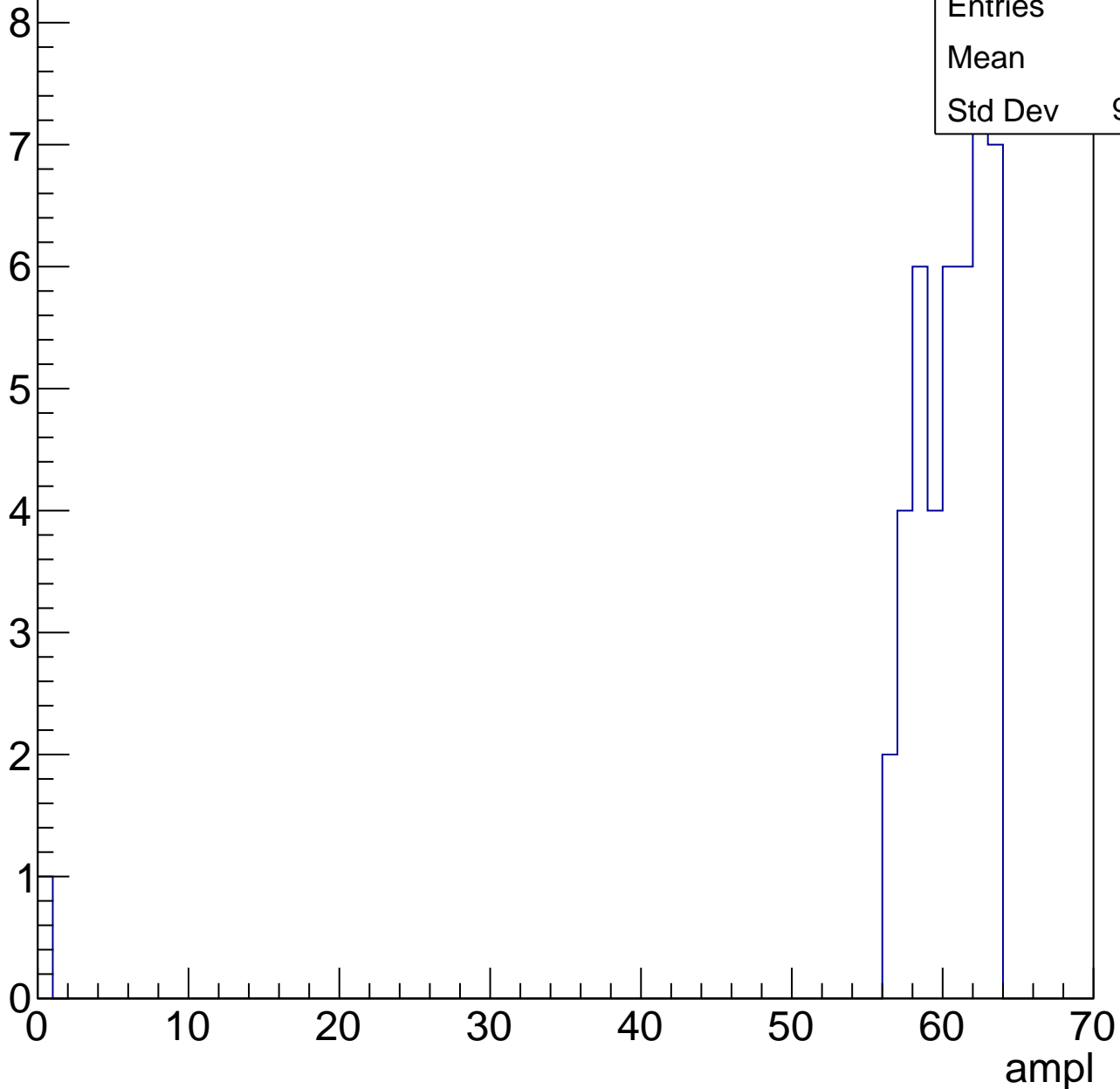


# B0L000S, U10-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.8
Std Dev	9.211



# B0L000S, U10-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	62.25
Std Dev	0.8292



# B0L000S, U10-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch99, adc0

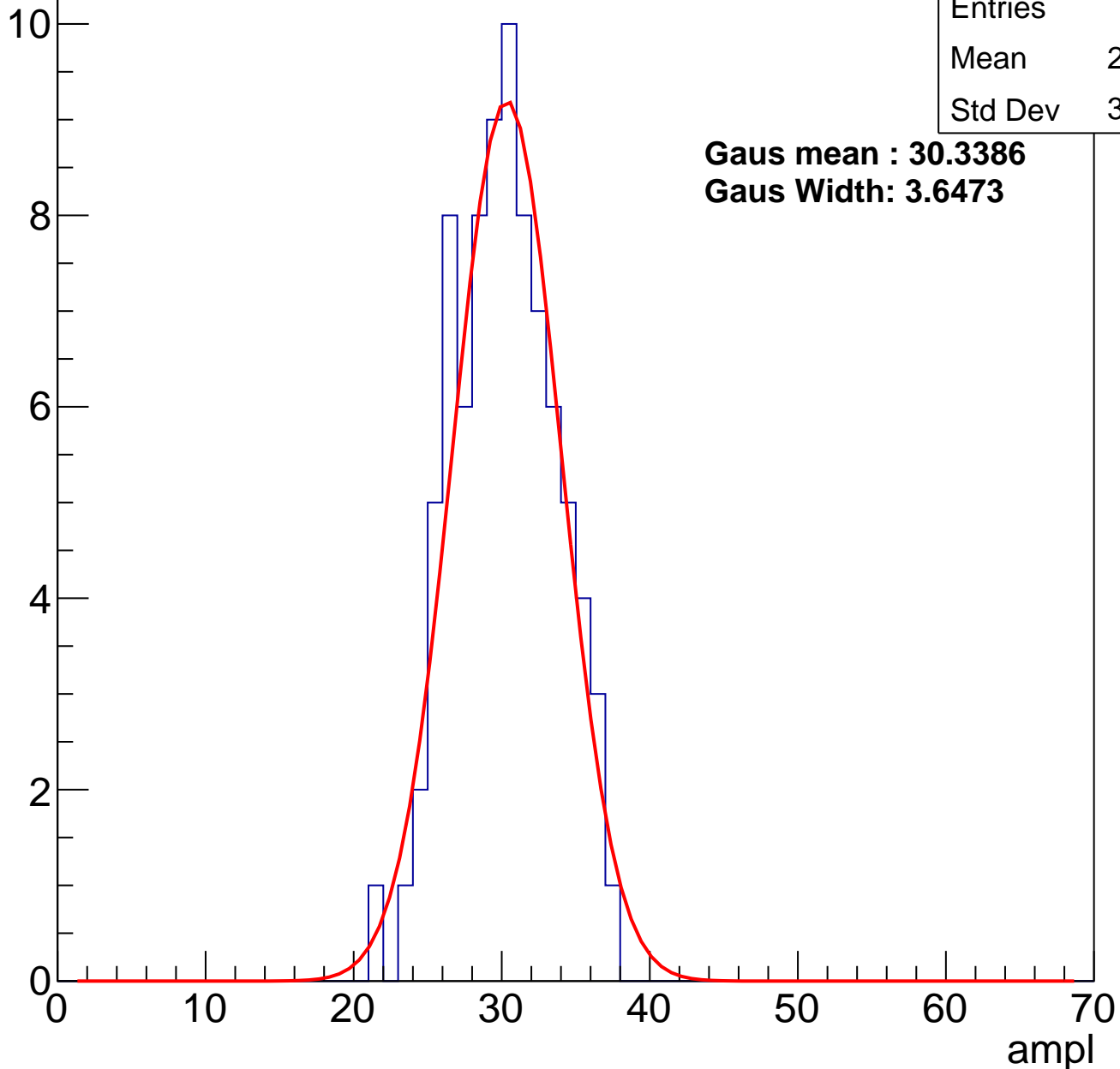
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	29.73
Std Dev	3.399

**Gaus mean : 30.3386**

**Gaus Width: 3.6473**

Entry



# B0L000S, U10-ch99, adc1

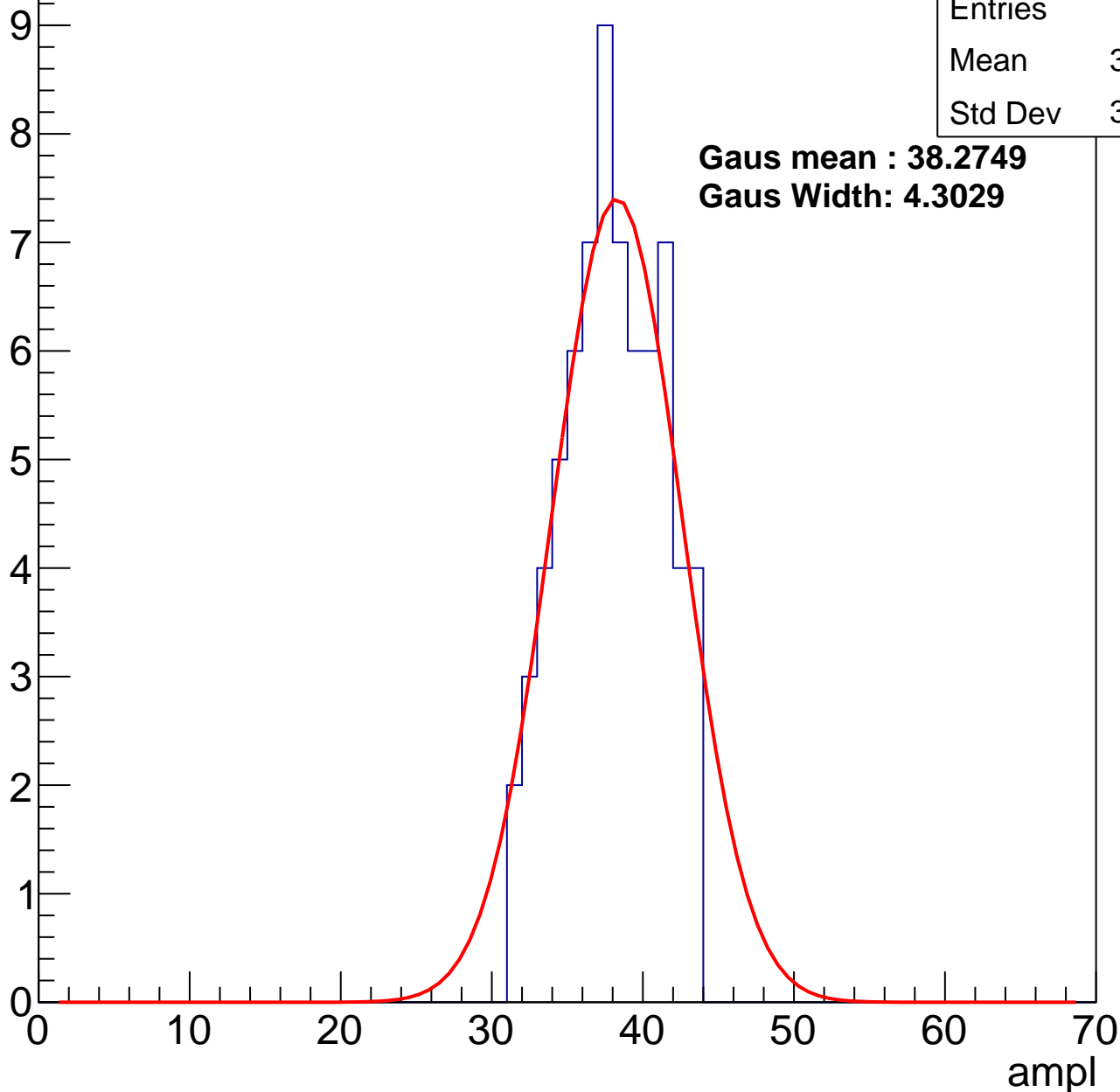
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.46
Std Dev	3.192

**Gaus mean : 38.2749**

**Gaus Width: 4.3029**



# B0L000S, U10-ch99, adc2

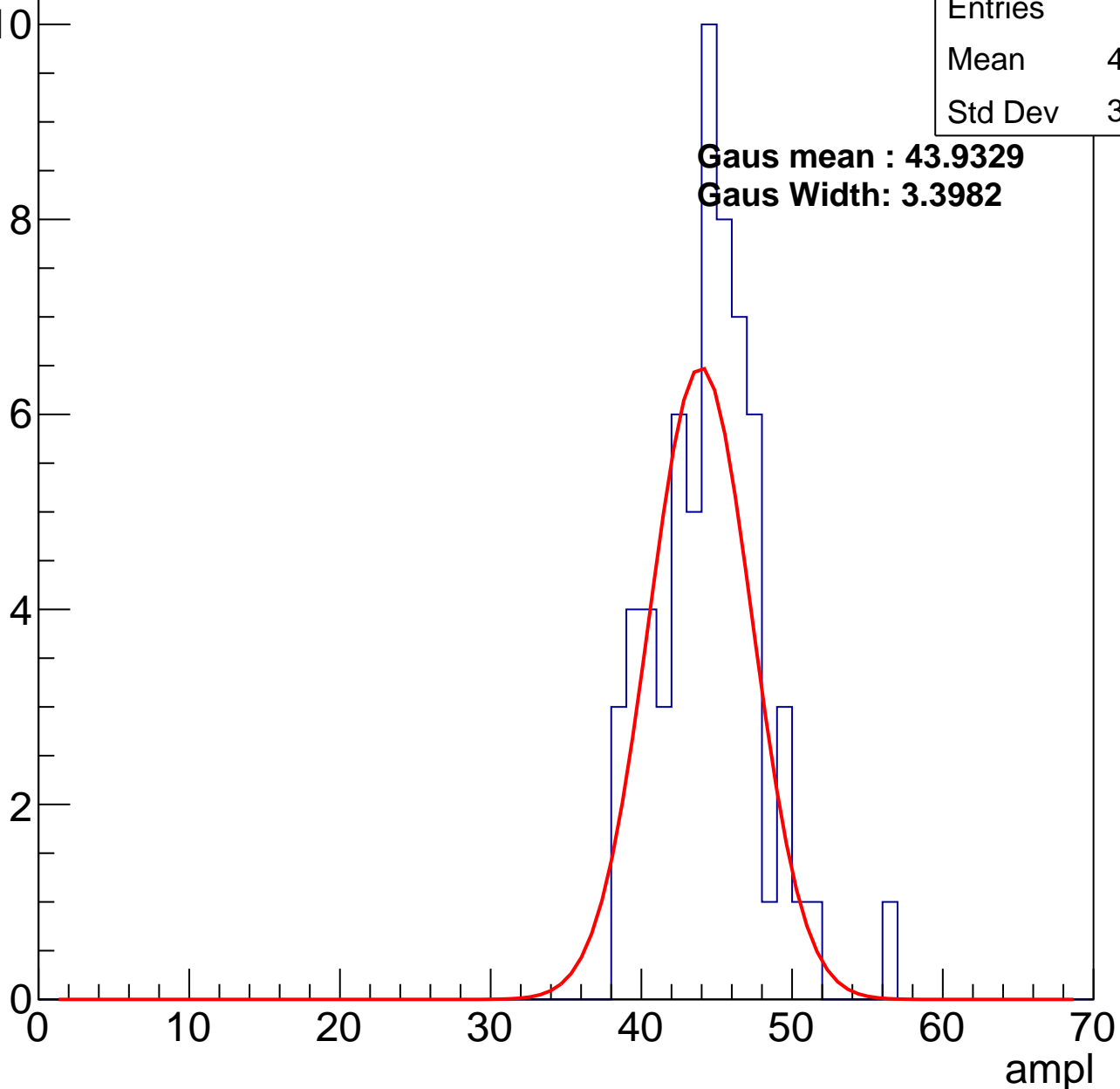
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.06
Std Dev	3.422

**Gaus mean : 43.9329**

**Gaus Width: 3.3982**

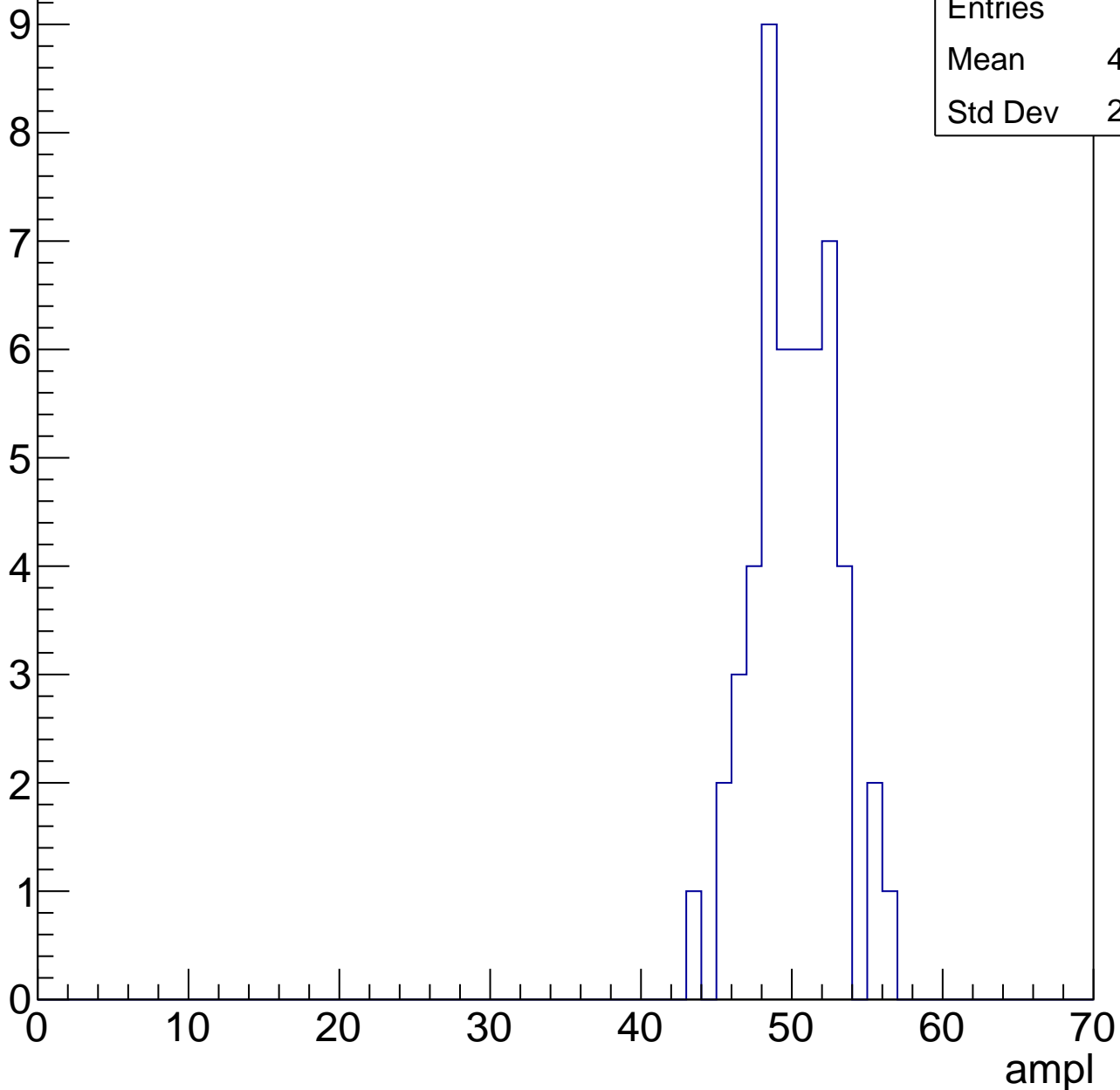


# B0L000S, U10-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.67
Std Dev	2.713



# B0L000S, U10-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	56.3
Std Dev	3.354

Entry

10

8

6

4

2

0

0

10

20

30

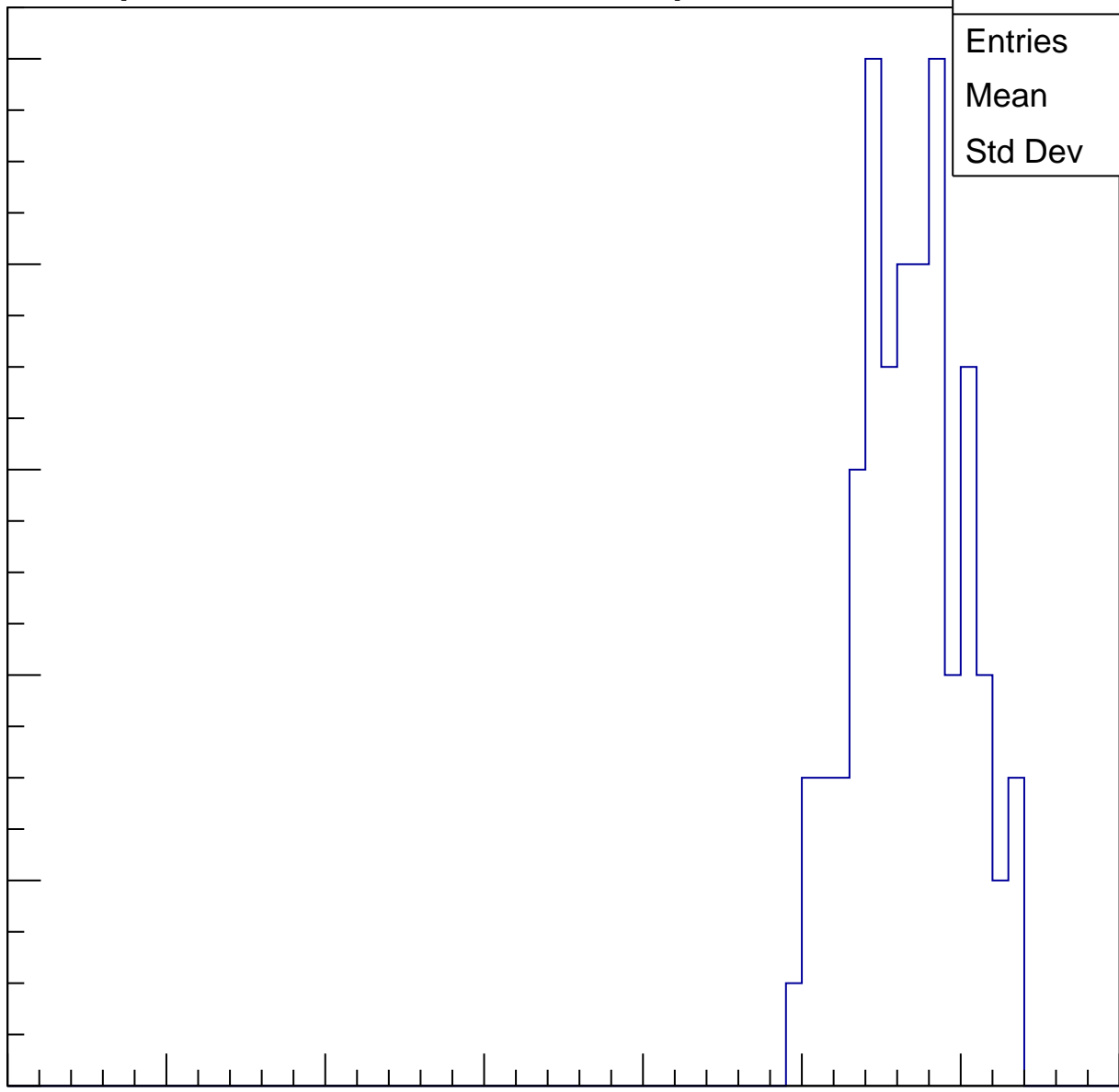
40

50

60

70

ampl

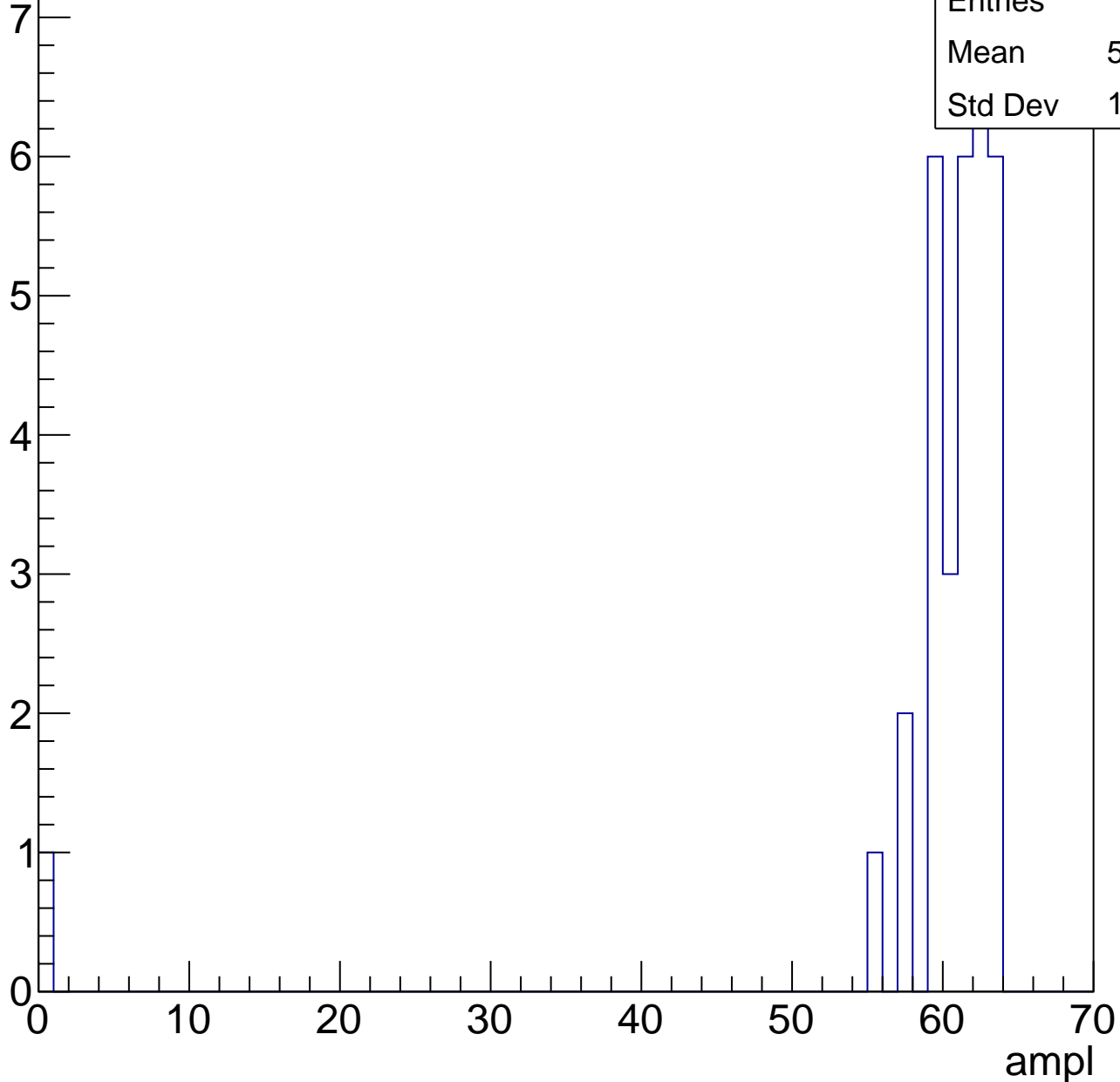


# B0L000S, U10-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.78
Std Dev	10.74



# B0L000S, U10-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch100, adc0

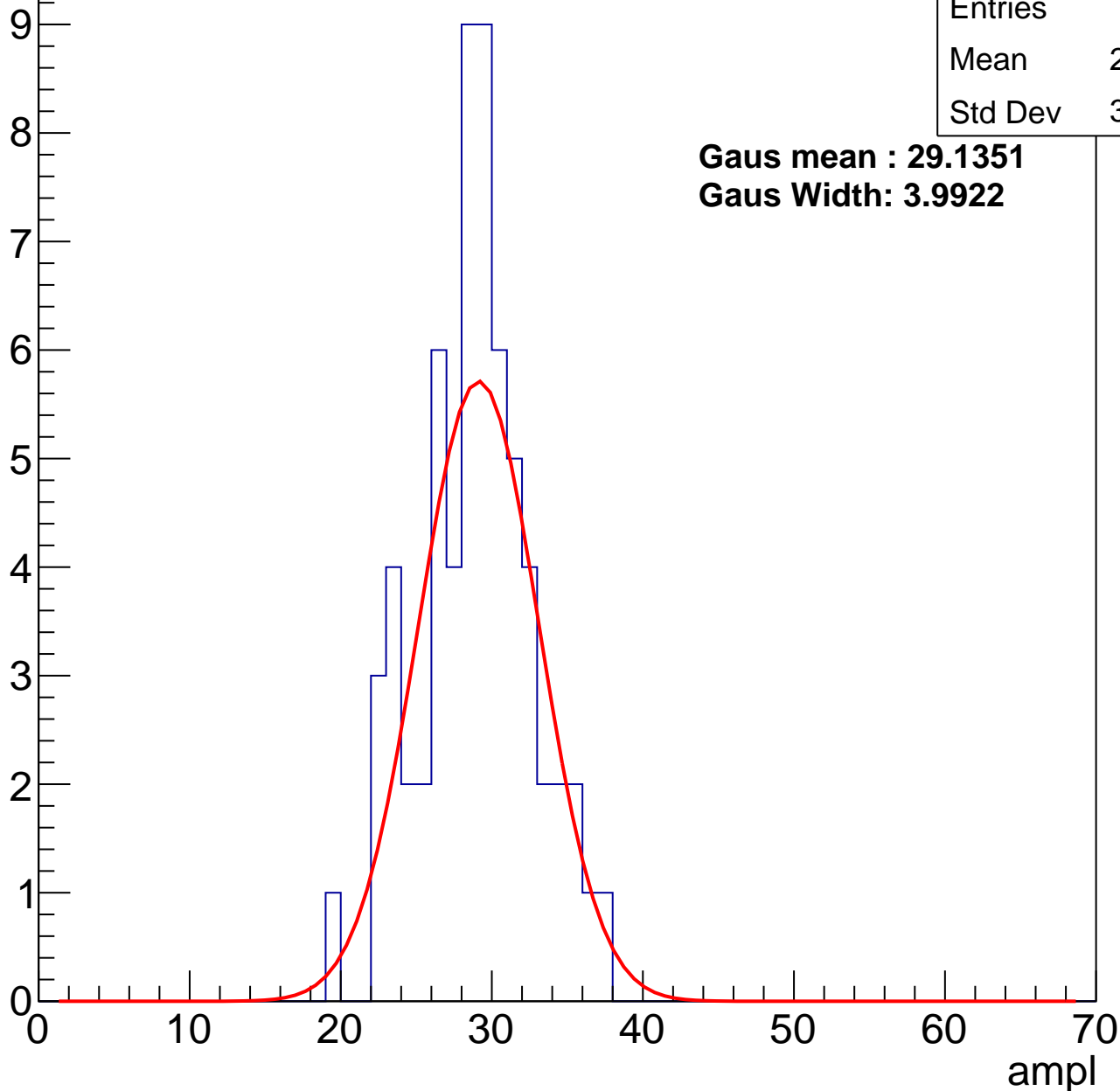
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	28.44
Std Dev	3.698

**Gaus mean : 29.1351**

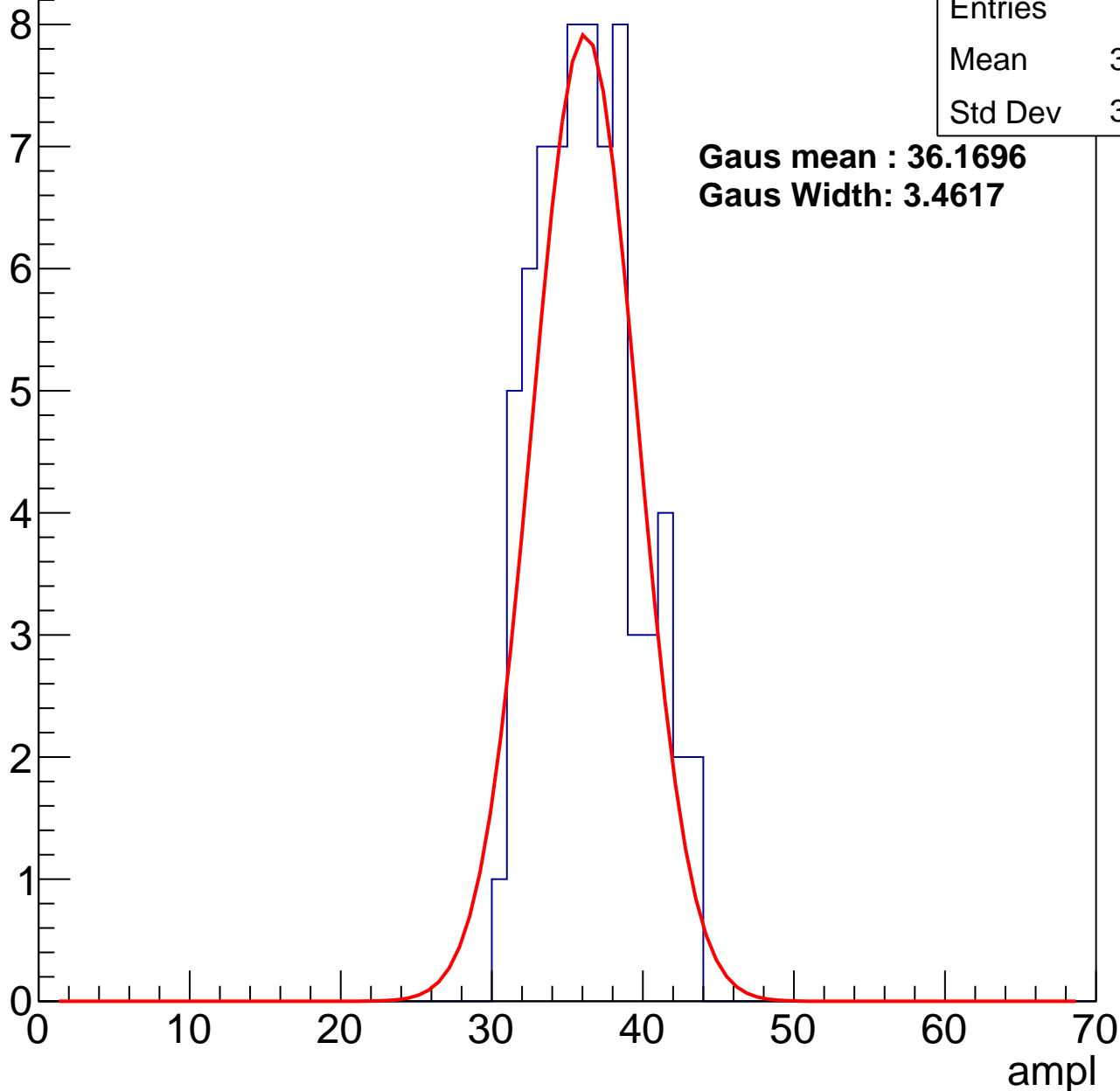
**Gaus Width: 3.9922**



# B0L000S, U10-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

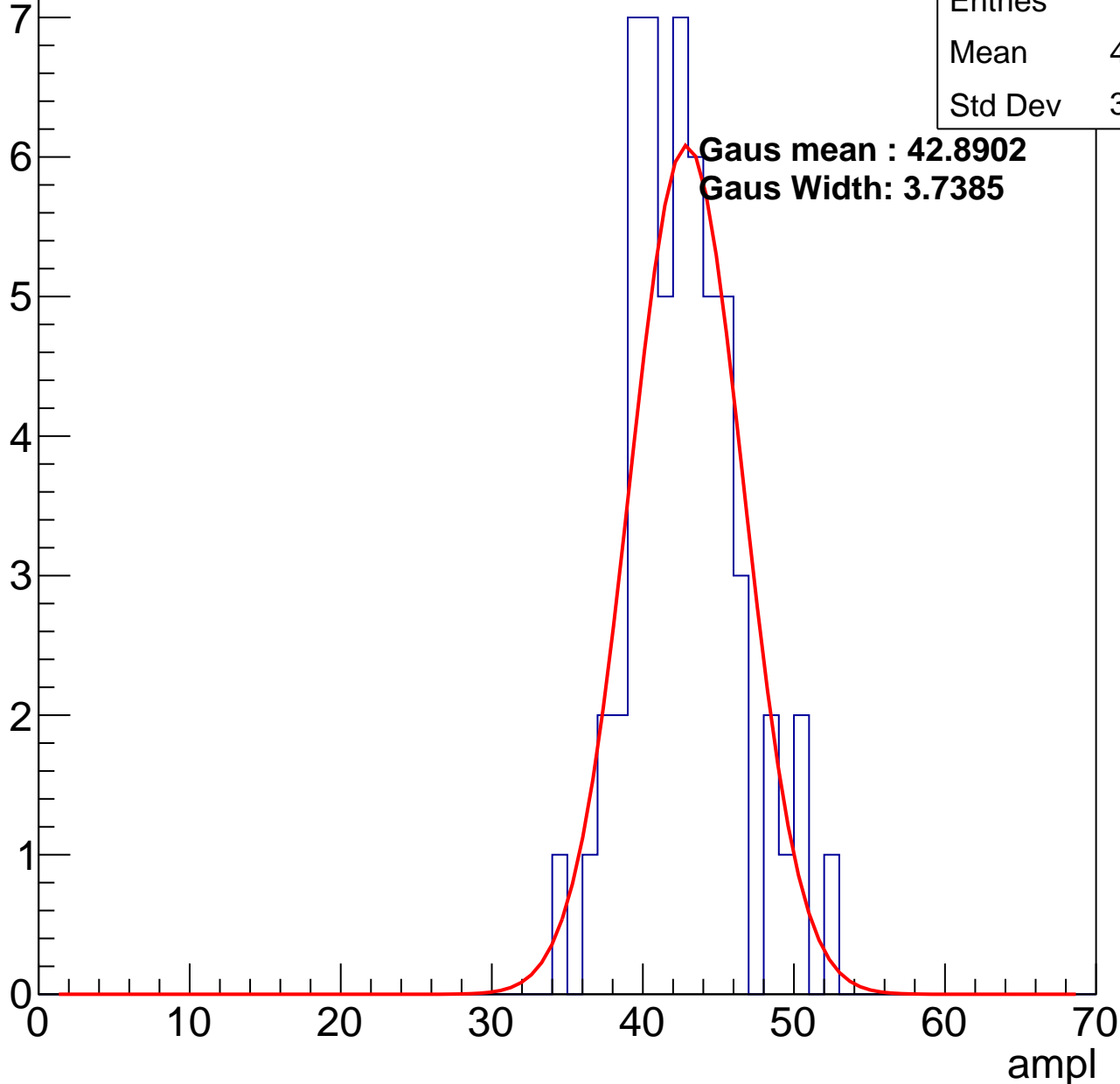


# B0L000S, U10-ch100, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.28
Std Dev	3.607

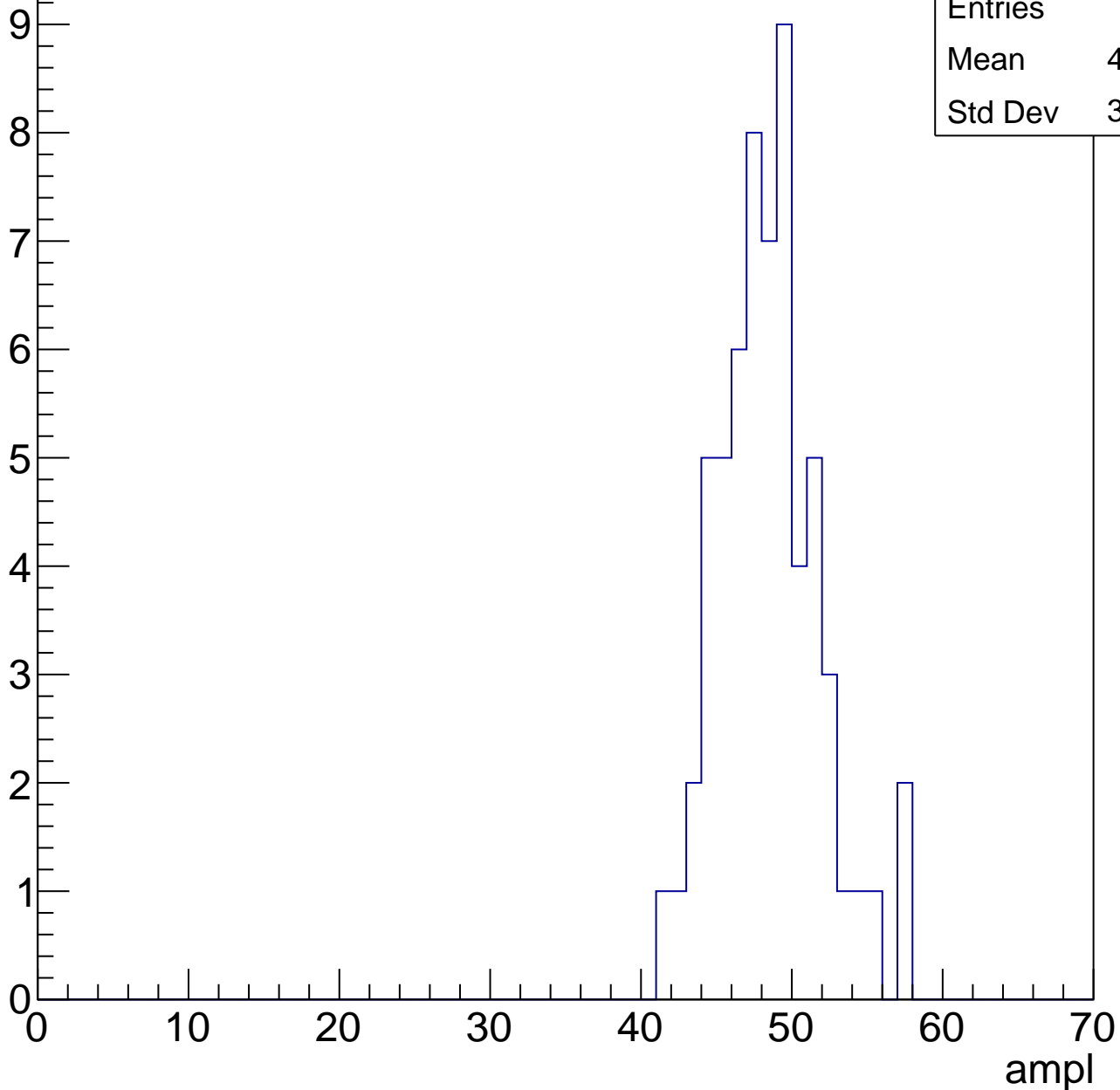


# B0L000S, U10-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.03
Std Dev	3.344

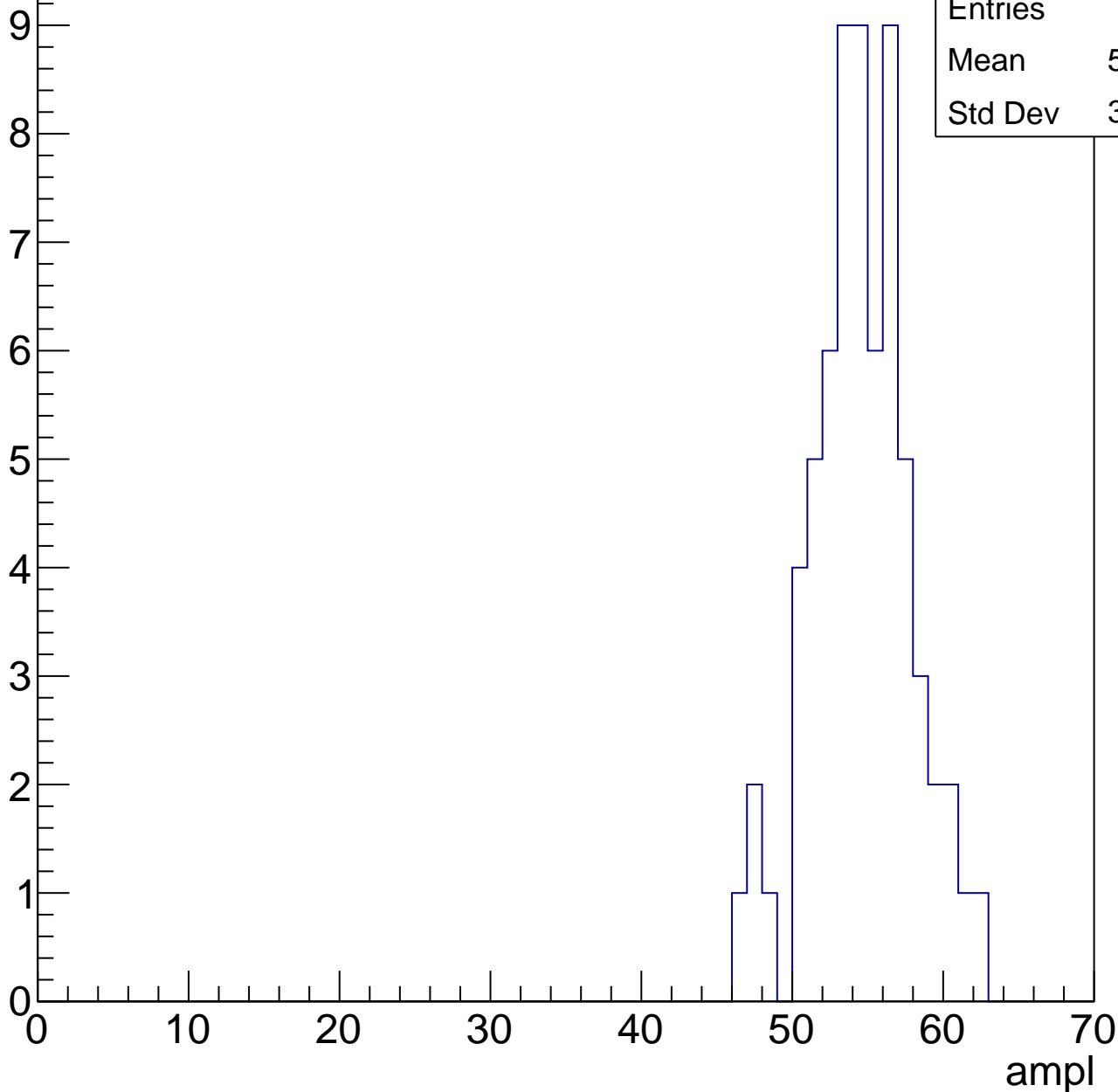


# B0L000S, U10-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

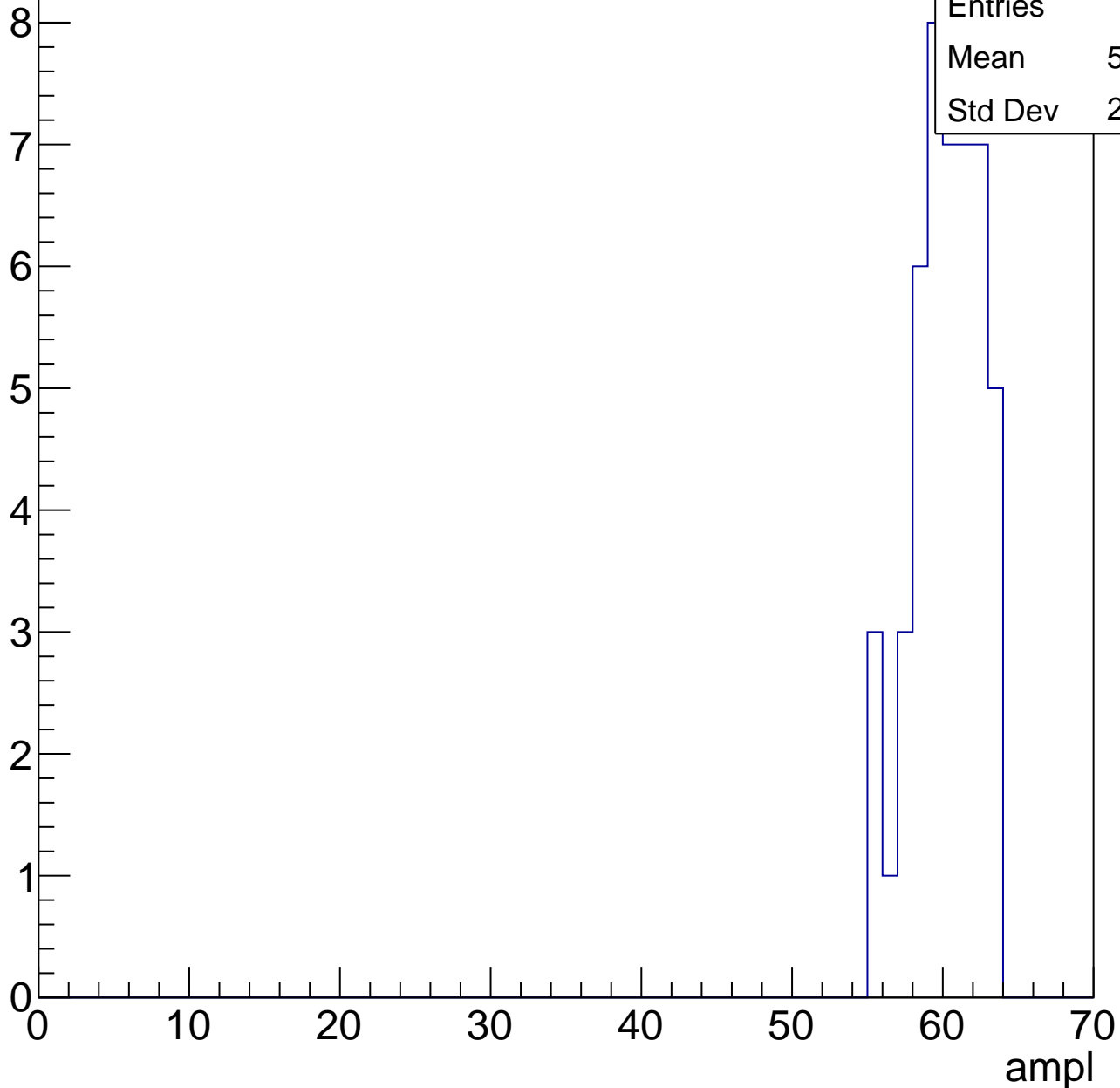
Entries	66
Mean	54.12
Std Dev	3.278



# B0L000S, U10-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



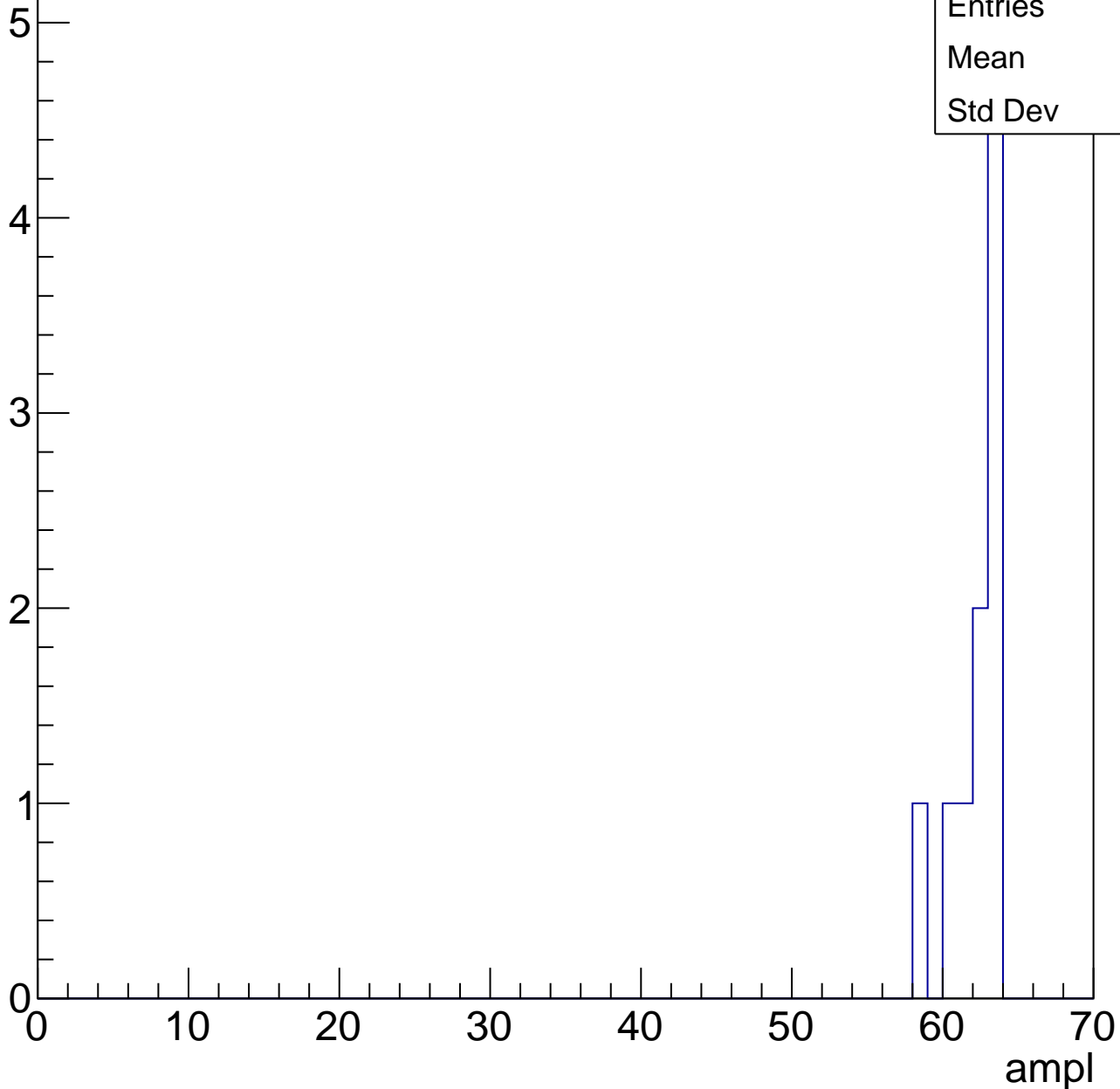
Entries	47
Mean	59.74
Std Dev	2.197

# B0L000S, U10-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	61.8
Std Dev	1.6

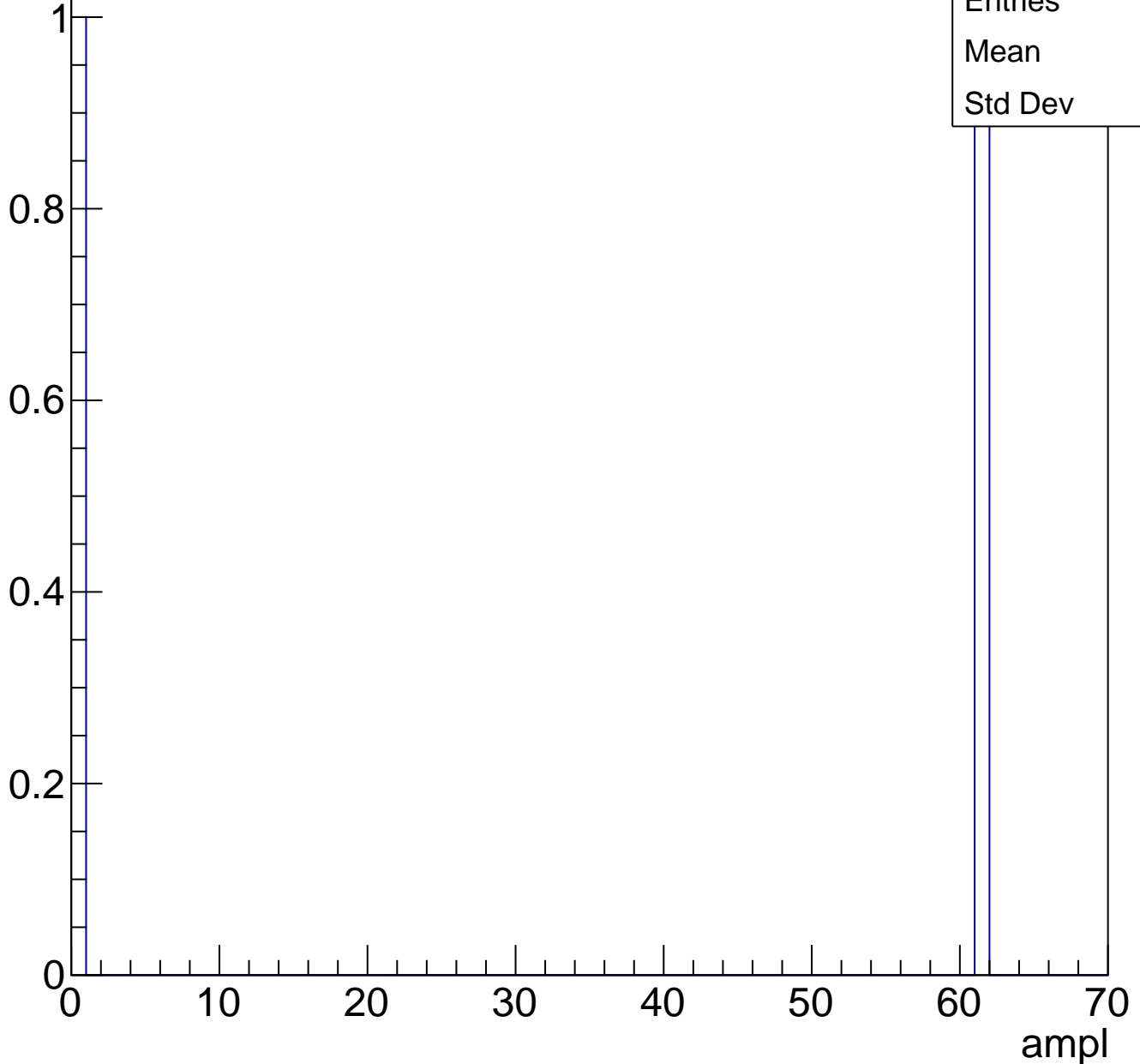




# B0L000S, U10-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch101, adc0

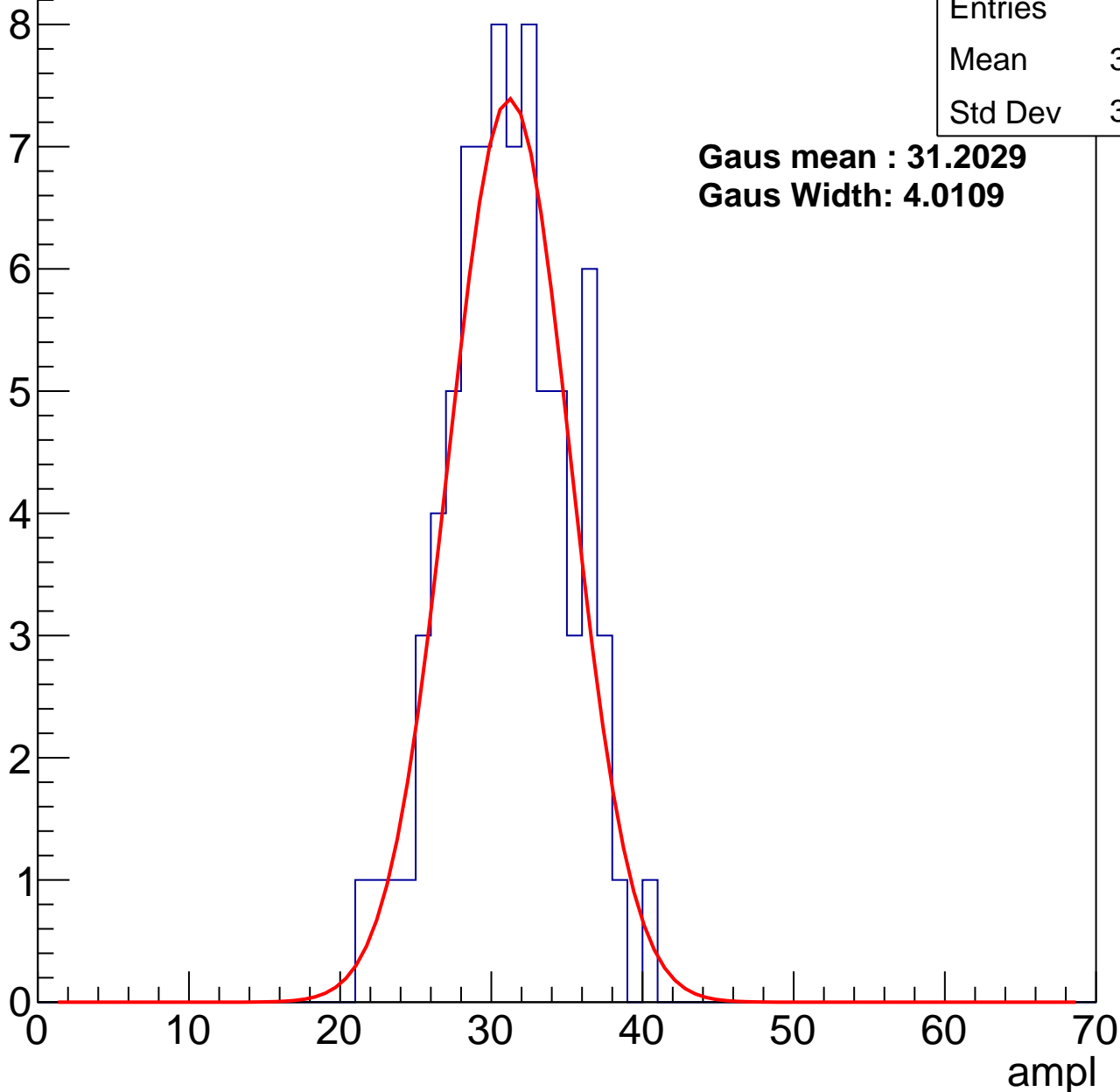
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	30.66
Std Dev	3.917

**Gaus mean : 31.2029**

**Gaus Width: 4.0109**



# B0L000S, U10-ch101, adc1

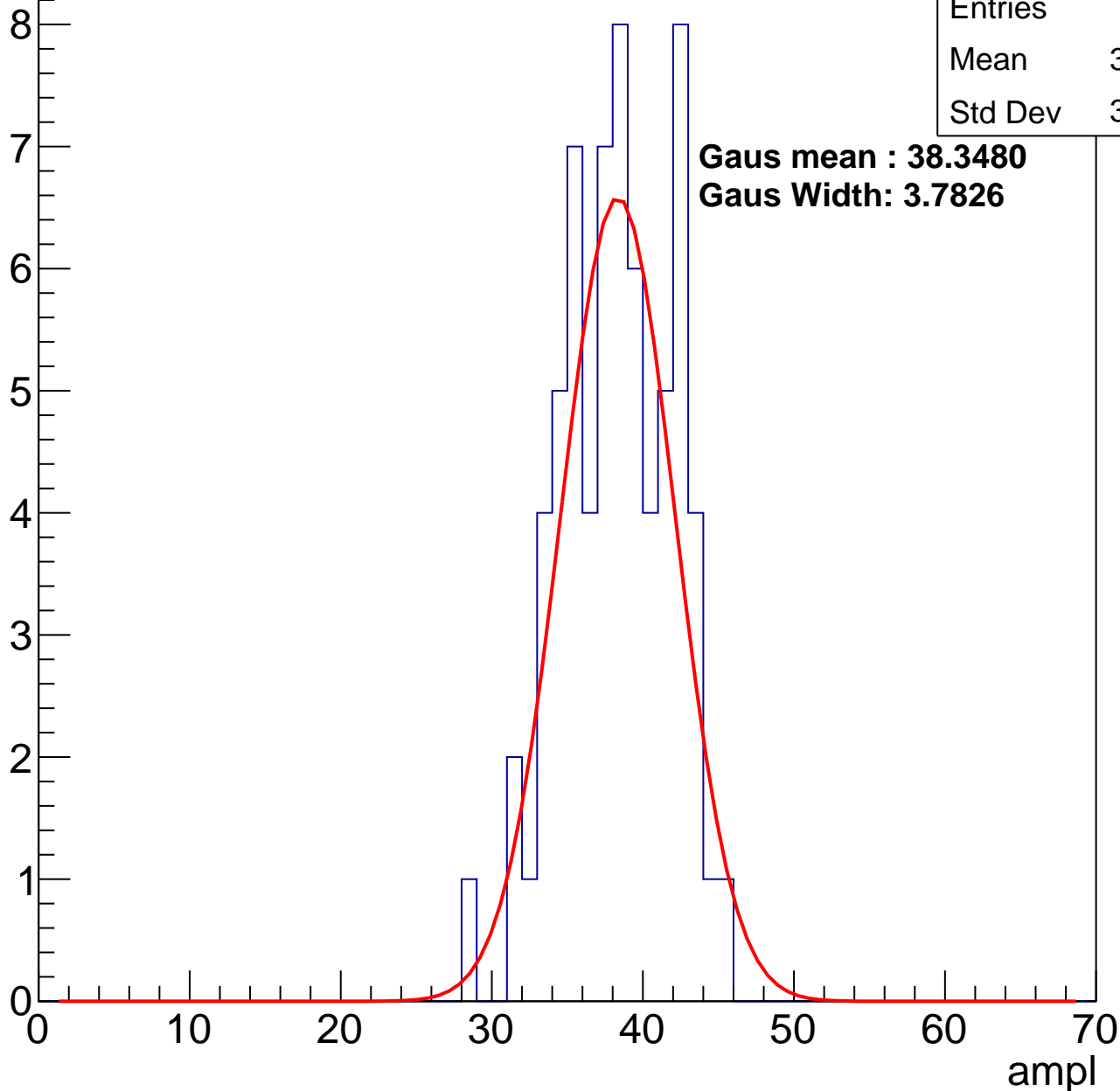
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.82
Std Dev	3.593

**Gaus mean : 38.3480**

**Gaus Width: 3.7826**



# B0L000S, U10-ch101, adc2

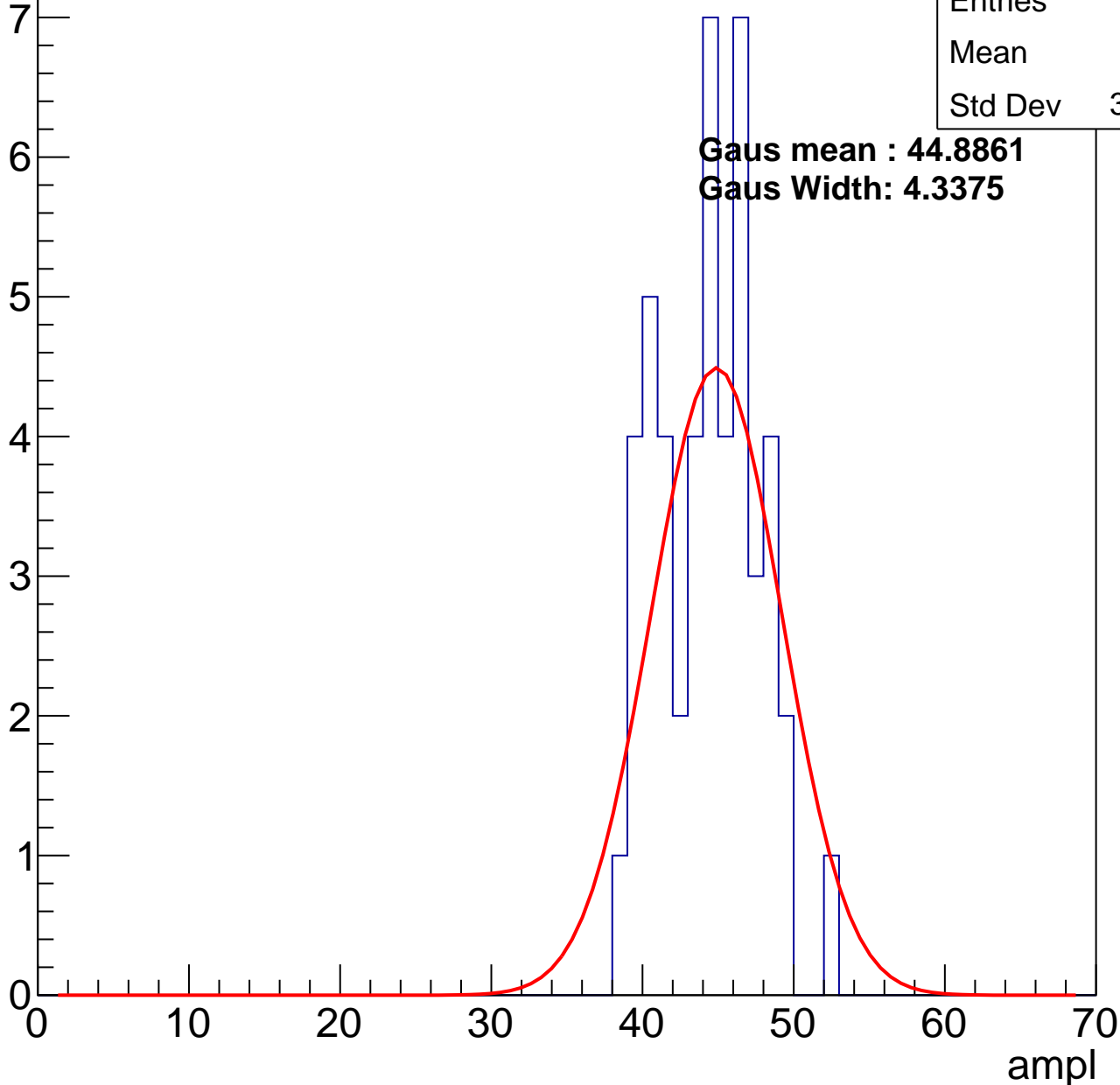
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	43.9
Std Dev	3.229

**Gaus mean : 44.8861**

**Gaus Width: 4.3375**

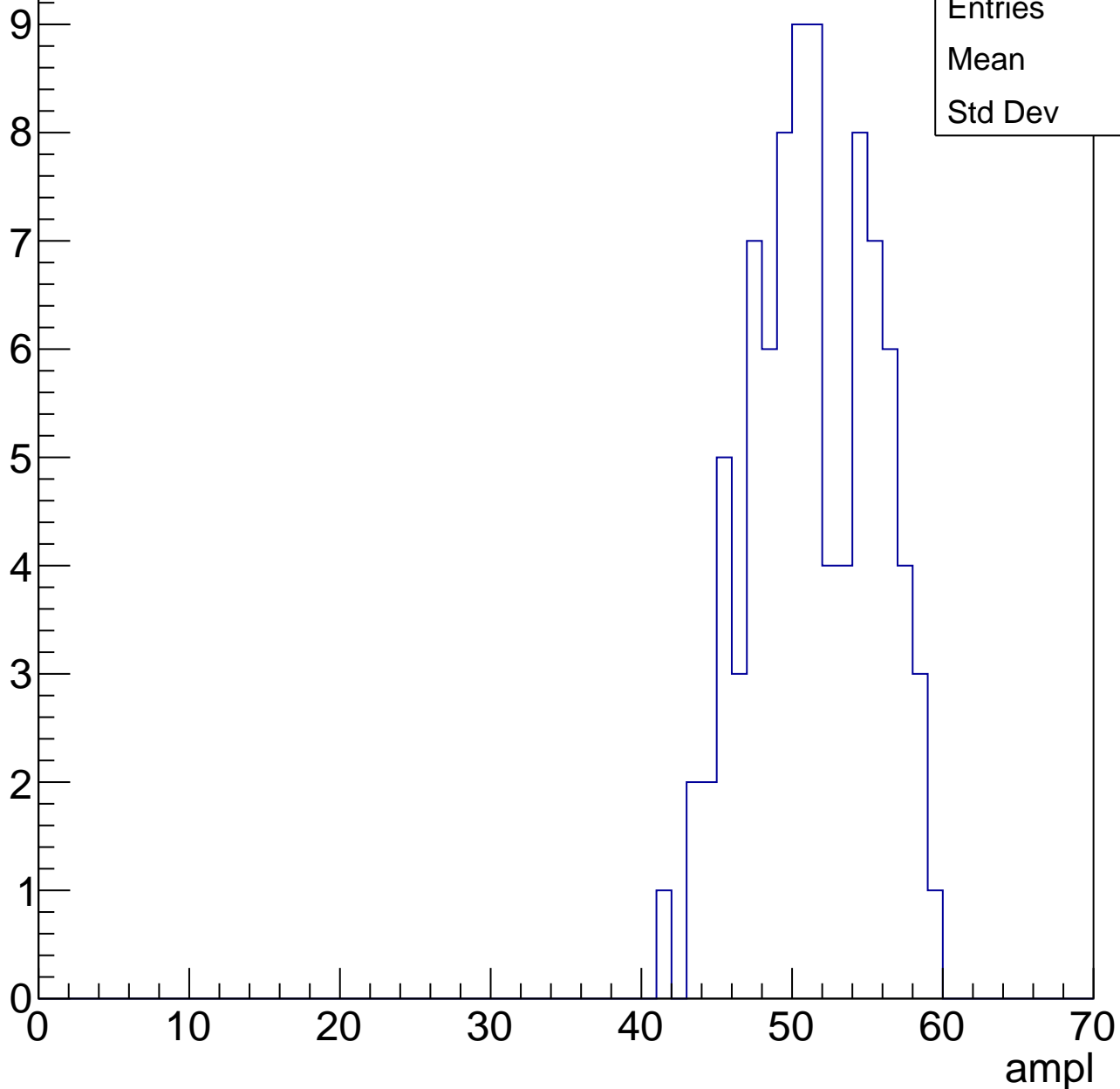


# B0L000S, U10-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	89
Mean	50.9
Std Dev	4.1

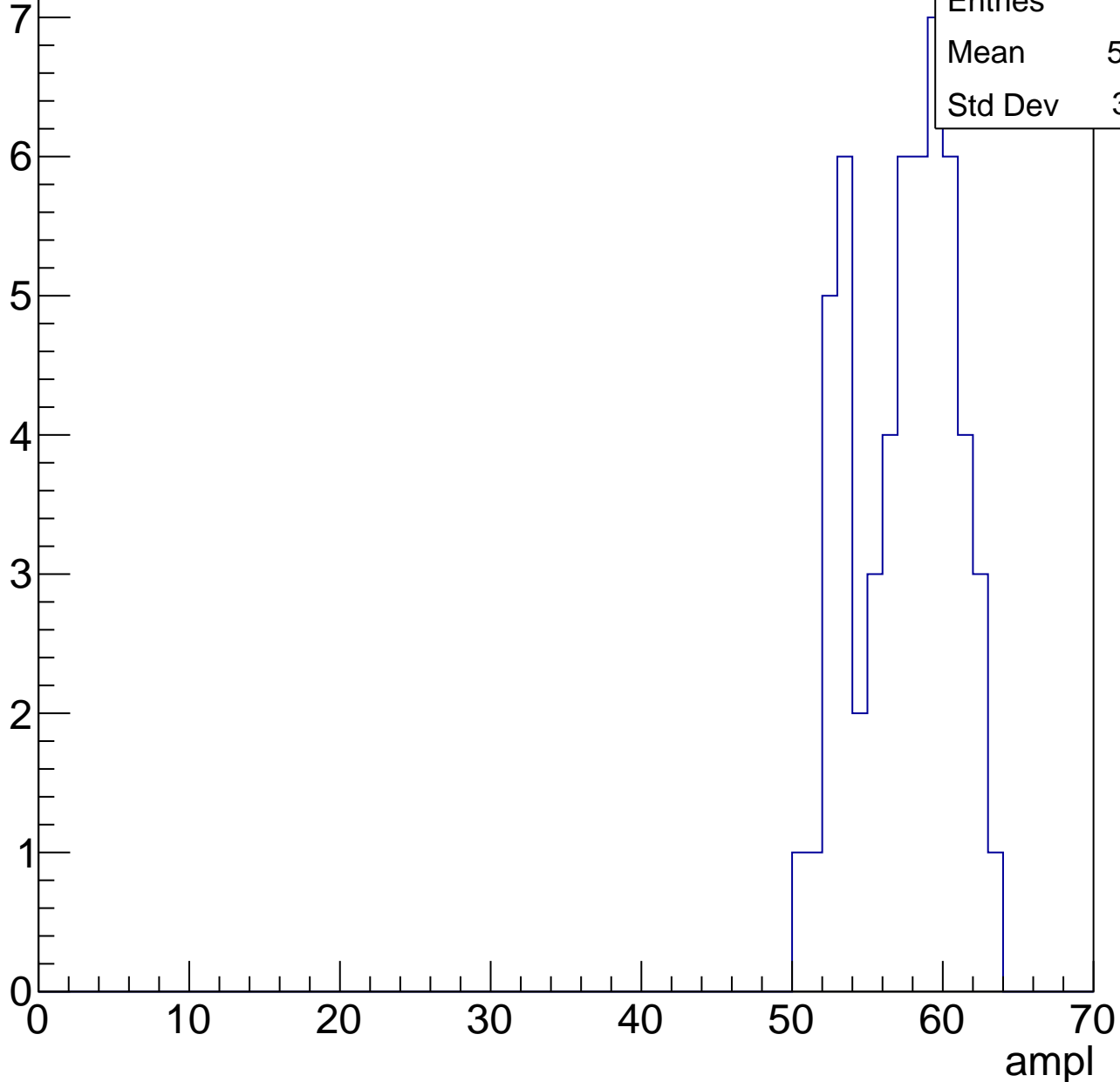


# B0L000S, U10-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.95
Std Dev	3.311

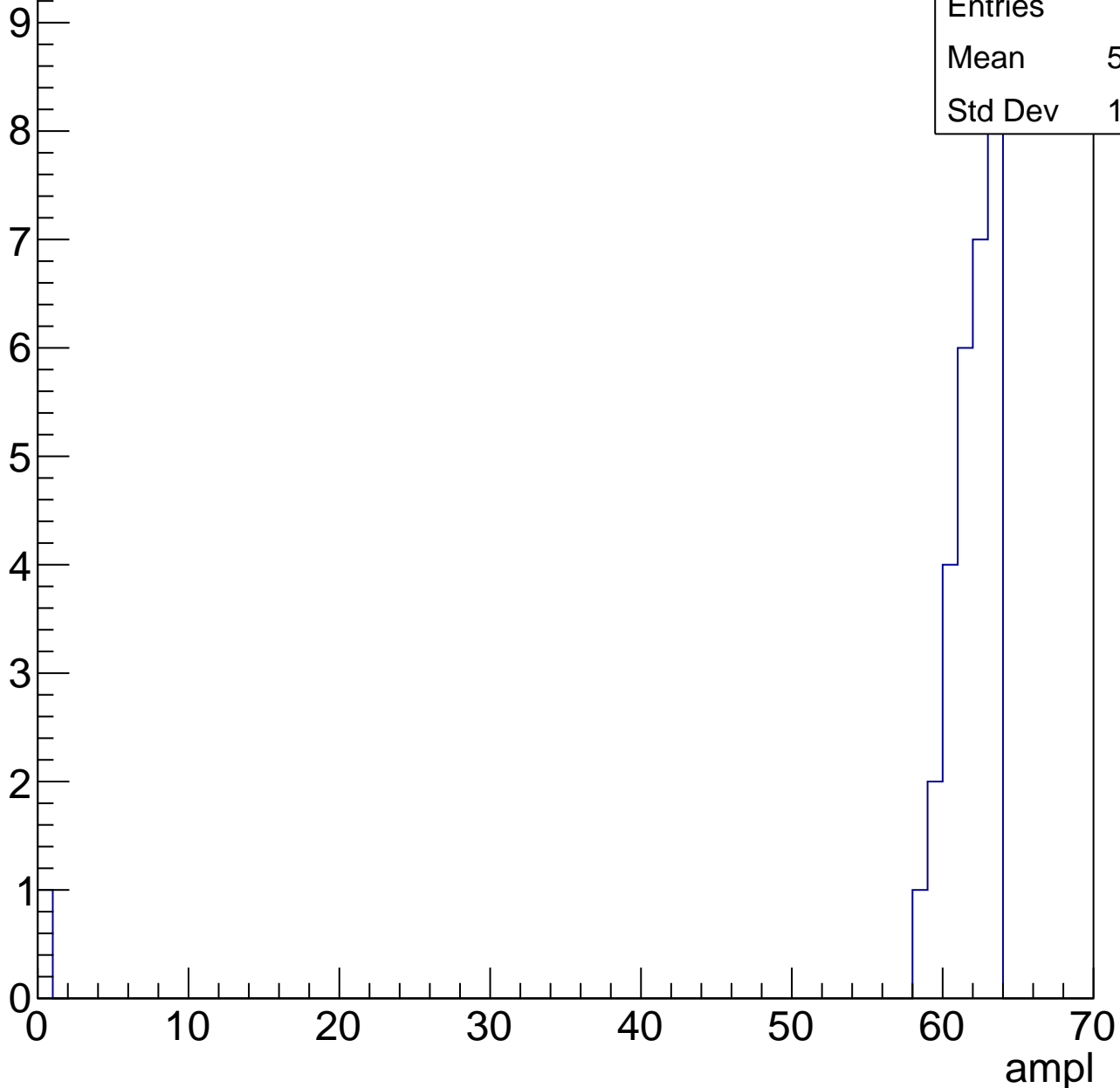


# B0L000S, U10-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	59.43
Std Dev	11.12



# B0L000S, U10-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch102, adc0

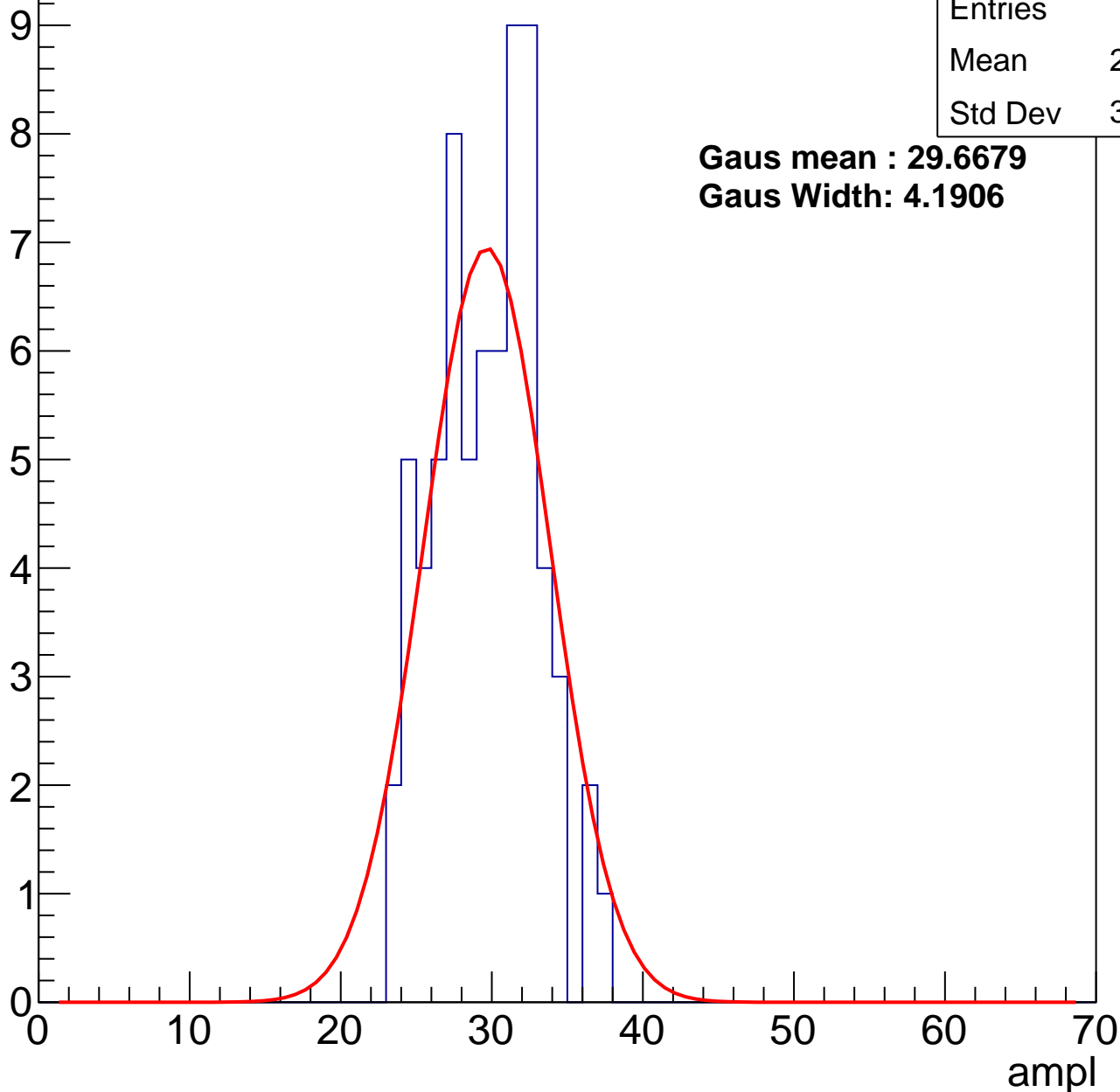
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.22
Std Dev	3.327

**Gaus mean : 29.6679**

**Gaus Width: 4.1906**



# B0L000S, U10-ch102, adc1

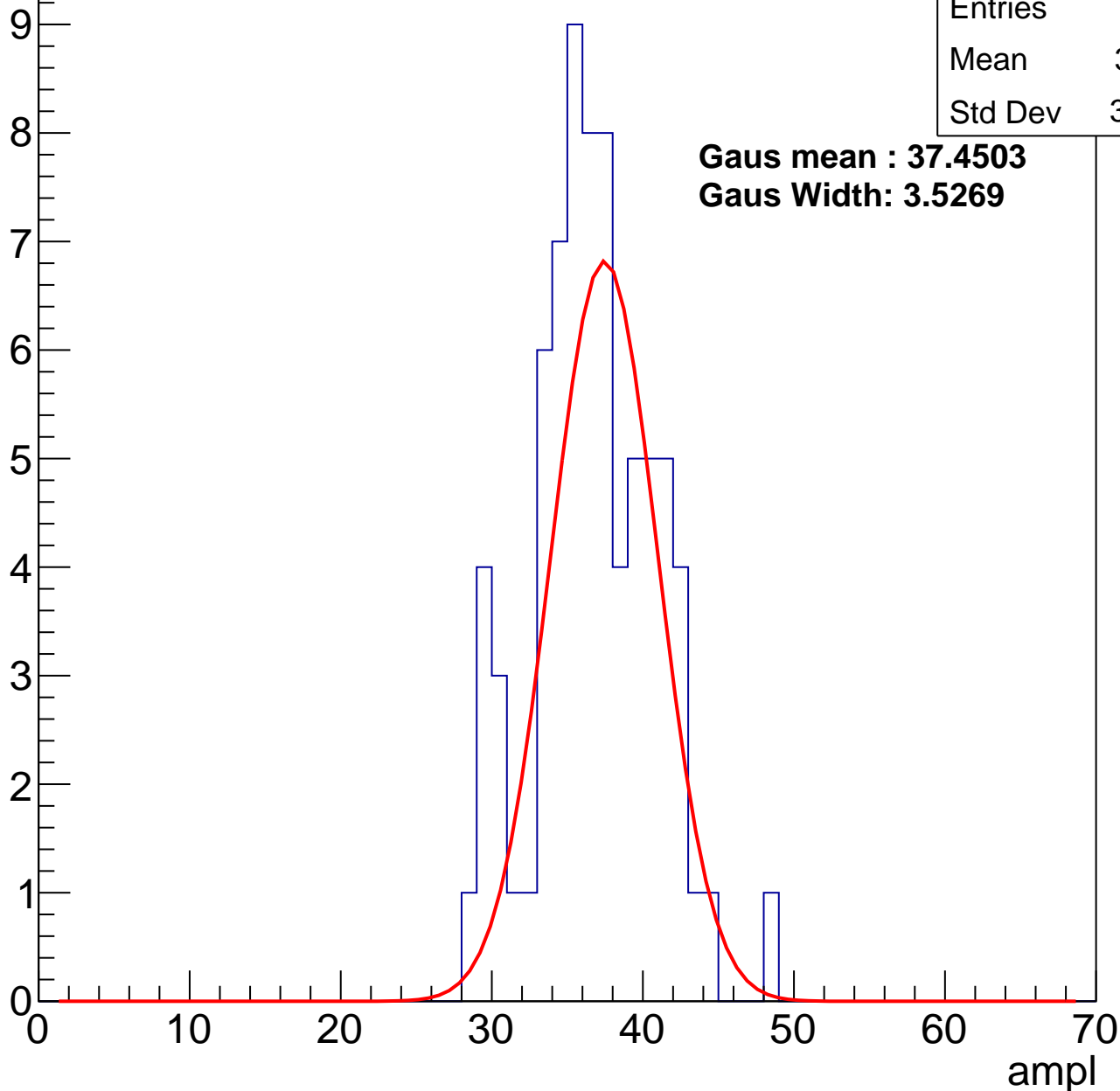
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.31
Std Dev	3.973

**Gaus mean : 37.4503**

**Gaus Width: 3.5269**

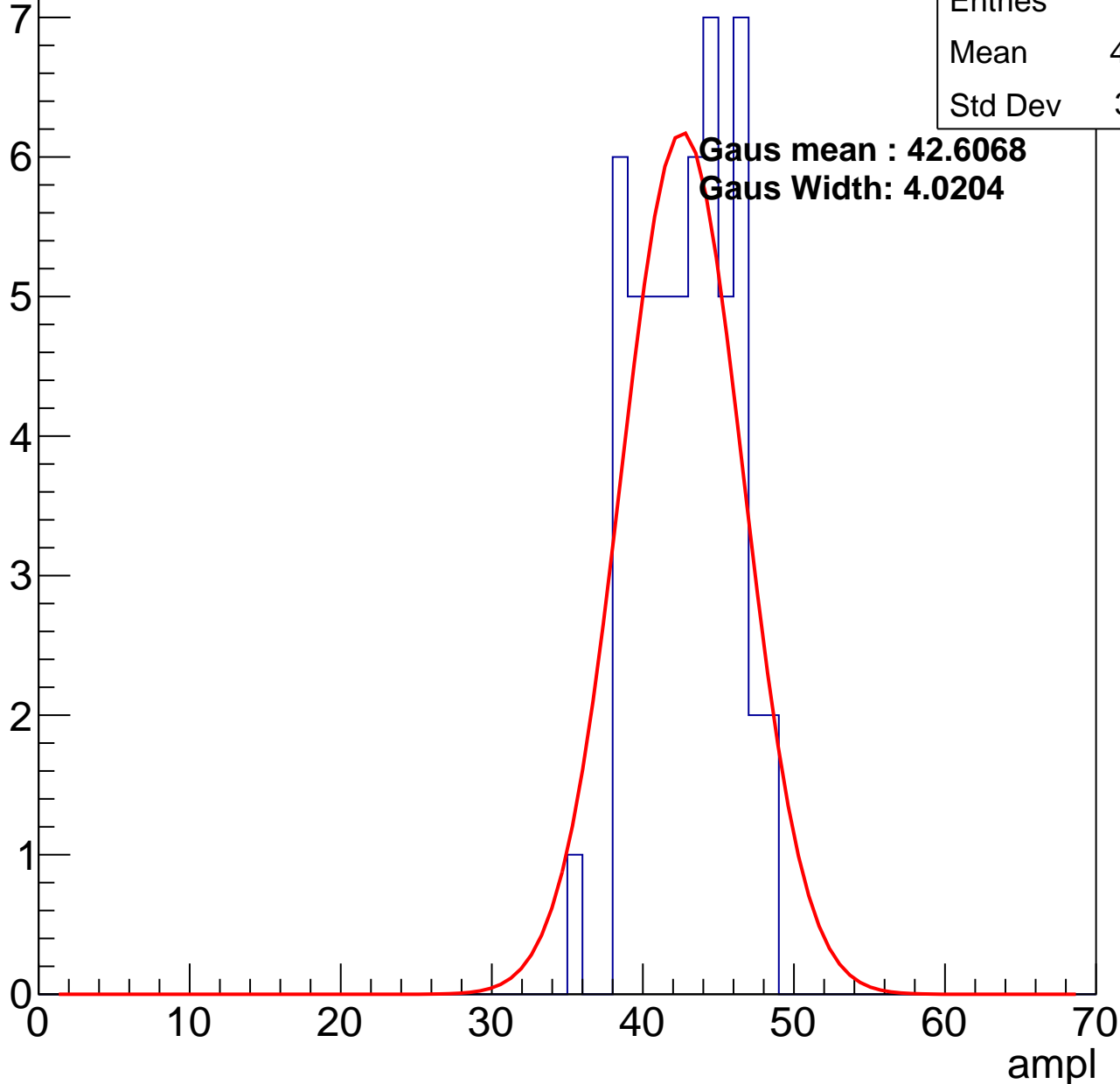


# B0L000S, U10-ch102, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	42.43
Std Dev	3.041



# B0L000S, U10-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	49.51
Std Dev	3.857

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

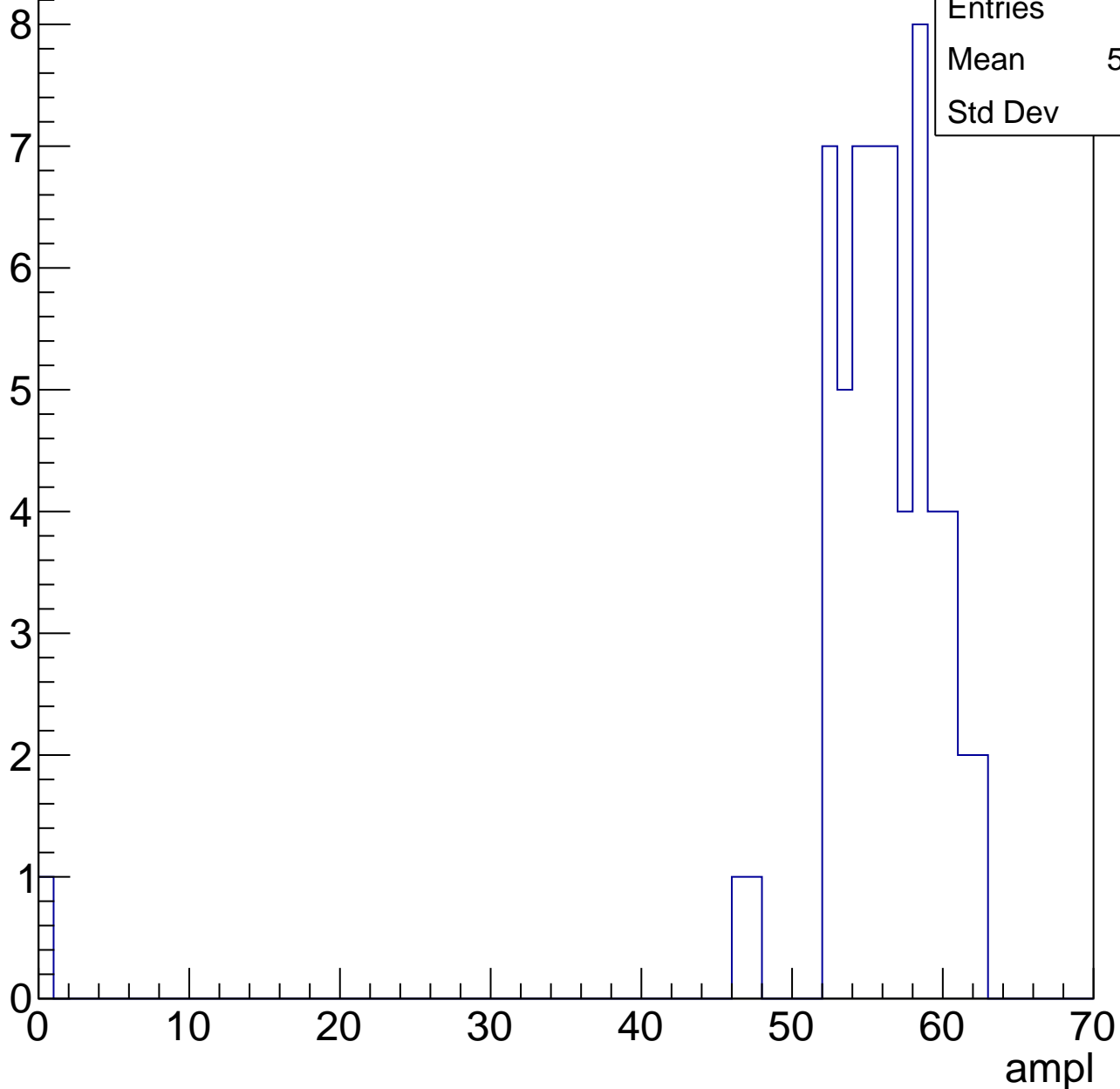
70

# B0L000S, U10-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.85
Std Dev	7.84

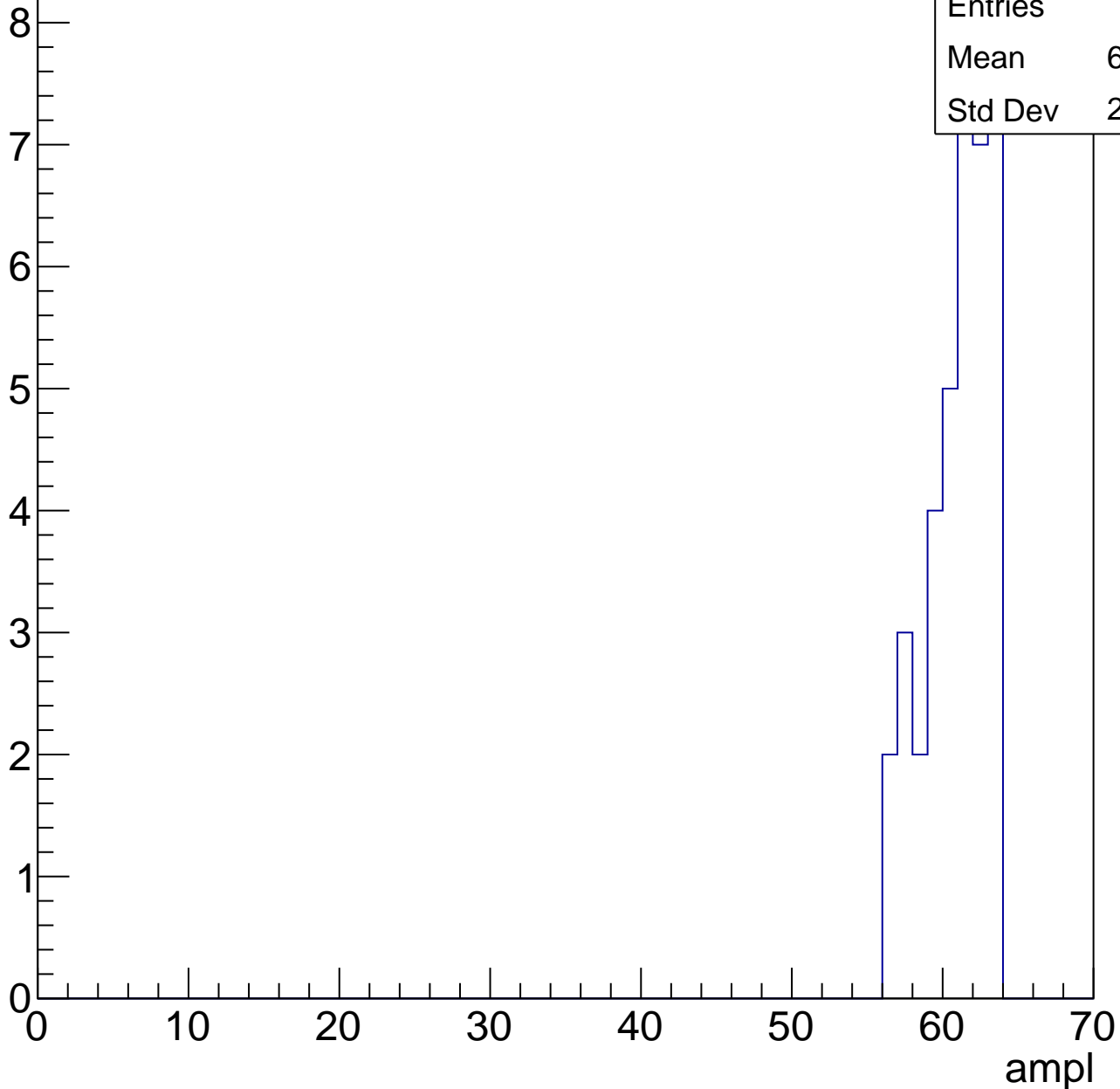


# B0L000S, U10-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	60.54
Std Dev	2.074



# B0L000S, U10-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

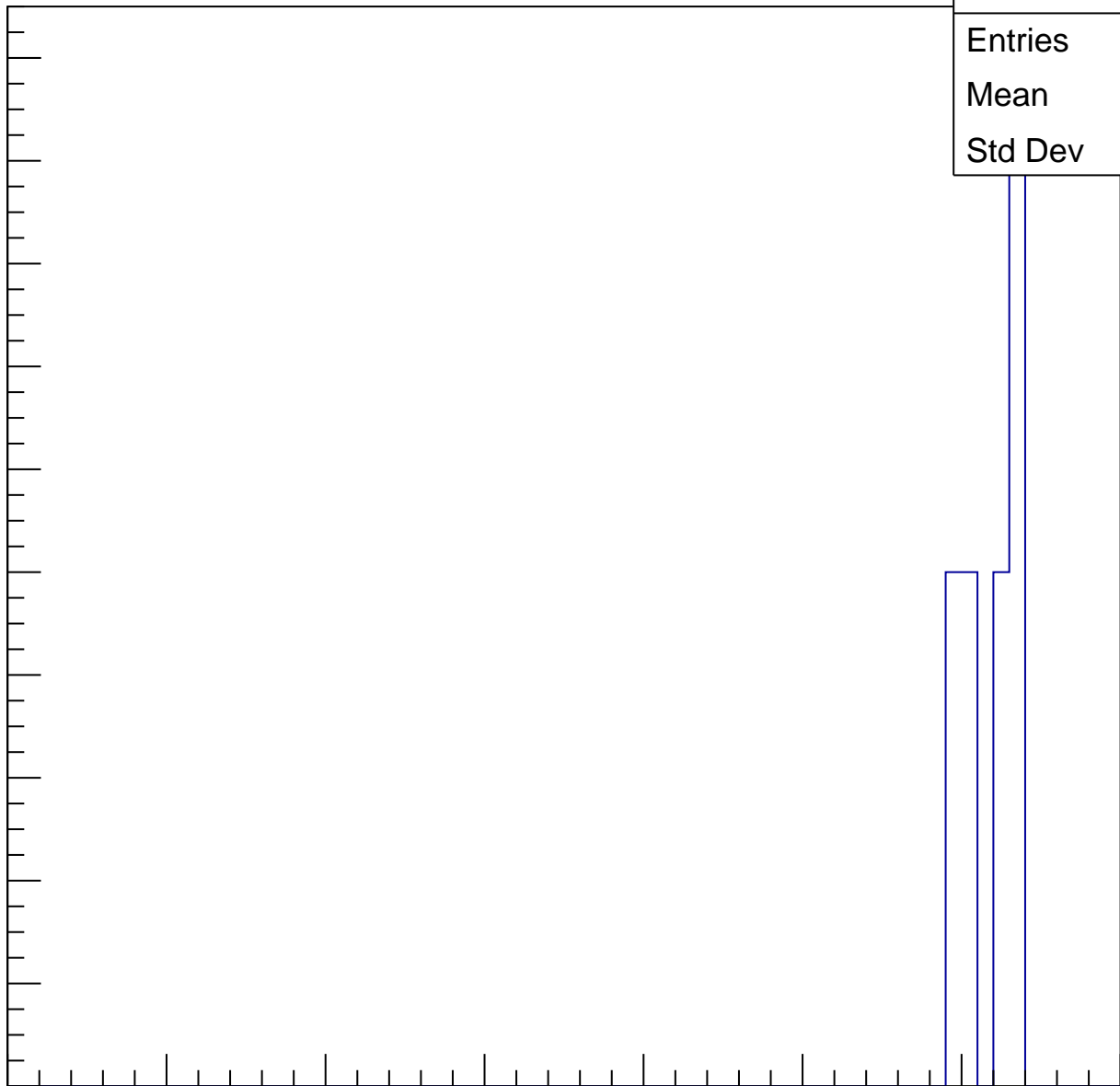
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.4
Std Dev	1.625

ampl

0 10 20 30 40 50 60 70

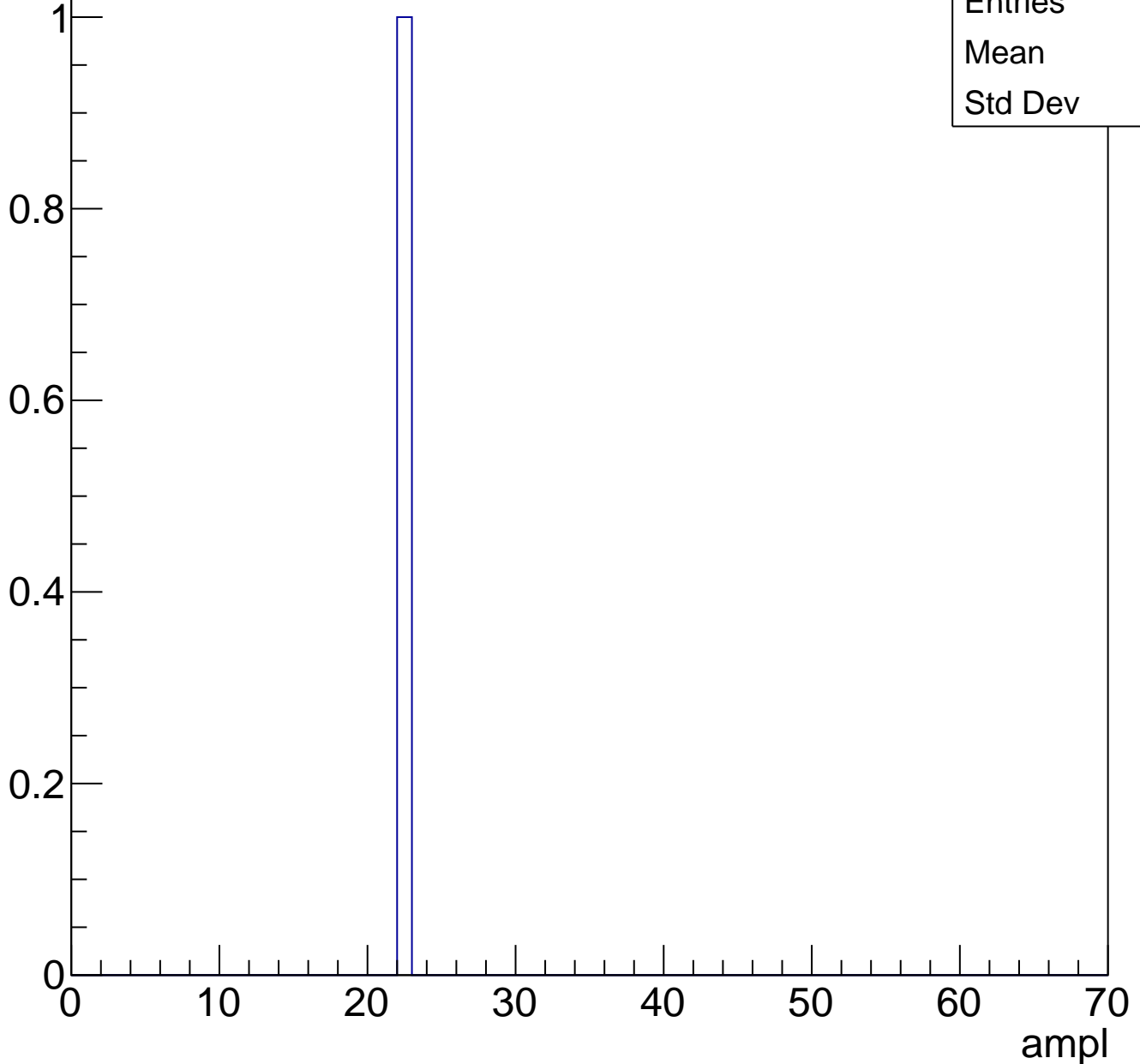




# B0L000S, U10-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L000S, U10-ch103, adc0

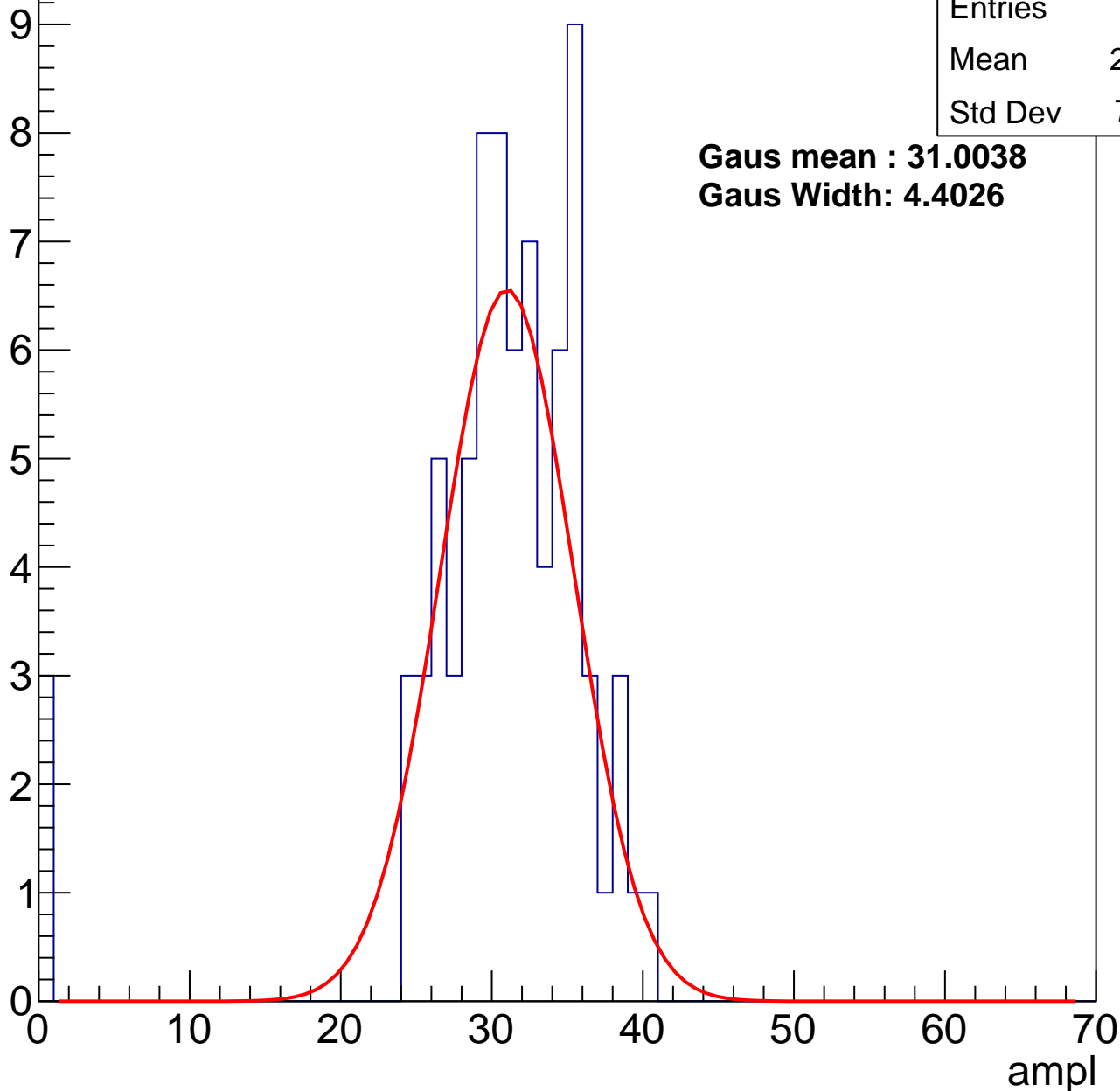
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	29.99
Std Dev	7.061

**Gaus mean : 31.0038**

**Gaus Width: 4.4026**



# B0L000S, U10-ch103, adc1

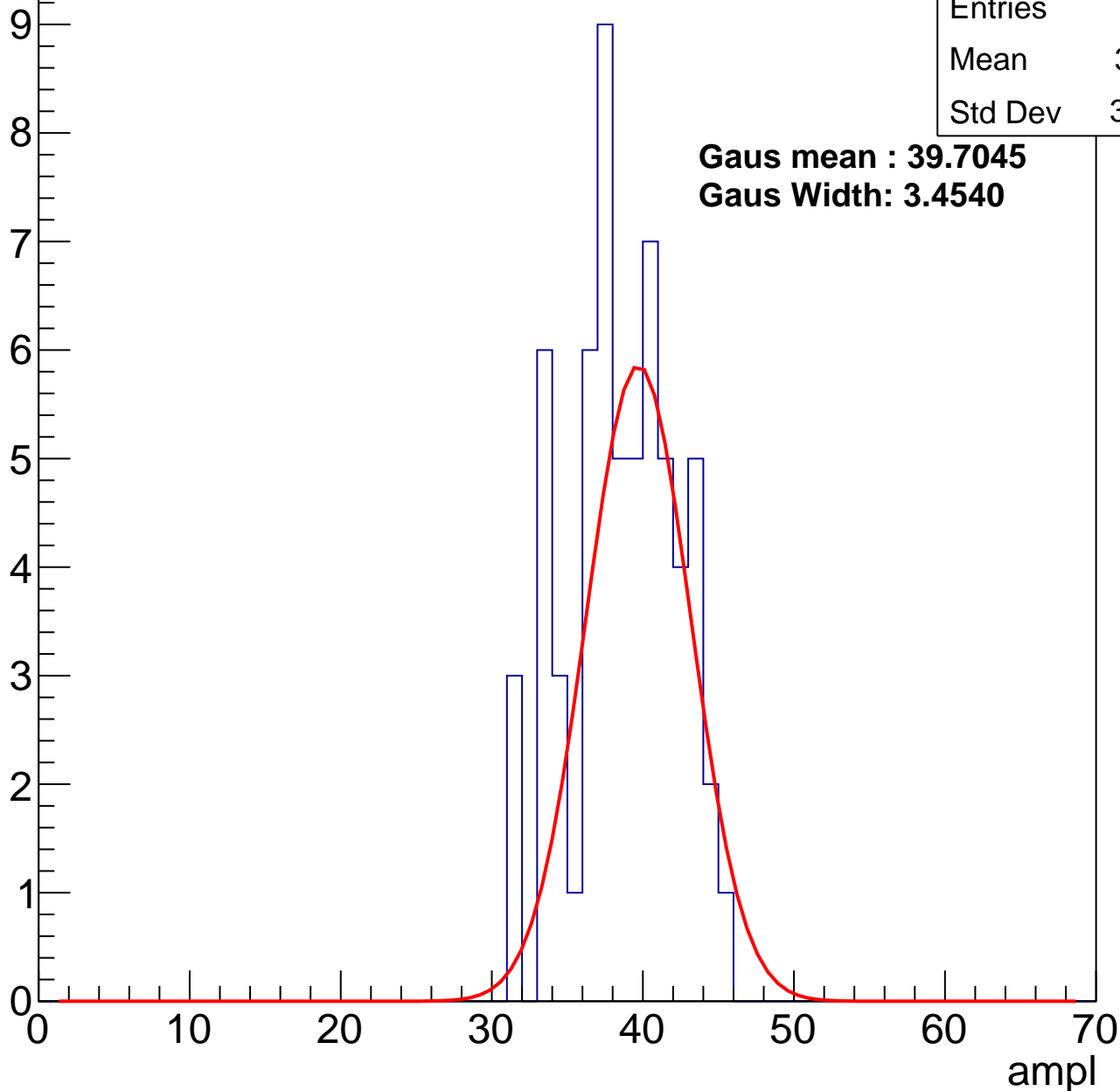
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.11
Std Dev	3.534

**Gaus mean : 39.7045**

**Gaus Width: 3.4540**



# B0L000S, U10-ch103, adc2

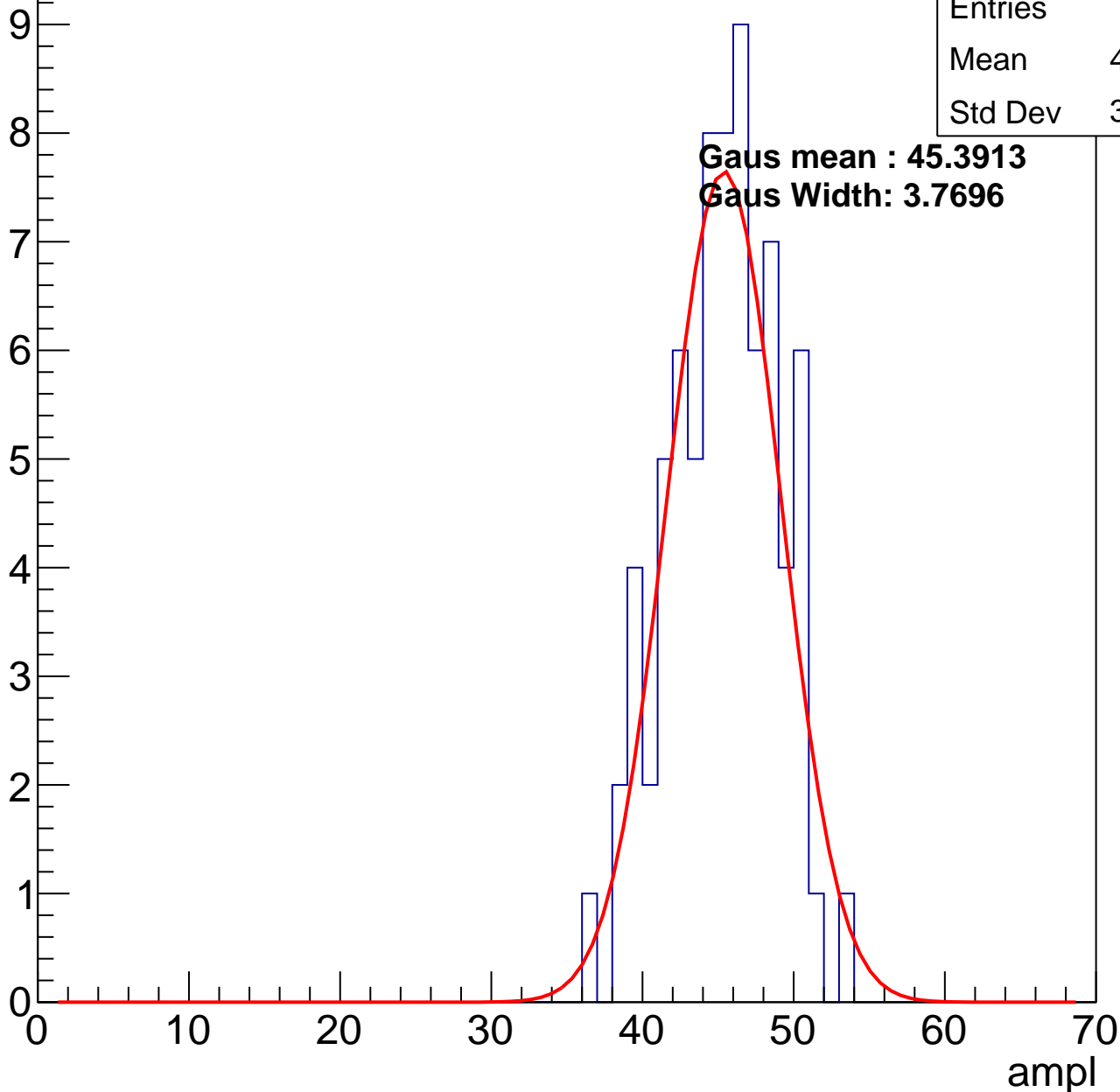
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.85
Std Dev	3.547

**Gaus mean : 45.3913**

**Gaus Width: 3.7696**

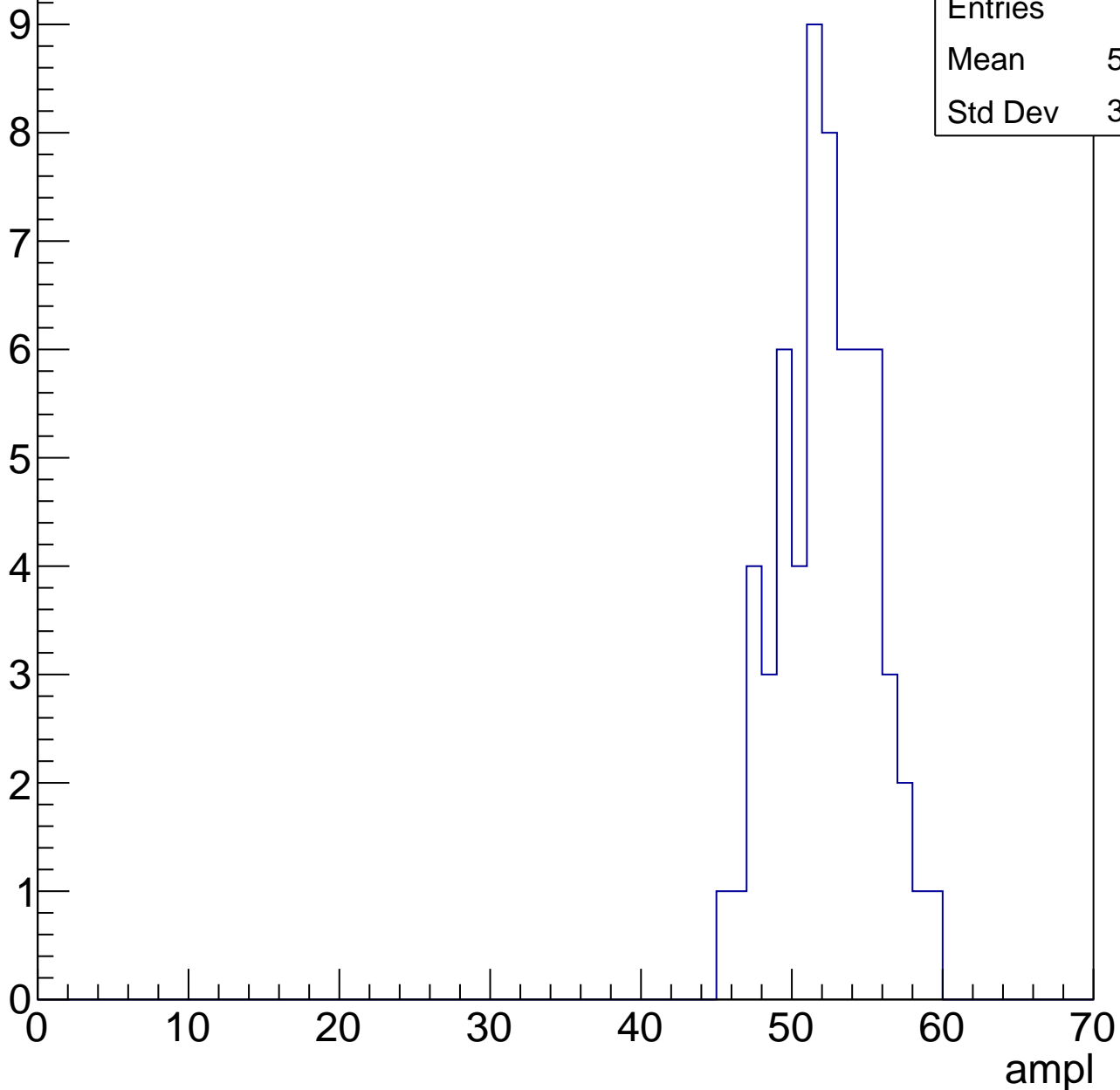


# B0L000S, U10-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

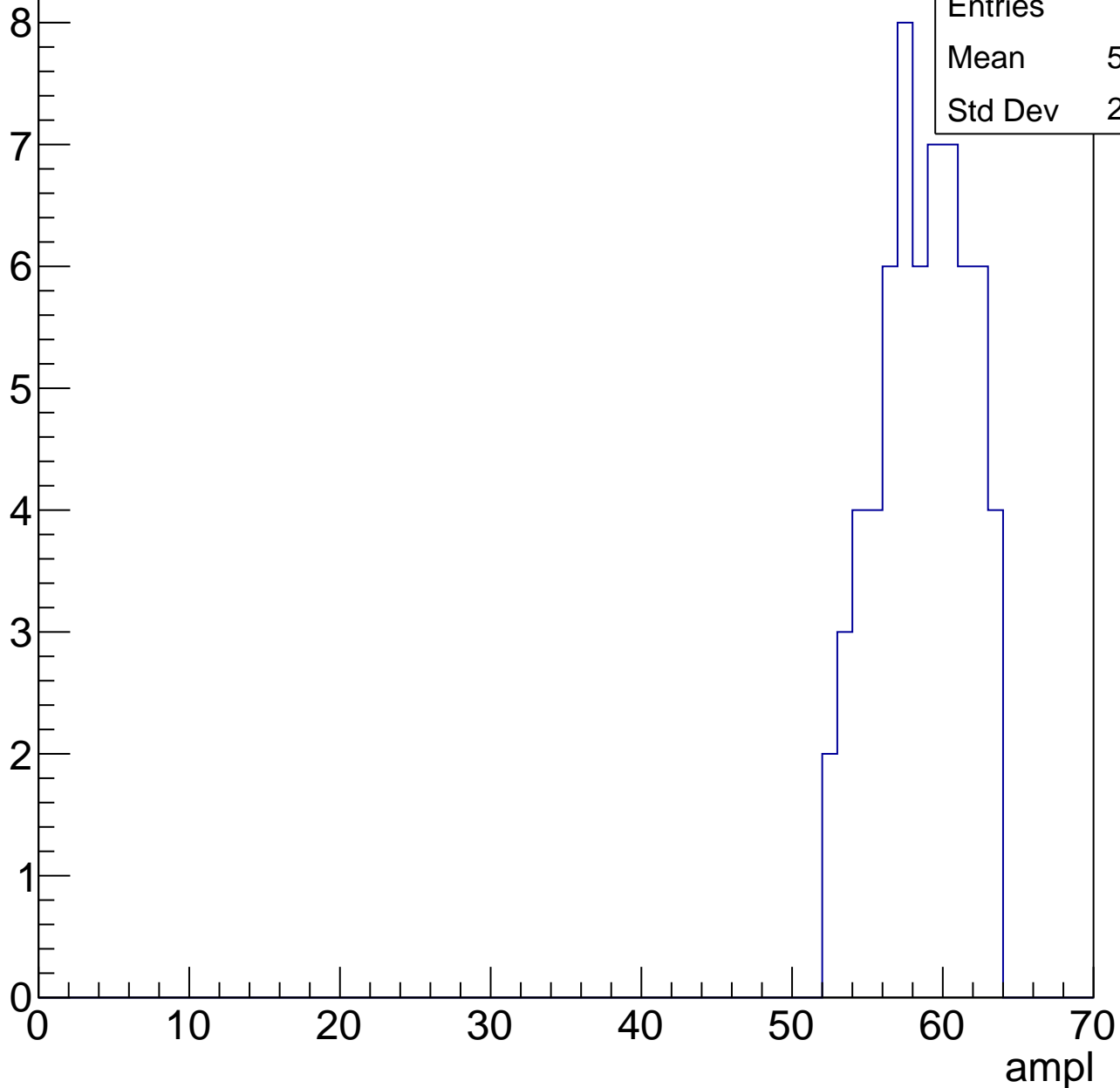
Entries	61
Mean	51.85
Std Dev	3.077



# B0L000S, U10-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

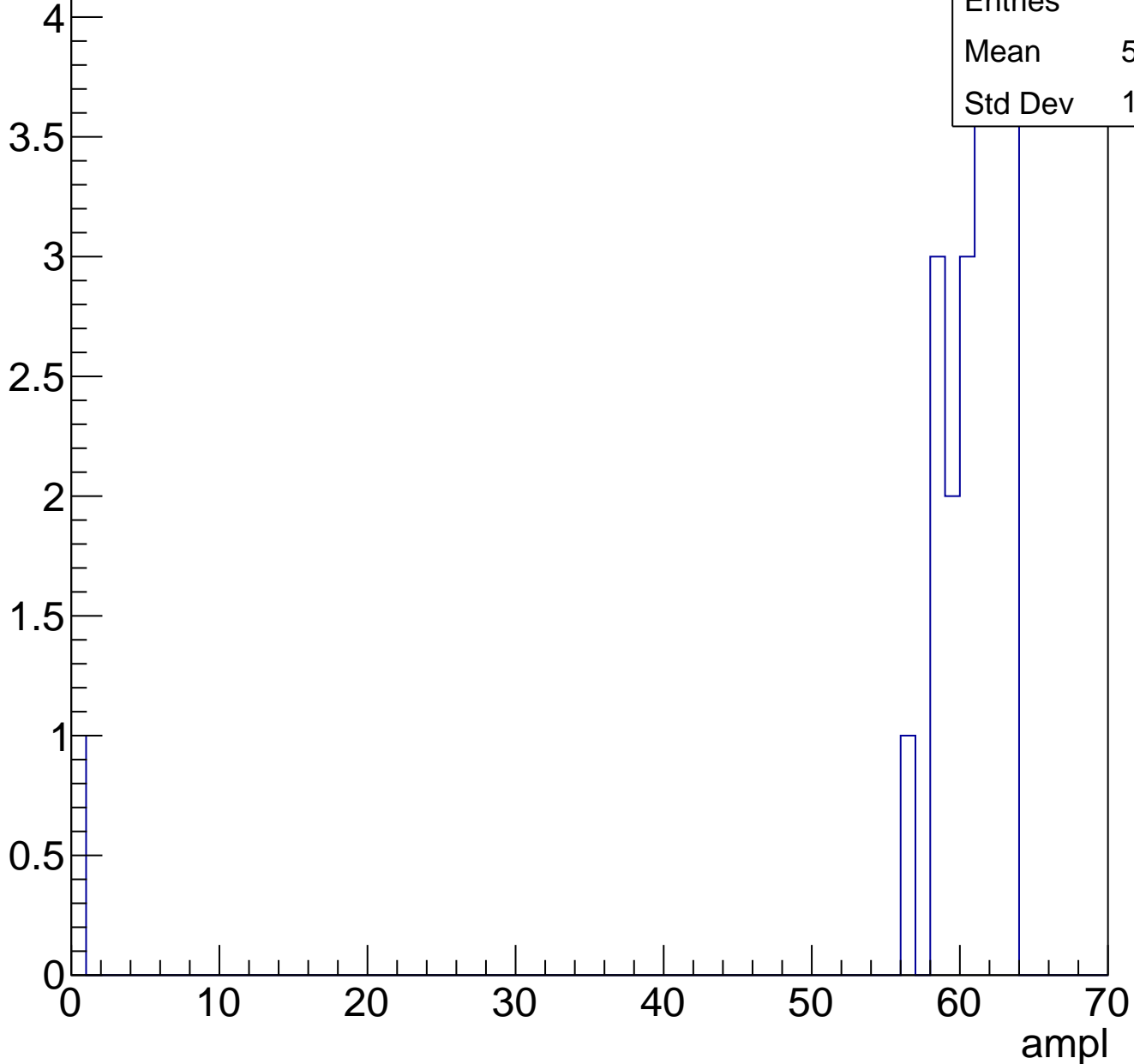
Entry



# B0L000S, U10-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U10-ch104, adc0

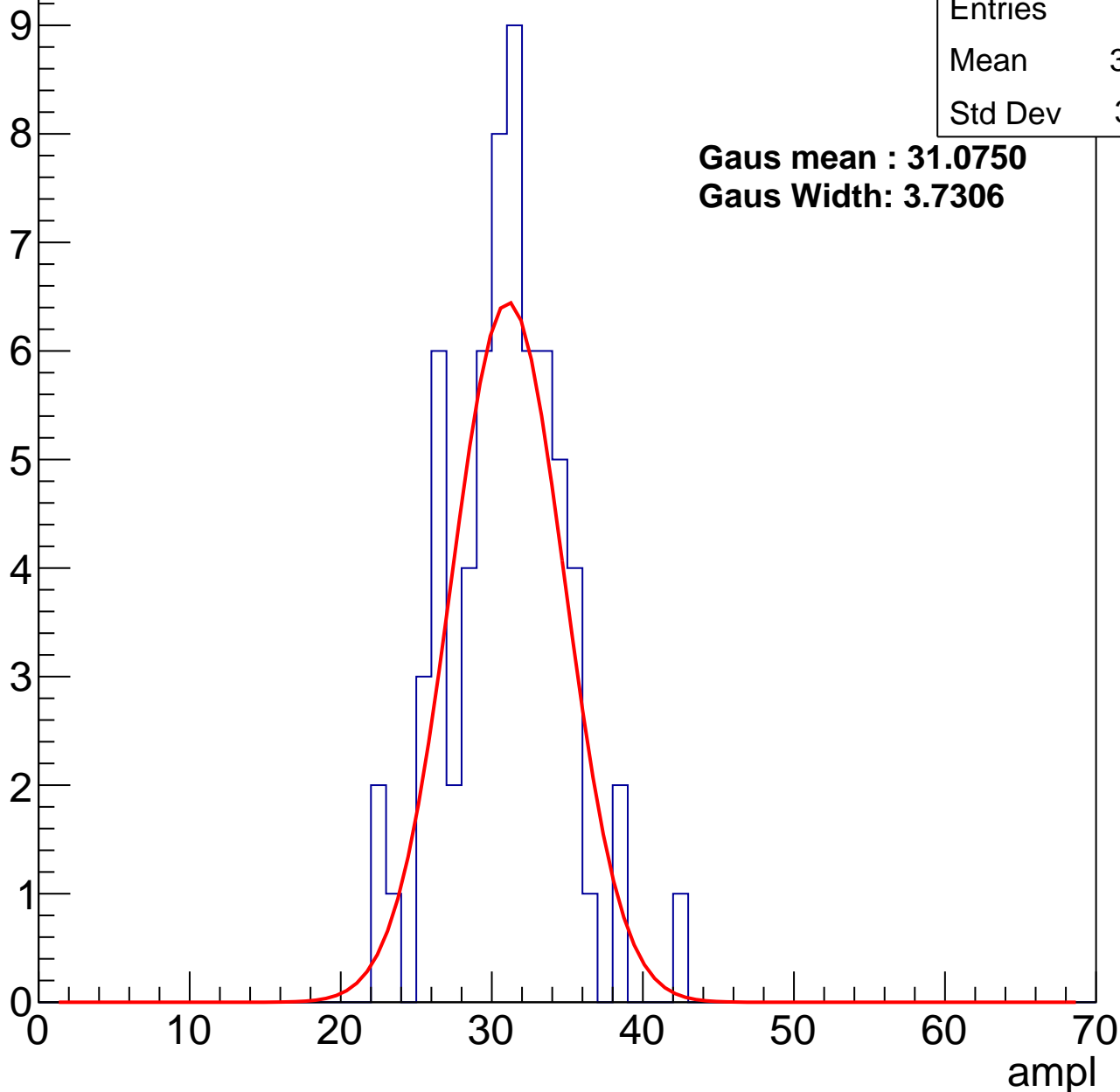
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	30.47
Std Dev	3.791

**Gaus mean : 31.0750**

**Gaus Width: 3.7306**



# B0L000S, U10-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	37.36
Std Dev	3.333

**Gaus mean : 37.1343**

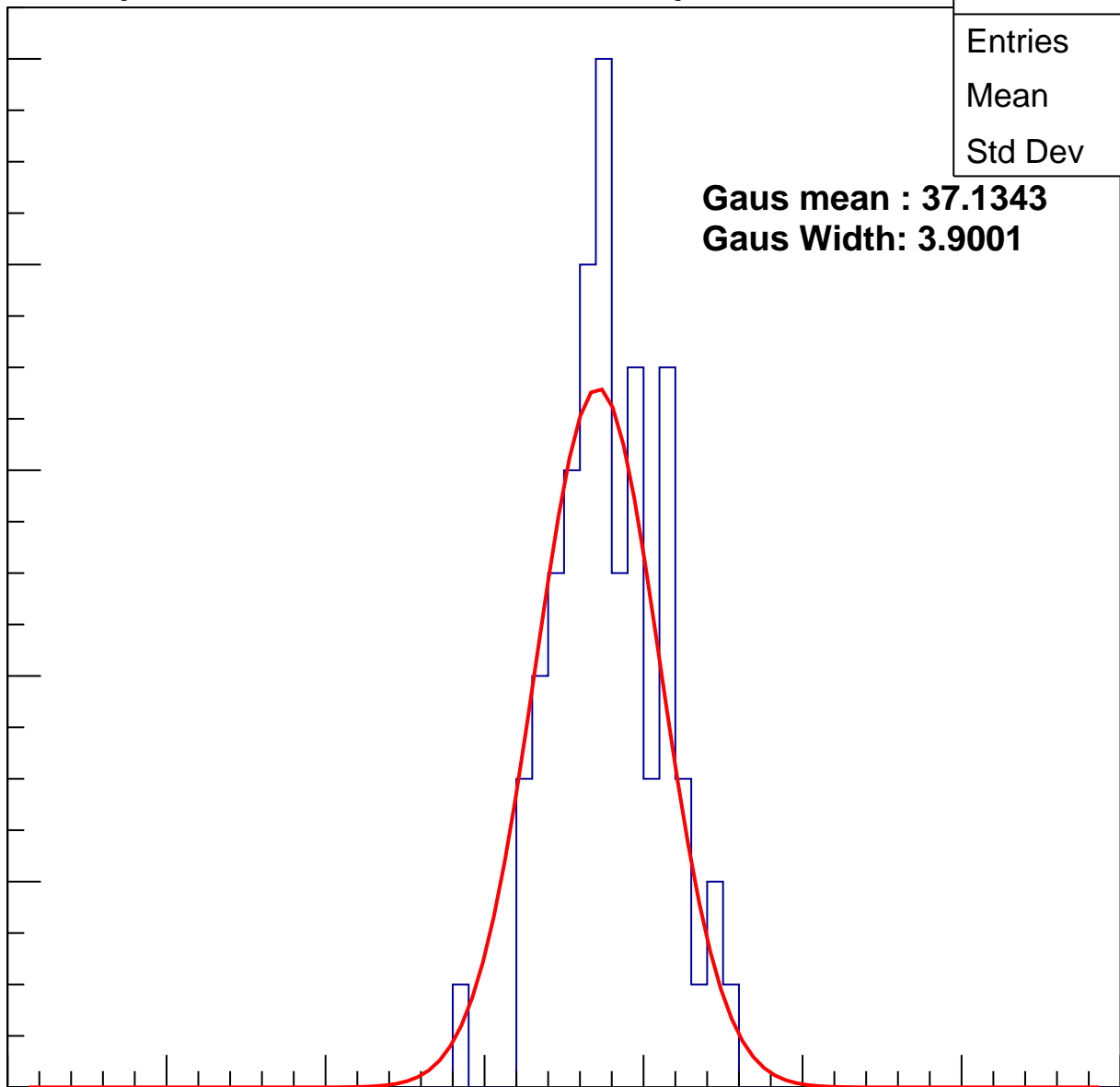
**Gaus Width: 3.9001**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch104, adc2

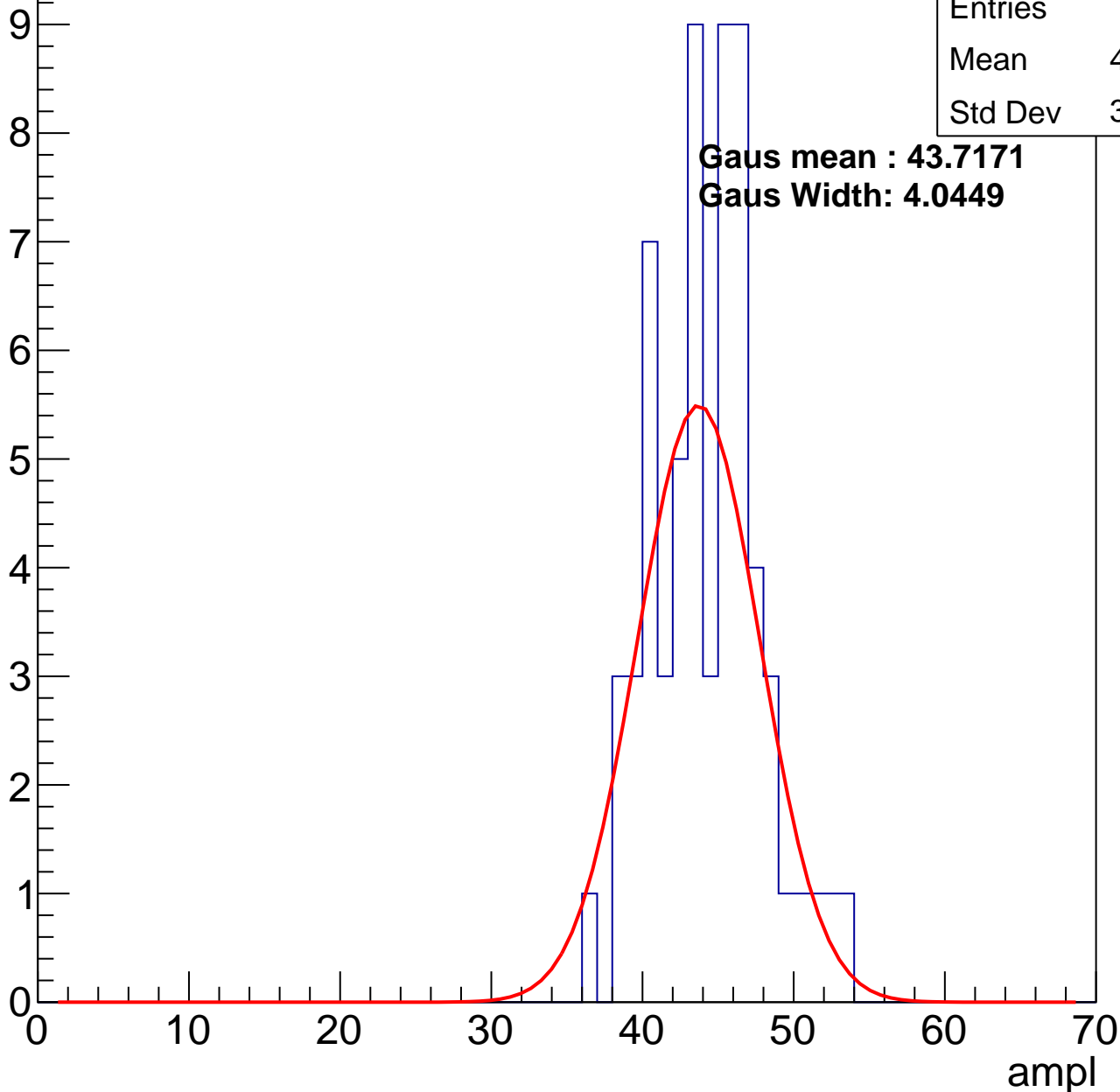
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.83
Std Dev	3.529

**Gaus mean : 43.7171**

**Gaus Width: 4.0449**

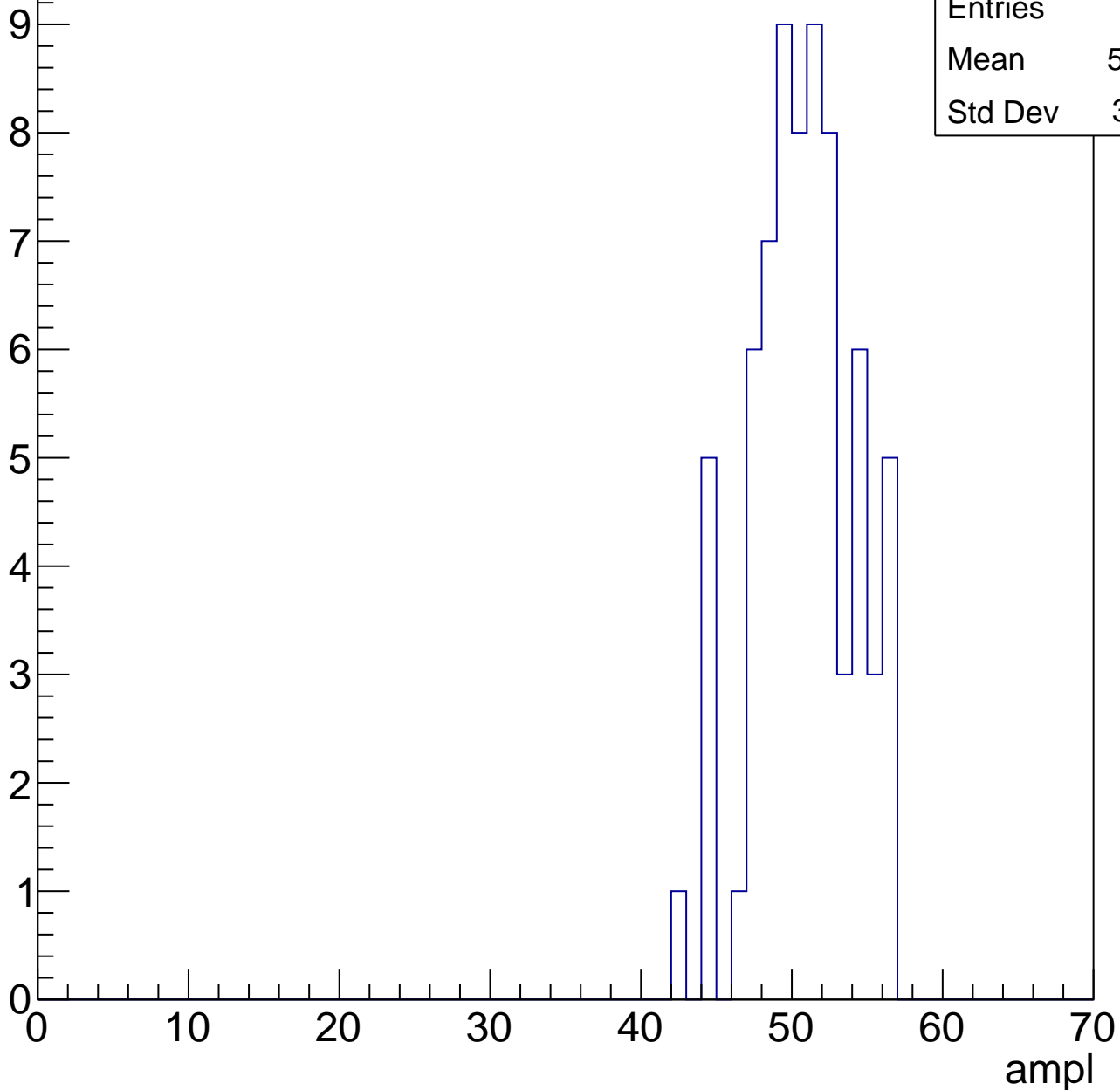


# B0L000S, U10-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	50.28
Std Dev	3.281

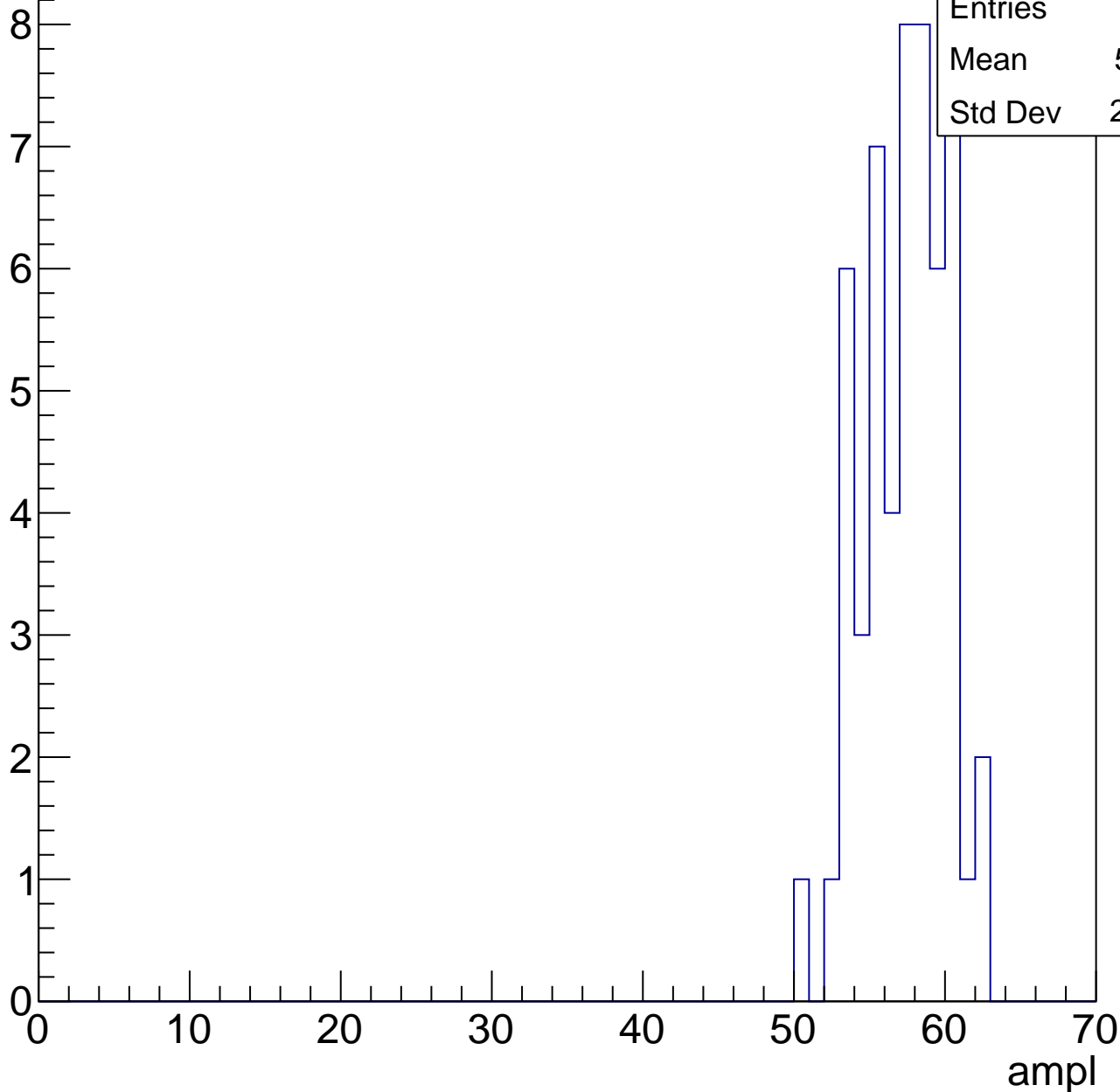


# B0L000S, U10-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.91
Std Dev	2.692

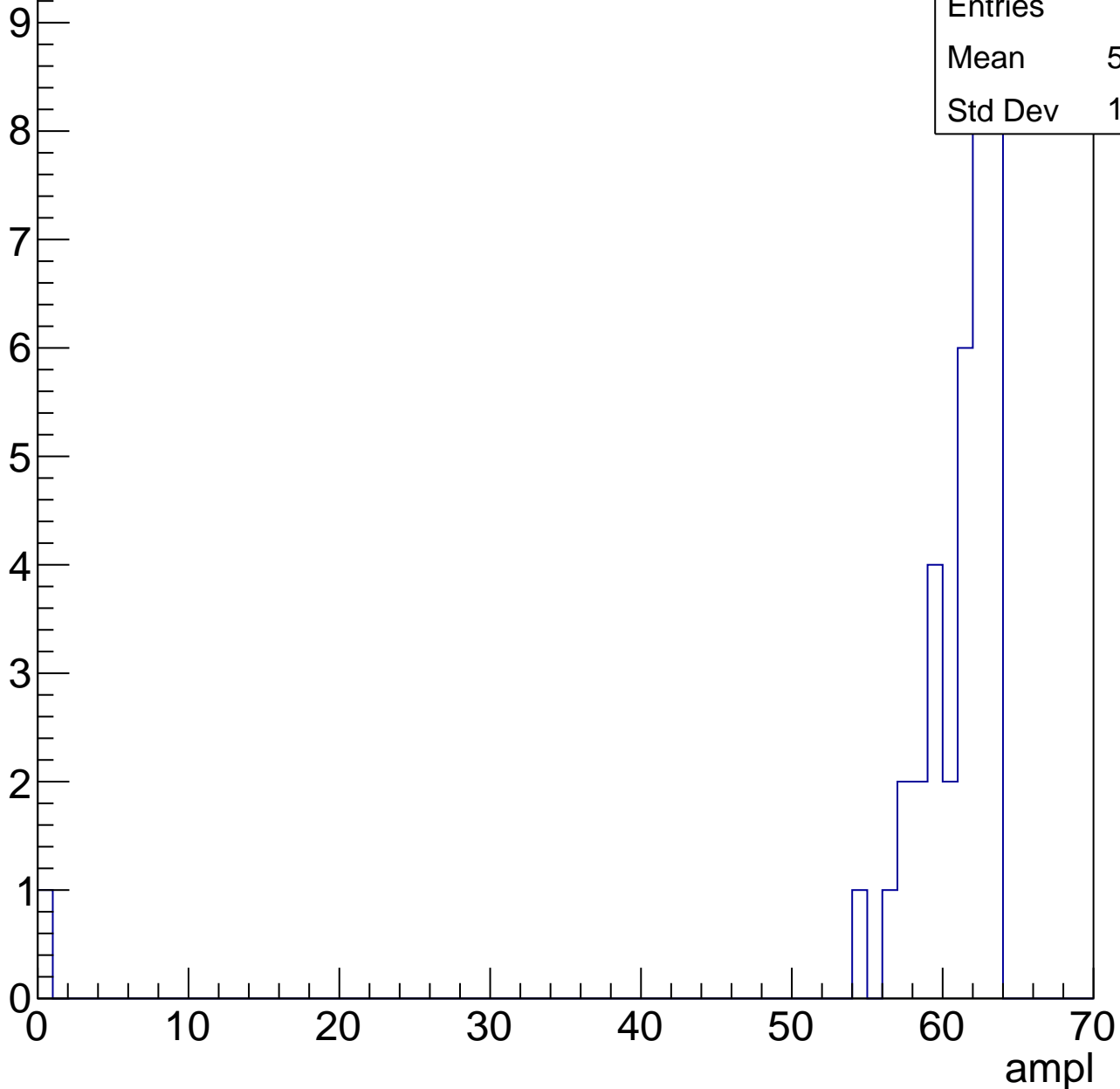


# B0L000S, U10-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

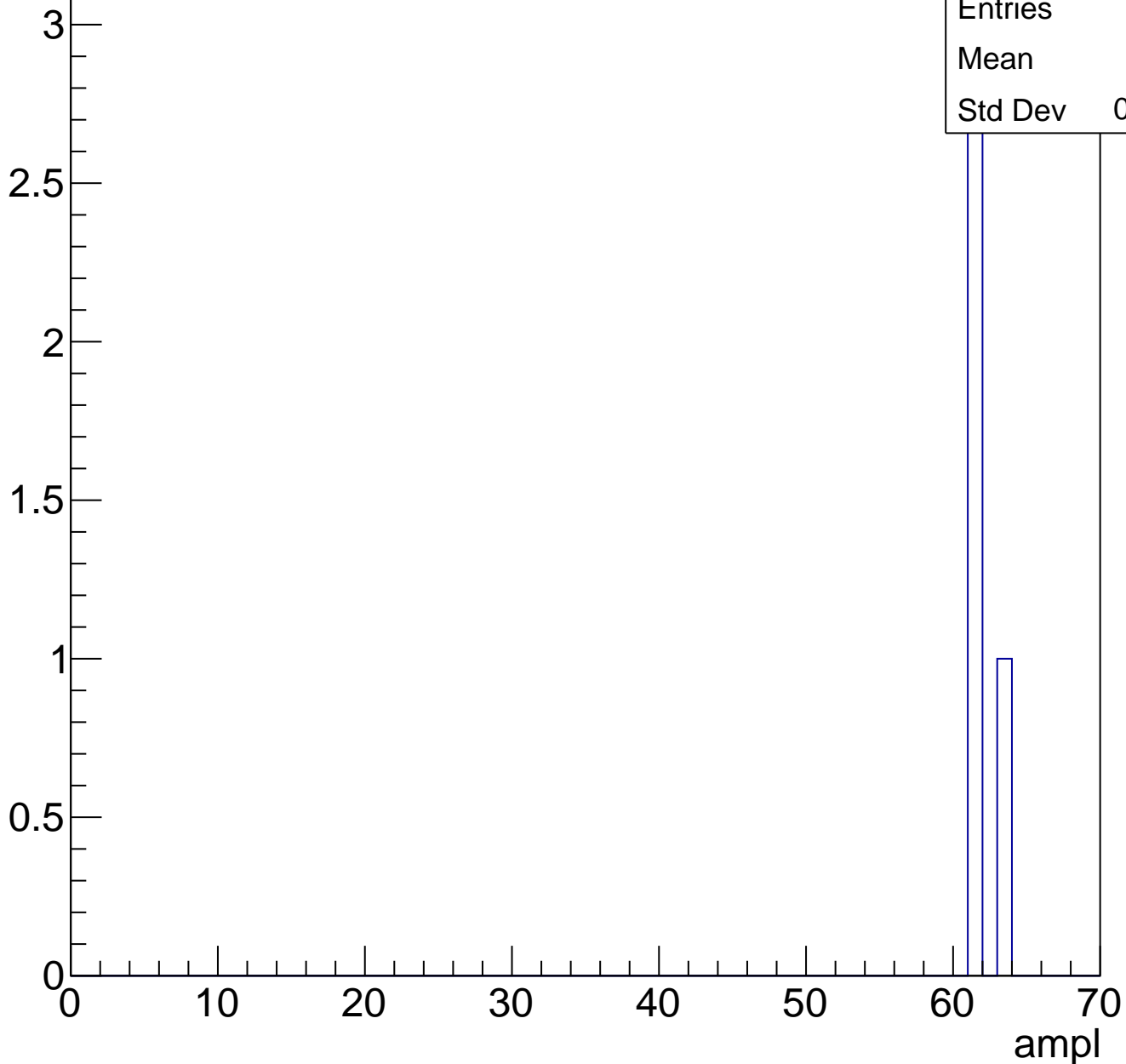
Entries	36
Mean	59.03
Std Dev	10.23



# B0L000S, U10-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch105, adc0

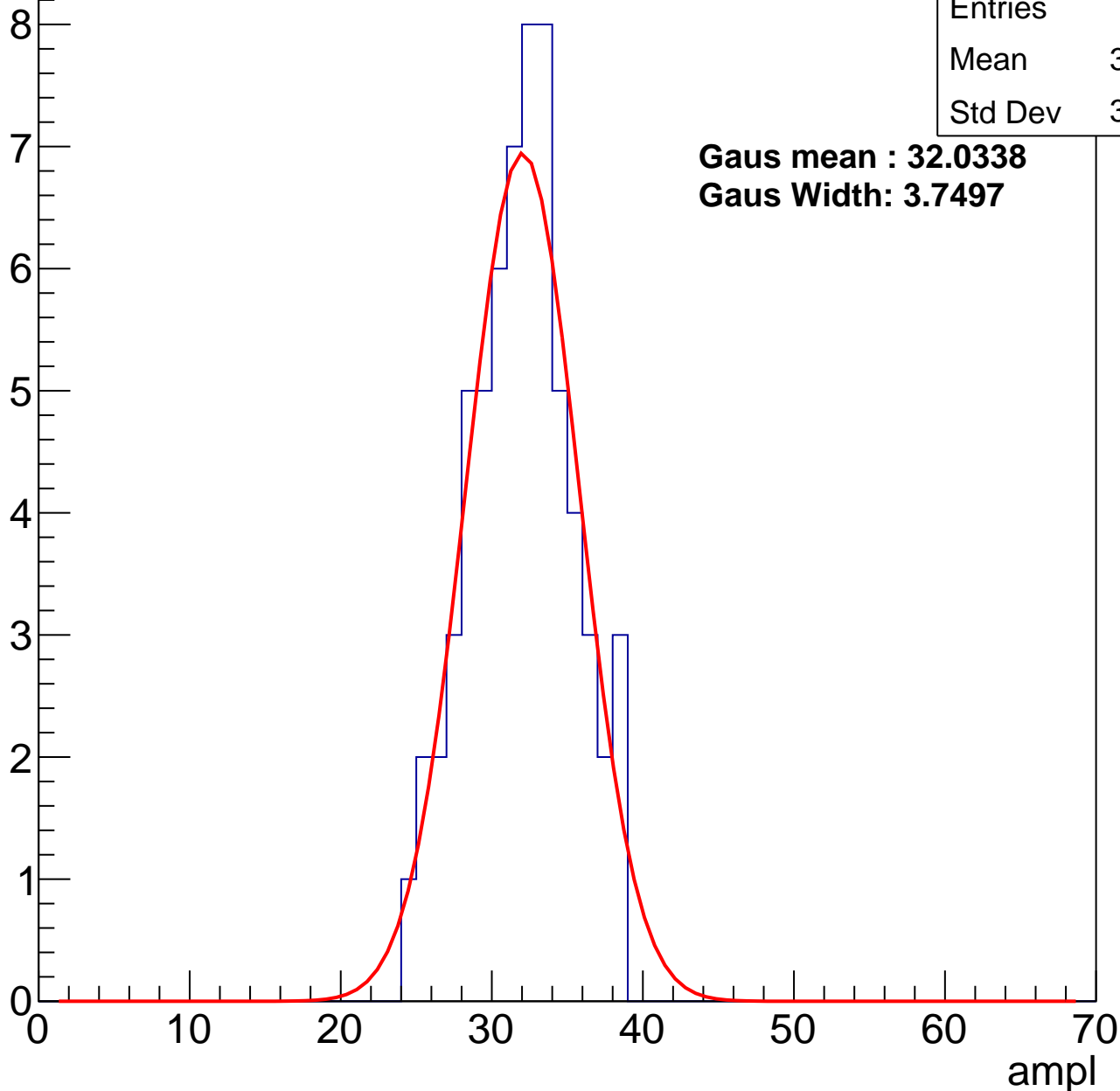
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.48
Std Dev	3.349

**Gaus mean : 32.0338**

**Gaus Width: 3.7497**



# B0L000S, U10-ch105, adc1

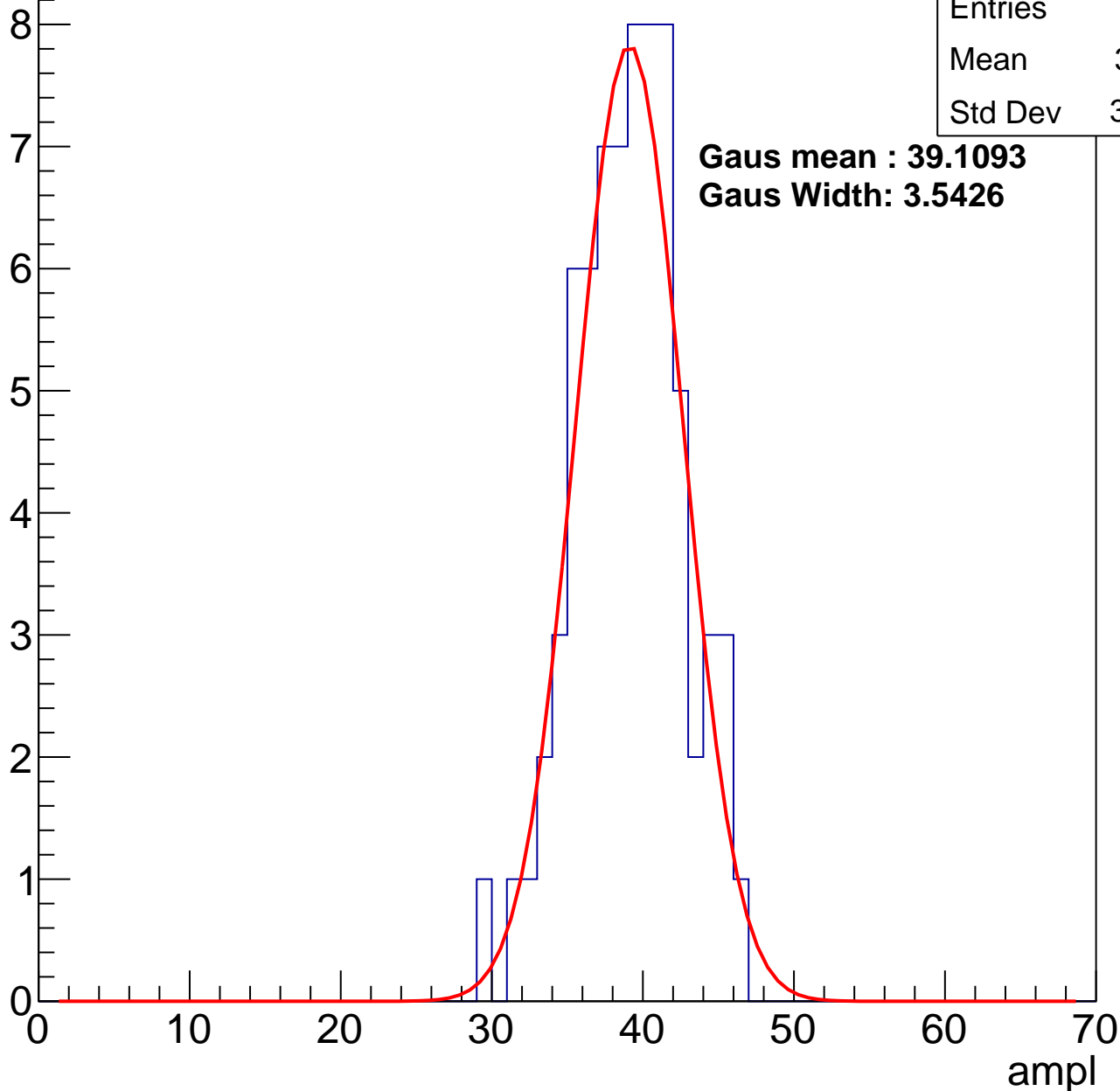
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	38.61
Std Dev	3.502

**Gaus mean : 39.1093**

**Gaus Width: 3.5426**



# B0L000S, U10-ch105, adc2

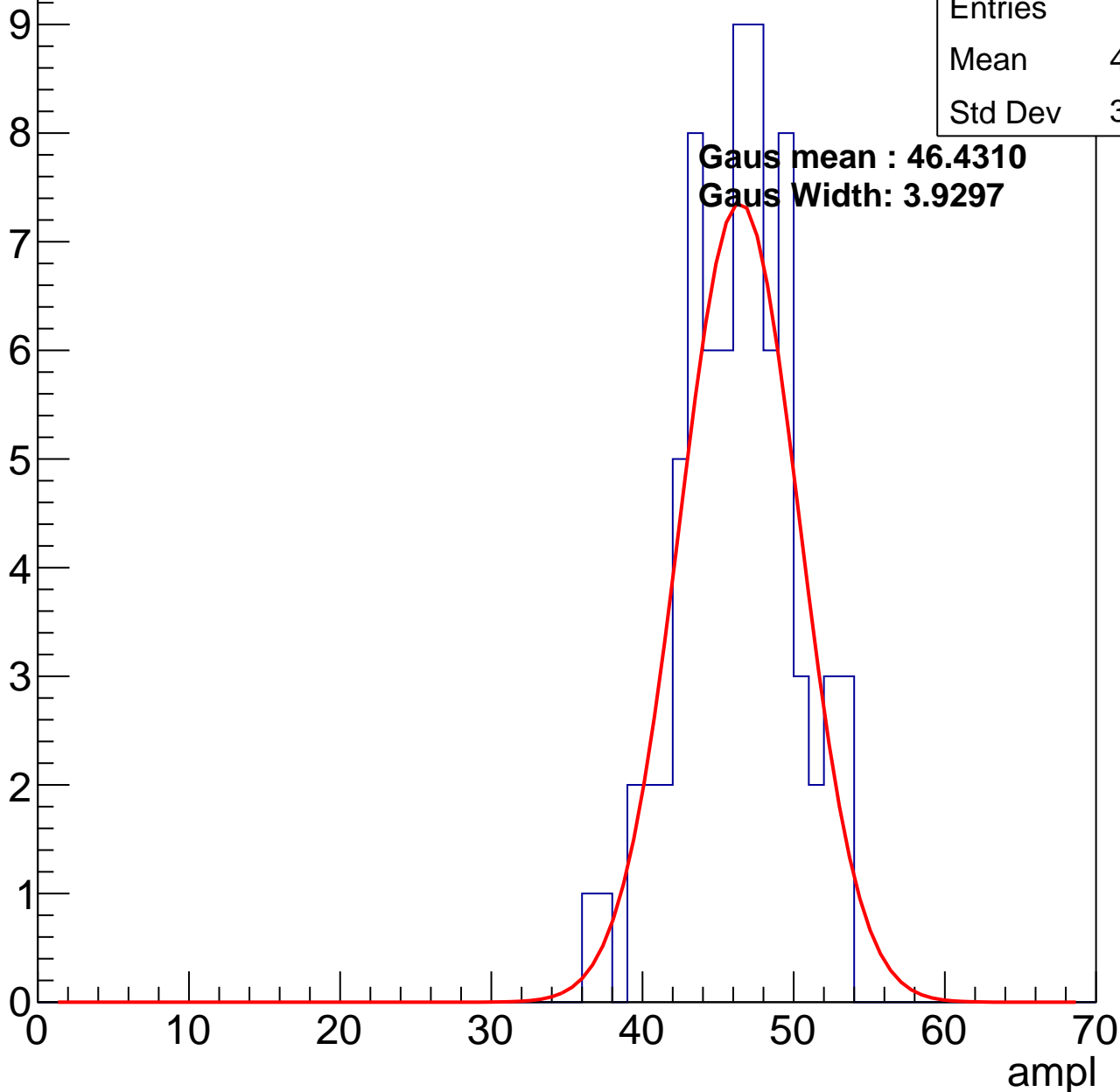
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	45.86
Std Dev	3.698

**Gaus mean : 46.4310**

**Gaus Width: 3.9297**

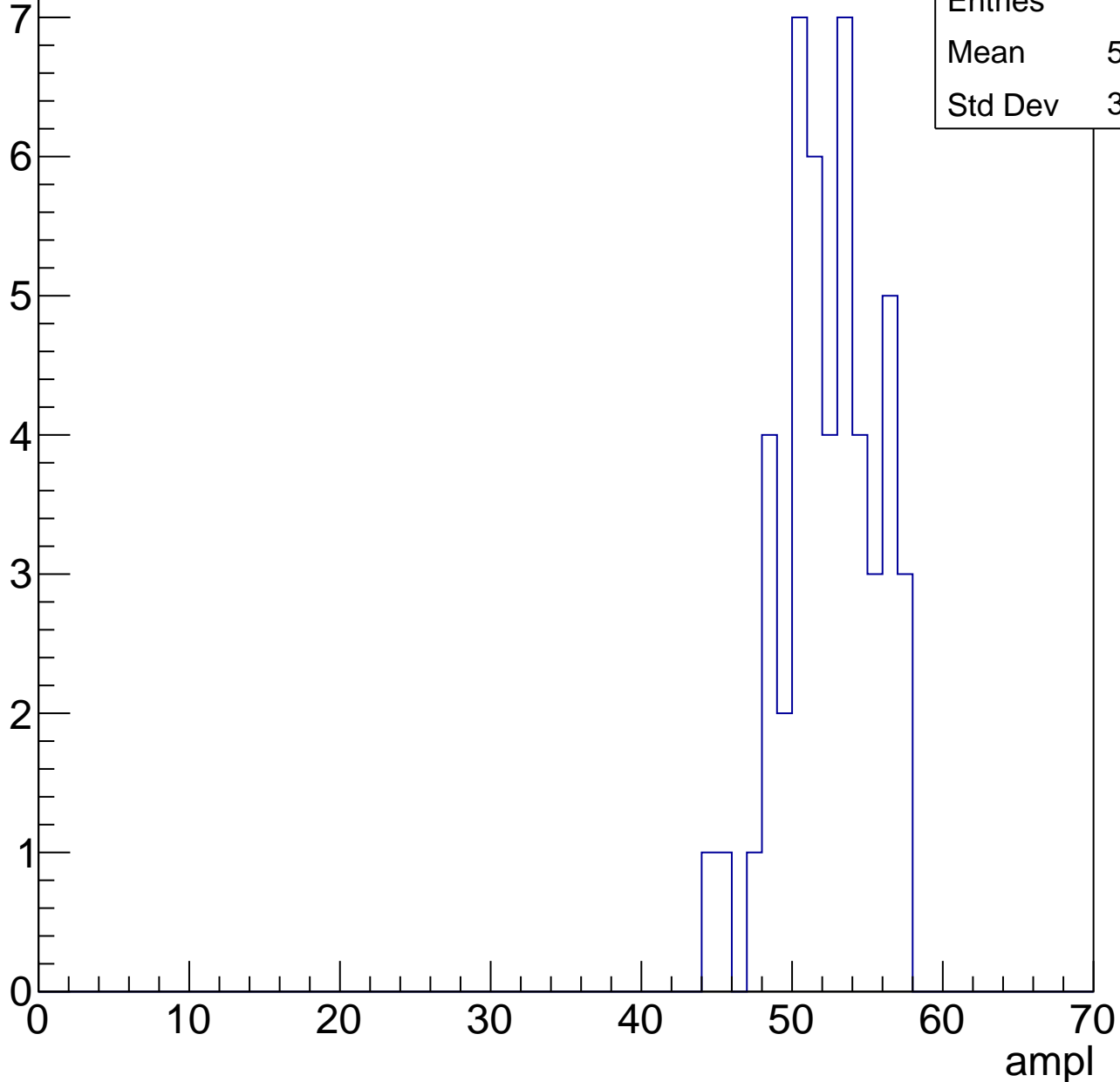


# B0L000S, U10-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

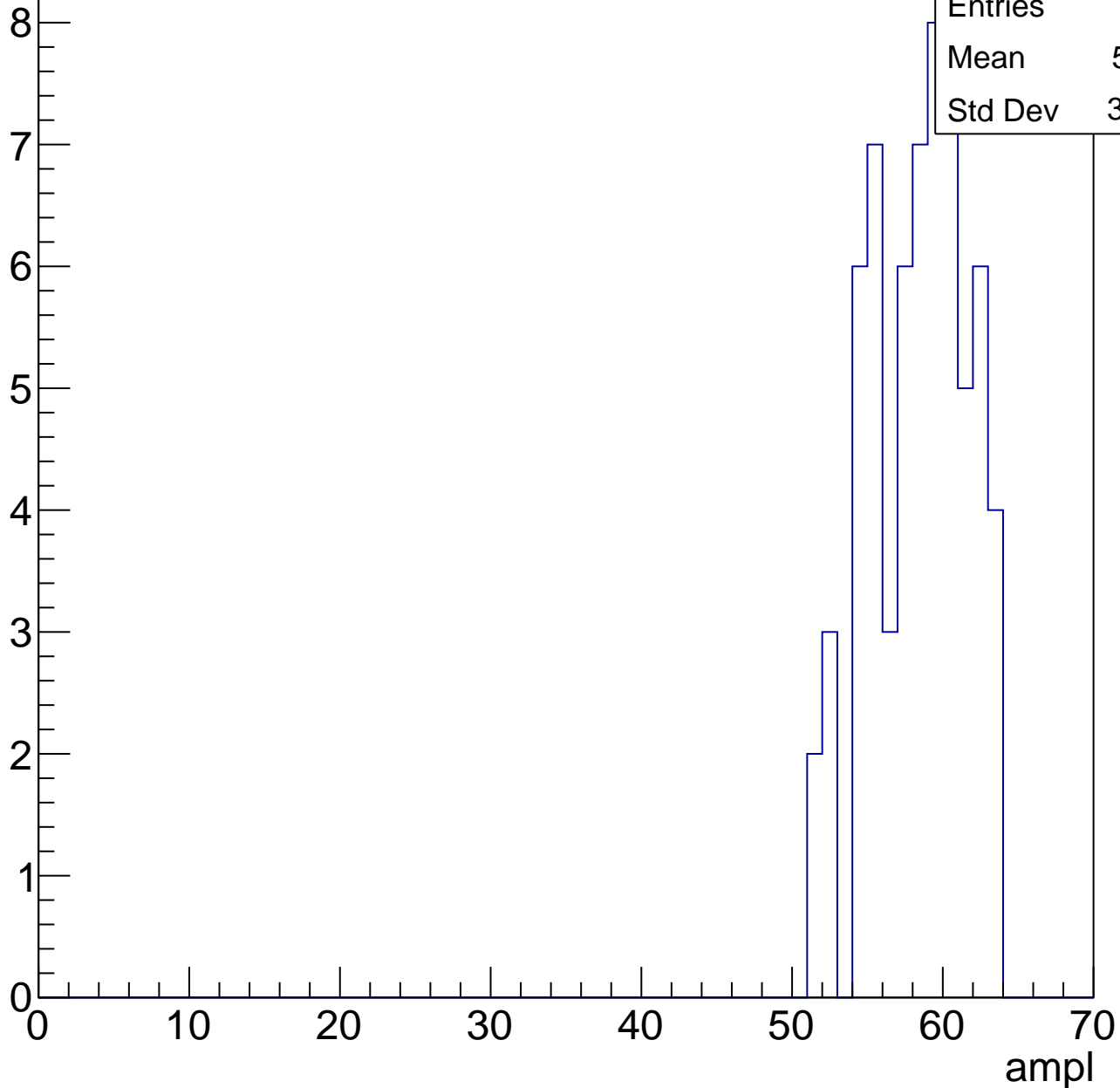
Entries	48
Mean	51.94
Std Dev	3.092



# B0L000S, U10-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

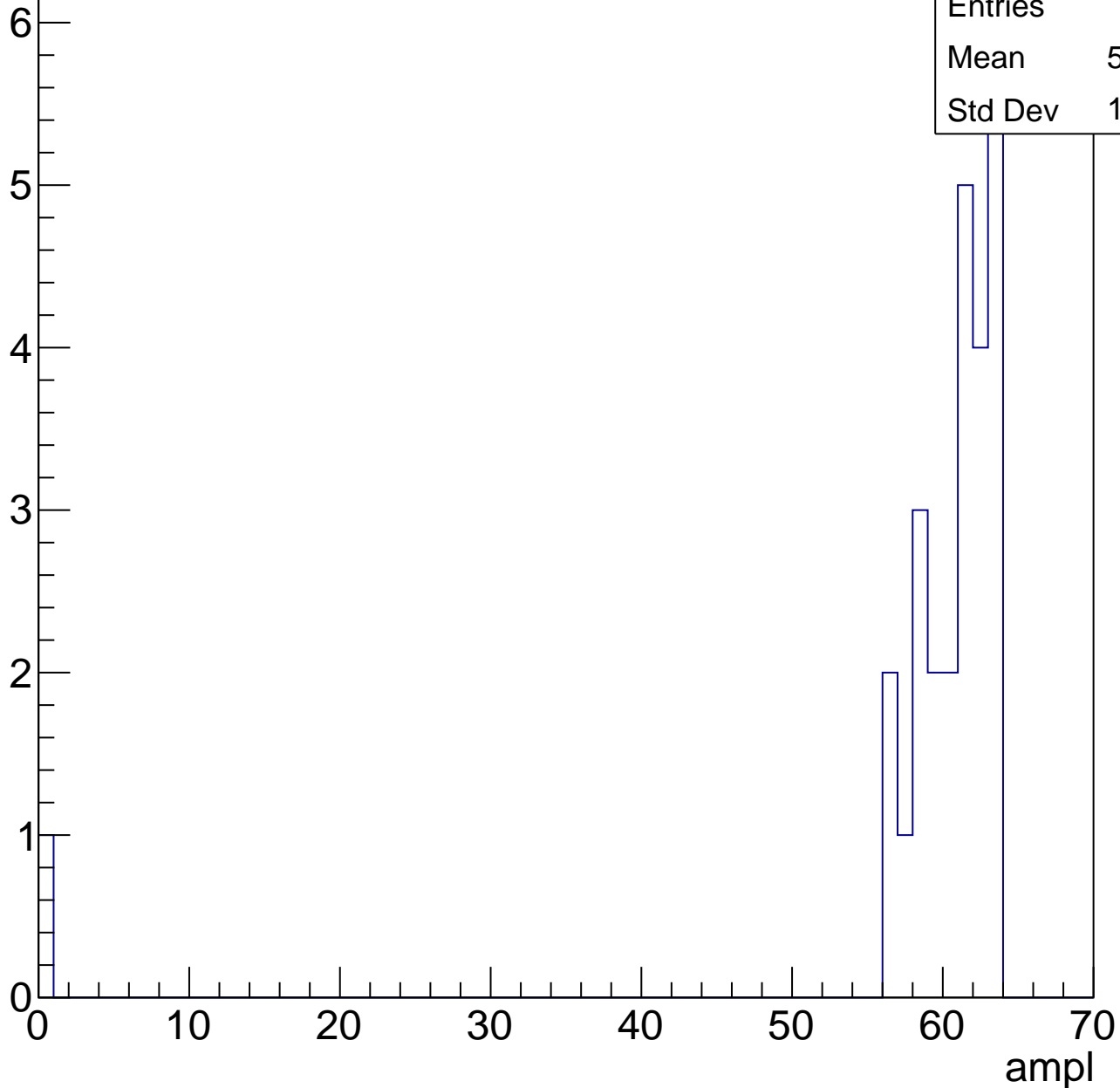


# B0L000S, U10-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	58.15
Std Dev	11.83



# B0L000S, U10-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch106, adc0

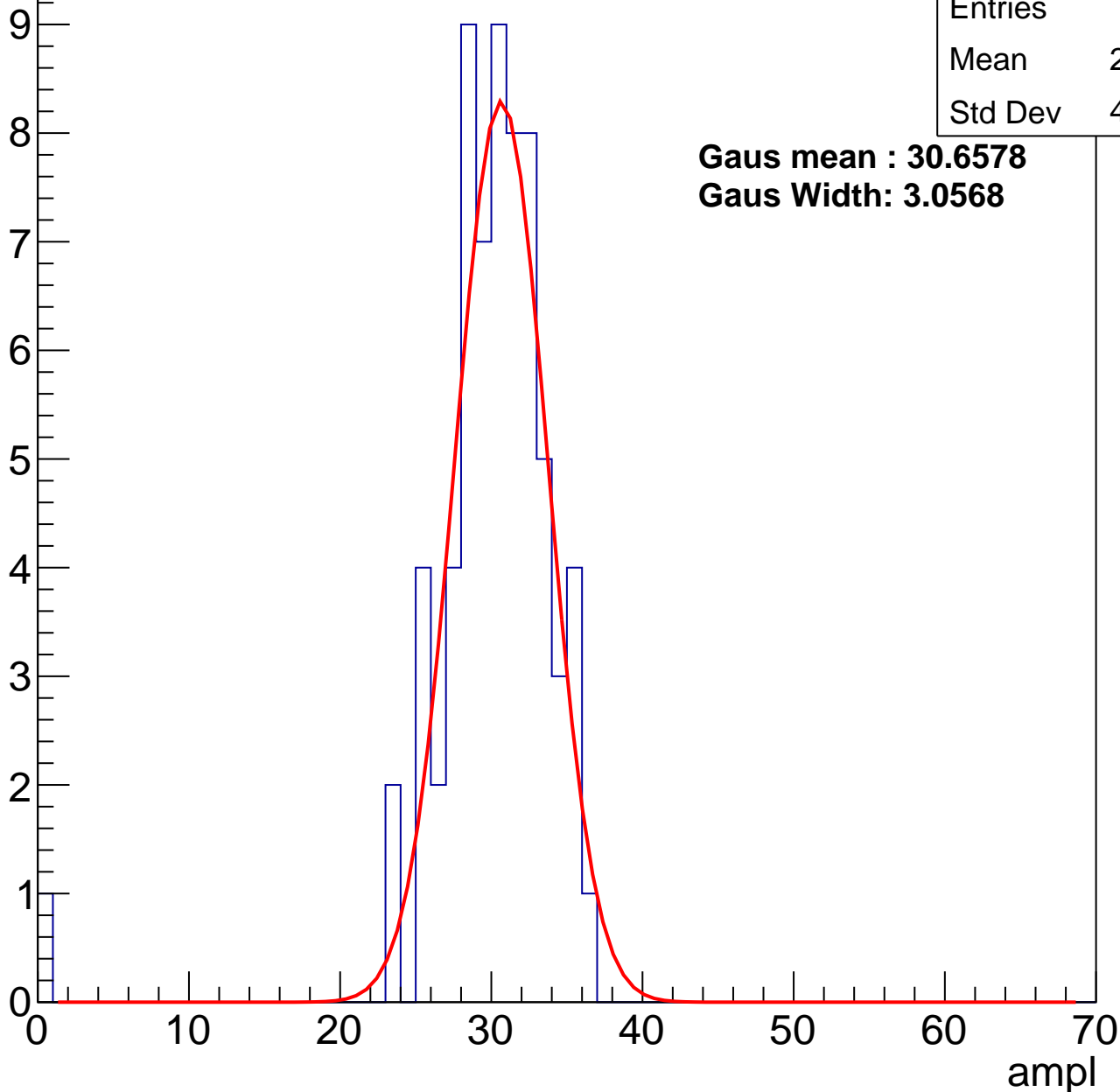
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.52
Std Dev	4.672

**Gaus mean : 30.6578**

**Gaus Width: 3.0568**



# B0L000S, U10-ch106, adc1

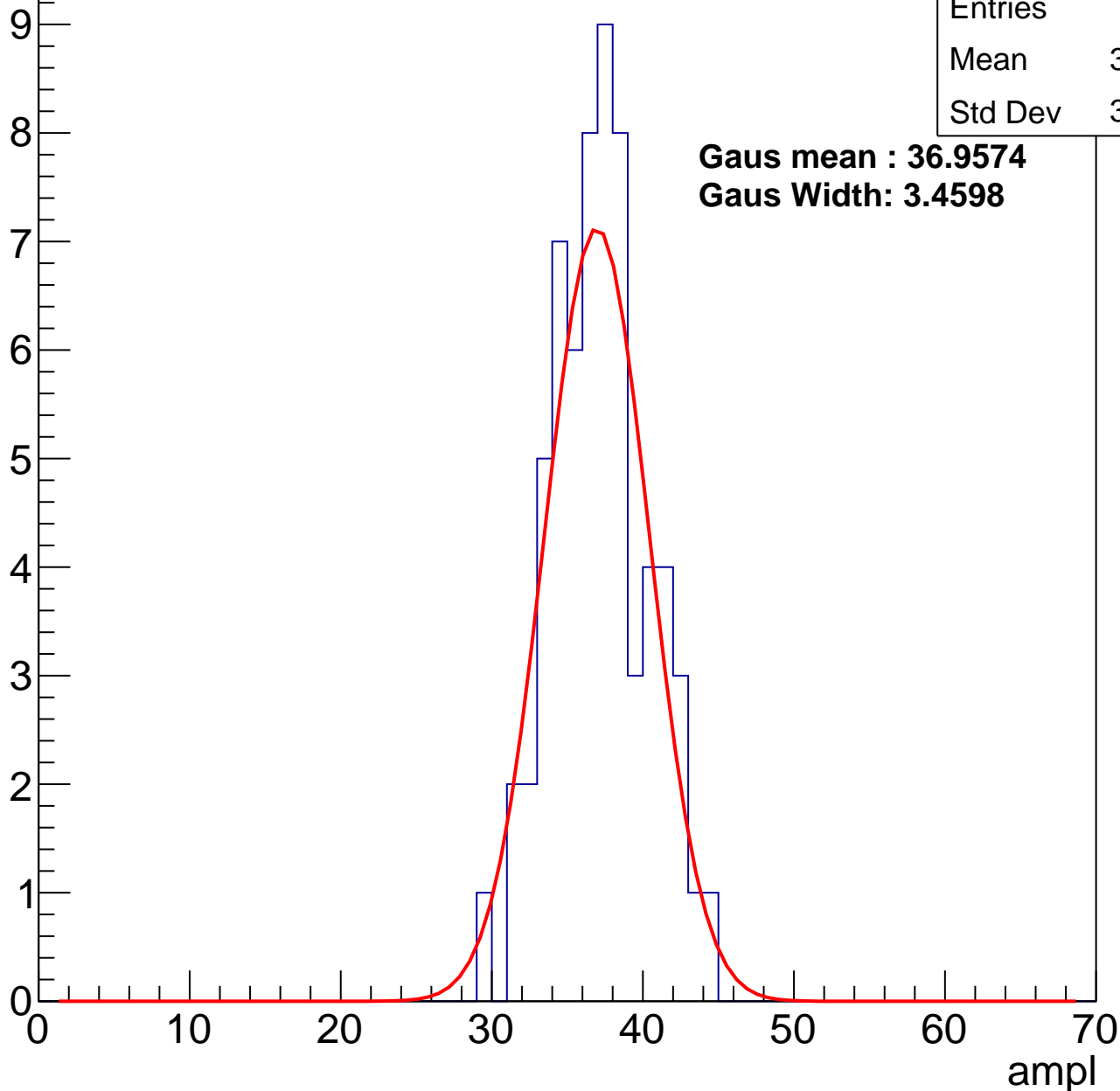
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.67
Std Dev	3.148

**Gaus mean : 36.9574**

**Gaus Width: 3.4598**



# B0L000S, U10-ch106, adc2

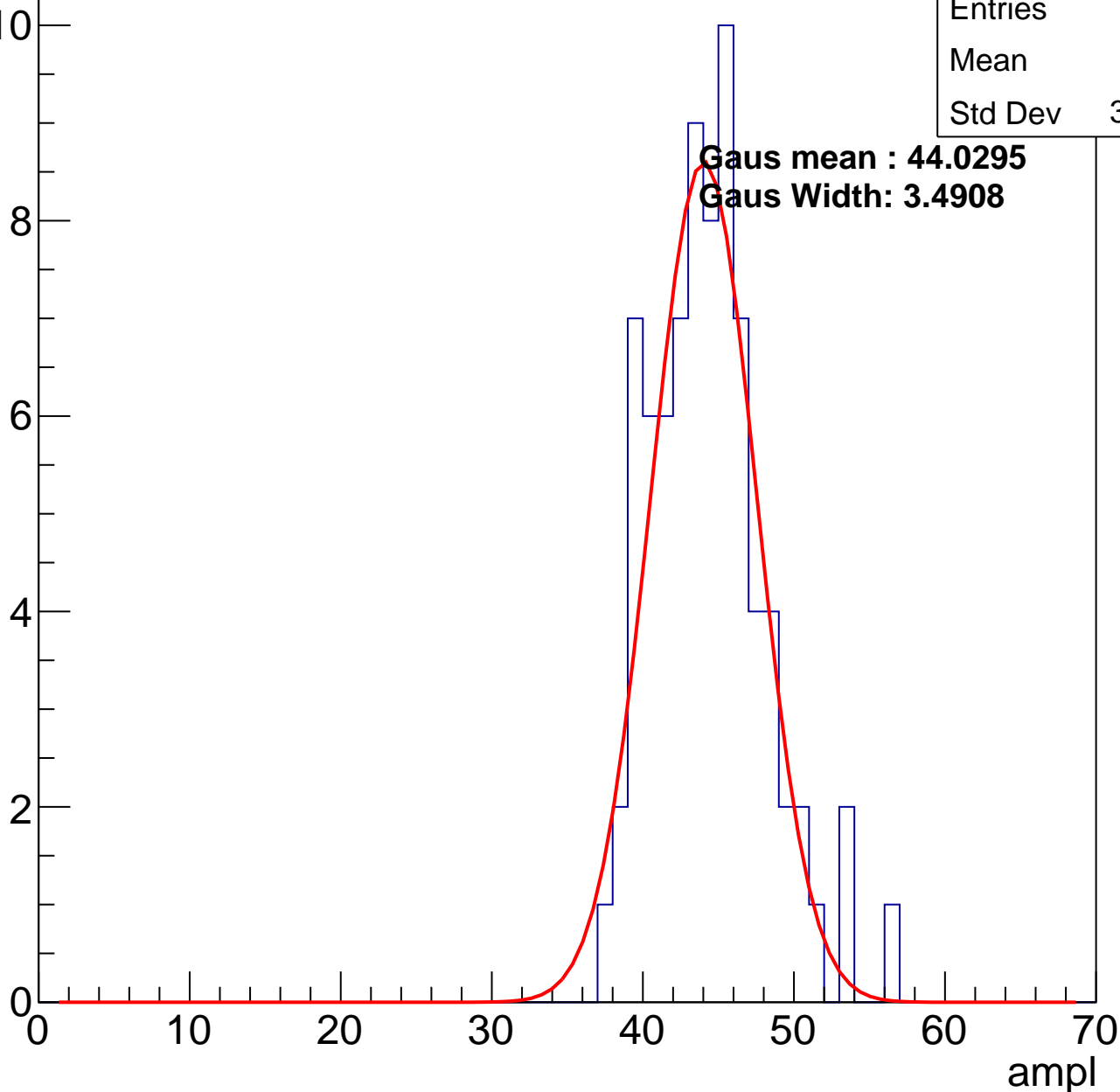
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	43.9
Std Dev	3.727

**Gaus mean : 44.0295**

**Gaus Width: 3.4908**

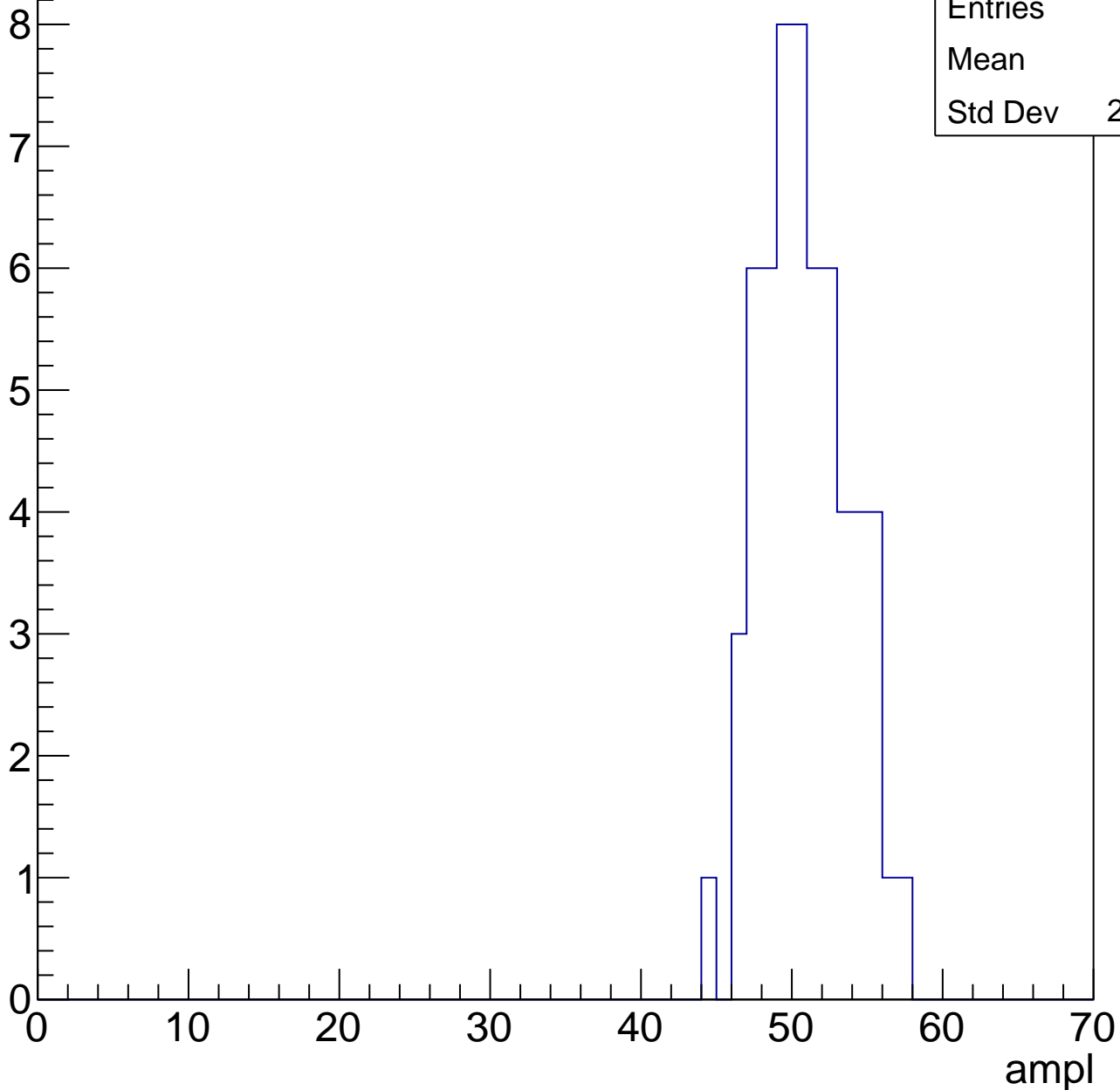


# B0L000S, U10-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

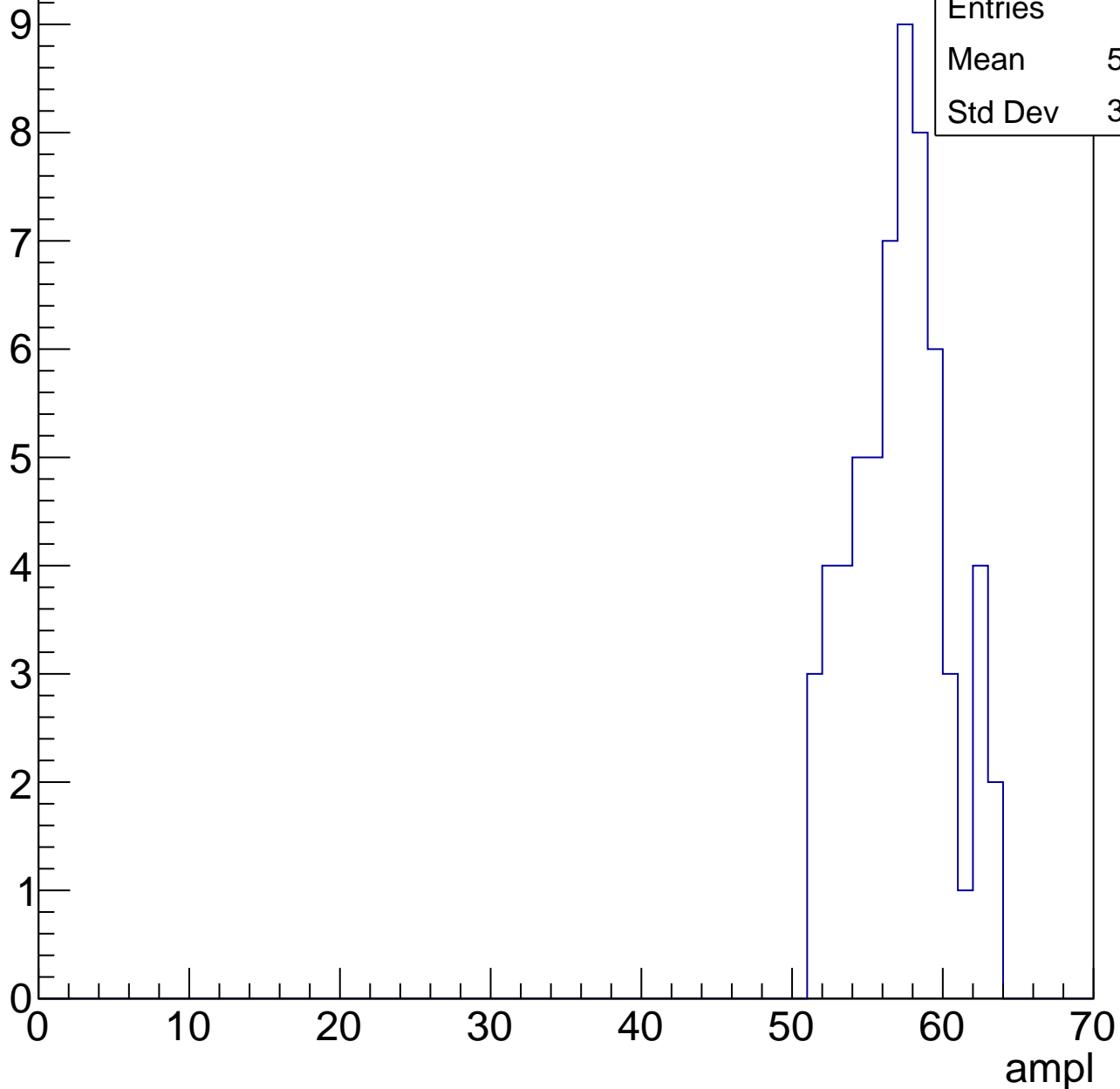
Entries	58
Mean	50.4
Std Dev	2.864



# B0L000S, U10-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

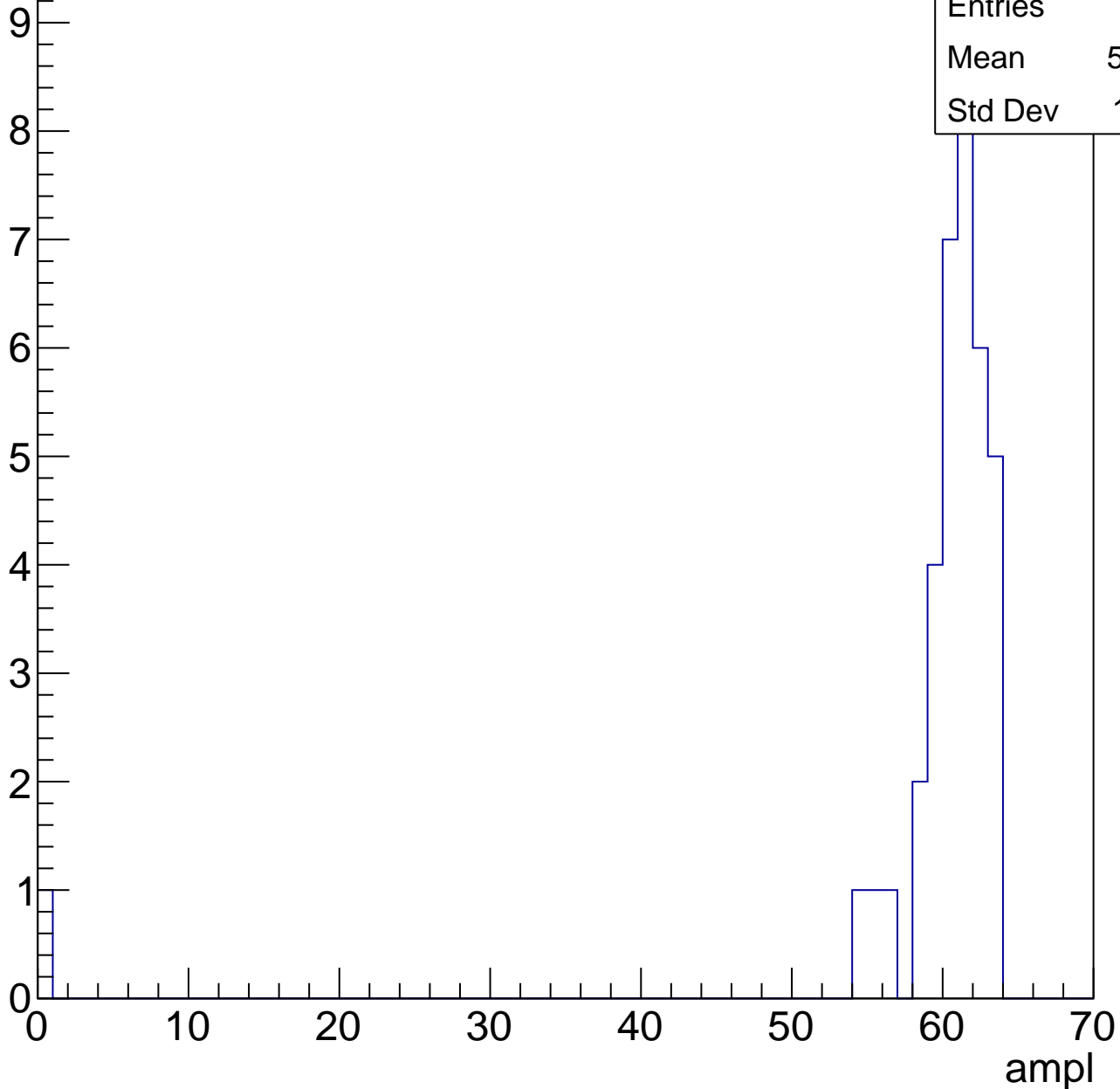


# B0L000S, U10-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.73
Std Dev	10.01



# B0L000S, U10-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

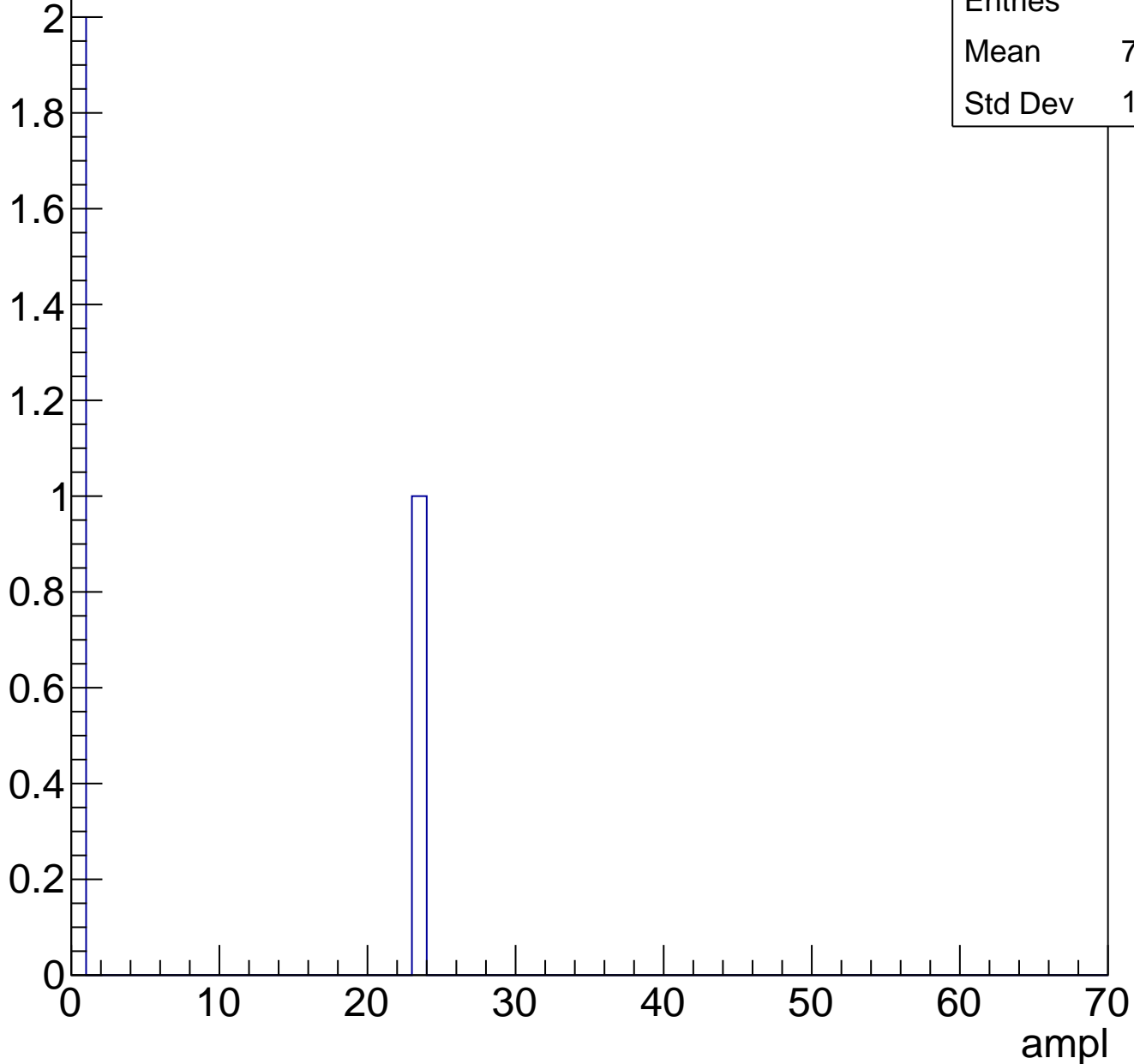




# B0L000S, U10-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U10-ch107, adc0

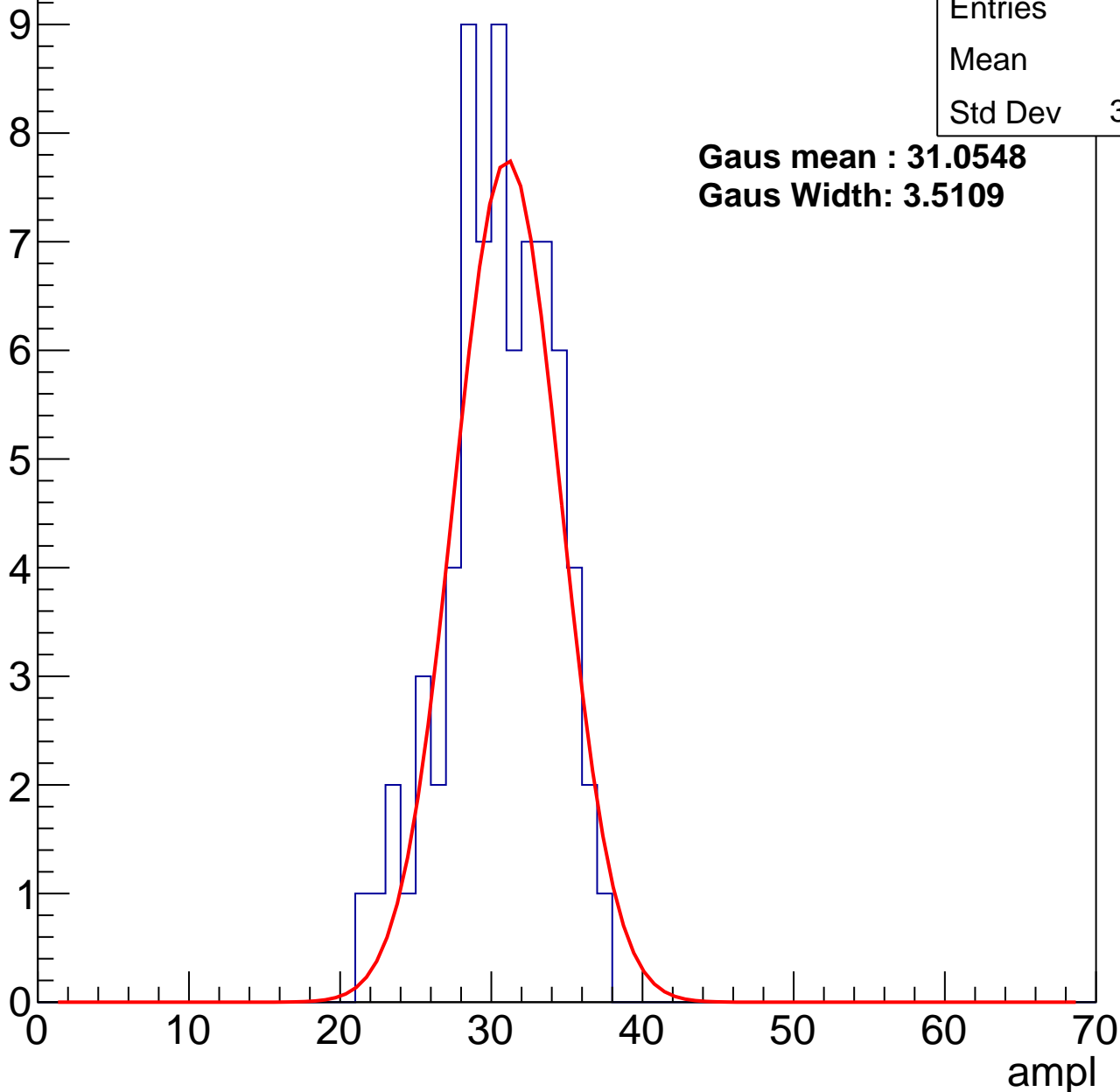
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.1
Std Dev	3.493

**Gaus mean : 31.0548**

**Gaus Width: 3.5109**



# B0L000S, U10-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	36.84
Std Dev	3.153

**Gaus mean : 37.2946**

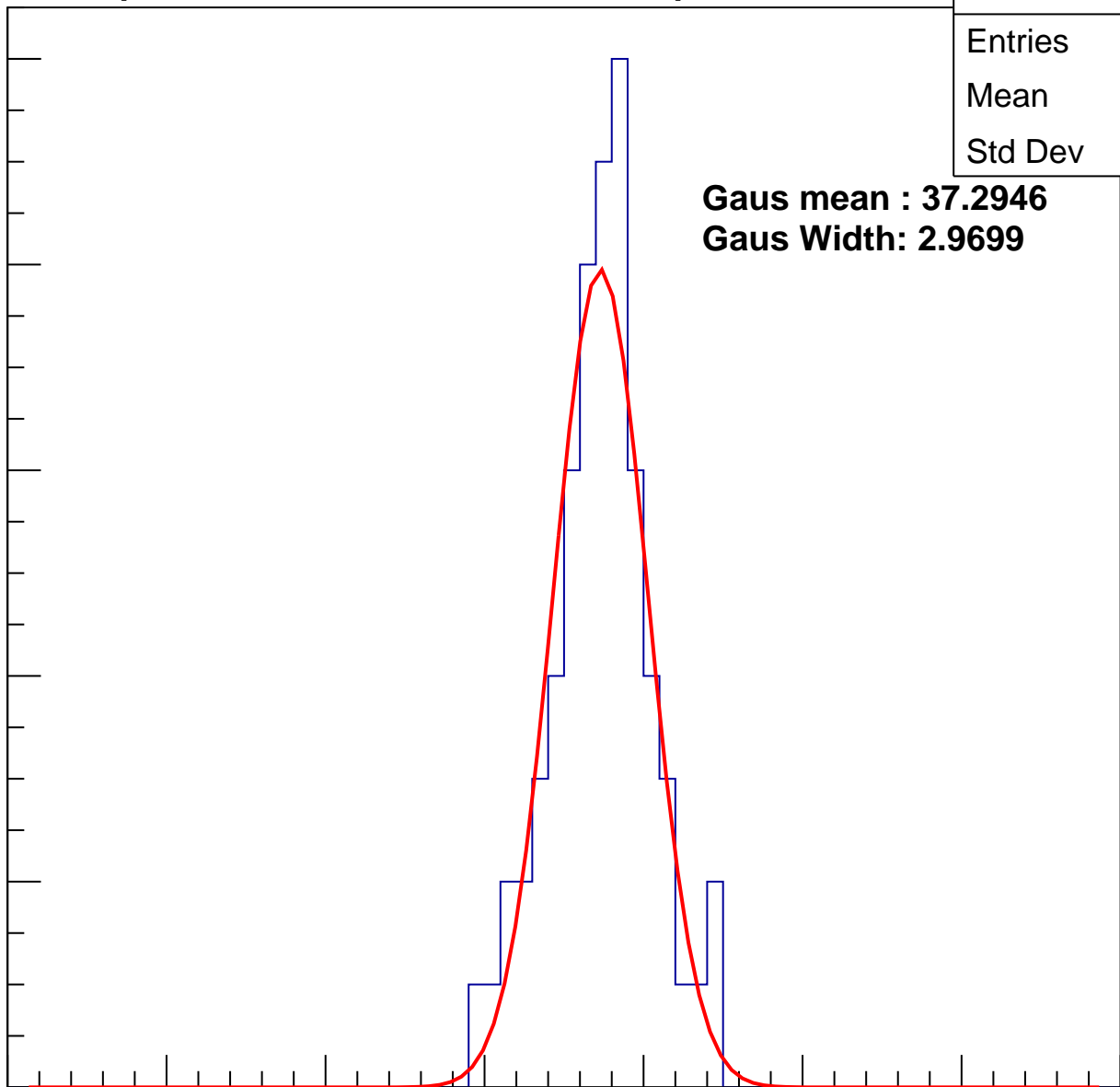
**Gaus Width: 2.9699**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch107, adc2

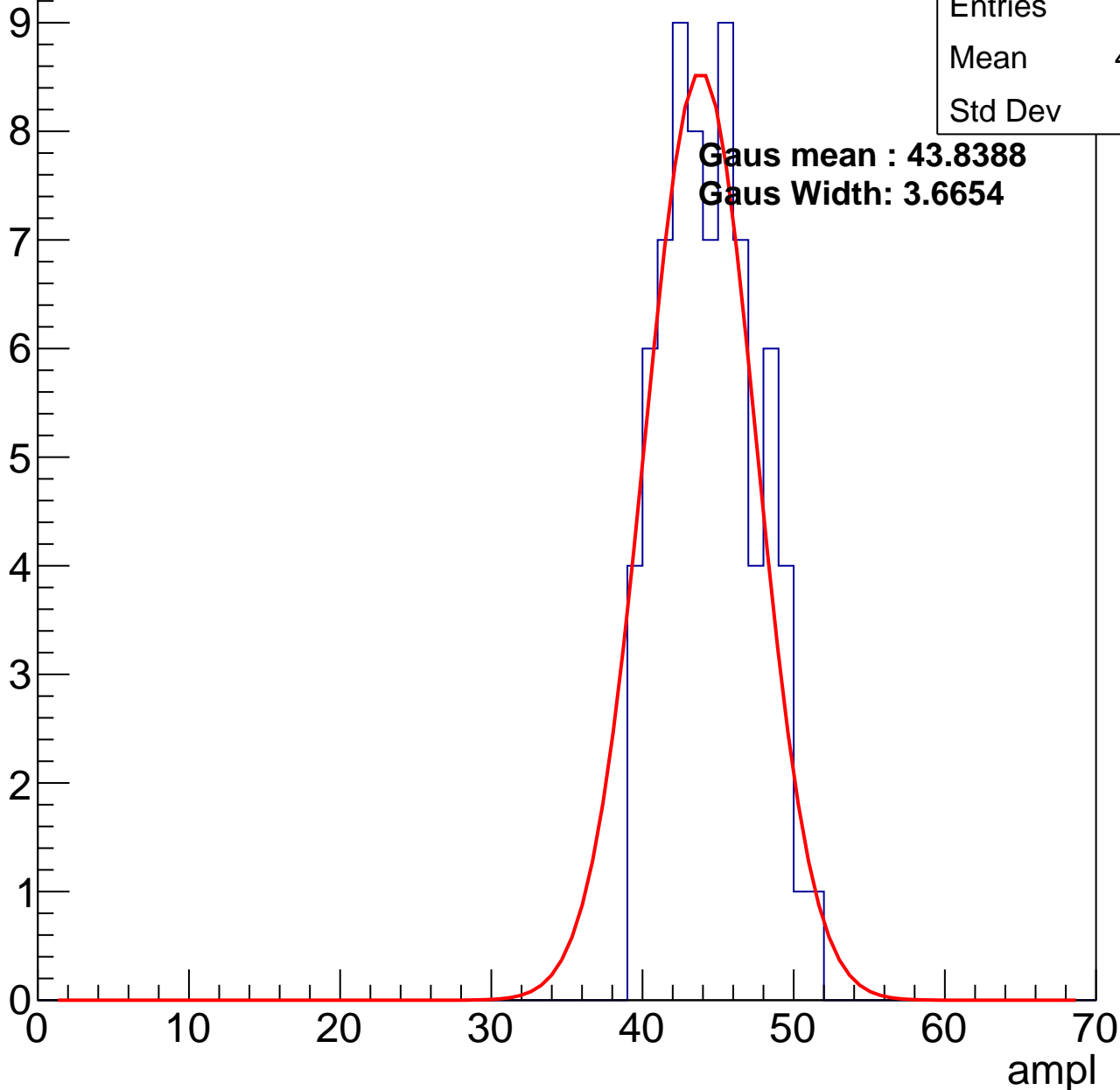
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	44.01
Std Dev	3

**Gaus mean : 43.8388**

**Gaus Width: 3.6654**

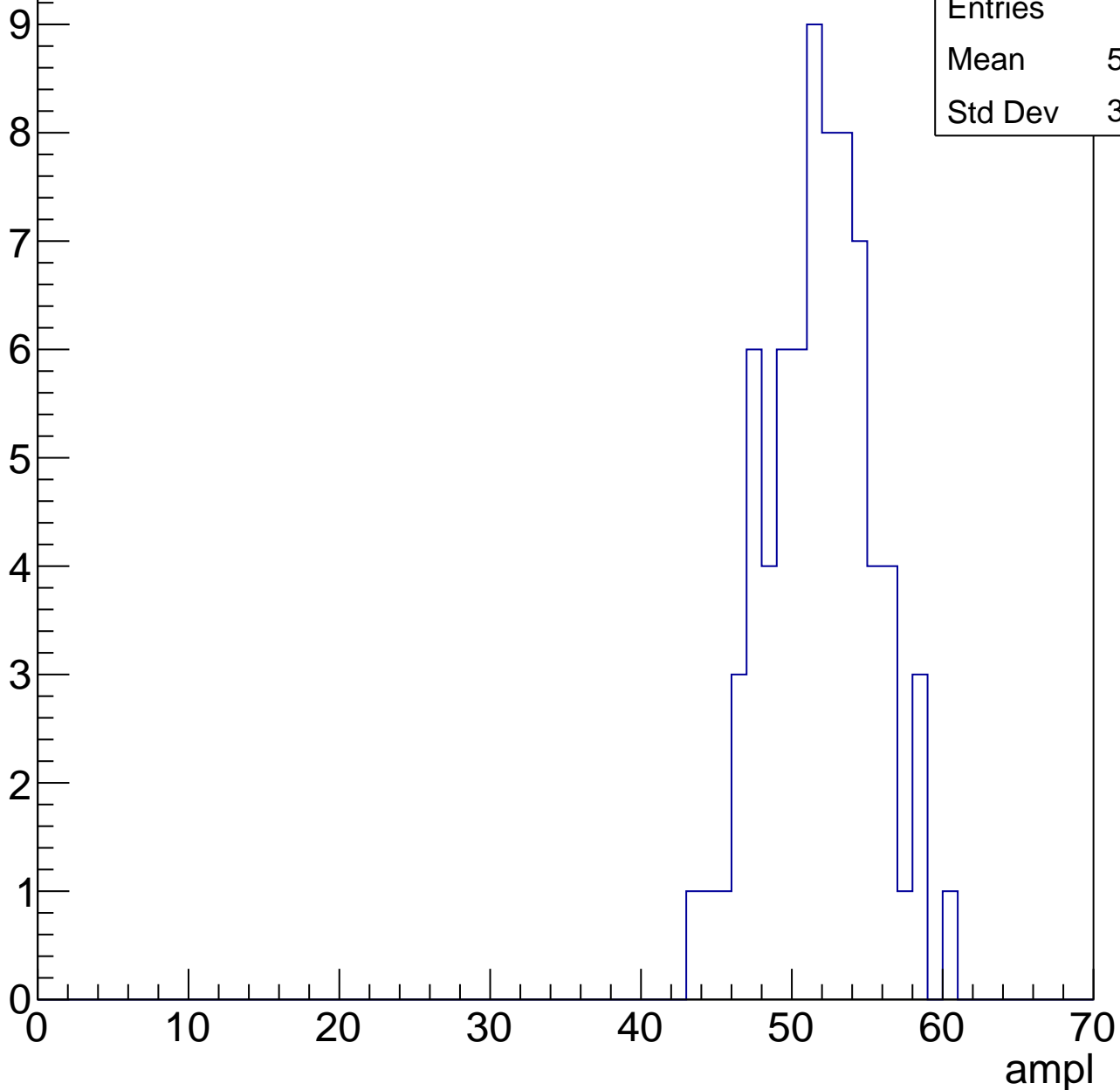


# B0L000S, U10-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	51.37
Std Dev	3.525

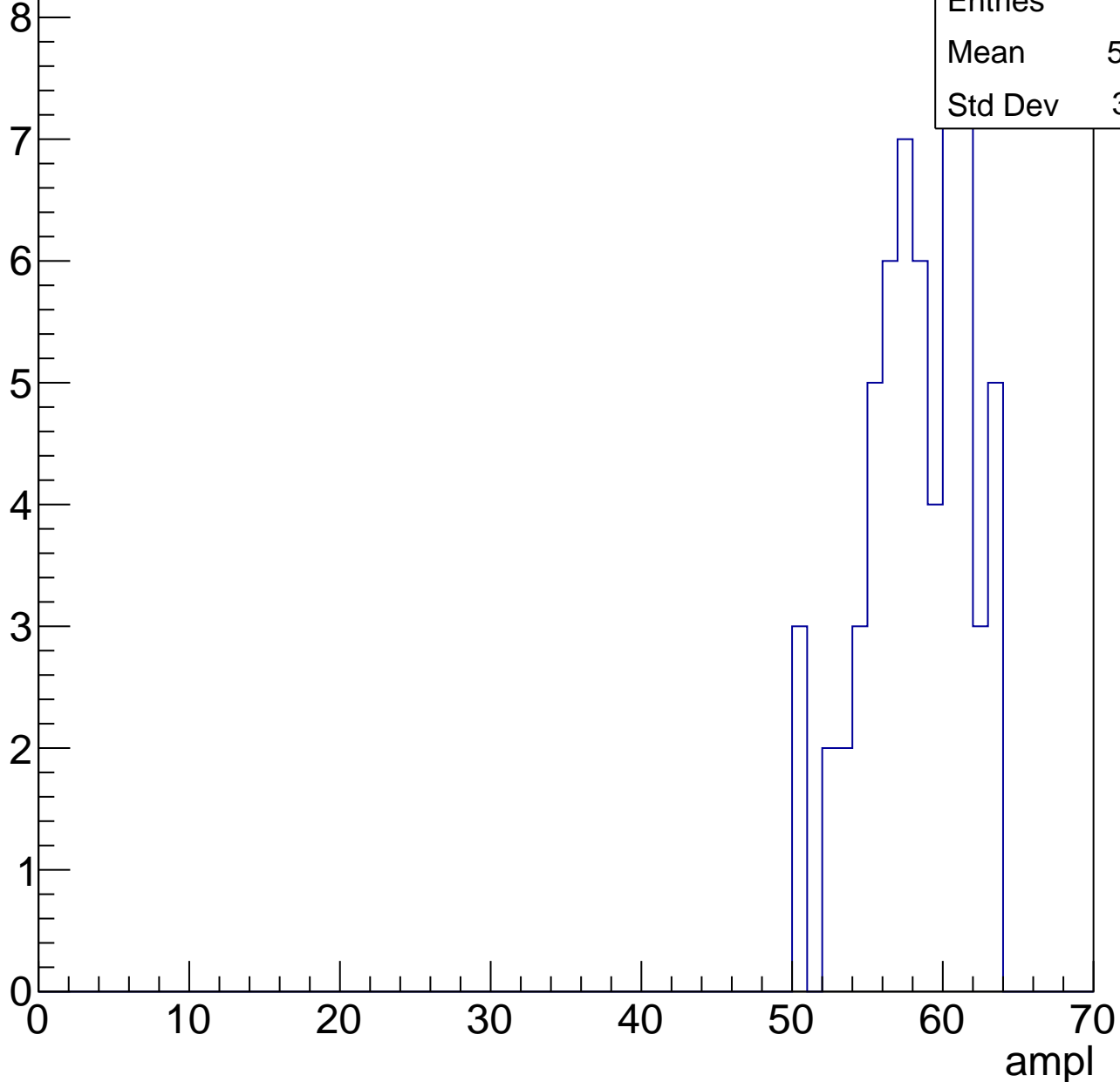


# B0L000S, U10-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	57.82
Std Dev	3.401



# B0L000S, U10-ch107, adc5

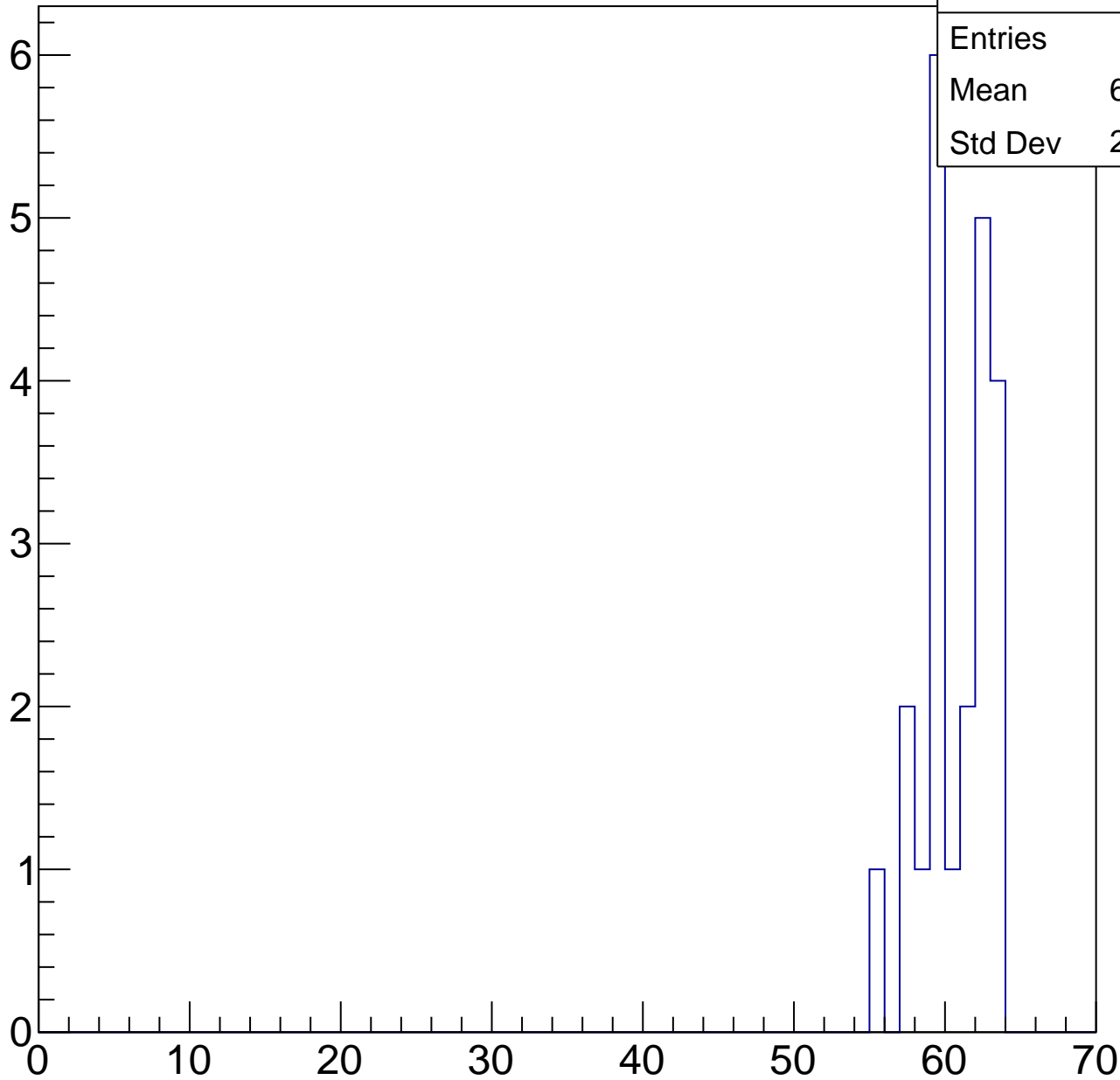
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

6  
5  
4  
3  
2  
1  
0

Entries	22
Mean	60.23
Std Dev	2.235

ampl



# B0L000S, U10-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	46.75
Std Dev	26.99

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch108, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	29.01
Std Dev	3.235

**Gaus mean : 29.1492**

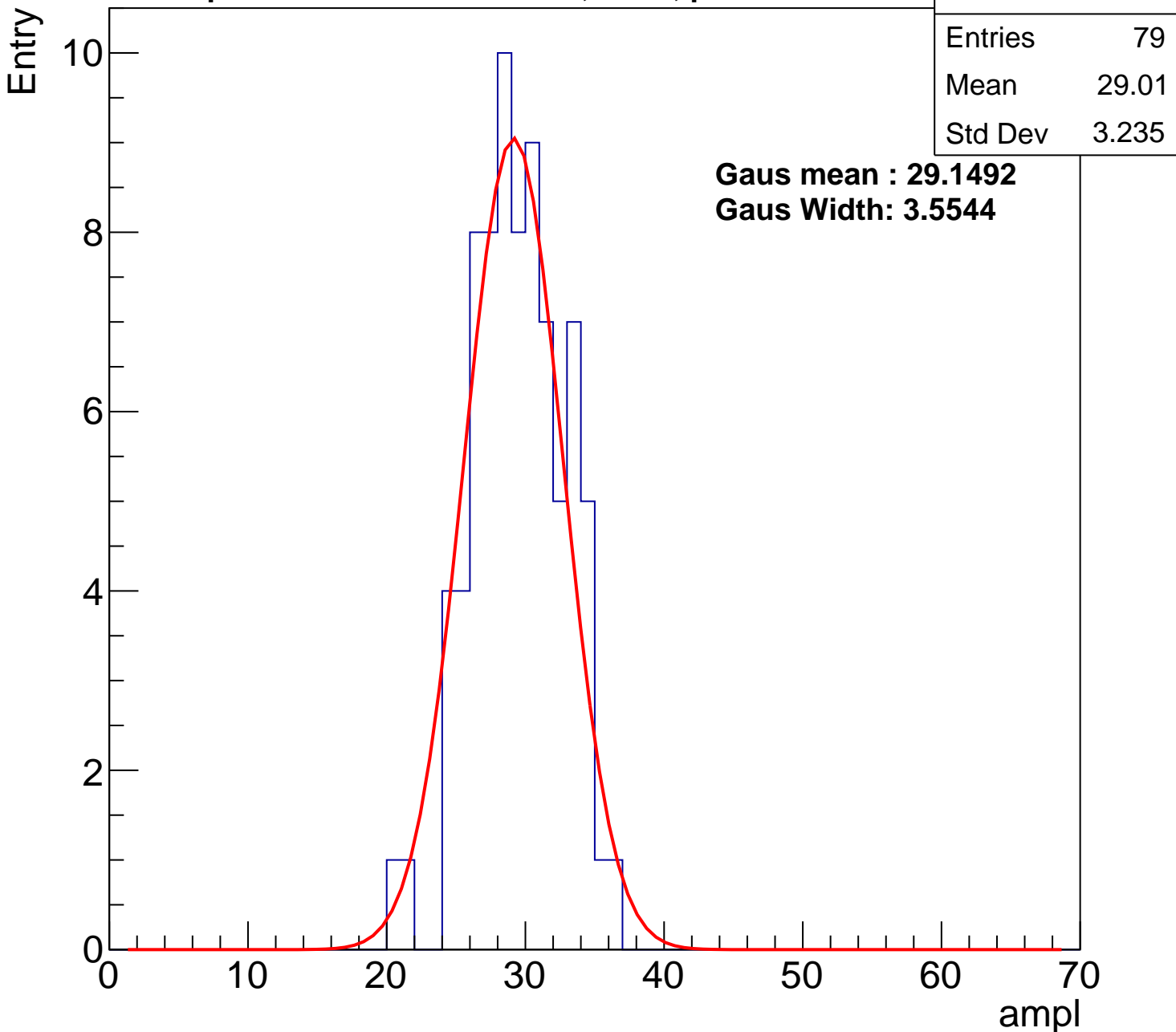
**Gaus Width: 3.5544**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



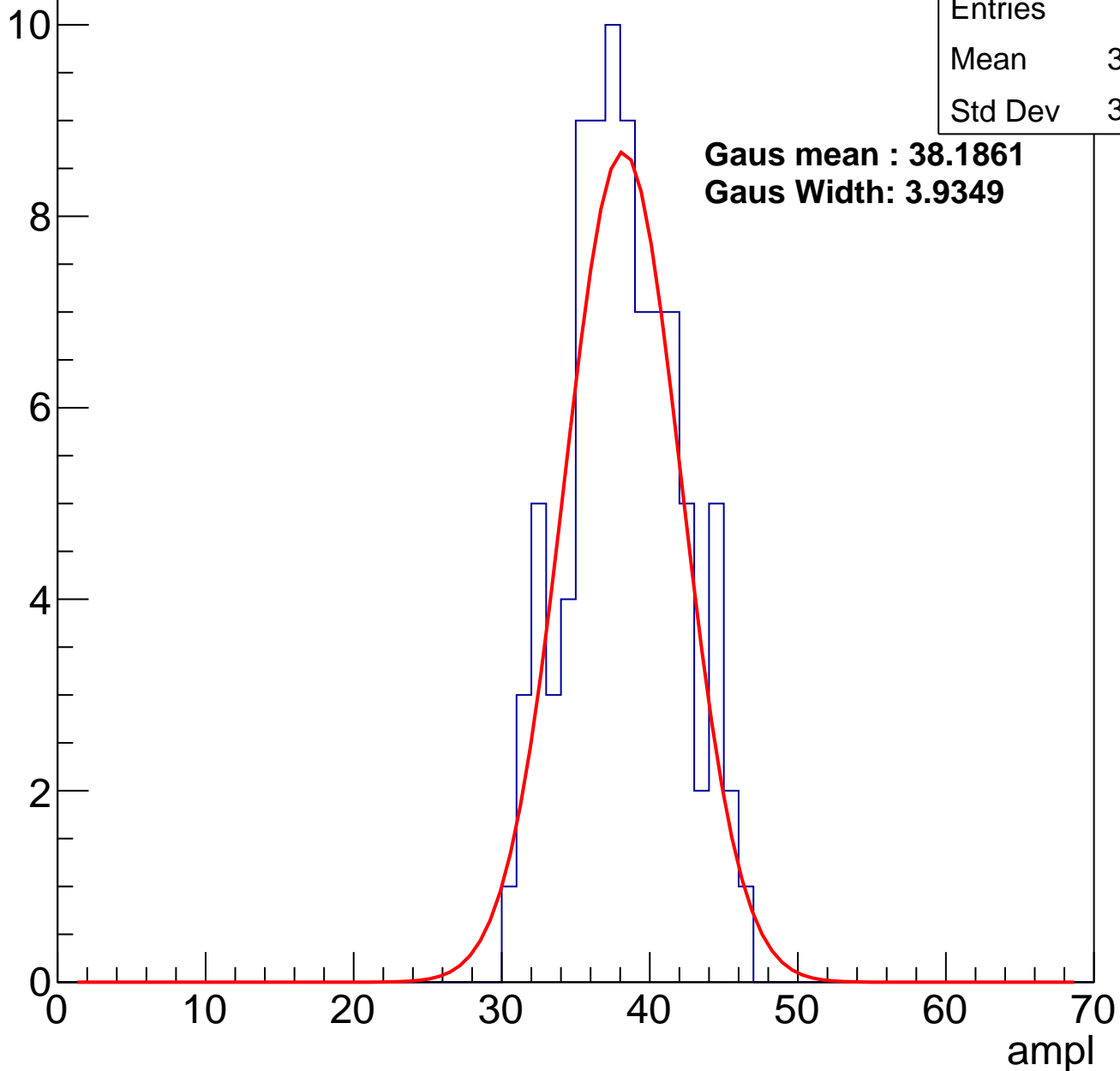
# B0L000S, U10-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	89
Mean	37.76
Std Dev	3.706

**Gaus mean : 38.1861**  
**Gaus Width: 3.9349**

Entry



# B0L000S, U10-ch108, adc2

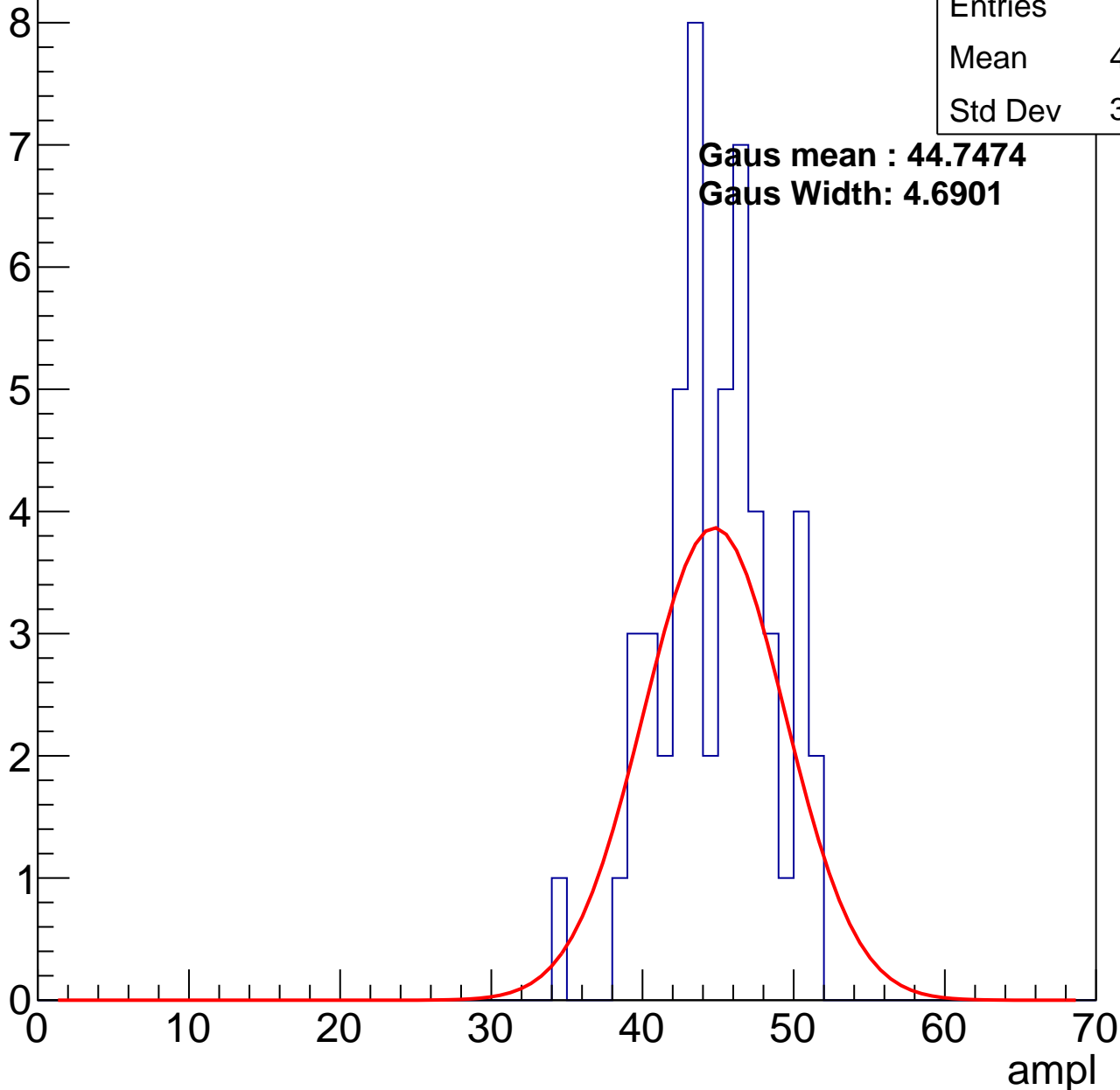
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44.37
Std Dev	3.657

**Gaus mean : 44.7474**

**Gaus Width: 4.6901**

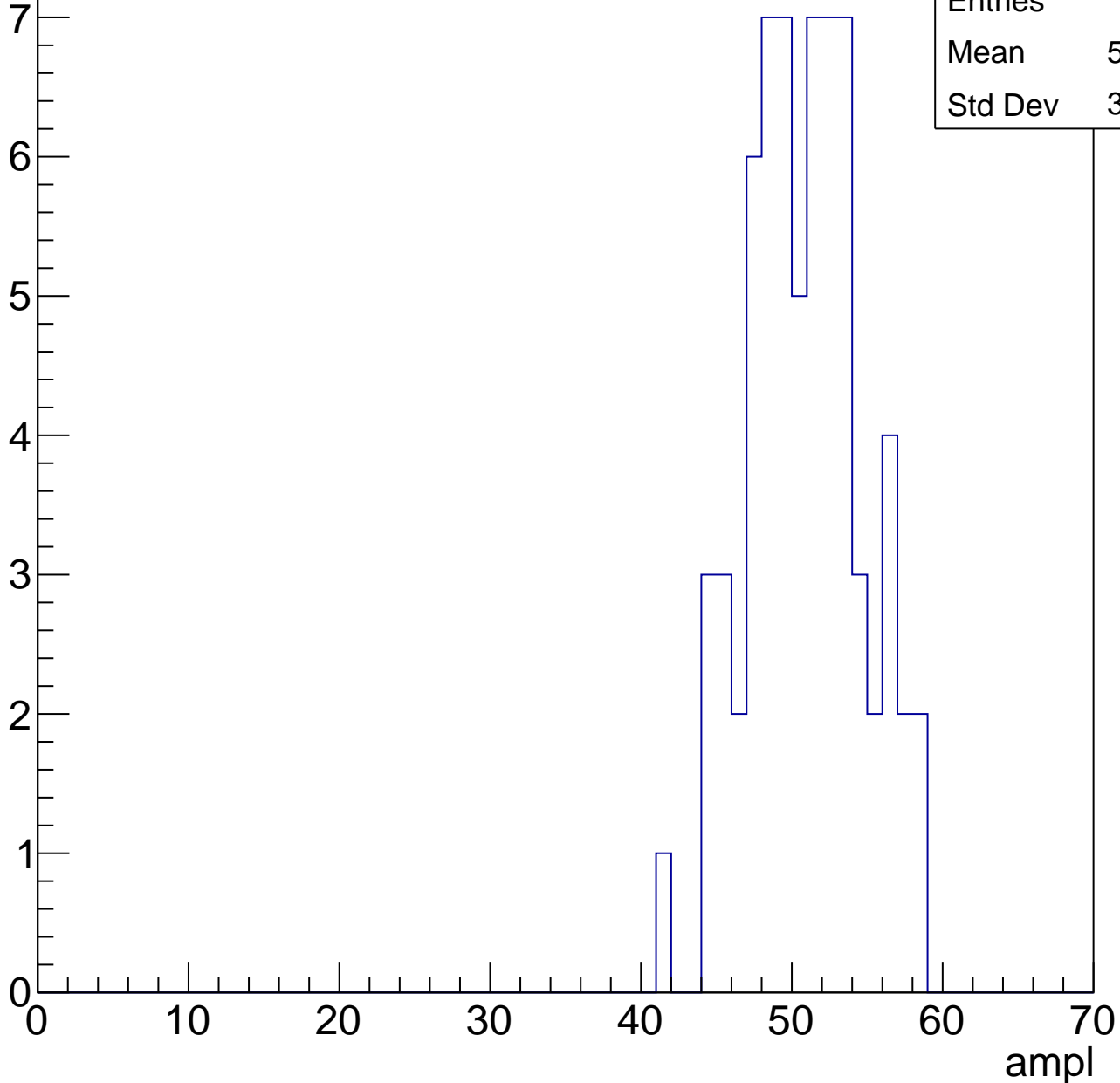


# B0L000S, U10-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

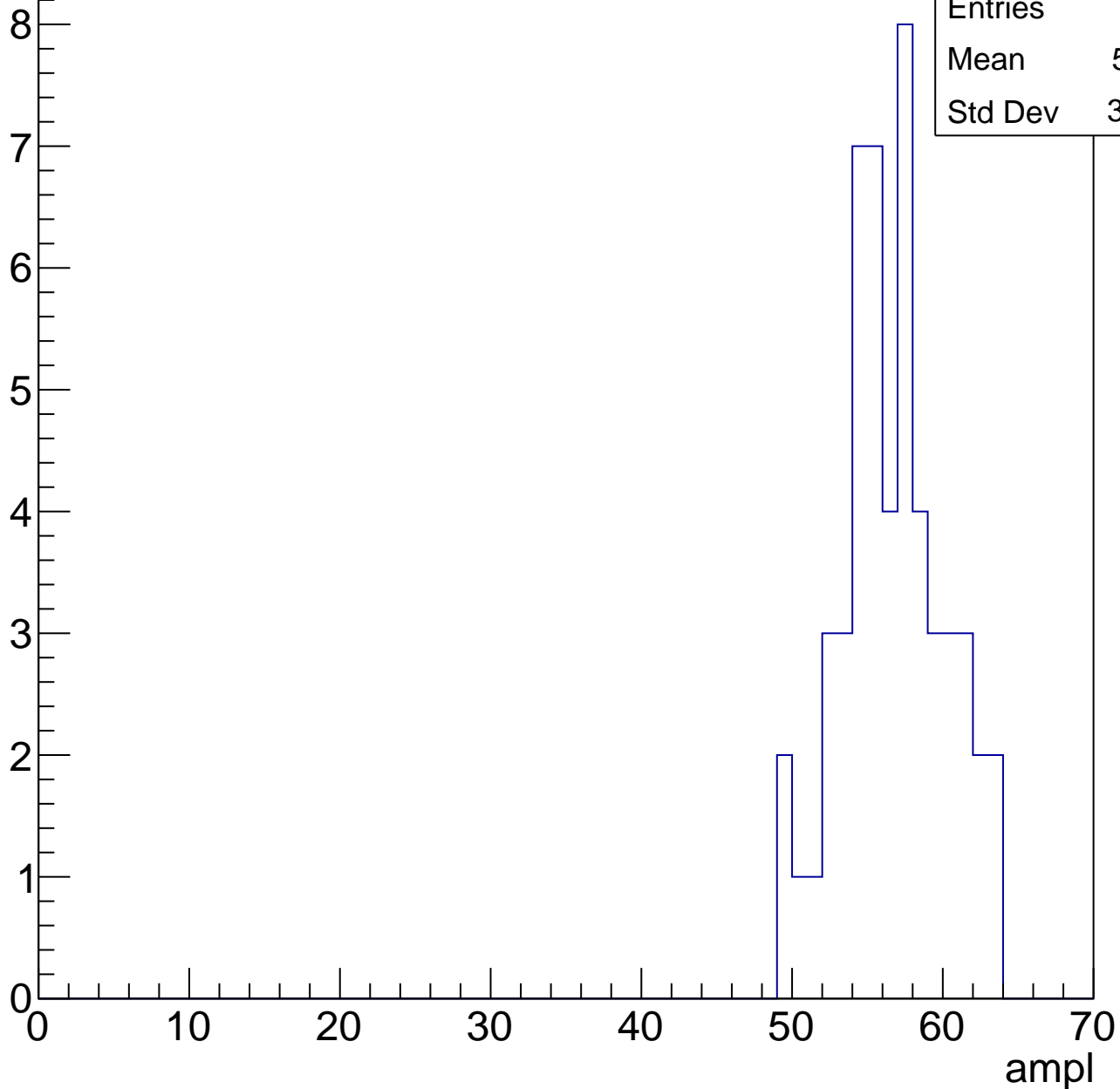
Entries	68
Mean	50.43
Std Dev	3.715



# B0L000S, U10-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

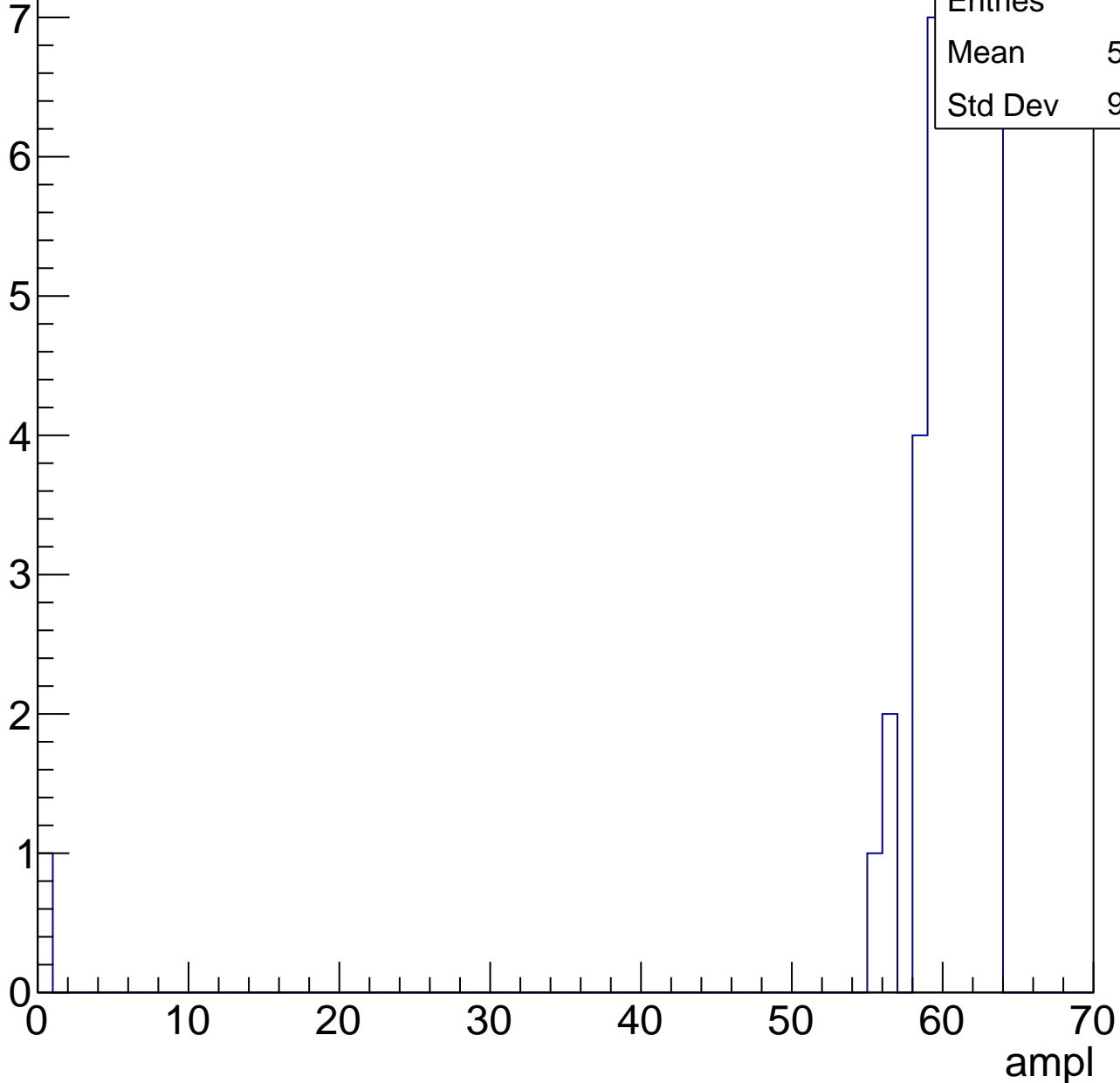


# B0L000S, U10-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.93
Std Dev	9.312



# B0L000S, U10-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U10-ch109, adc0

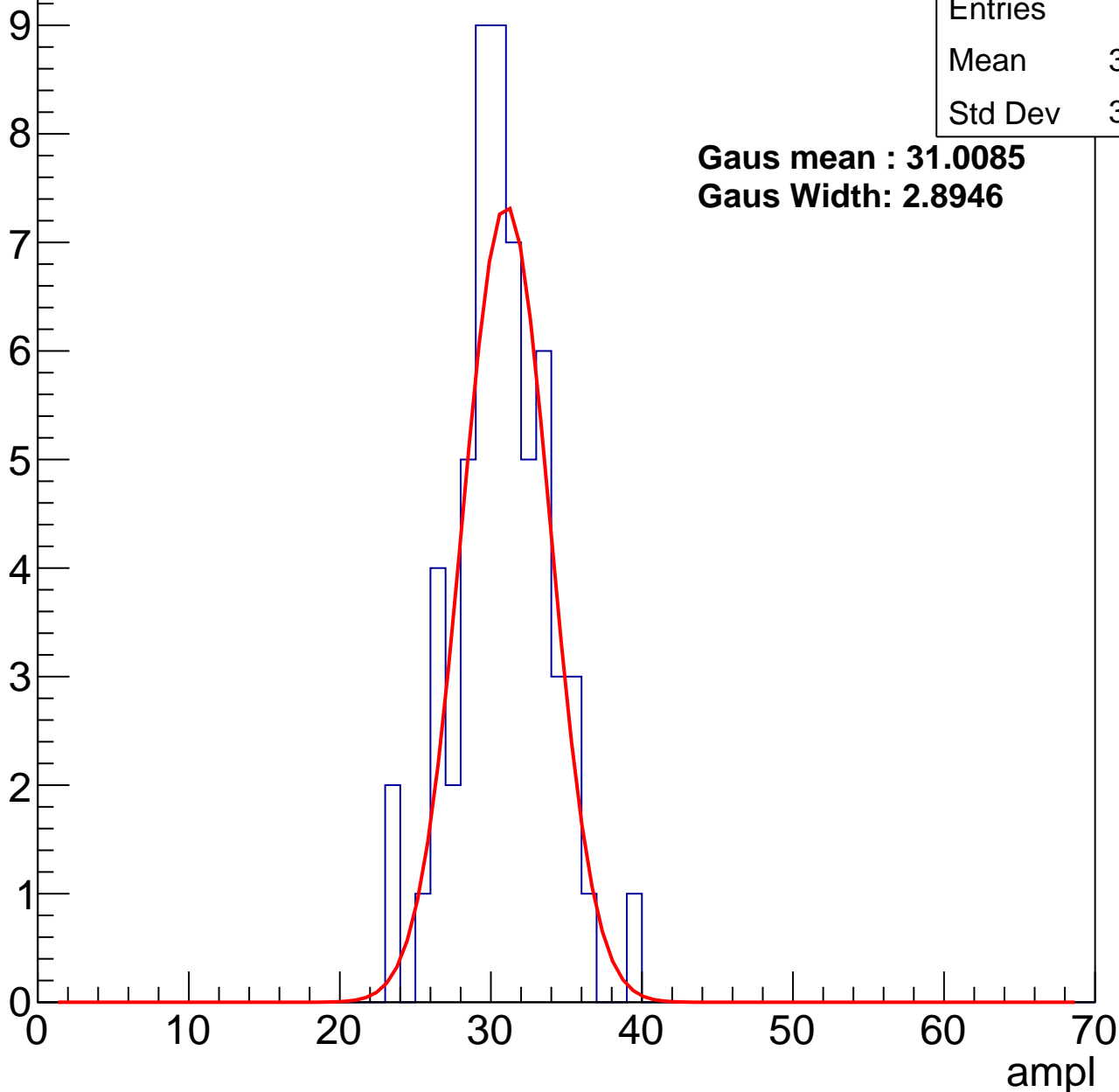
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	30.29
Std Dev	3.079

**Gaus mean : 31.0085**

**Gaus Width: 2.8946**



# B0L000S, U10-ch109, adc1

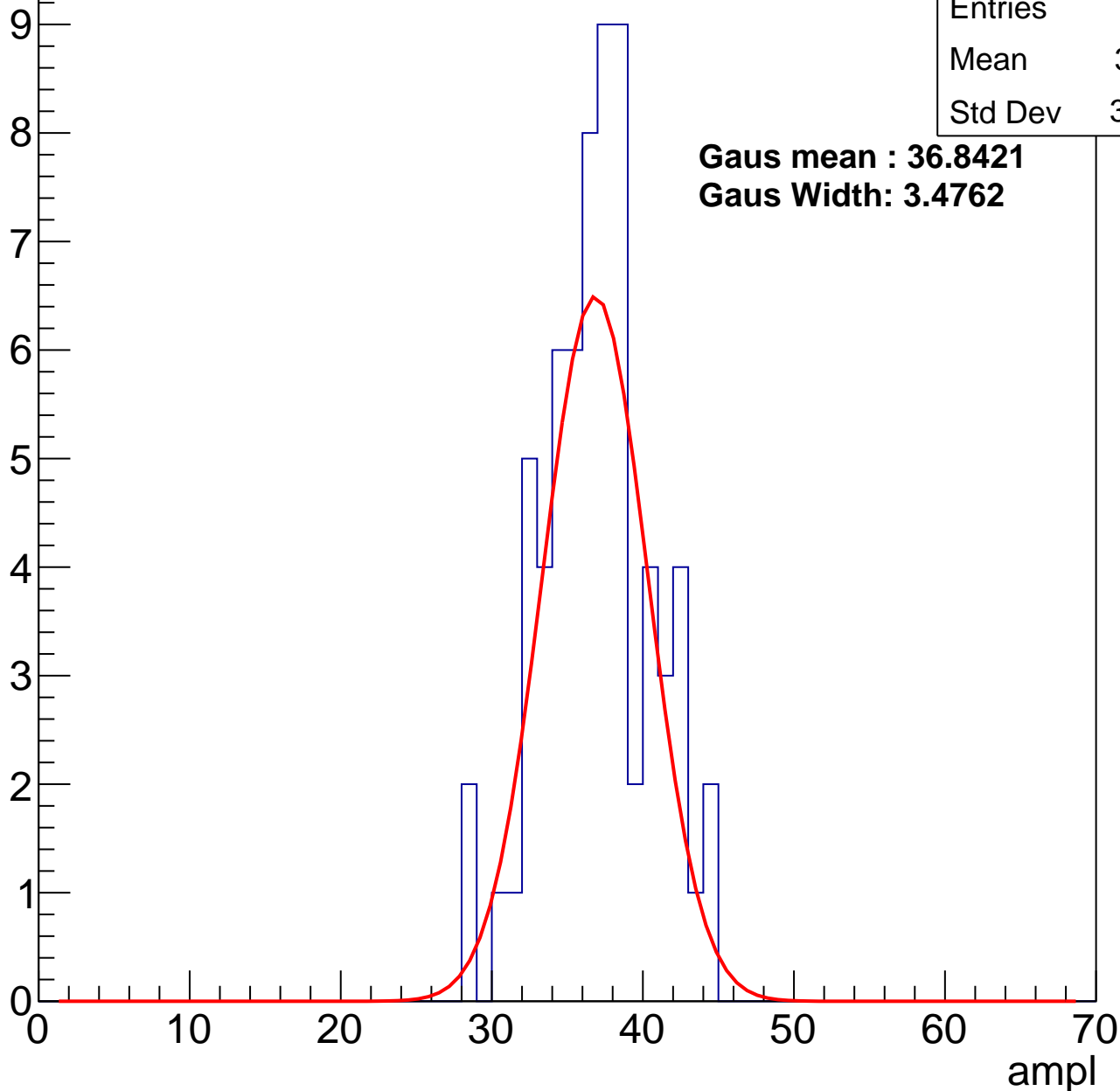
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.51
Std Dev	3.542

**Gaus mean : 36.8421**

**Gaus Width: 3.4762**



# B0L000S, U10-ch109, adc2

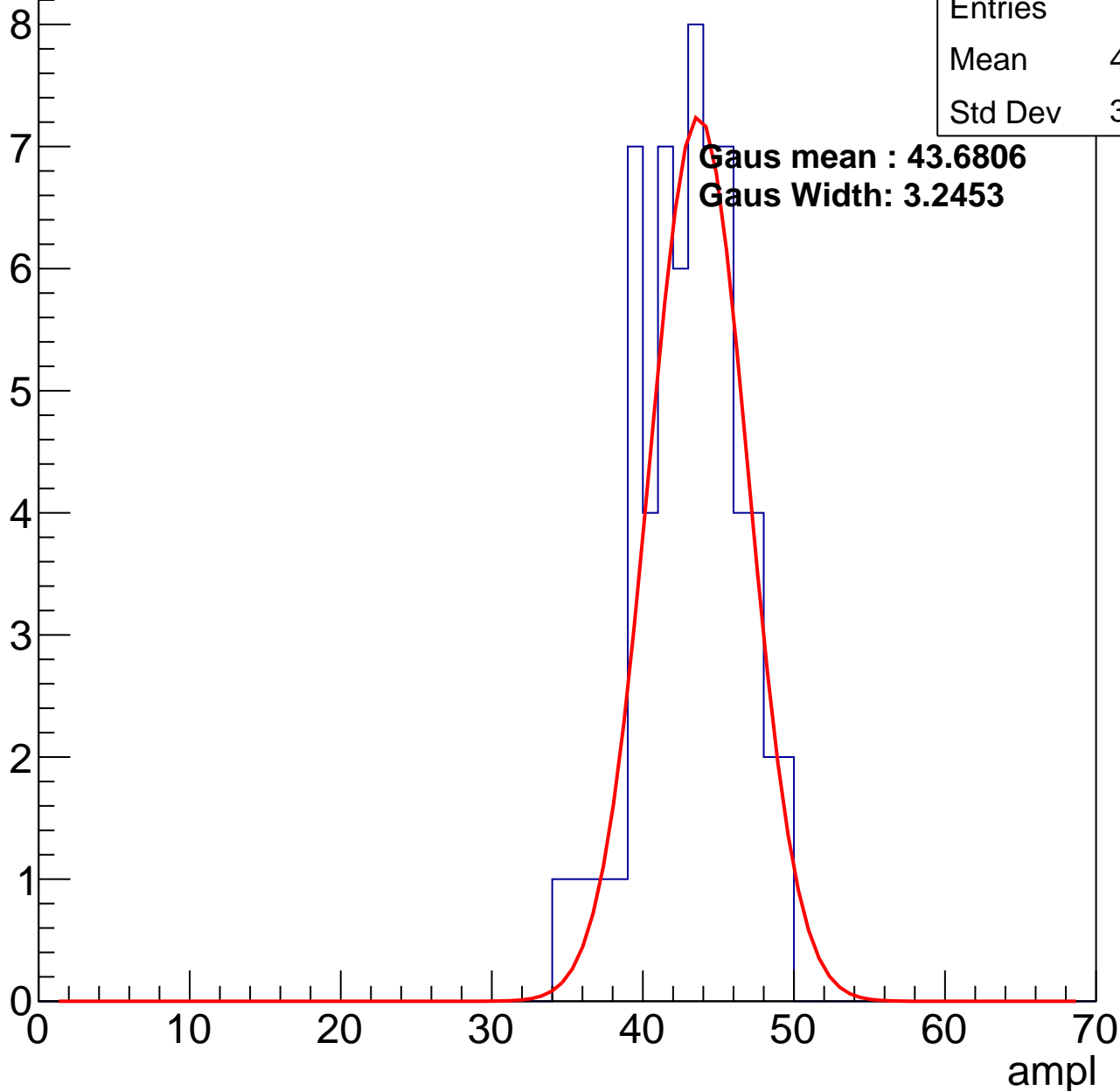
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.62
Std Dev	3.297

**Gaus mean : 43.6806**

**Gaus Width: 3.2453**



# B0L000S, U10-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	49.84
Std Dev	3.698

Entry

10

8

6

4

2

0

0

10

20

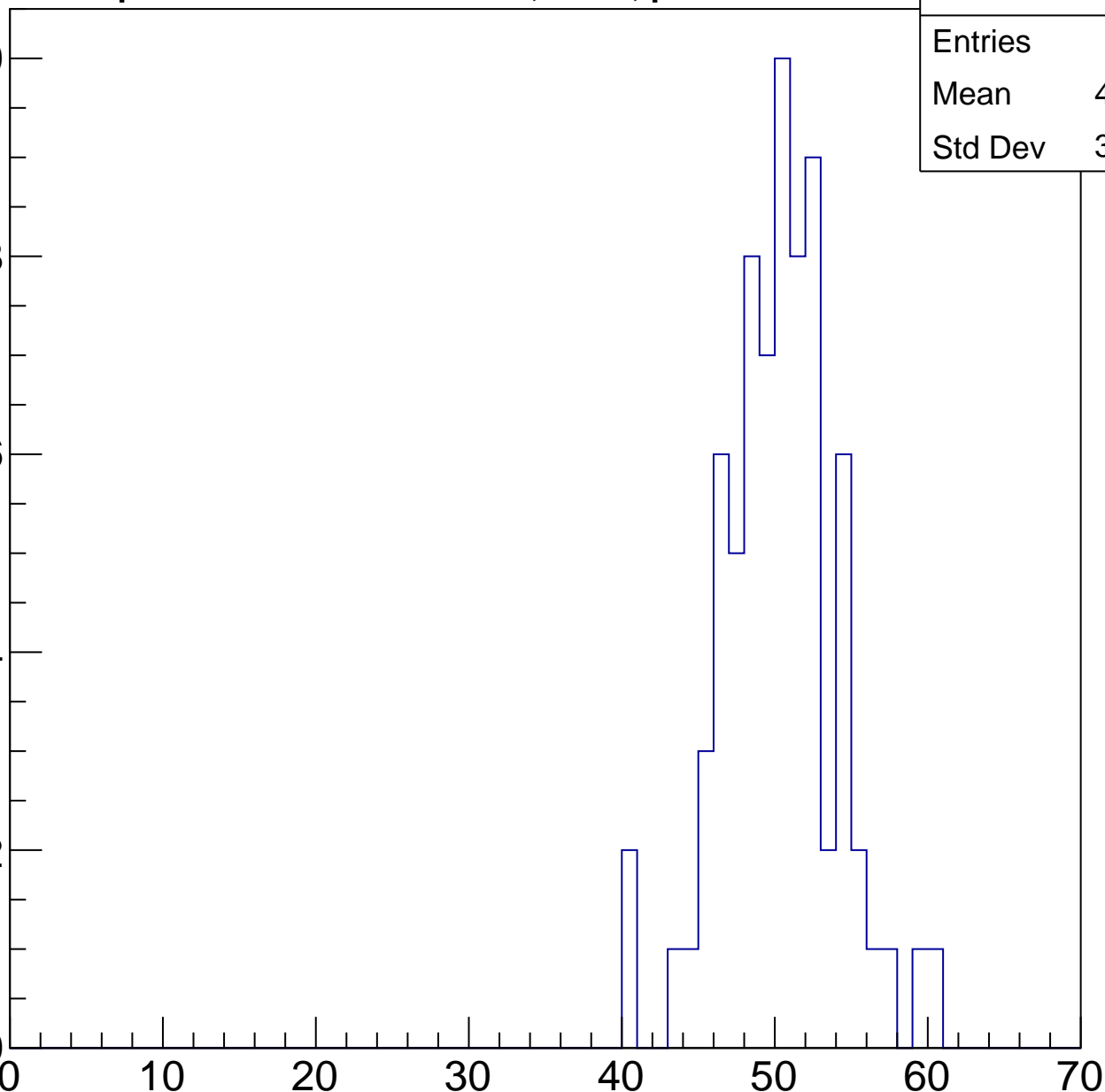
30

40

50

60

ampl

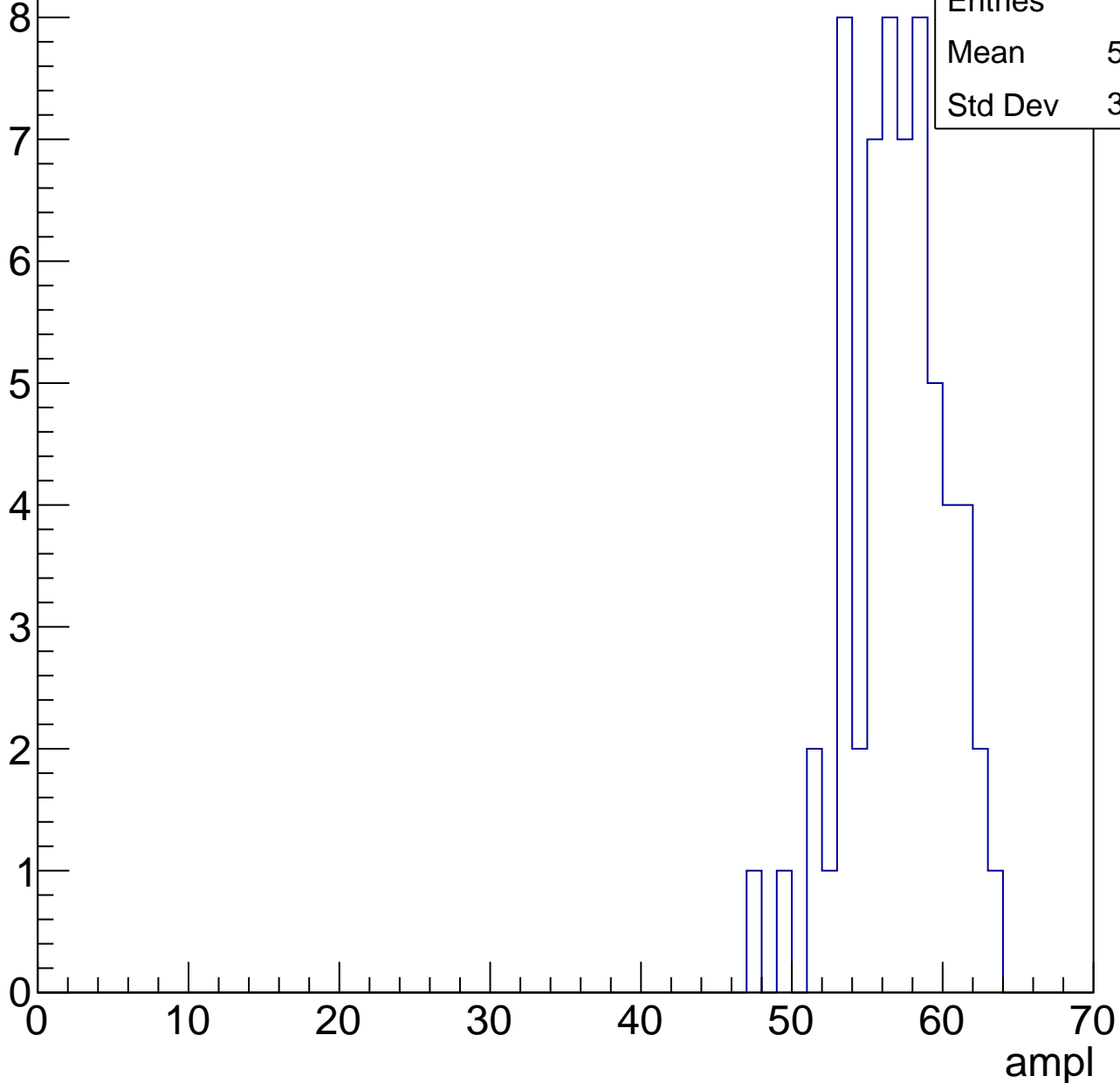


# B0L000S, U10-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.46
Std Dev	3.237

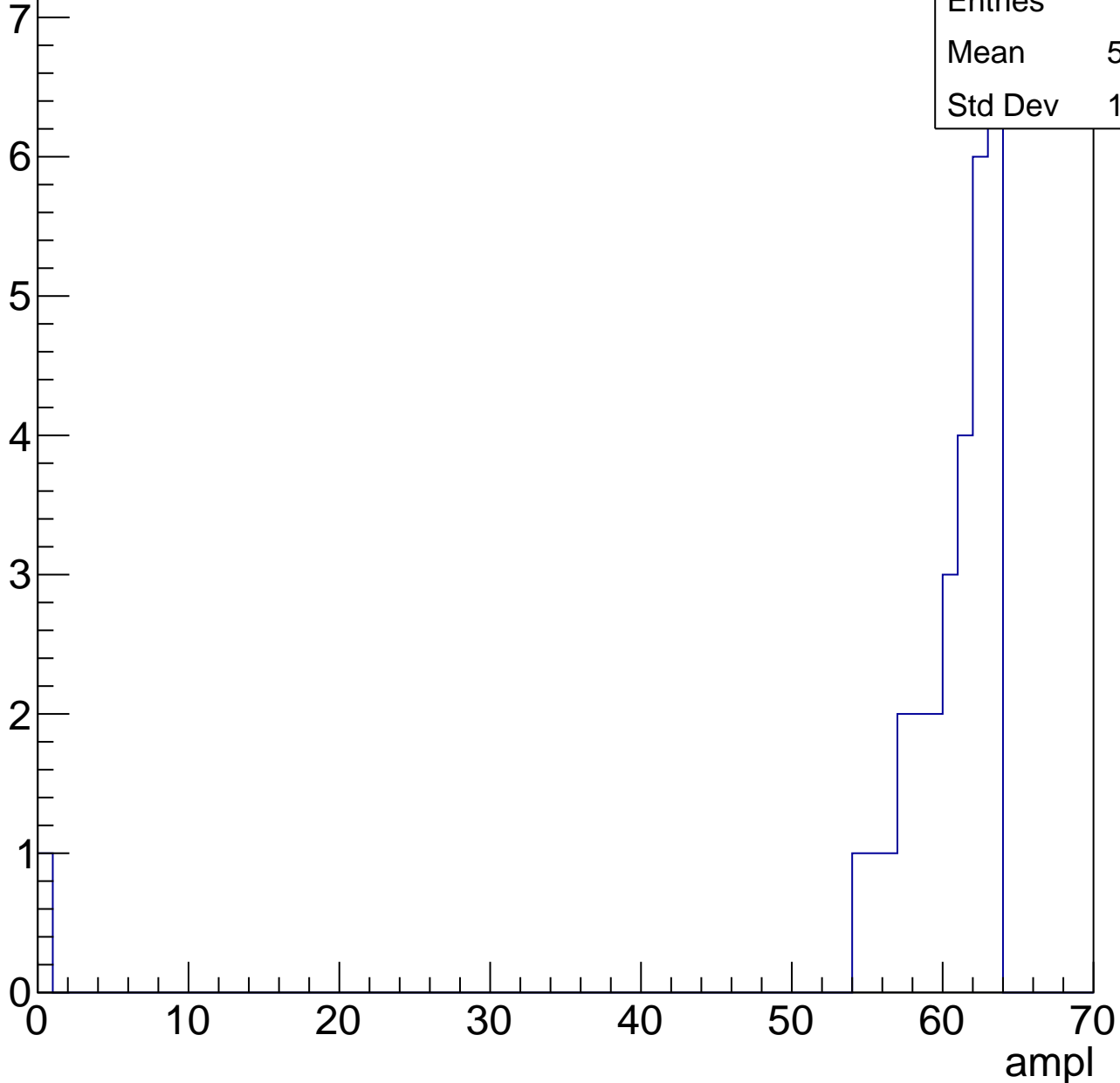


# B0L000S, U10-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	58.33
Std Dev	11.12



# B0L000S, U10-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

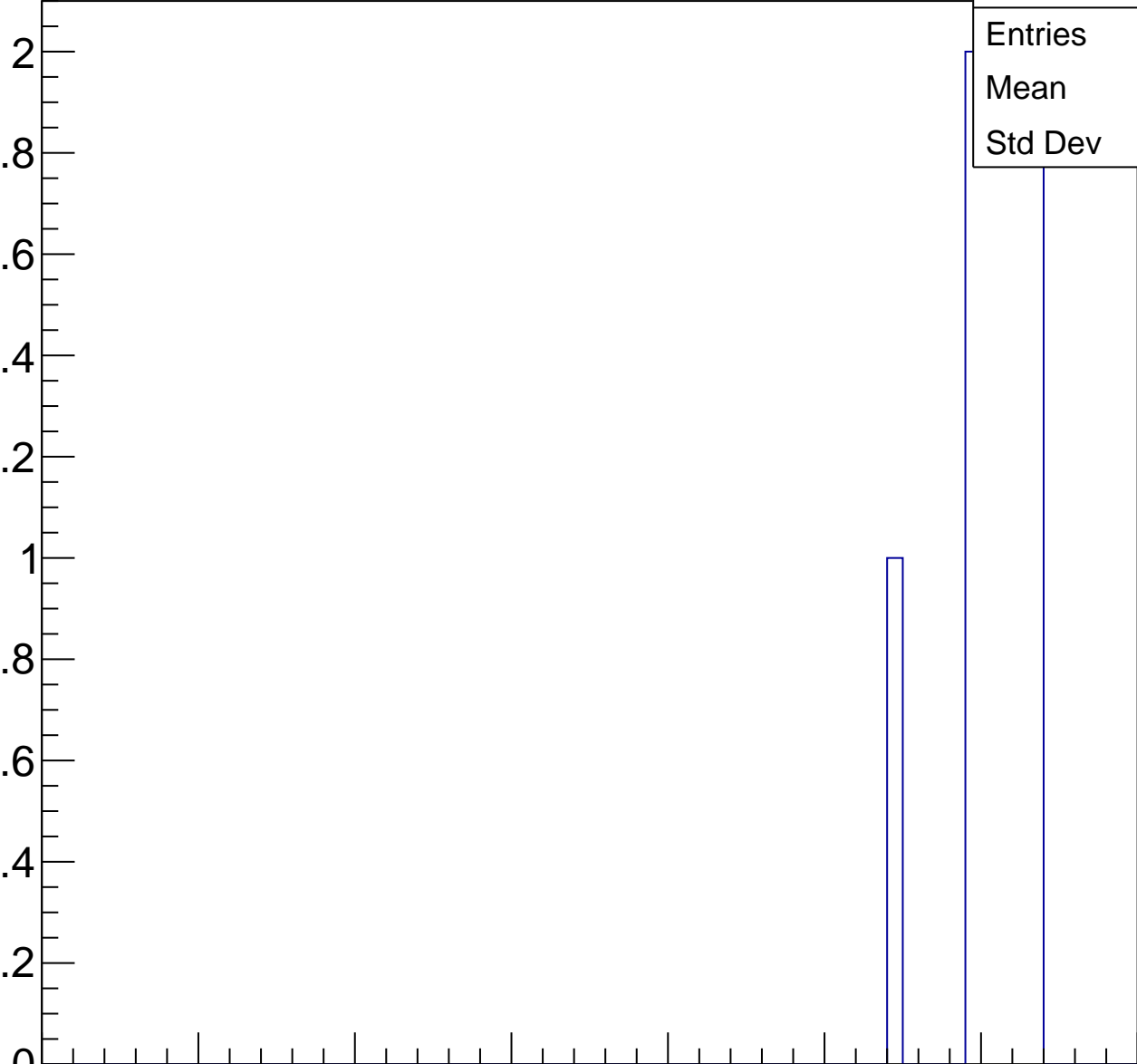
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	11
Mean	60.36
Std Dev	2.422

0 10 20 30 40 50 60 70

ampl





# B0L000S, U10-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch110, adc0

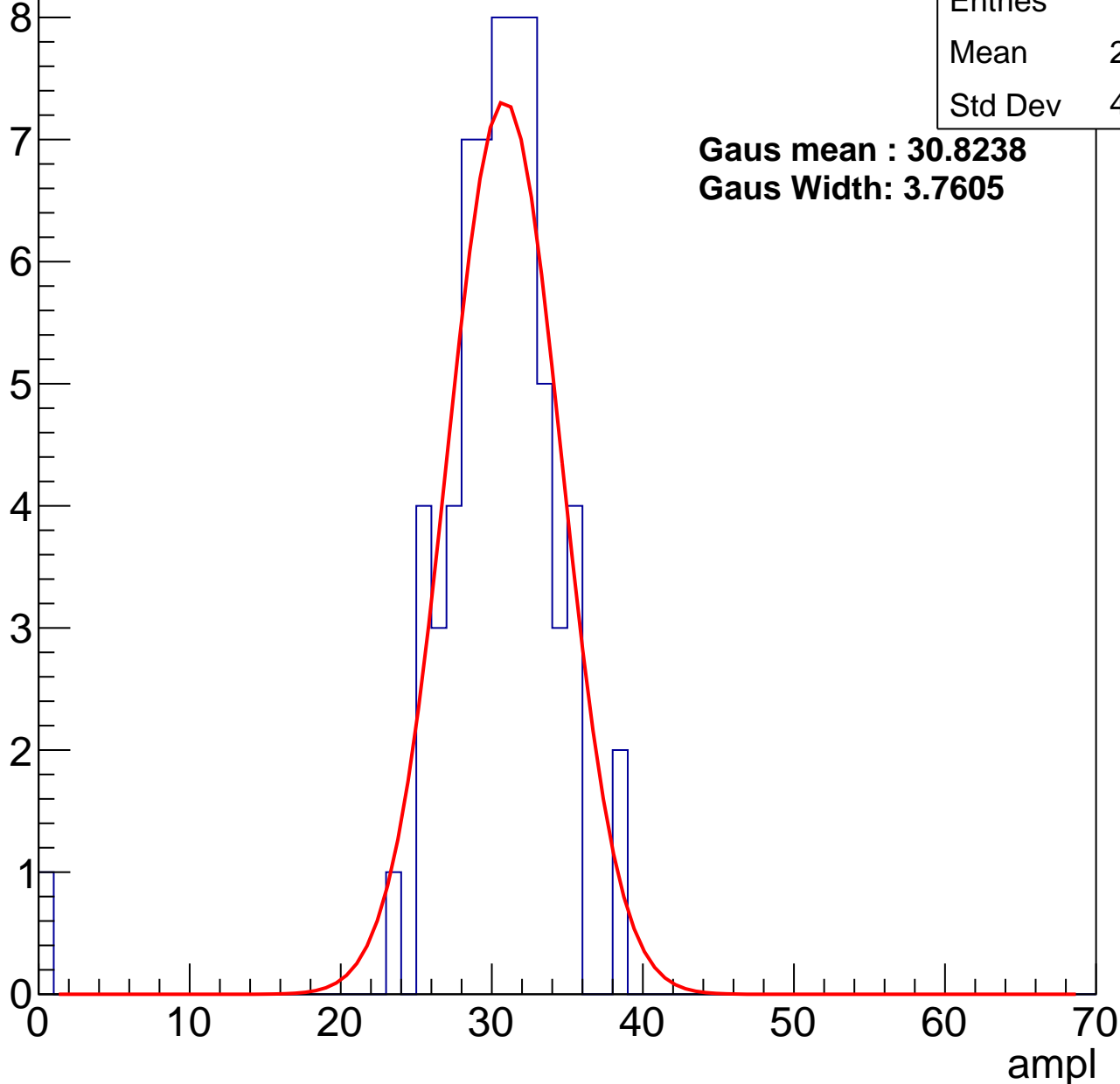
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.77
Std Dev	4.845

**Gaus mean : 30.8238**

**Gaus Width: 3.7605**



# B0L000S, U10-ch110, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	37.32
Std Dev	3.393

**Gaus mean : 37.3458**

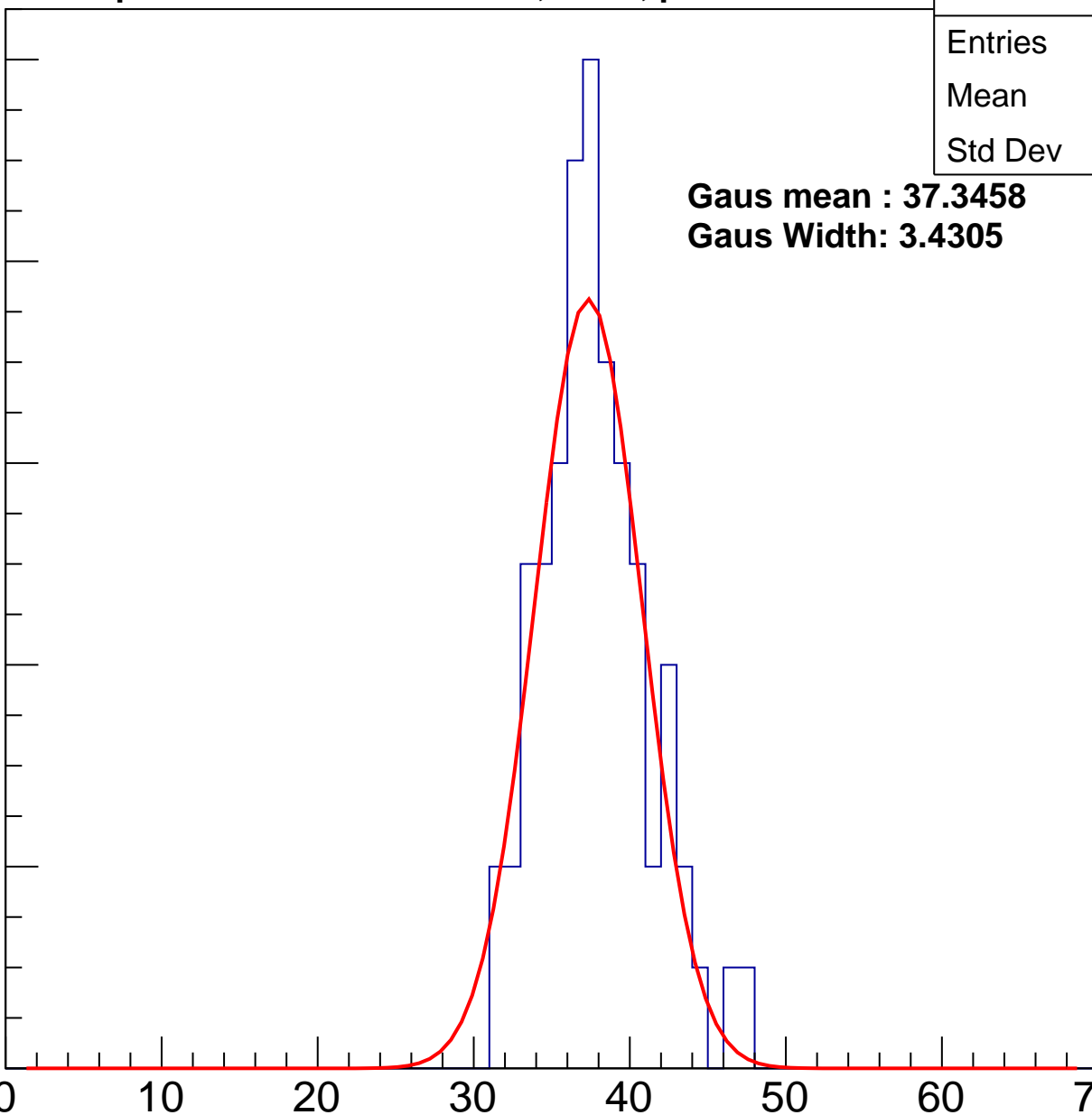
**Gaus Width: 3.4305**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch110, adc2

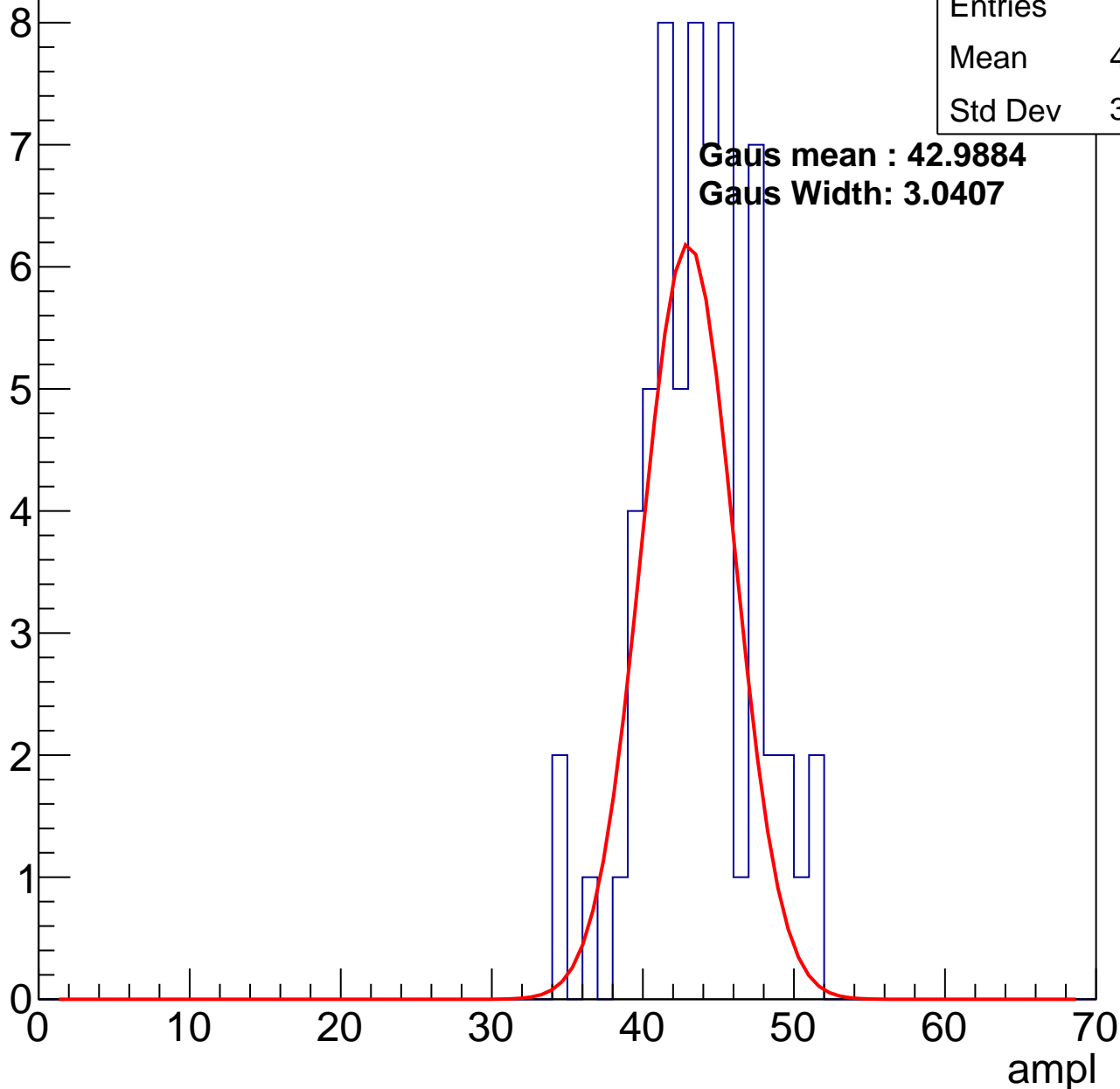
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.27
Std Dev	3.615

**Gaus mean : 42.9884**

**Gaus Width: 3.0407**

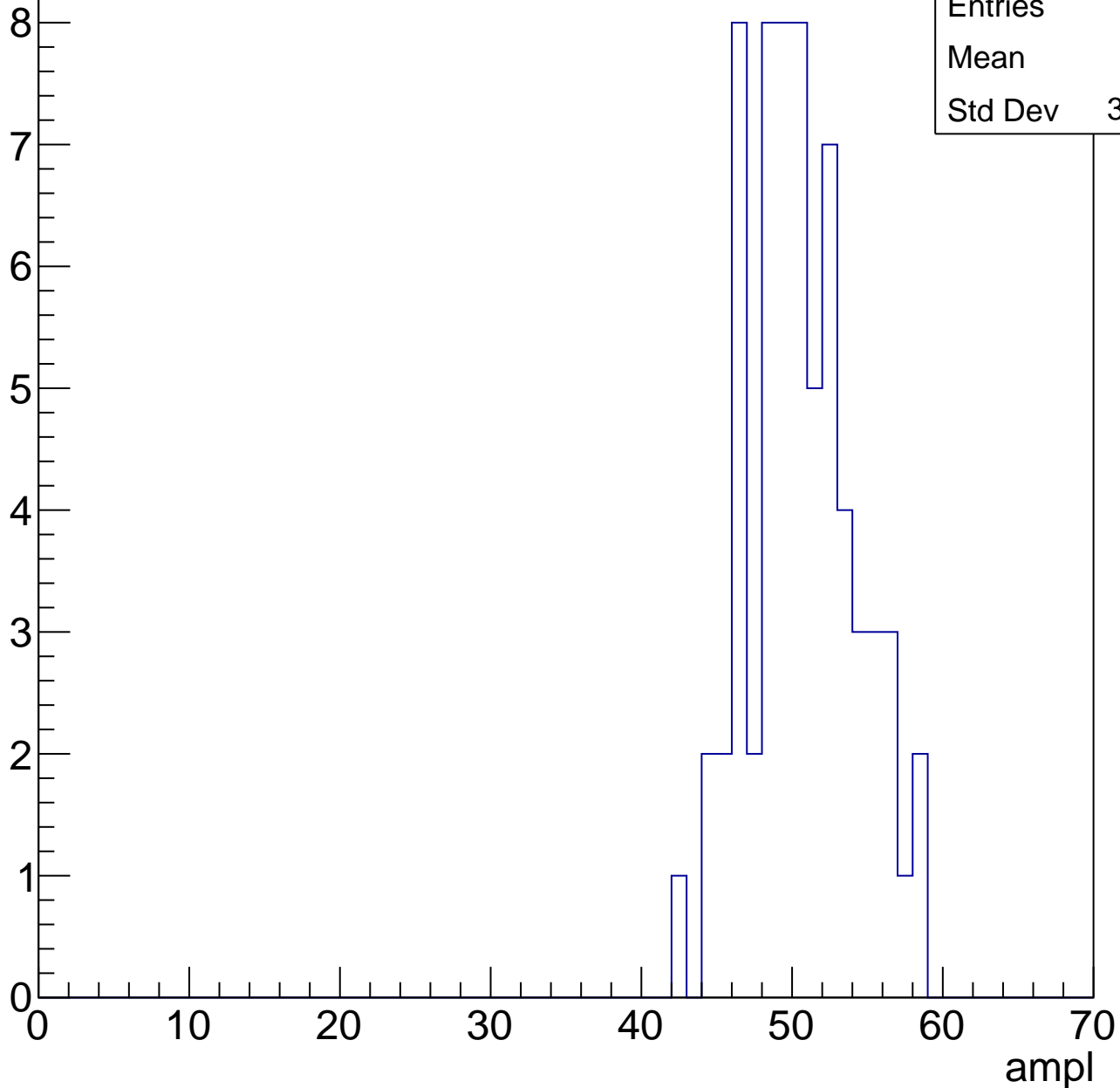


# B0L000S, U10-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

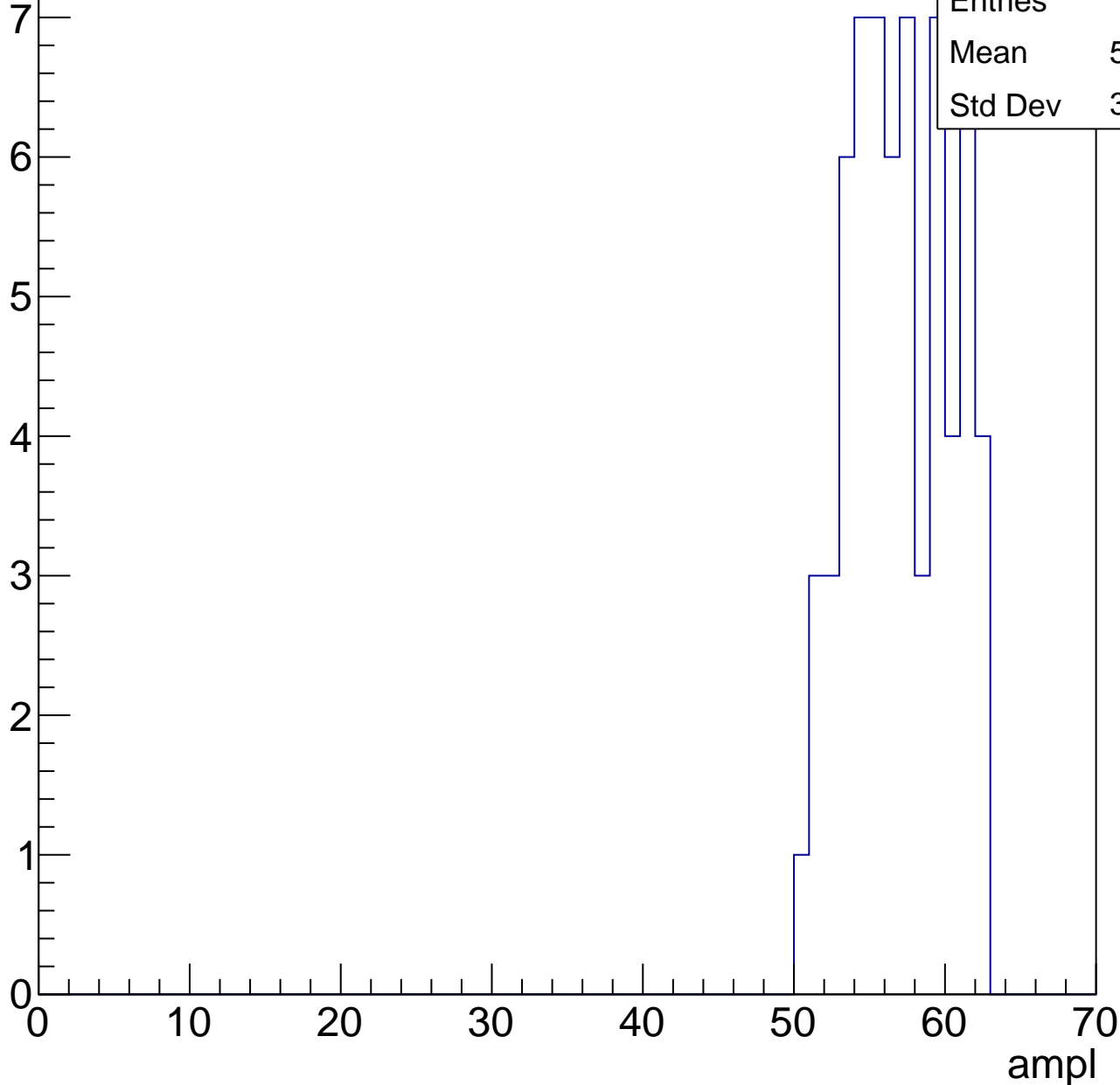
Entries	67
Mean	50.1
Std Dev	3.558



# B0L000S, U10-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



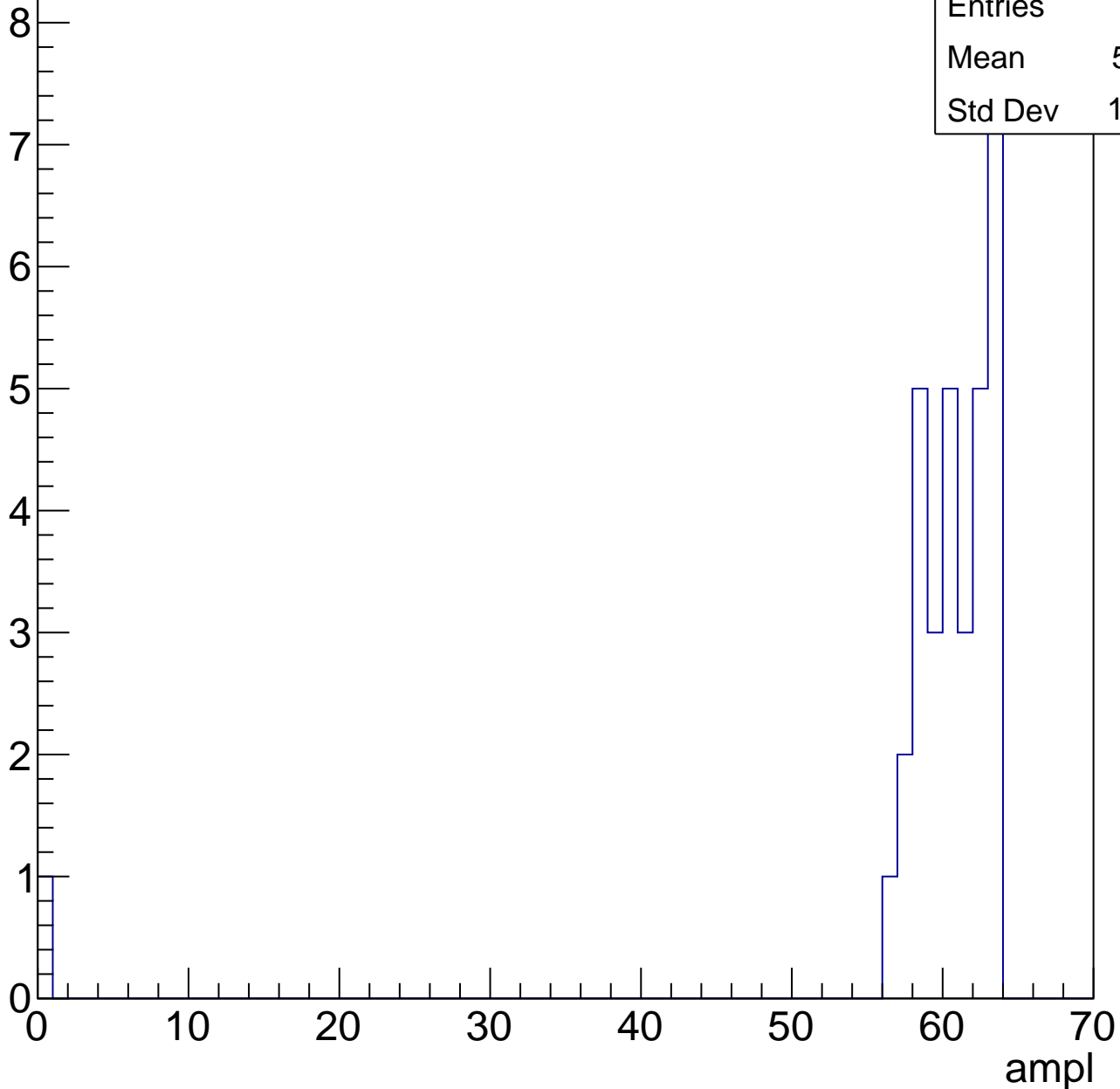
Entries	65
Mean	56.57
Std Dev	3.263

# B0L000S, U10-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	58.61
Std Dev	10.57



# B0L000S, U10-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	62
Std Dev	1.225



# B0L000S, U10-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch111, adc0

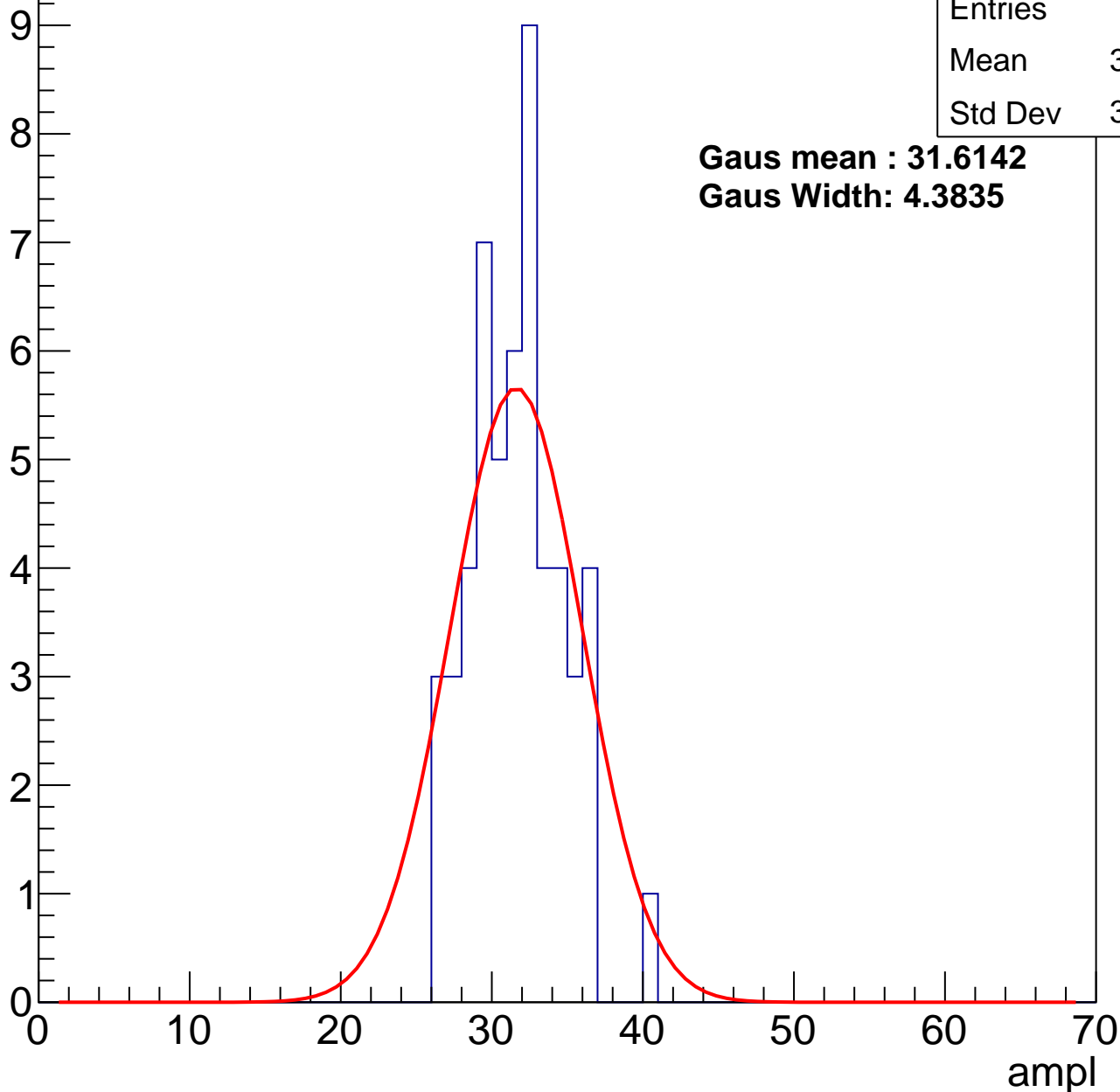
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	31.23
Std Dev	3.007

**Gaus mean : 31.6142**

**Gaus Width: 4.3835**



# B0L000S, U10-ch111, adc1

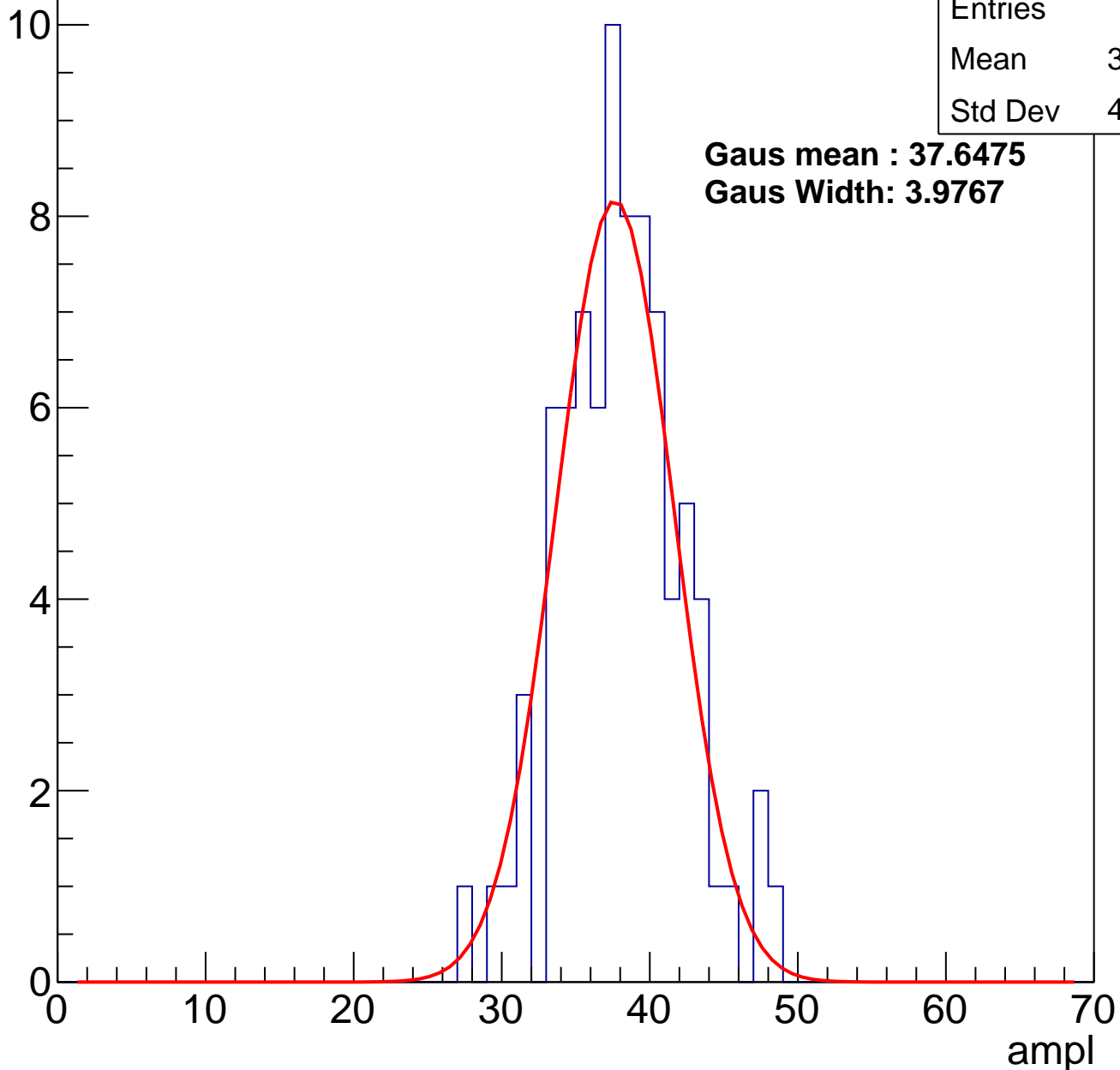
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	37.62
Std Dev	4.047

**Gaus mean : 37.6475**

**Gaus Width: 3.9767**

Entry



# B0L000S, U10-ch111, adc2

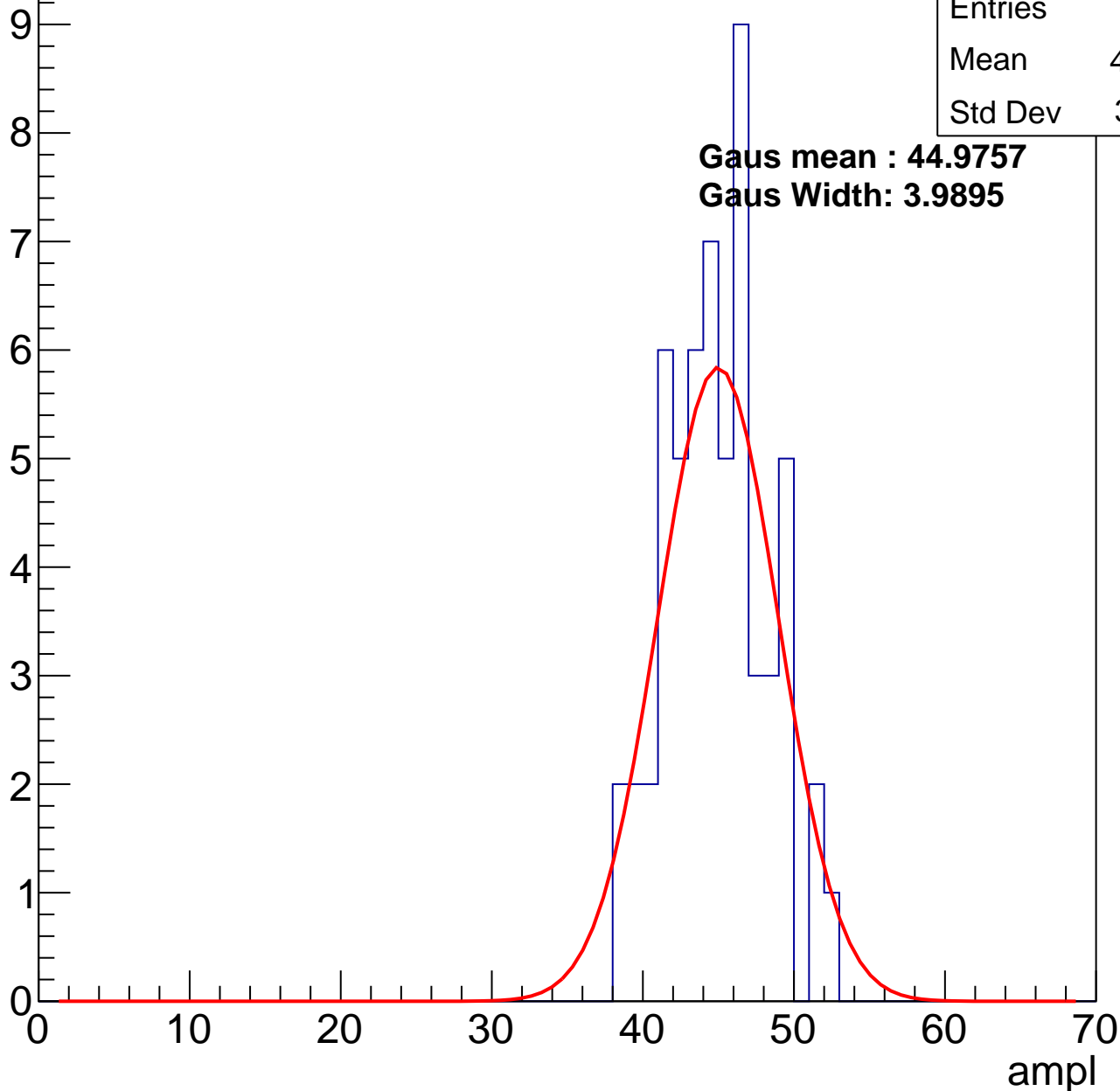
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.47
Std Dev	3.281

**Gaus mean : 44.9757**

**Gaus Width: 3.9895**



# B0L000S, U10-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

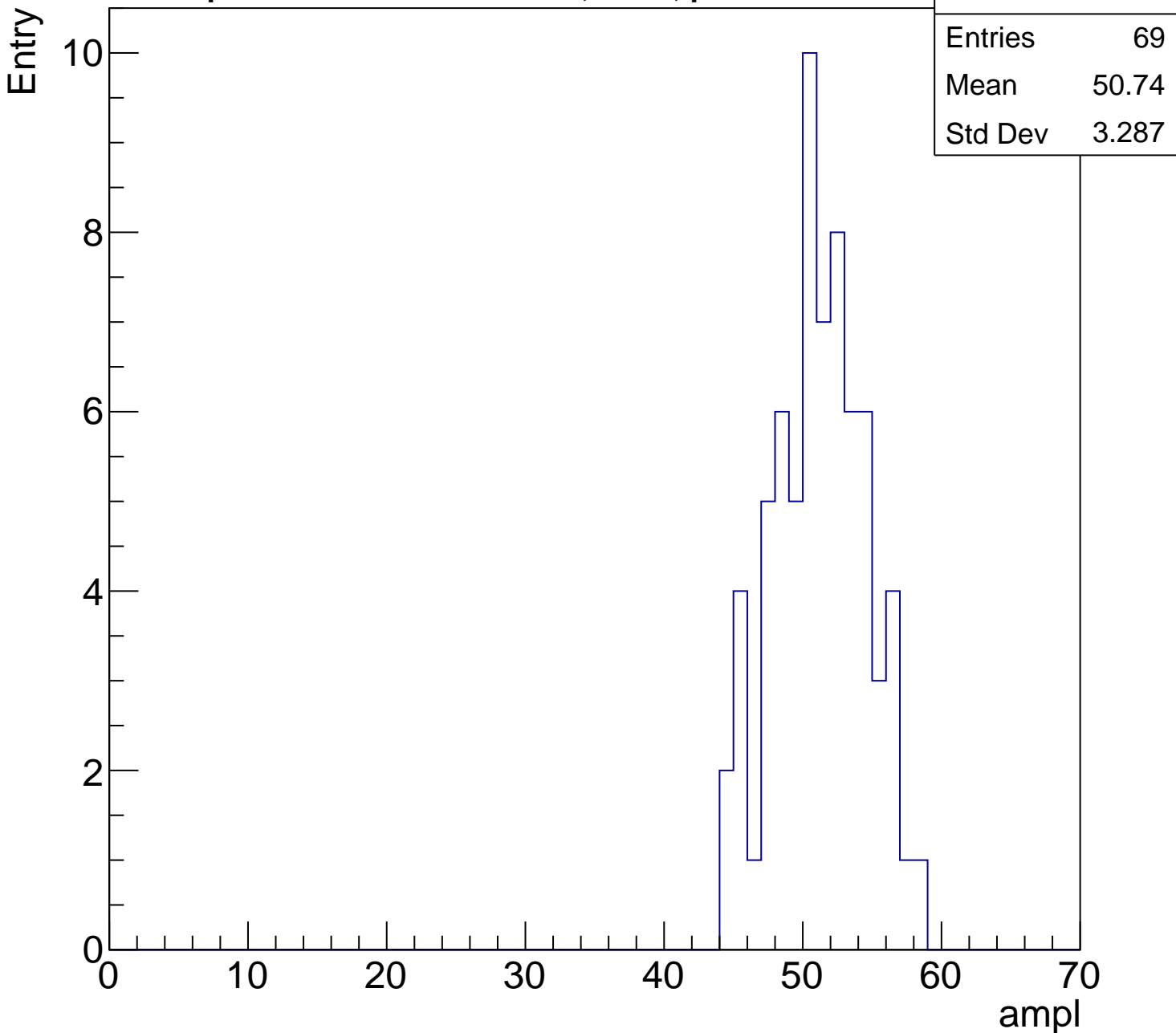
Entries	69
Mean	50.74
Std Dev	3.287

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

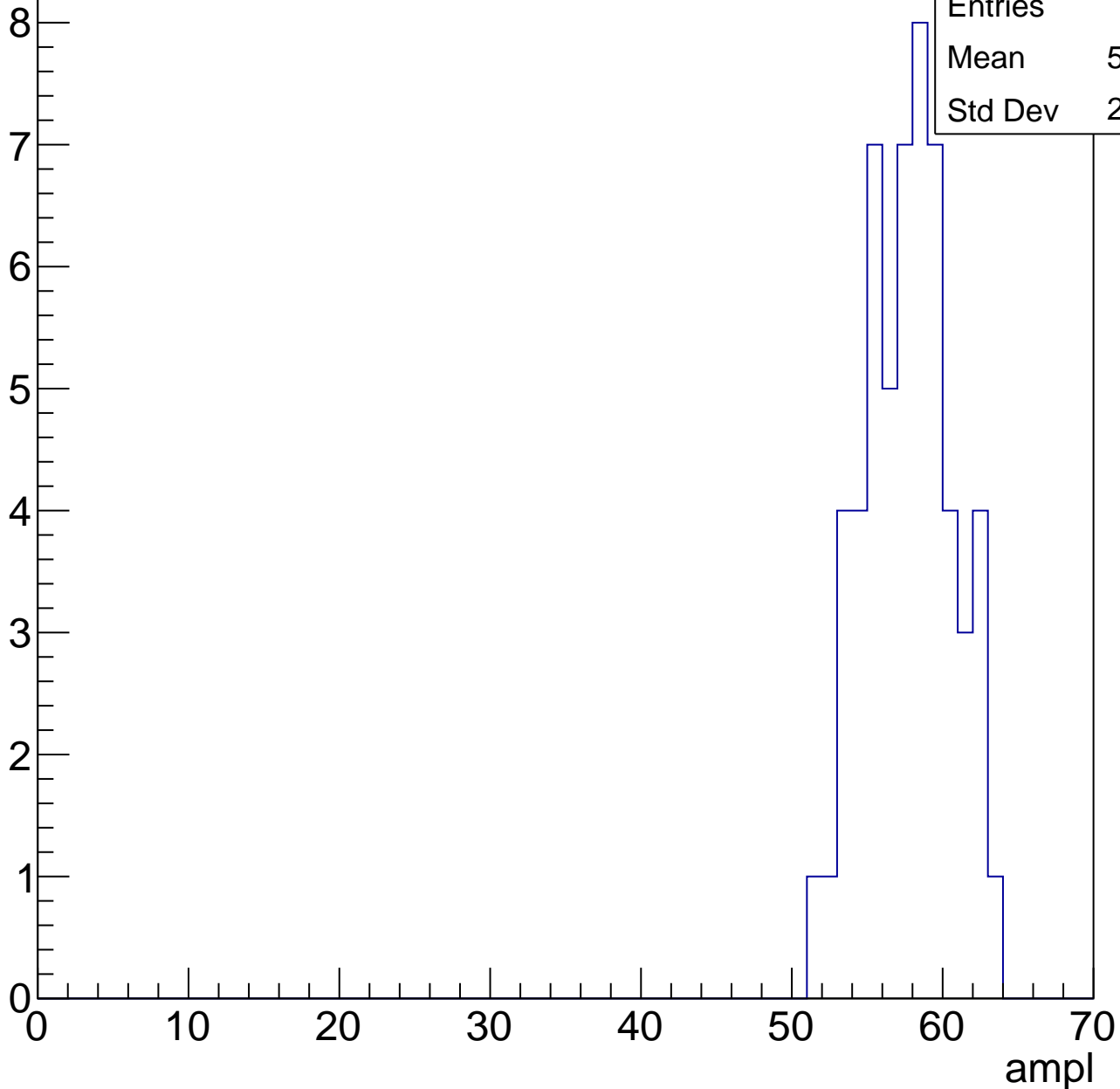


# B0L000S, U10-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	57.25
Std Dev	2.824

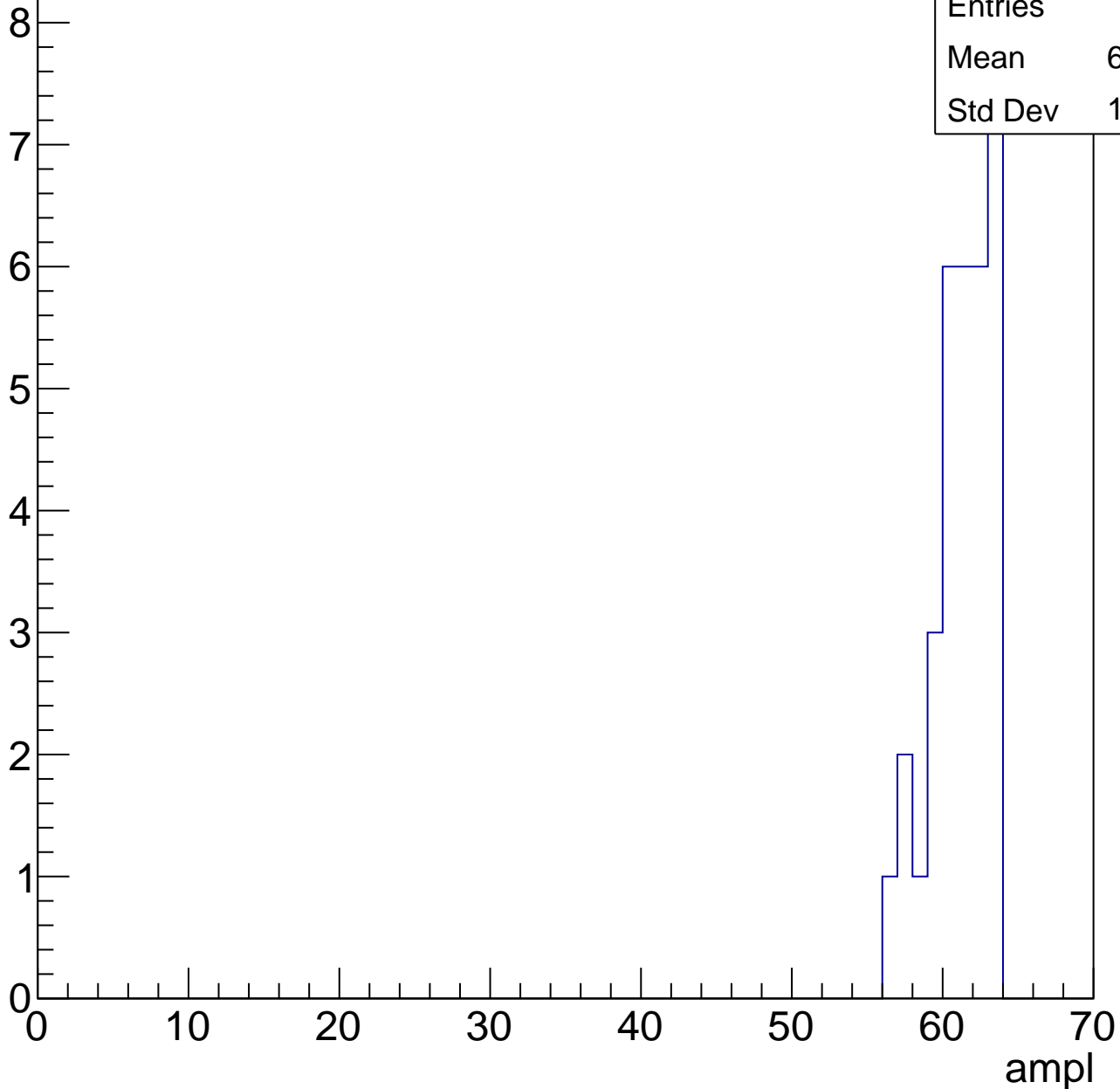


# B0L000S, U10-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	60.82
Std Dev	1.914



# B0L000S, U10-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch112, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.89
Std Dev	3.205

**Gaus mean : 29.9252**

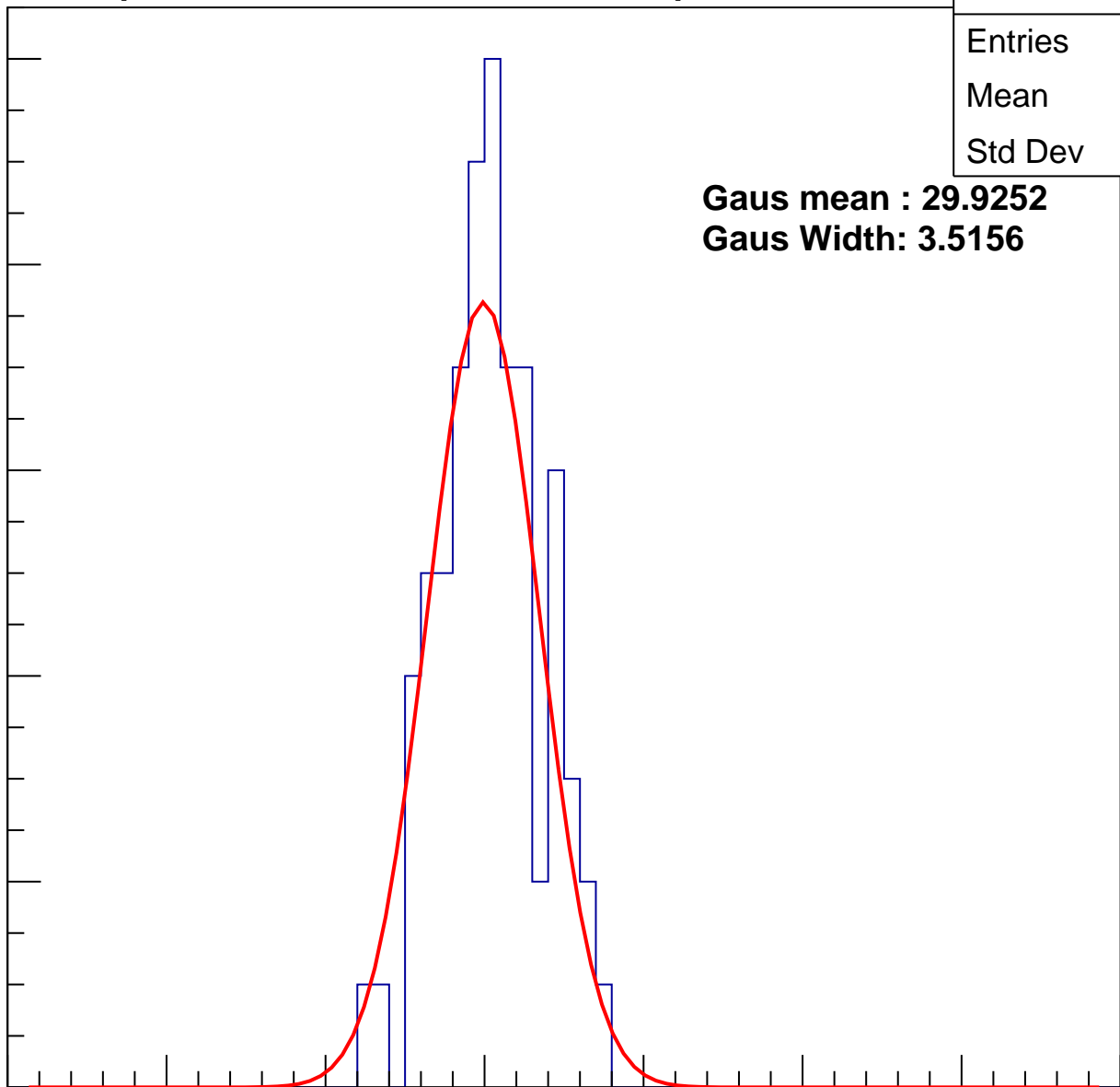
**Gaus Width: 3.5156**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	37.64
Std Dev	3.388

**Gaus mean : 38.1274**

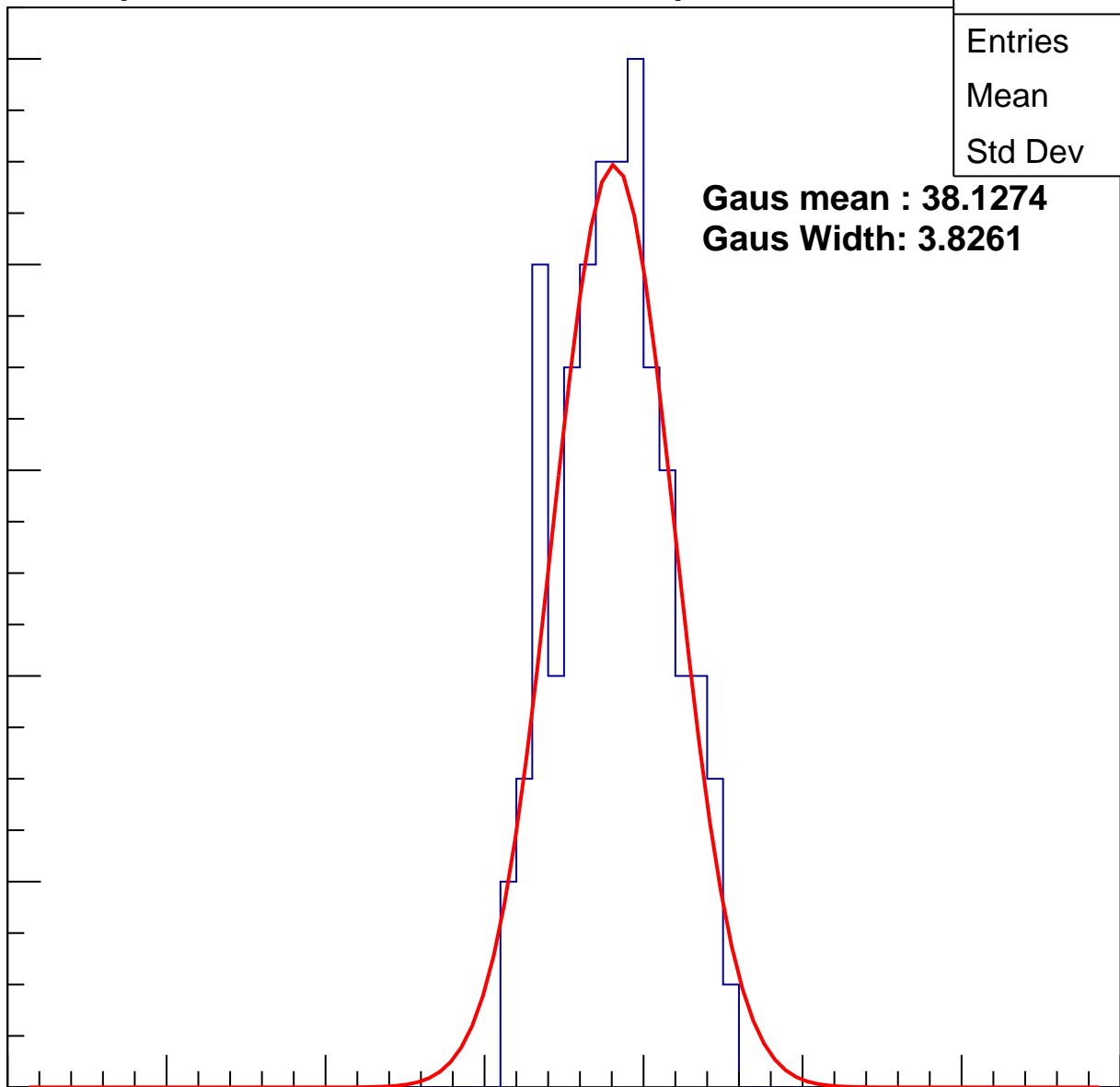
**Gaus Width: 3.8261**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch112, adc2

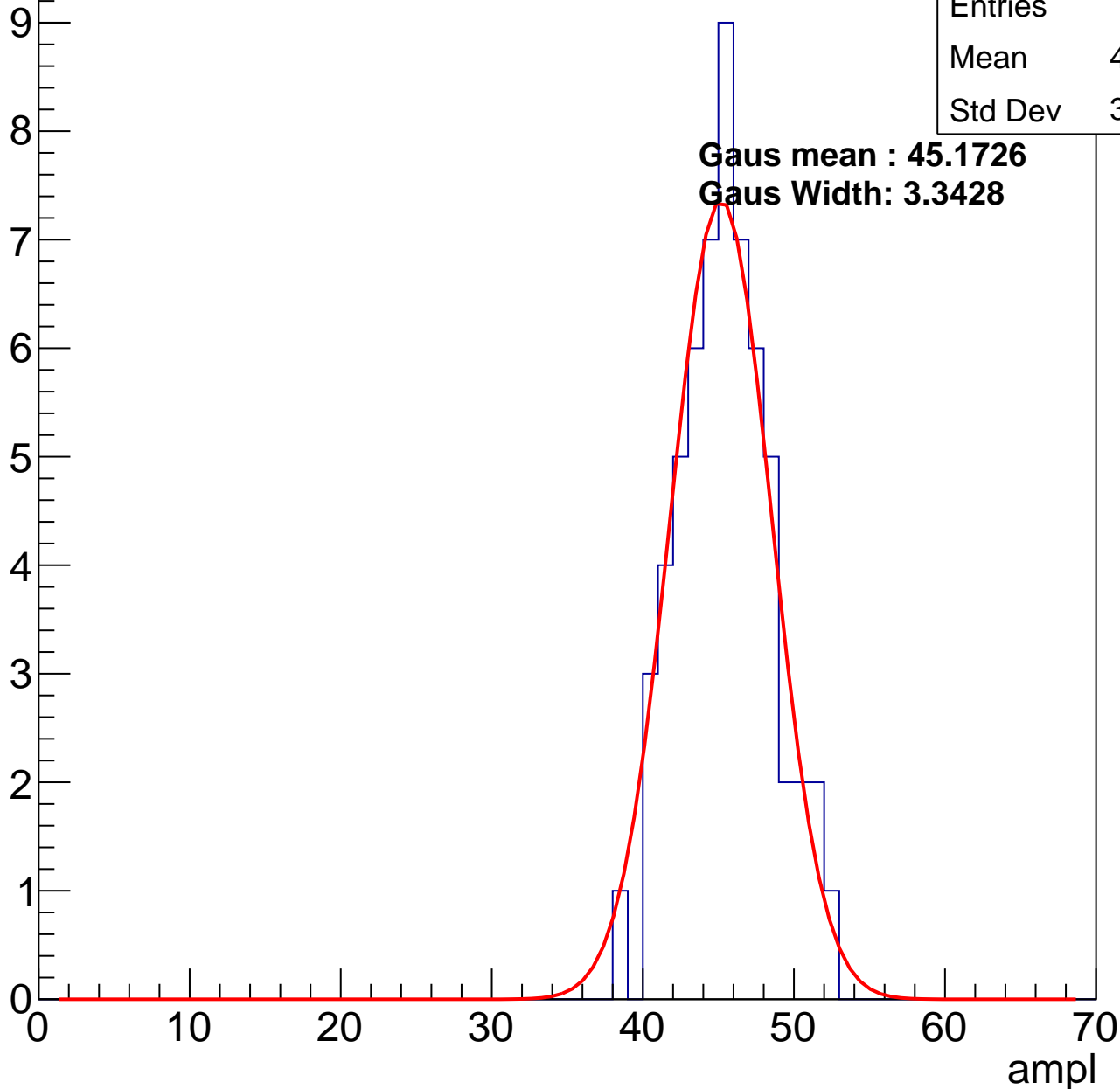
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.98
Std Dev	3.008

**Gaus mean : 45.1726**

**Gaus Width: 3.3428**

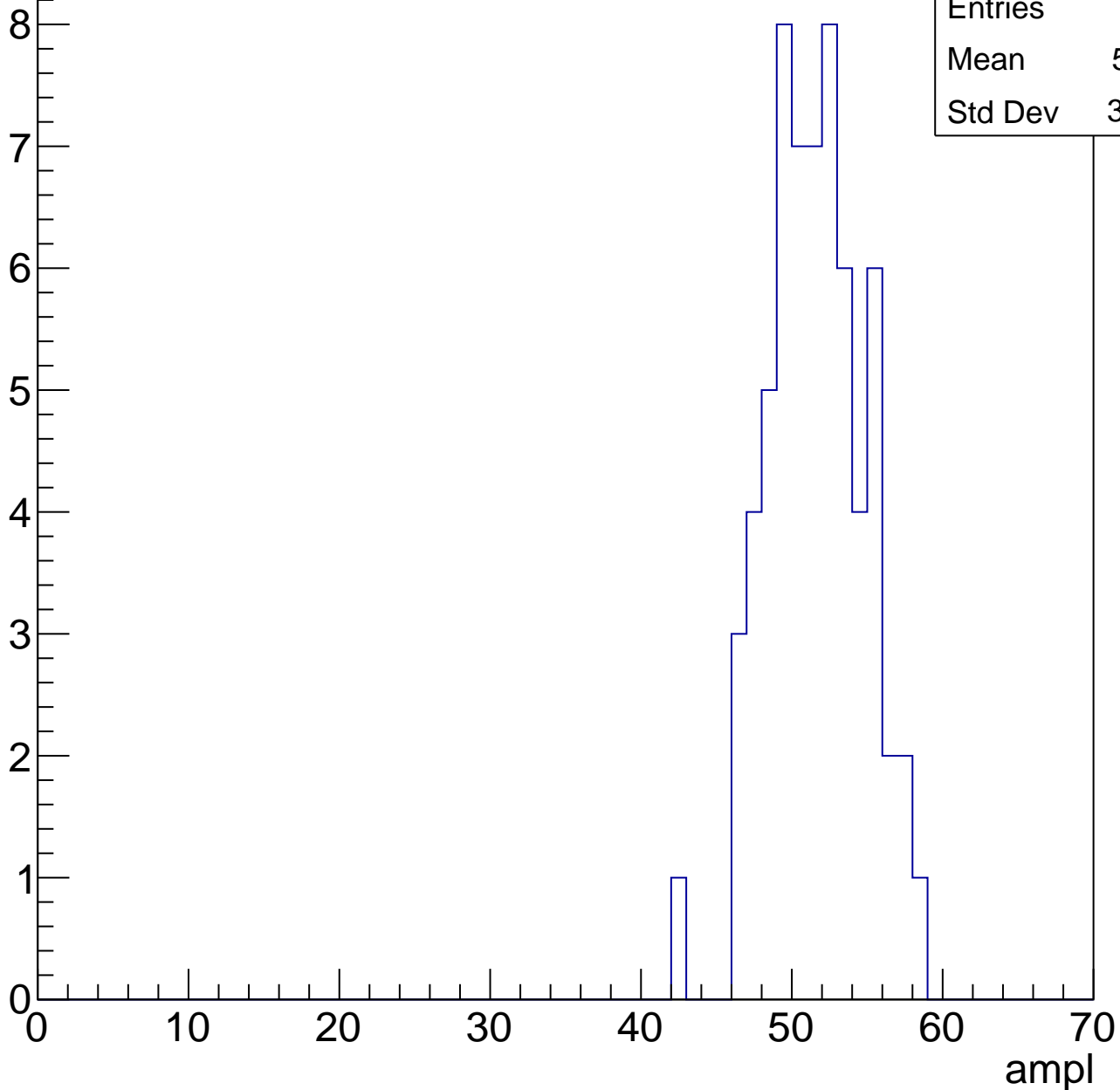


# B0L000S, U10-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.11
Std Dev	3.158

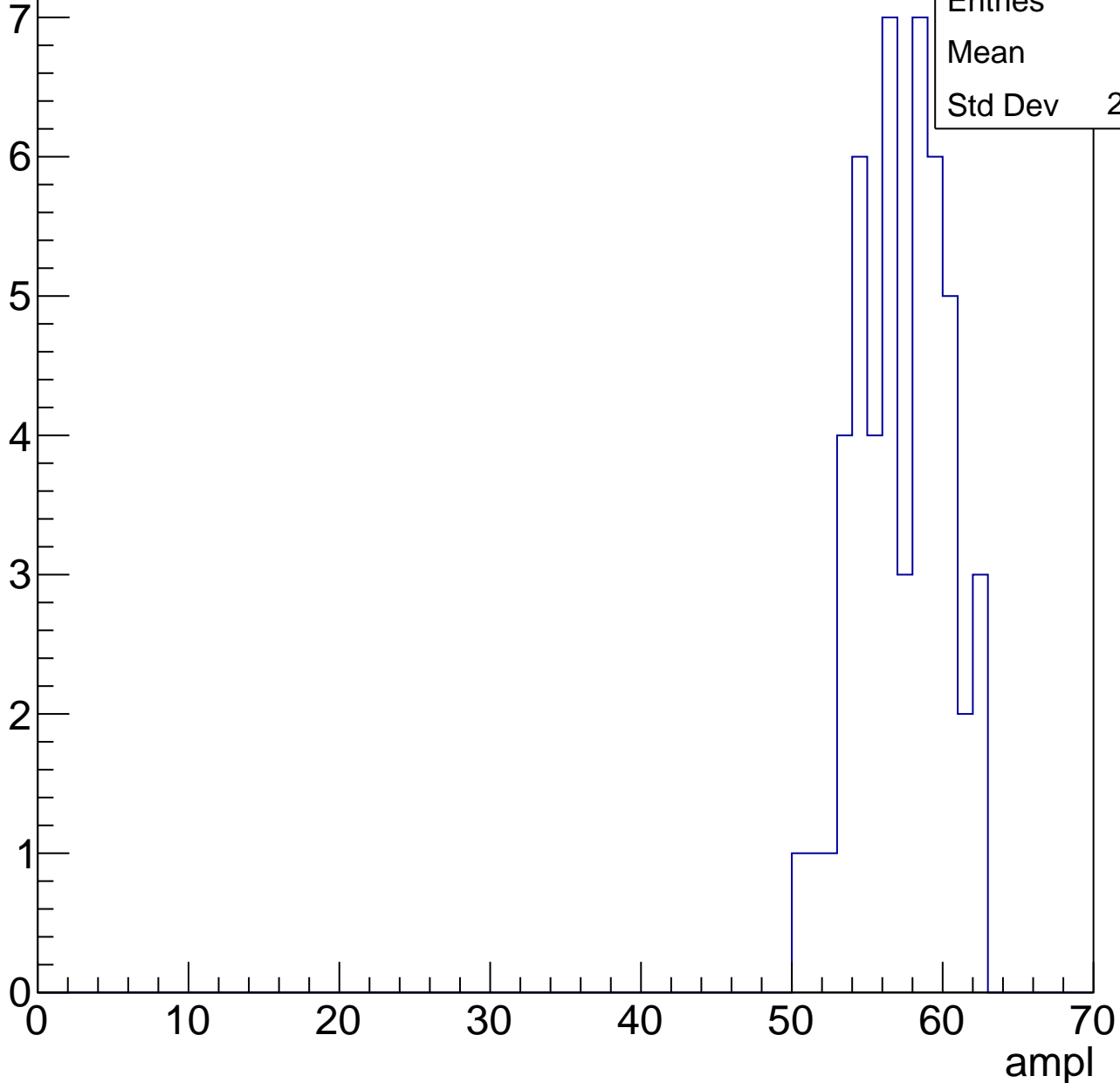


# B0L000S, U10-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.8
Std Dev	2.939

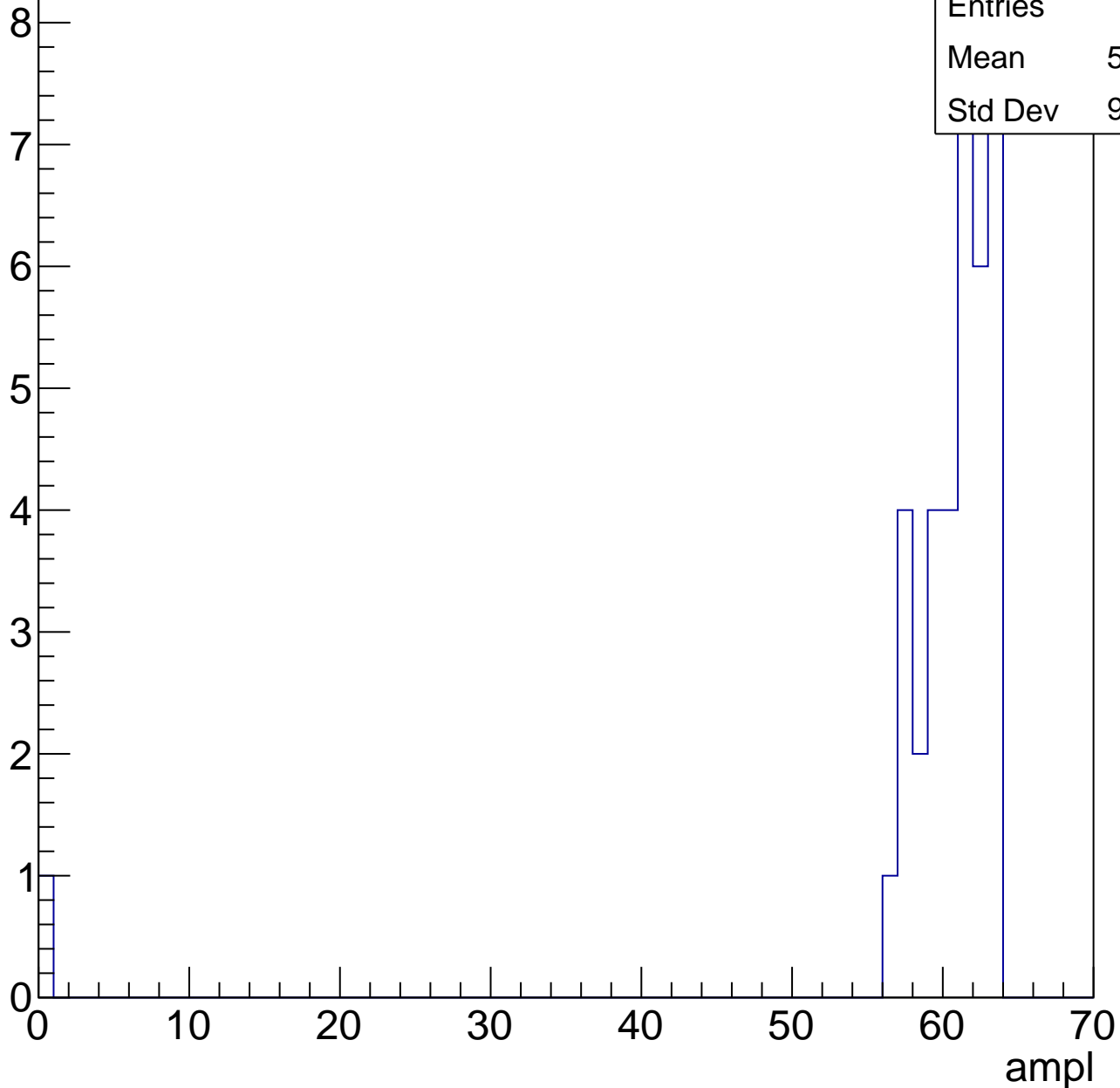


# B0L000S, U10-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

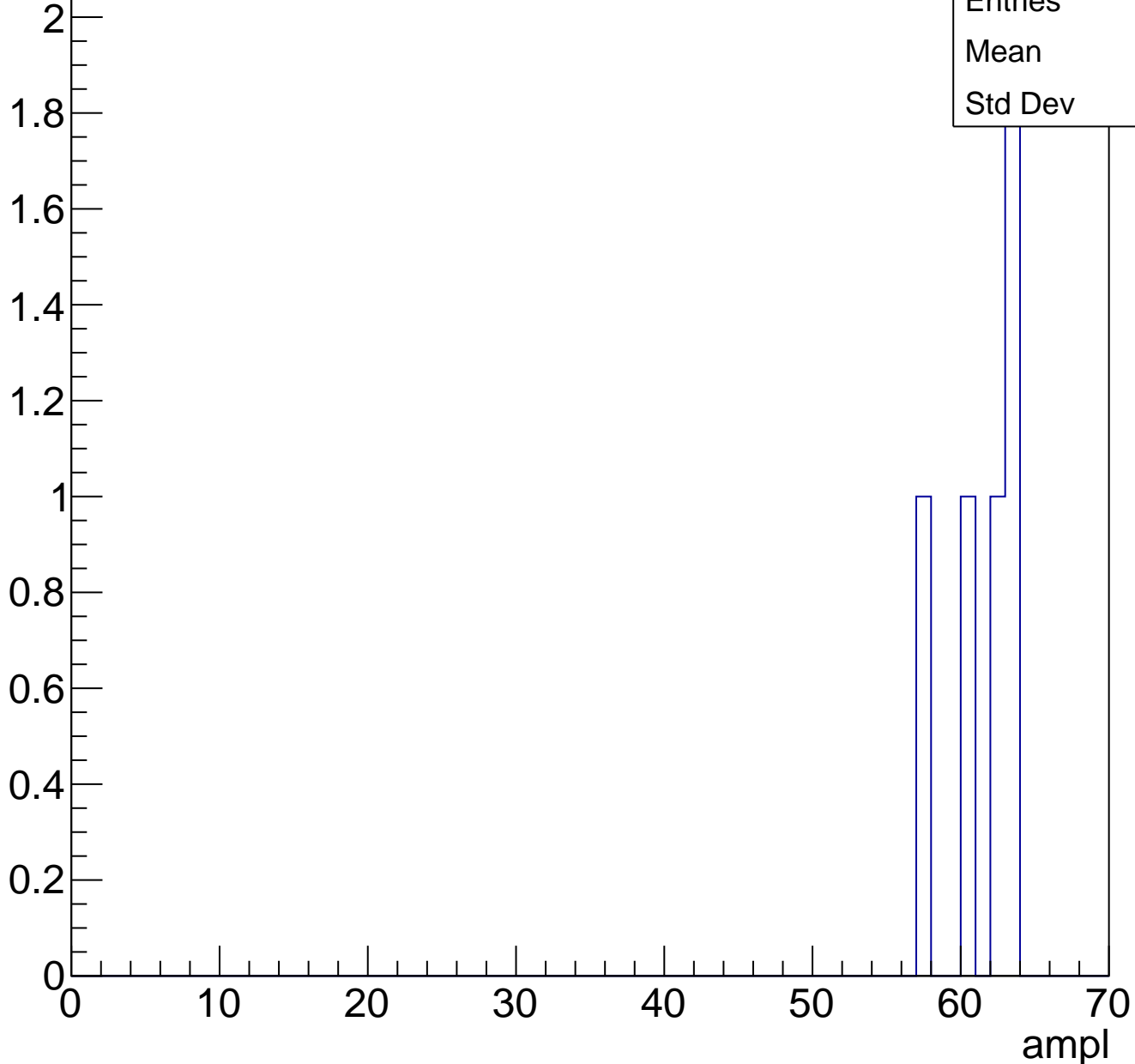
Entries	38
Mean	58.95
Std Dev	9.902



# B0L000S, U10-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch113, adc0

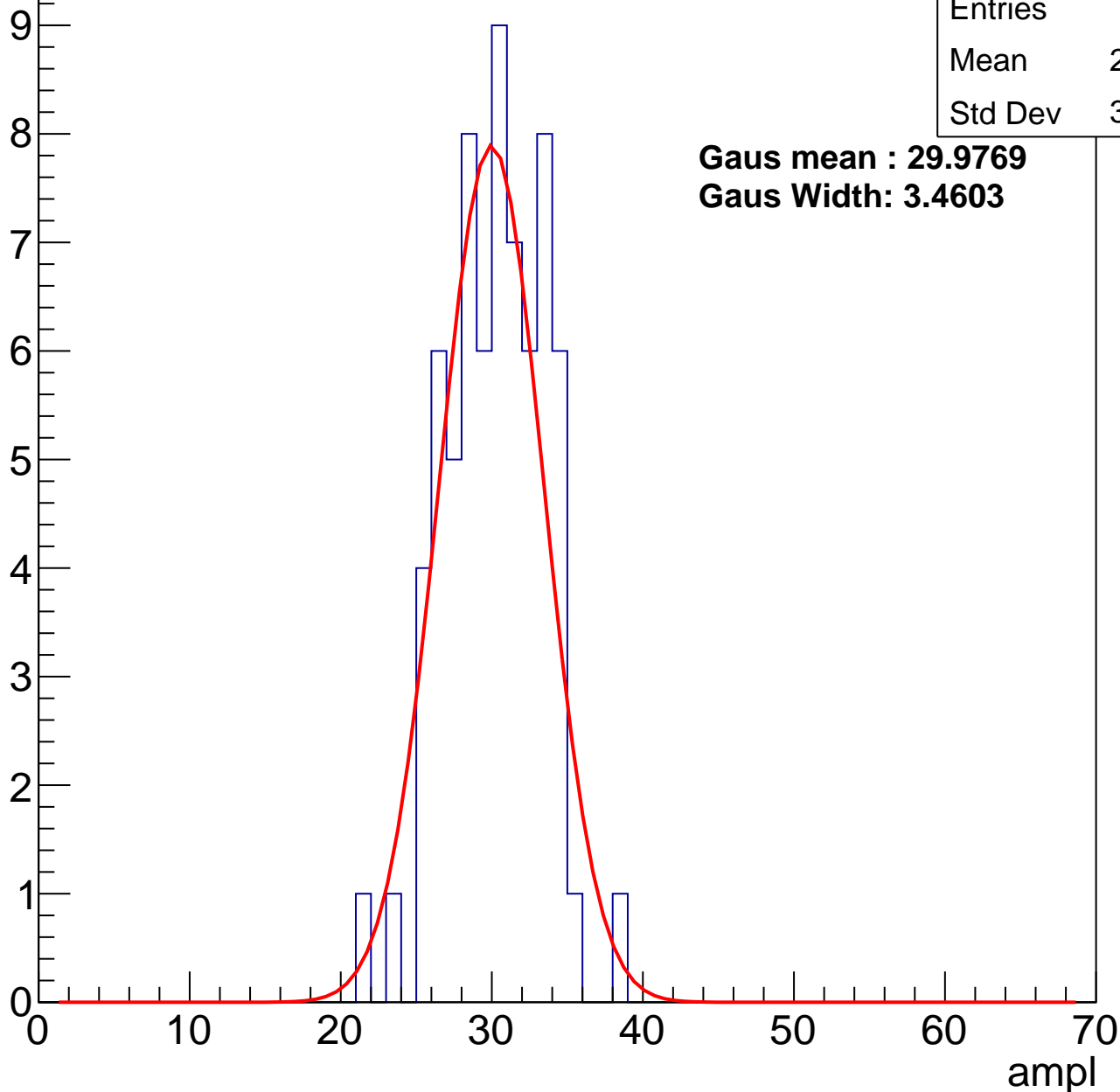
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.75
Std Dev	3.169

**Gaus mean : 29.9769**

**Gaus Width: 3.4603**



# B0L000S, U10-ch113, adc1

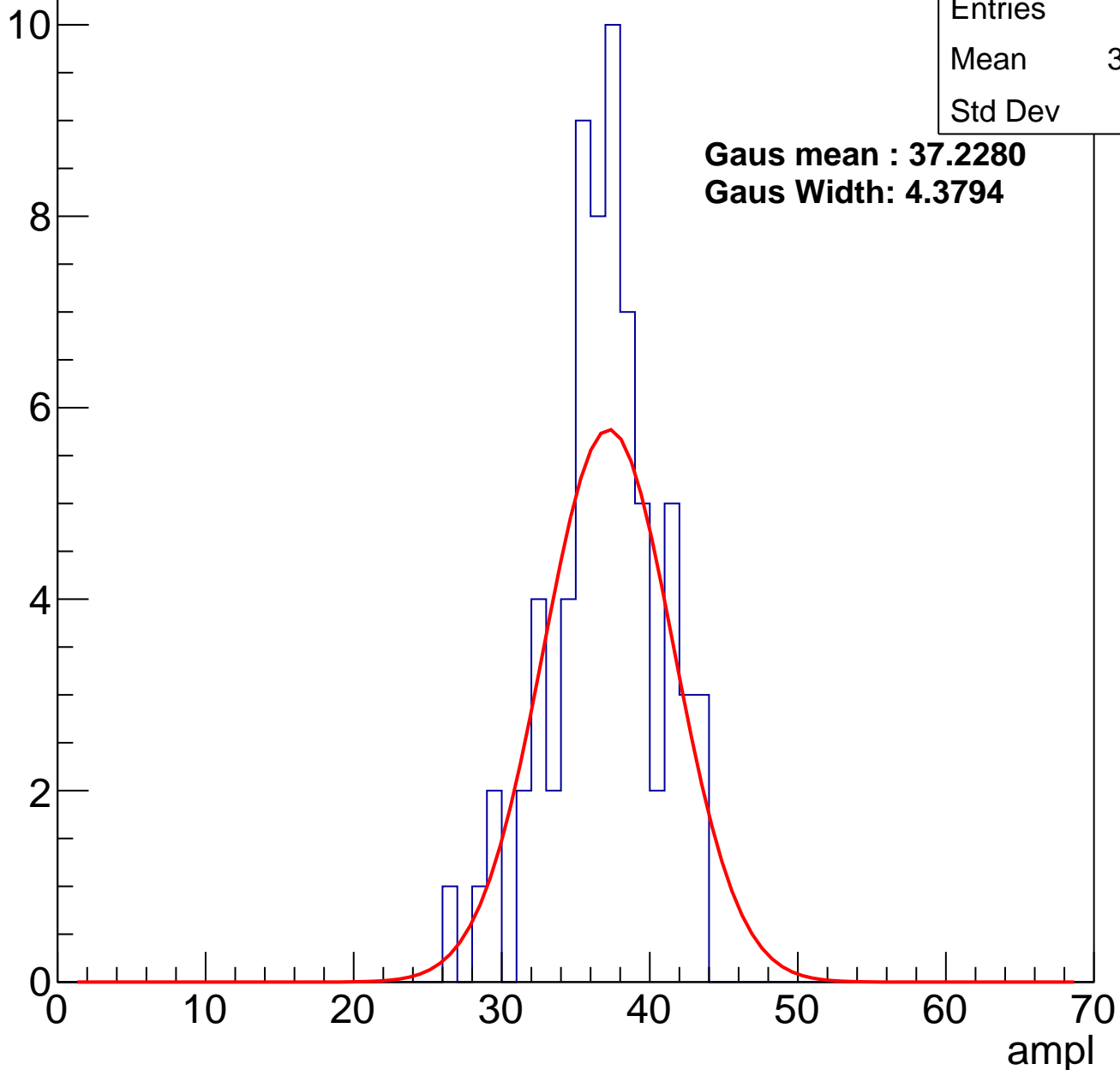
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	36.44
Std Dev	3.64

**Gaus mean : 37.2280**

**Gaus Width: 4.3794**

Entry



# B0L000S, U10-ch113, adc2

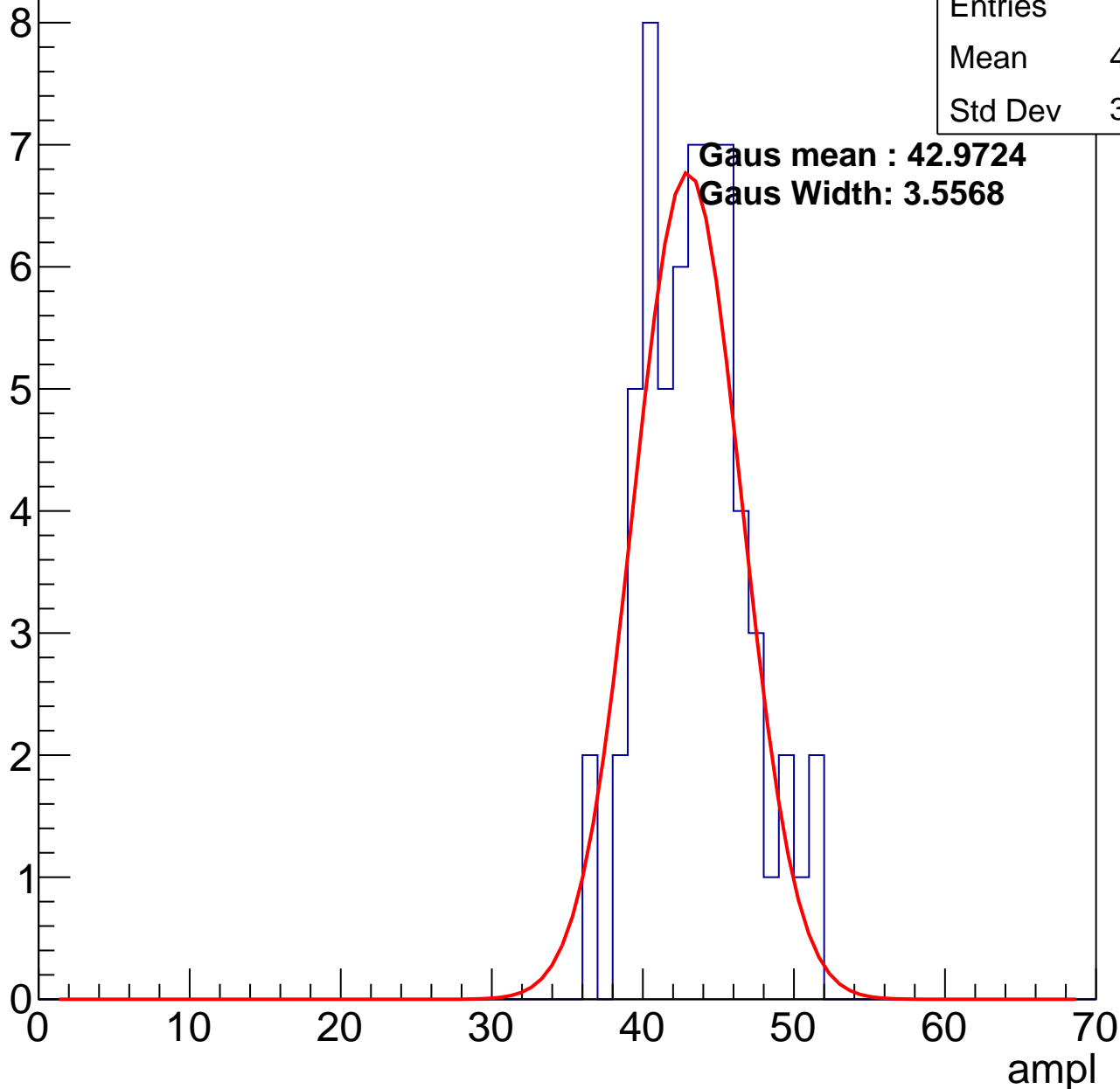
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.02
Std Dev	3.405

**Gaus mean : 42.9724**

**Gaus Width: 3.5568**



# B0L000S, U10-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	50.31
Std Dev	3.69

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

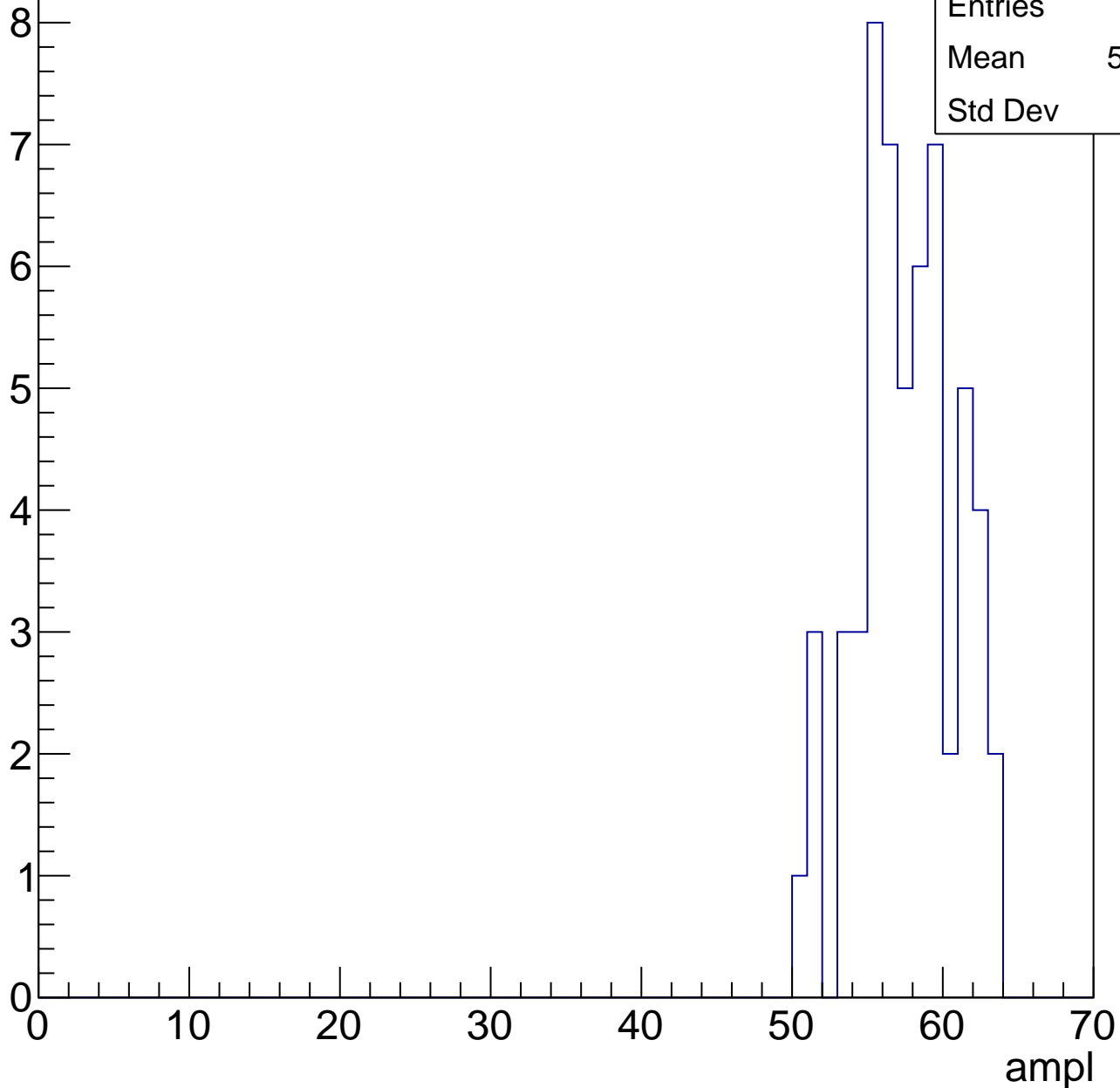
ampl

70

# B0L000S, U10-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

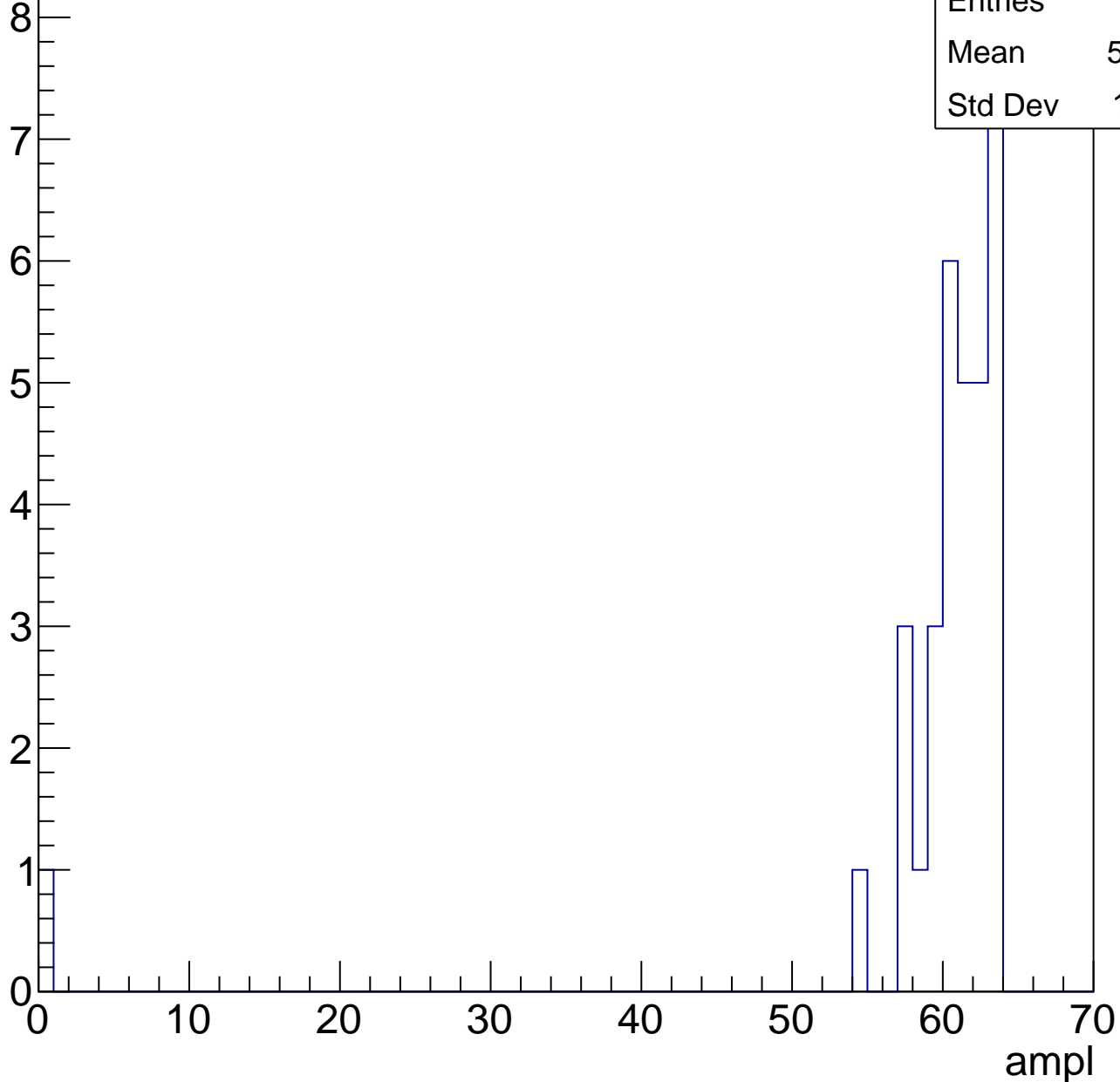


# B0L000S, U10-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

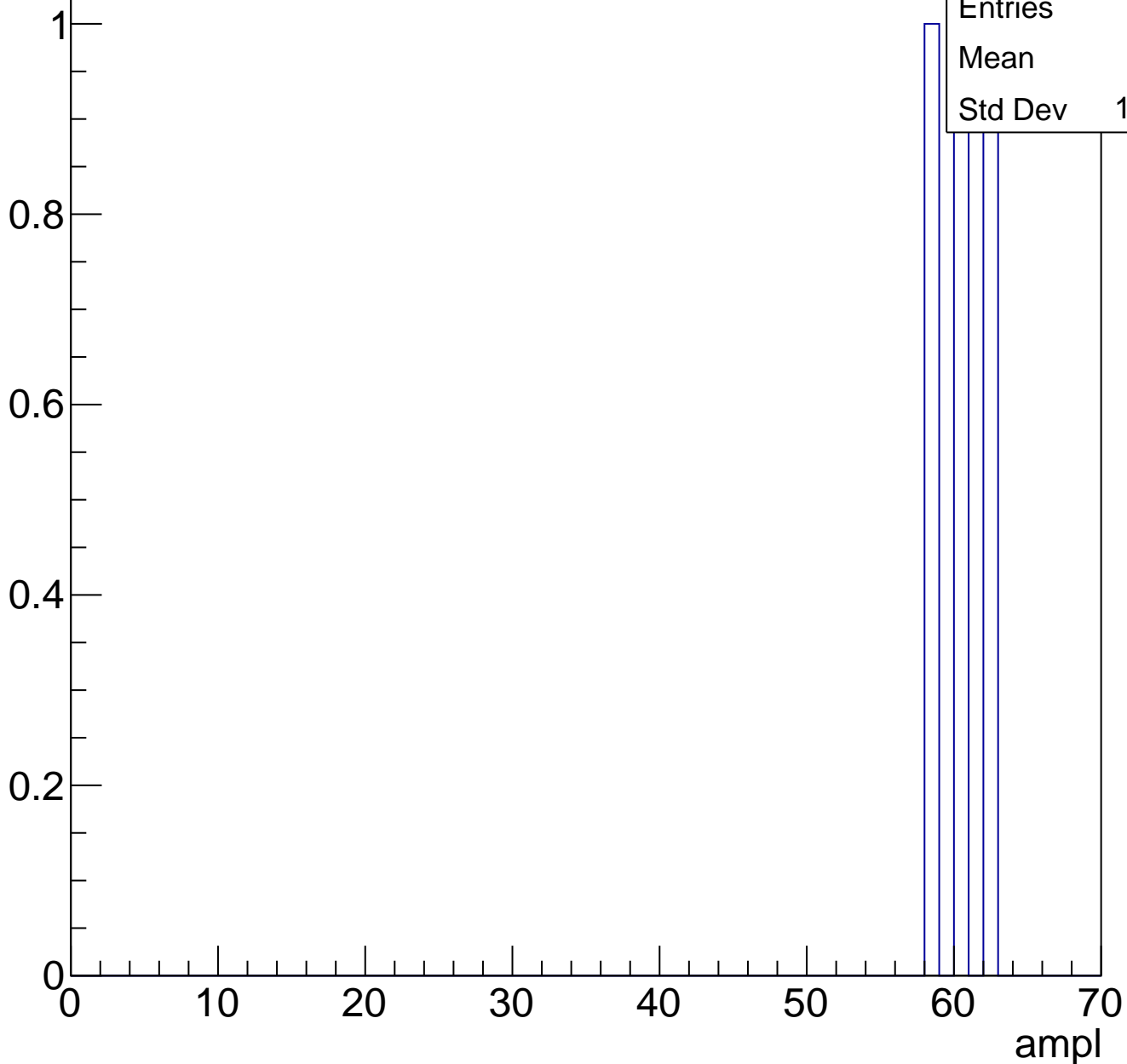
Entries	33
Mean	58.76
Std Dev	10.61



# B0L000S, U10-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch114, adc0

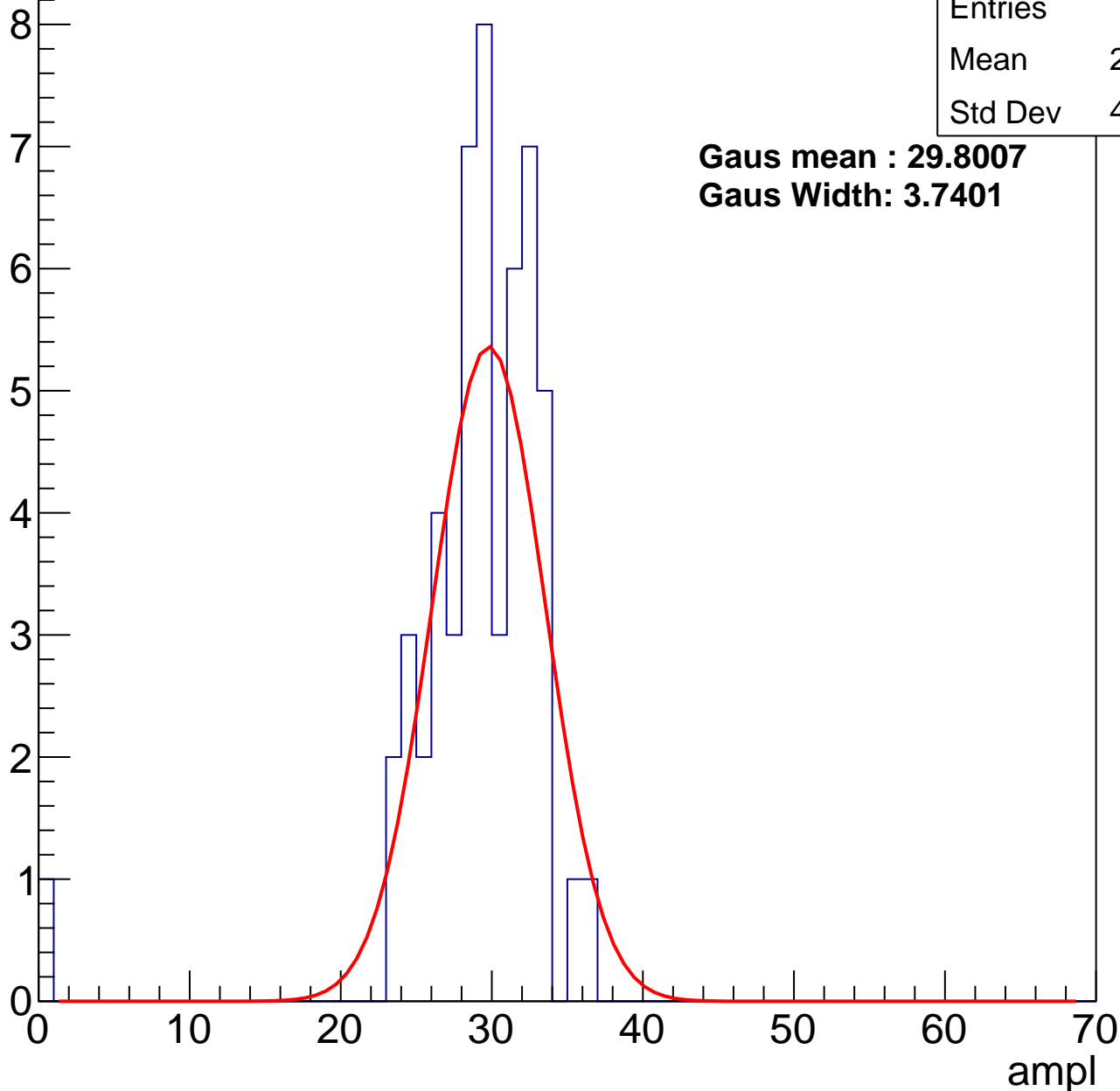
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	28.62
Std Dev	4.995

**Gaus mean : 29.8007**

**Gaus Width: 3.7401**



# B0L000S, U10-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	36.3
Std Dev	3.814

**Gaus mean : 36.7284**

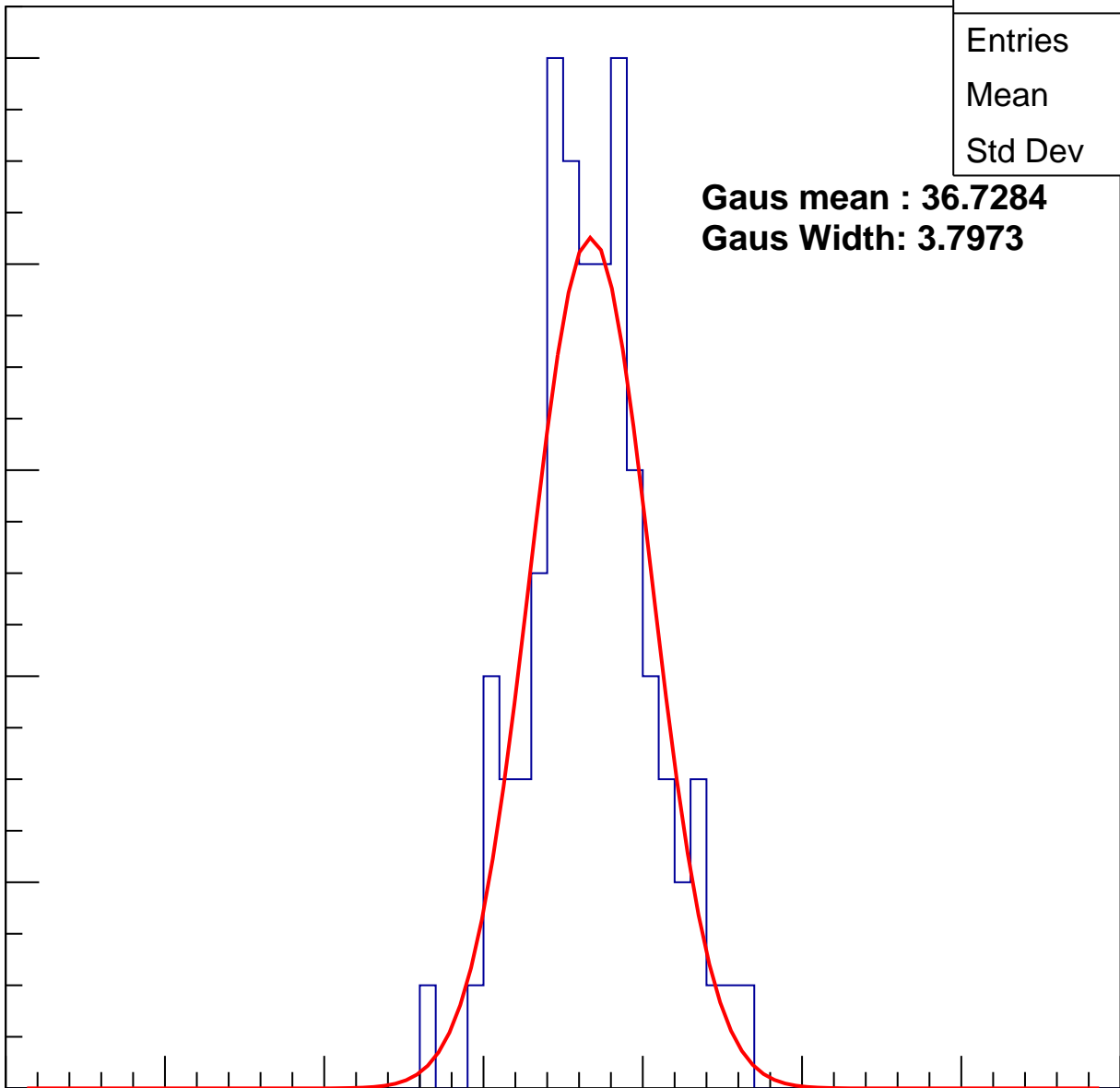
**Gaus Width: 3.7973**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch114, adc2

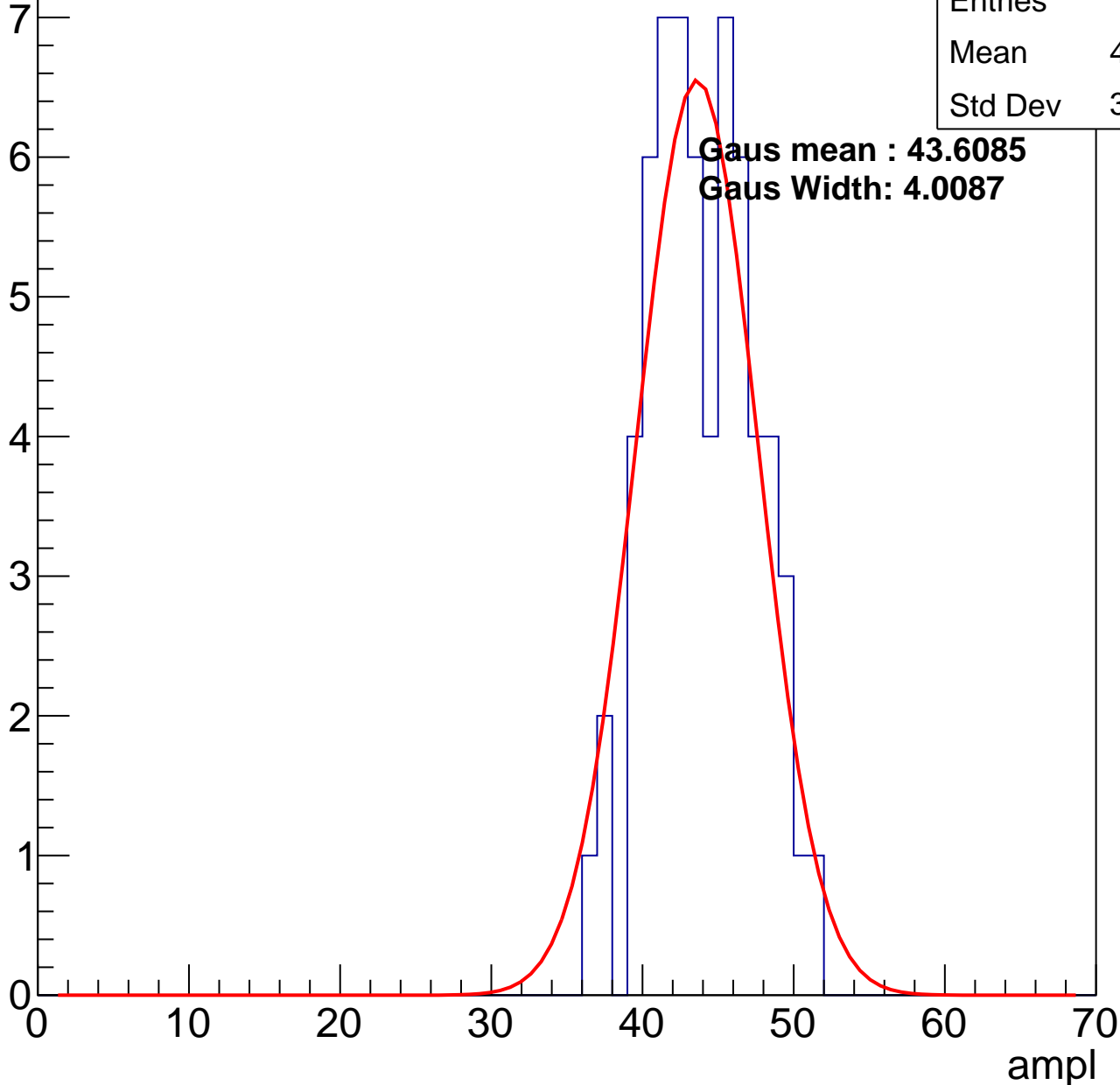
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.49
Std Dev	3.403

**Gaus mean : 43.6085**

**Gaus Width: 4.0087**

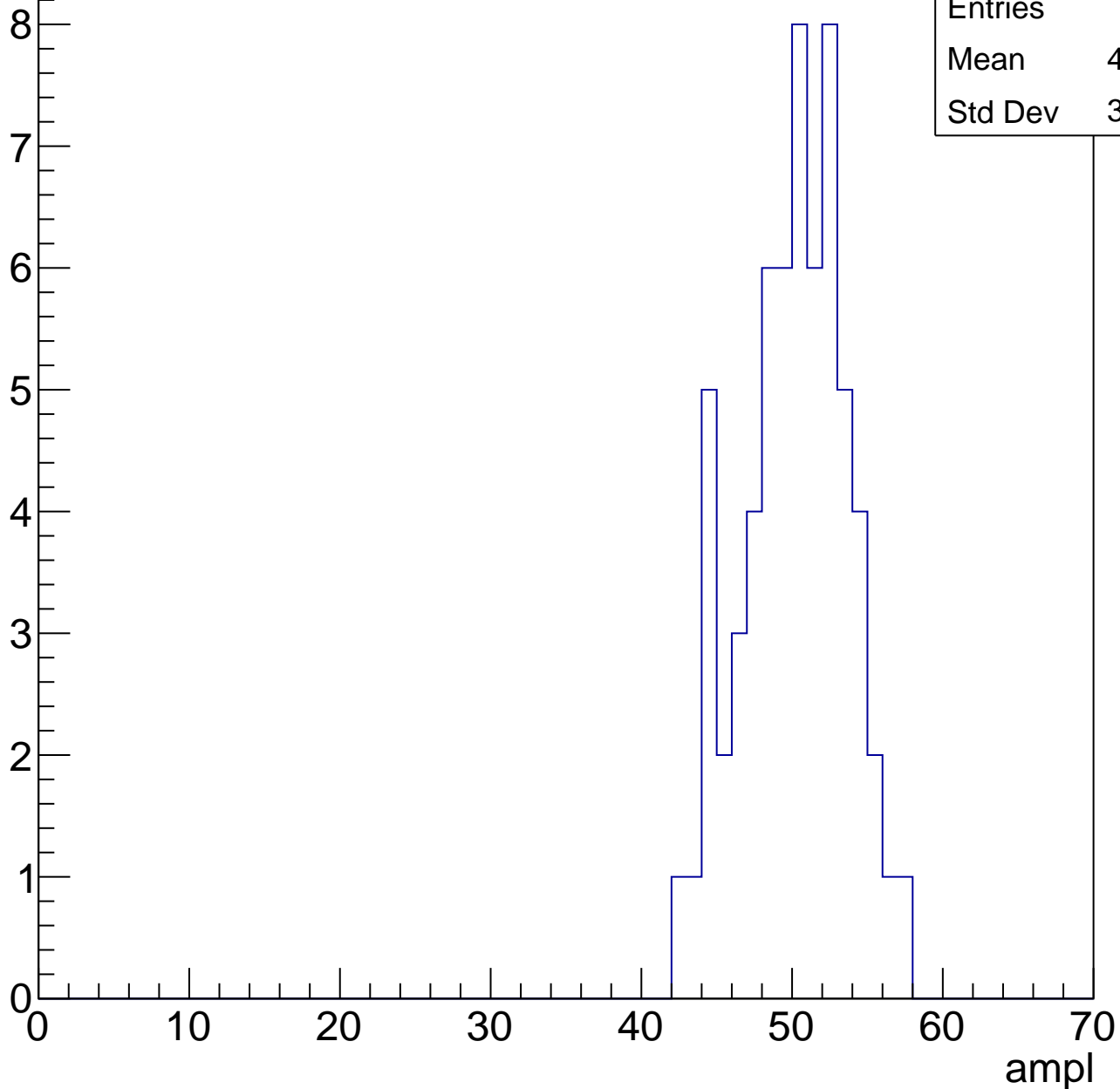


# B0L000S, U10-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.67
Std Dev	3.409

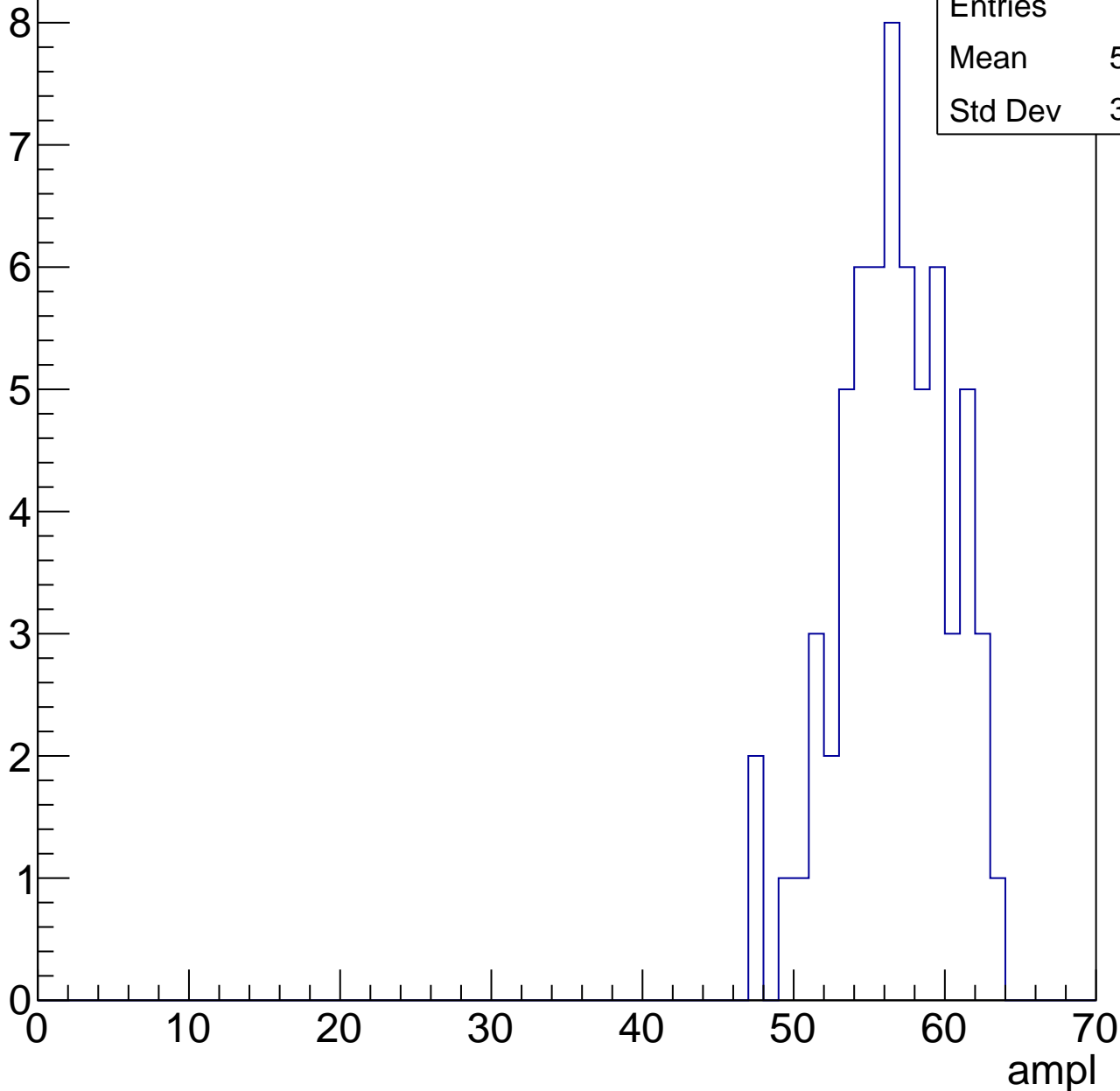


# B0L000S, U10-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	56.14
Std Dev	3.647

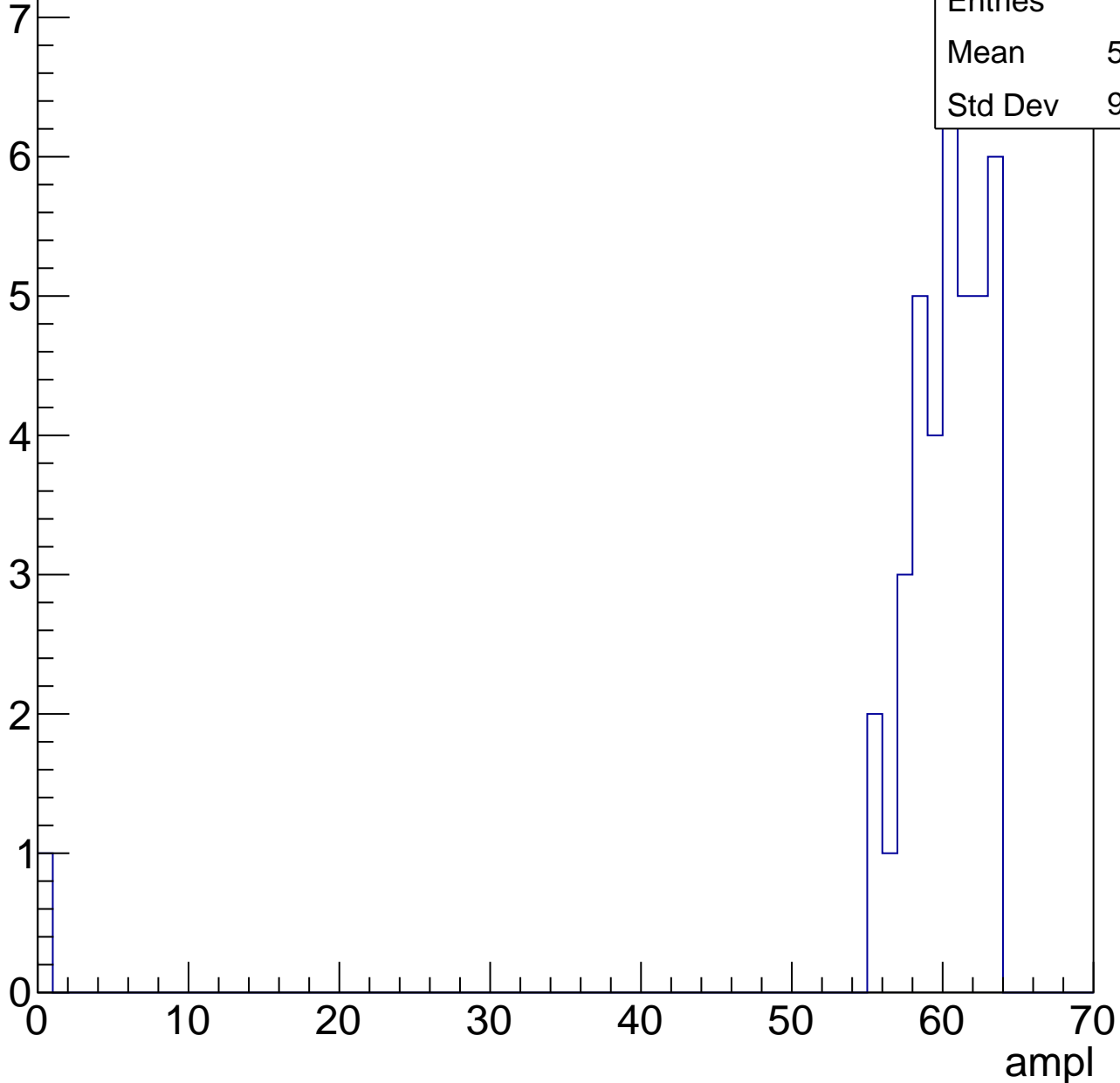


# B0L000S, U10-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

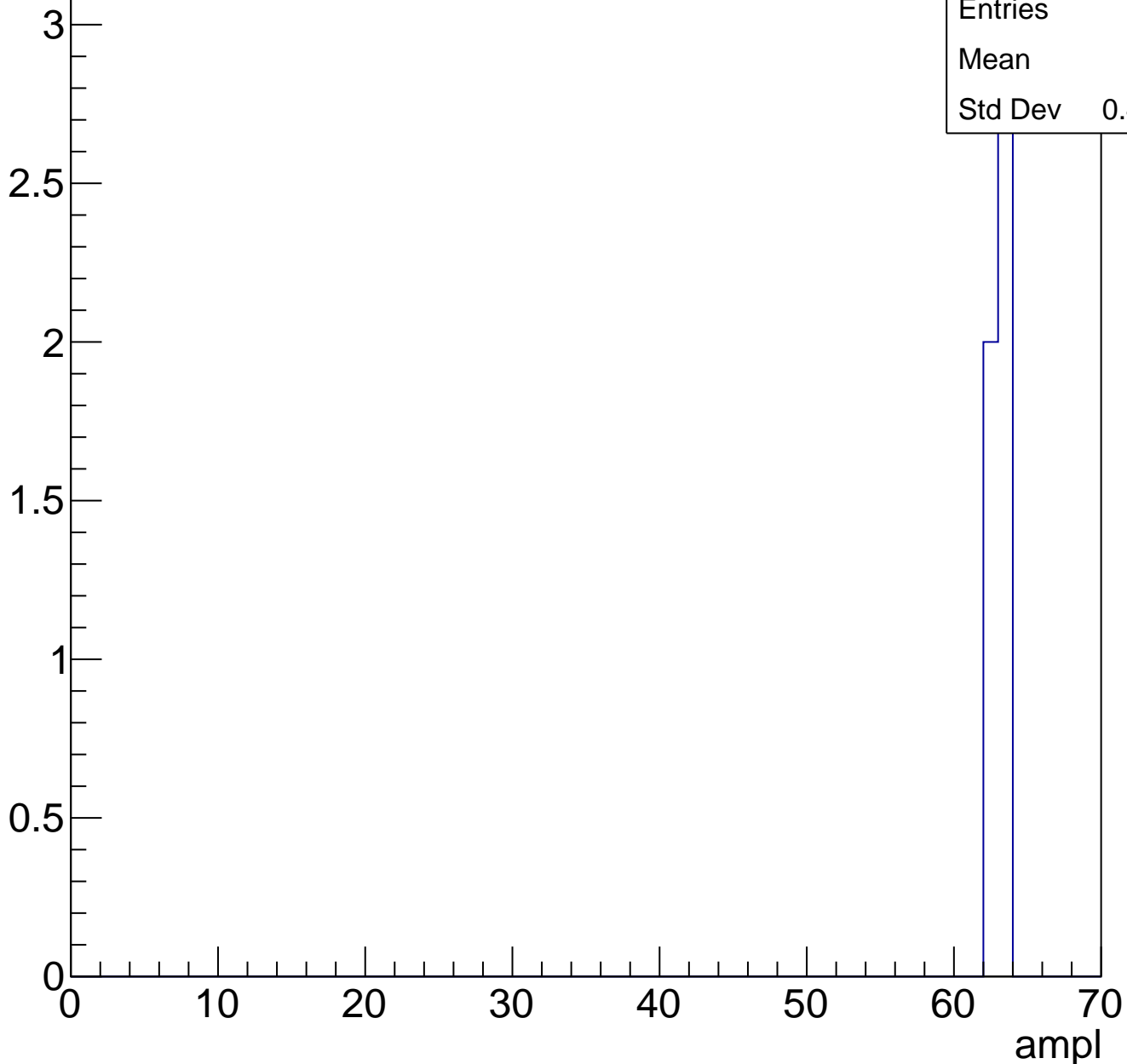
Entries	39
Mean	58.36
Std Dev	9.728



# B0L000S, U10-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	62.6
Std Dev	0.4899



# B0L000S, U10-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L000S, U10-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	86
Mean	28.59
Std Dev	5.78

**Gaus mean : 29.2150**

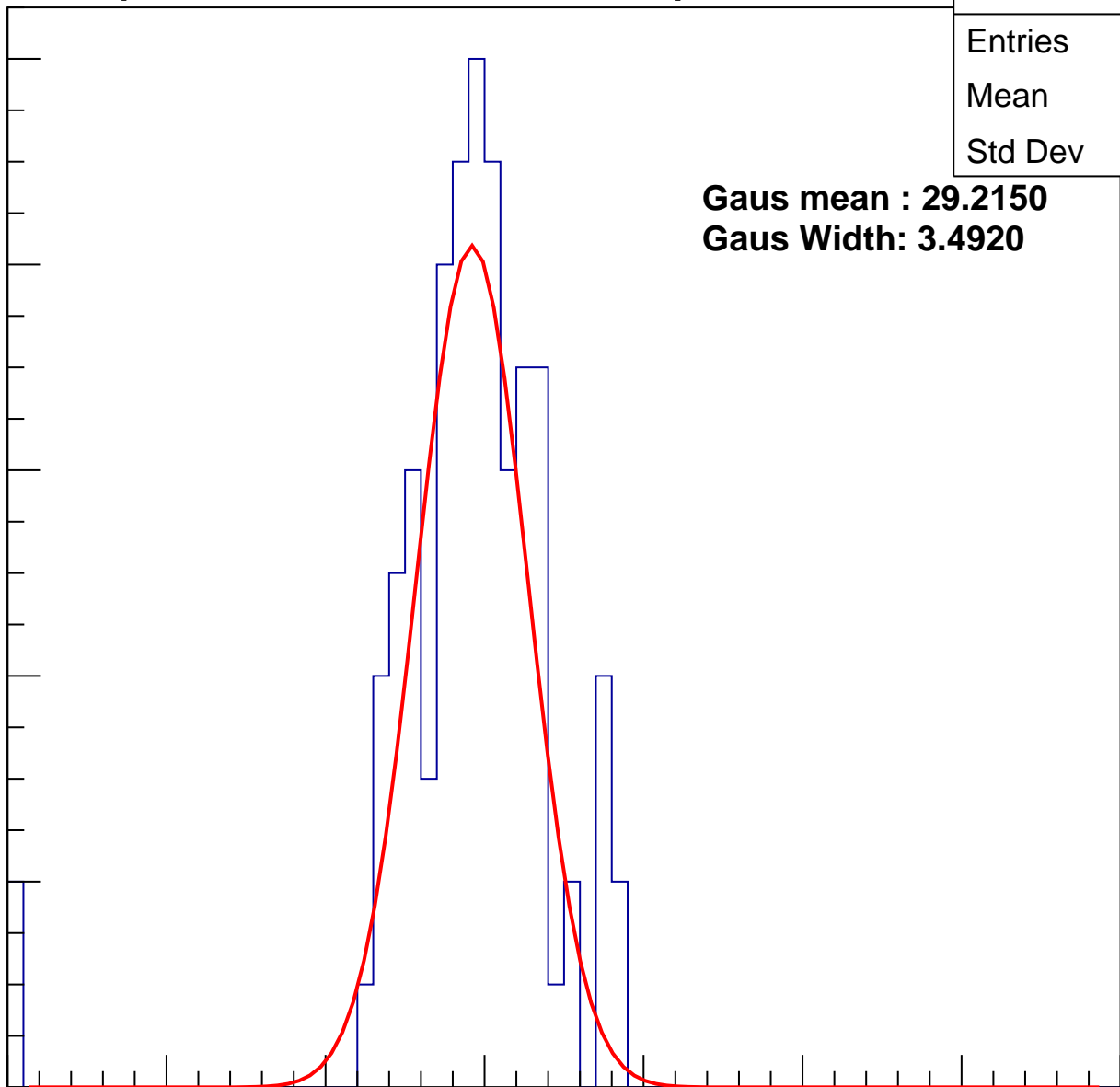
**Gaus Width: 3.4920**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch115, adc1

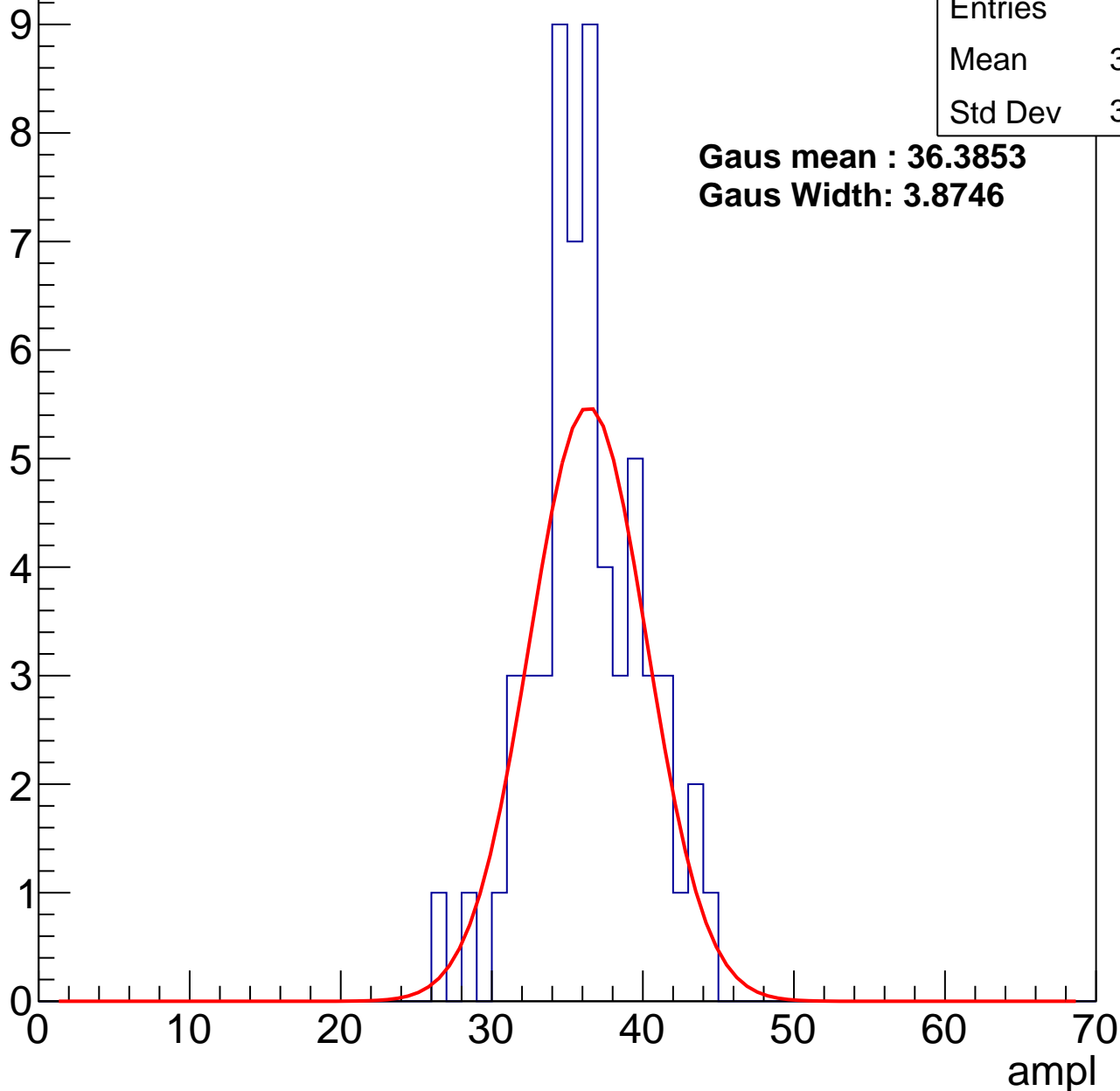
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.92
Std Dev	3.623

**Gaus mean : 36.3853**

**Gaus Width: 3.8746**



# B0L000S, U10-ch115, adc2

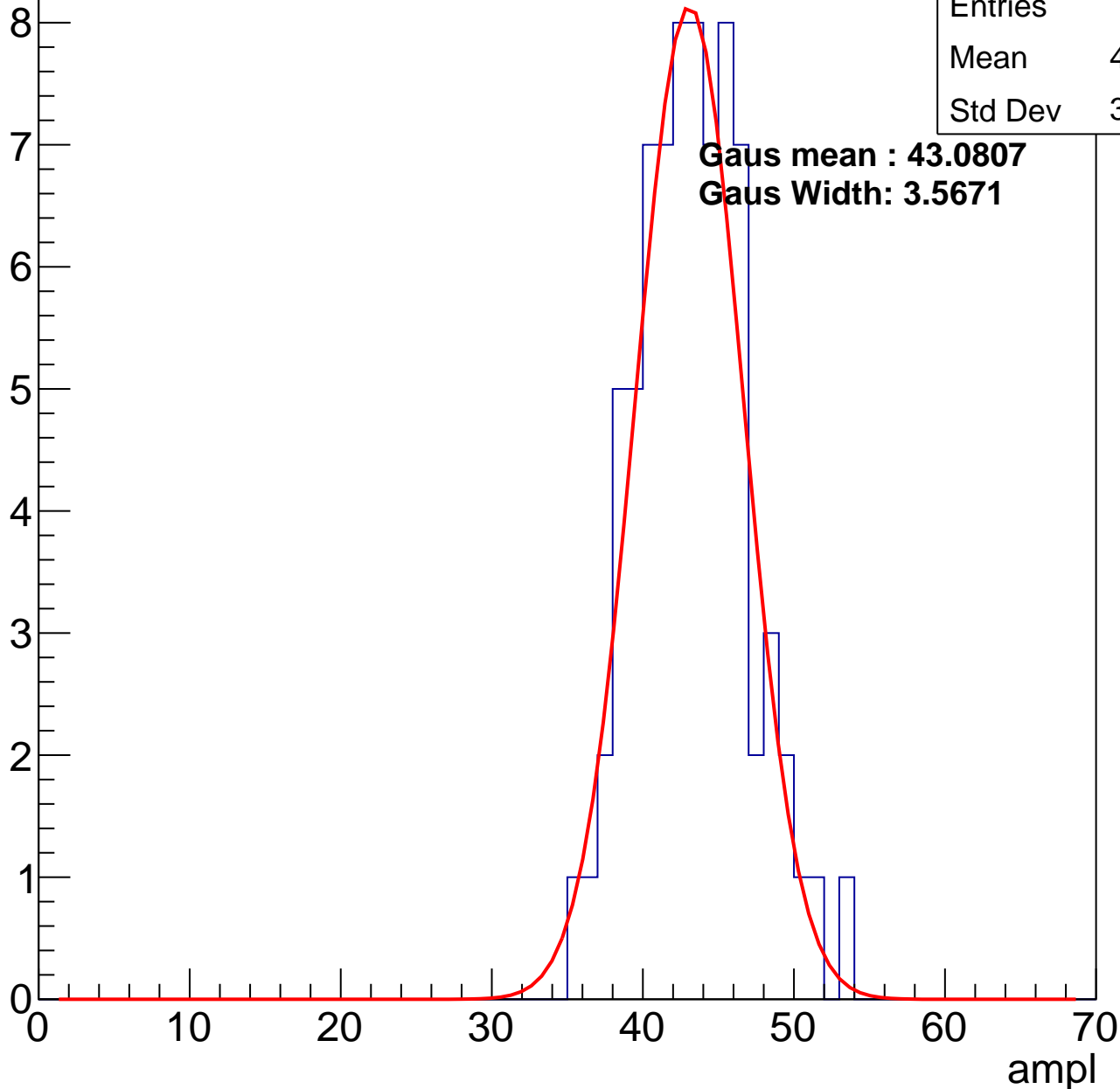
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.86
Std Dev	3.594

**Gaus mean : 43.0807**

**Gaus Width: 3.5671**

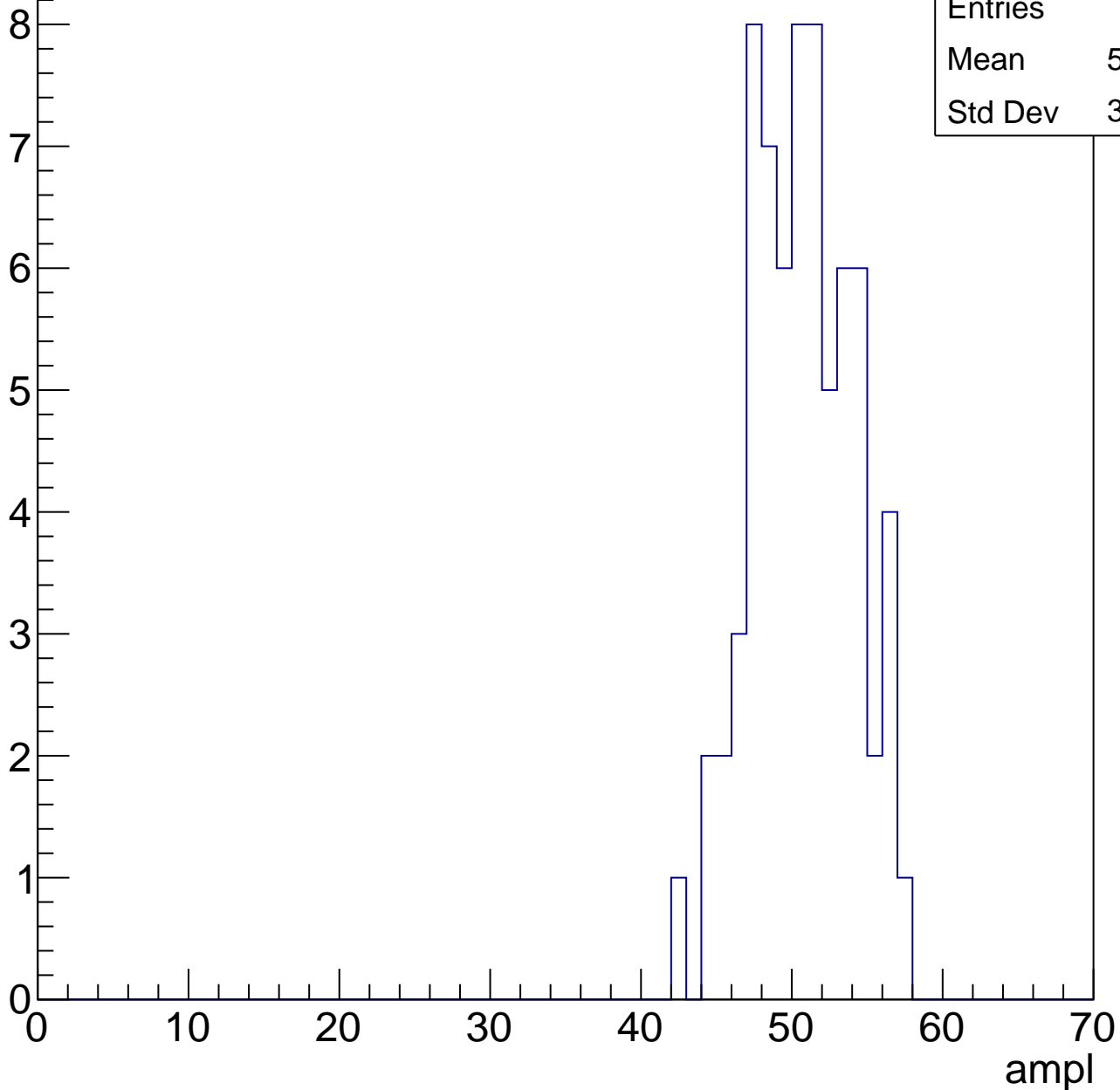


# B0L000S, U10-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	50.22
Std Dev	3.314

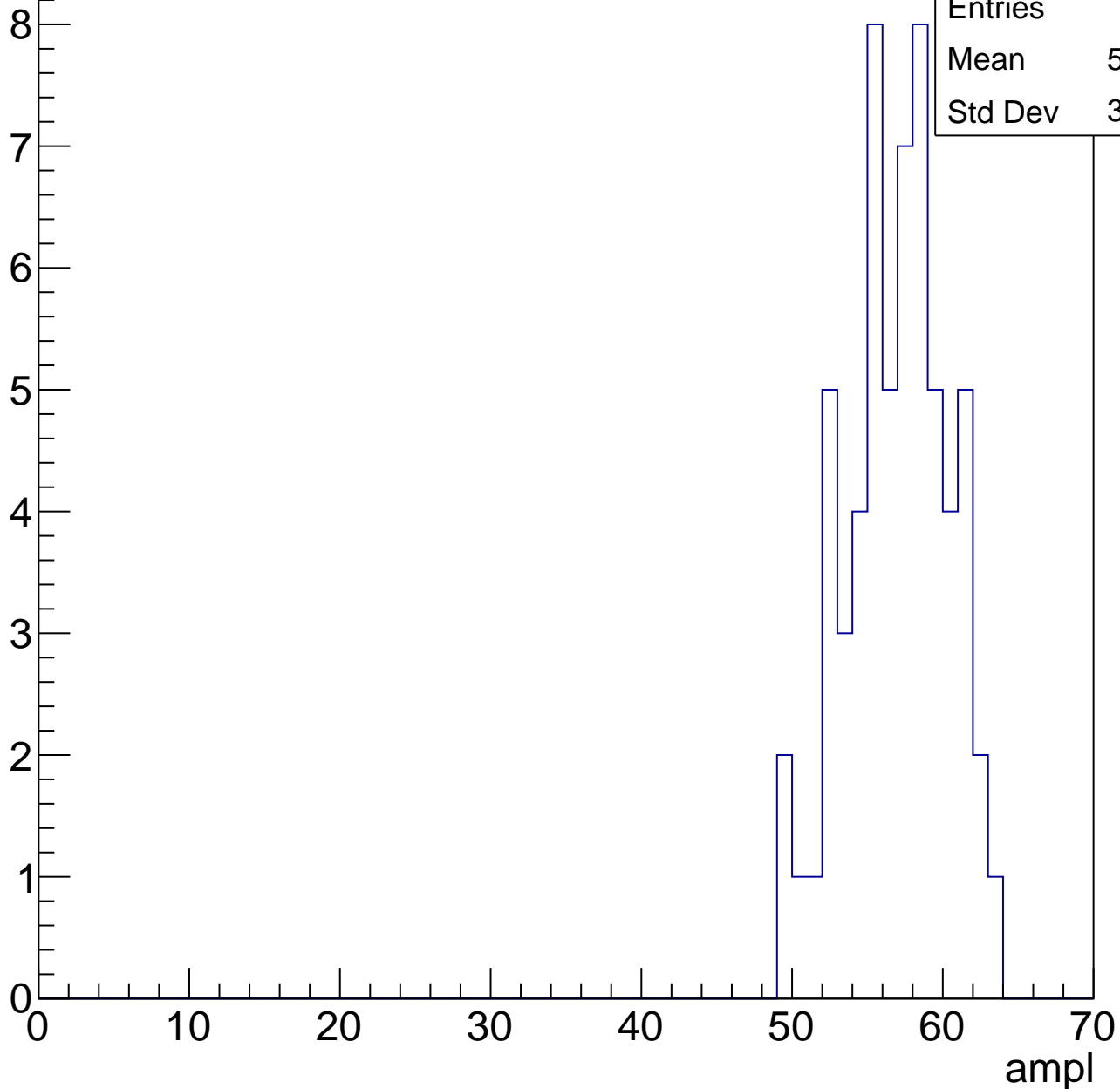


# B0L000S, U10-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.46
Std Dev	3.317

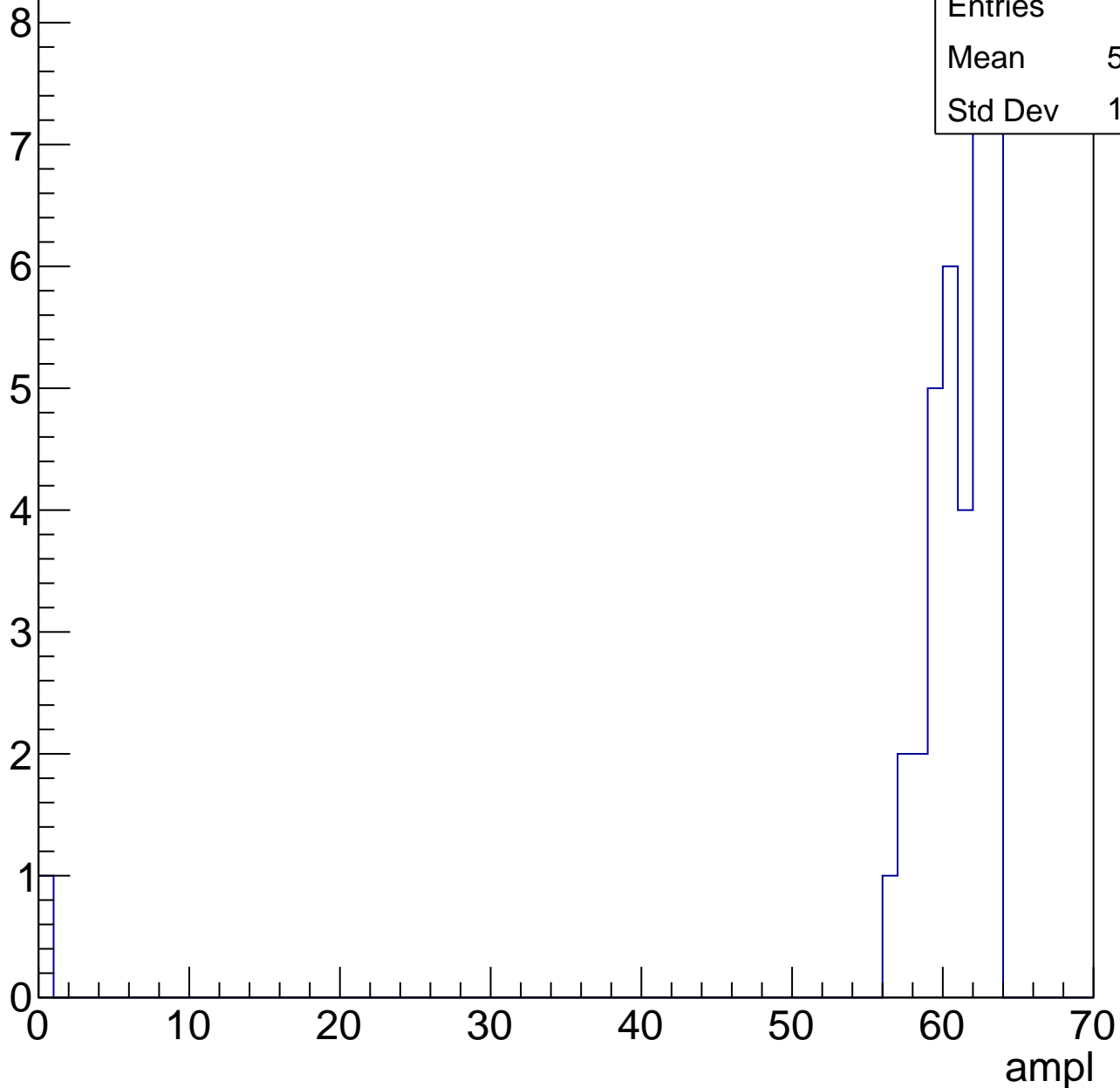


# B0L000S, U10-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

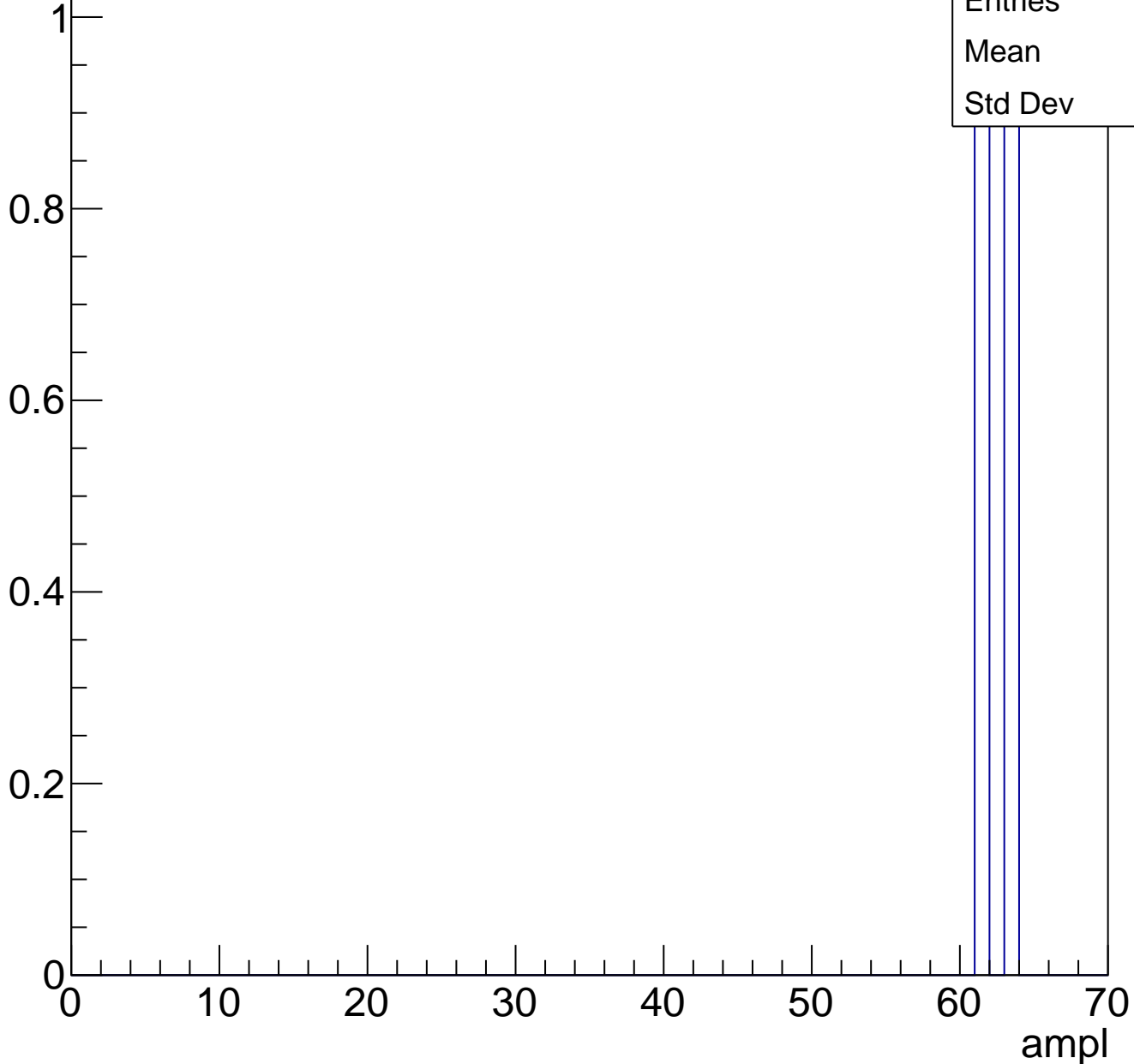
Entries	37
Mean	59.05
Std Dev	10.03



# B0L000S, U10-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch116, adc0

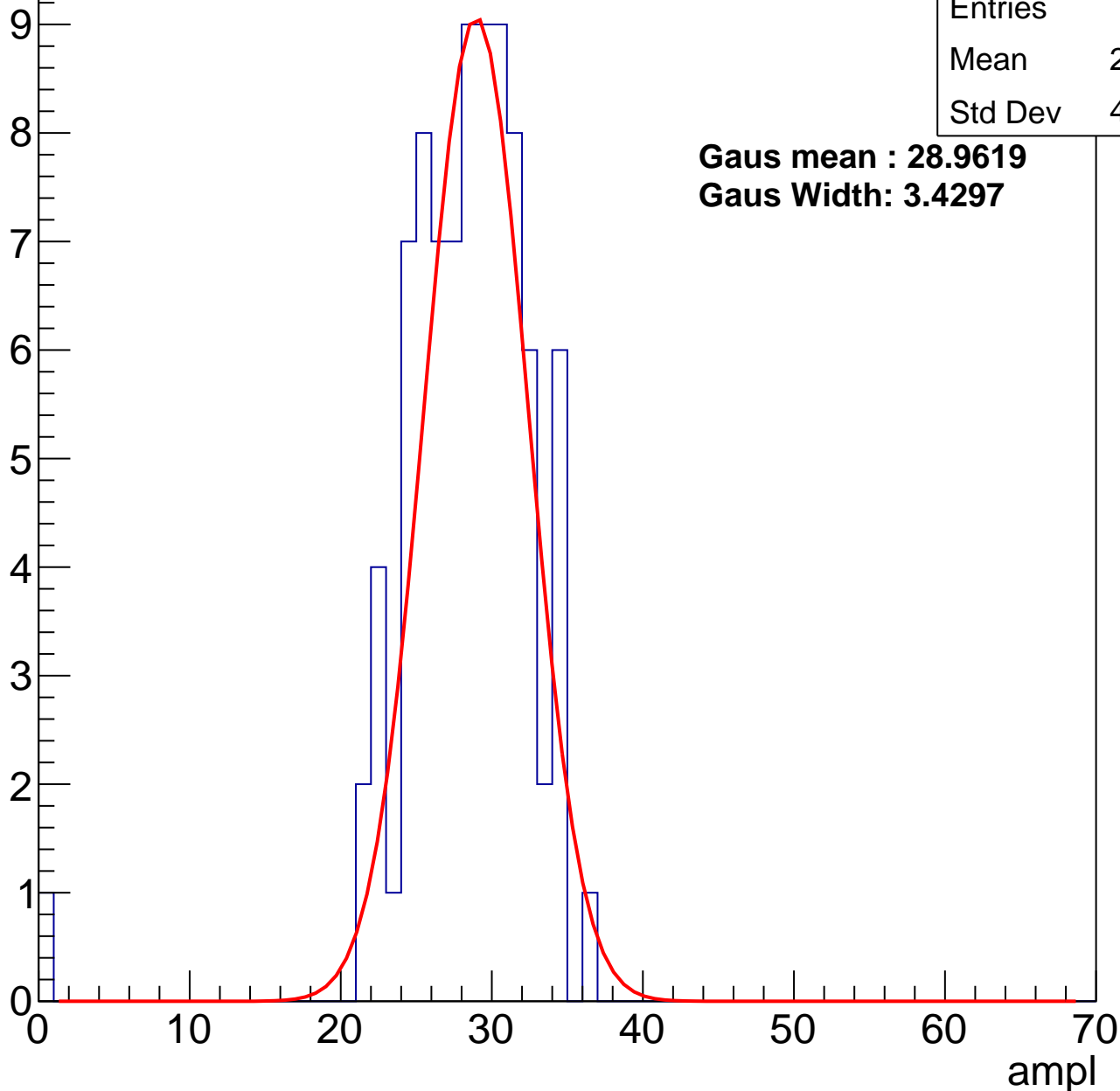
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	27.83
Std Dev	4.562

**Gaus mean : 28.9619**

**Gaus Width: 3.4297**



# B0L000S, U10-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.15
Std Dev	2.984

**Gaus mean : 36.8007**

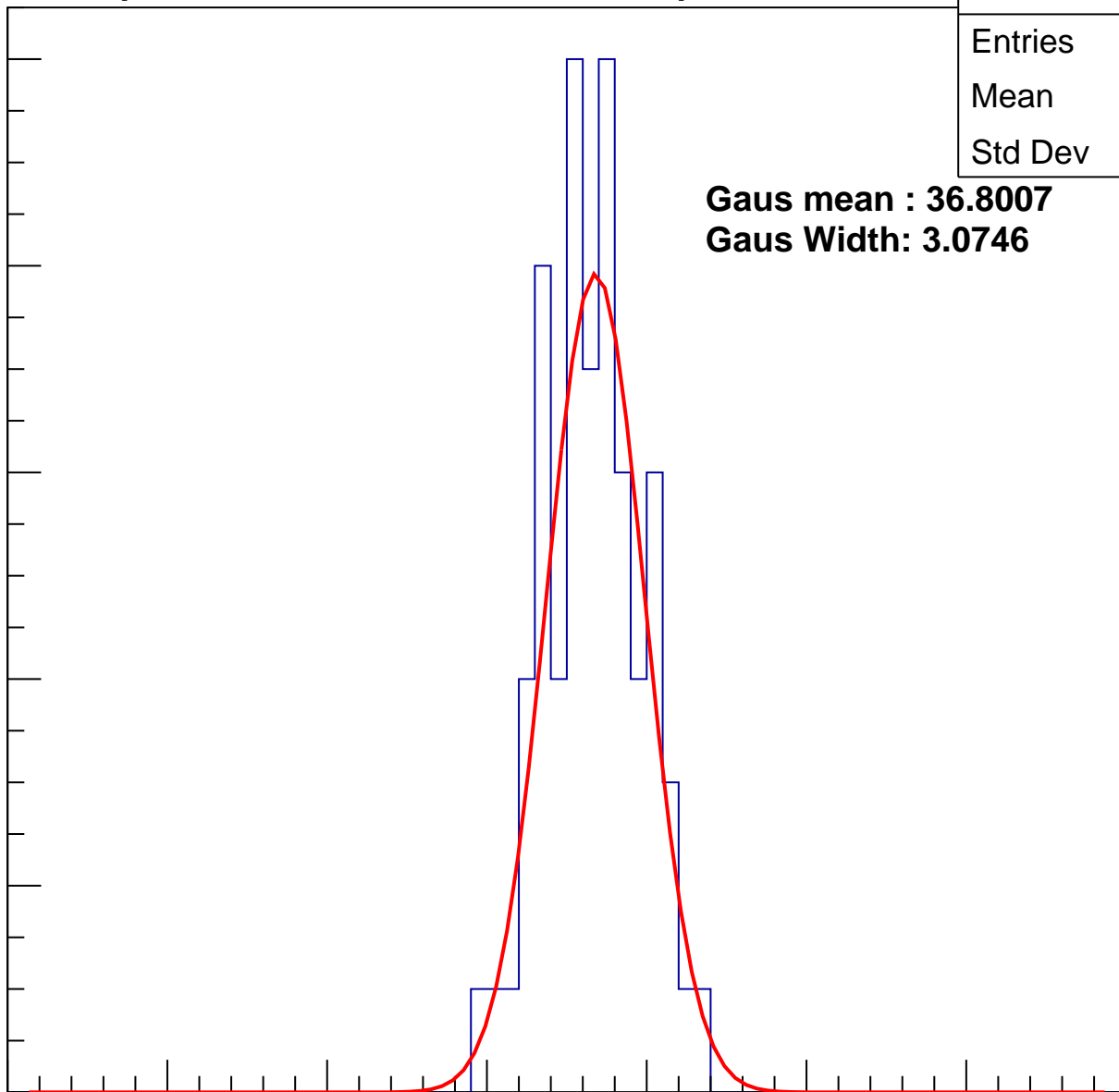
**Gaus Width: 3.0746**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U10-ch116, adc2

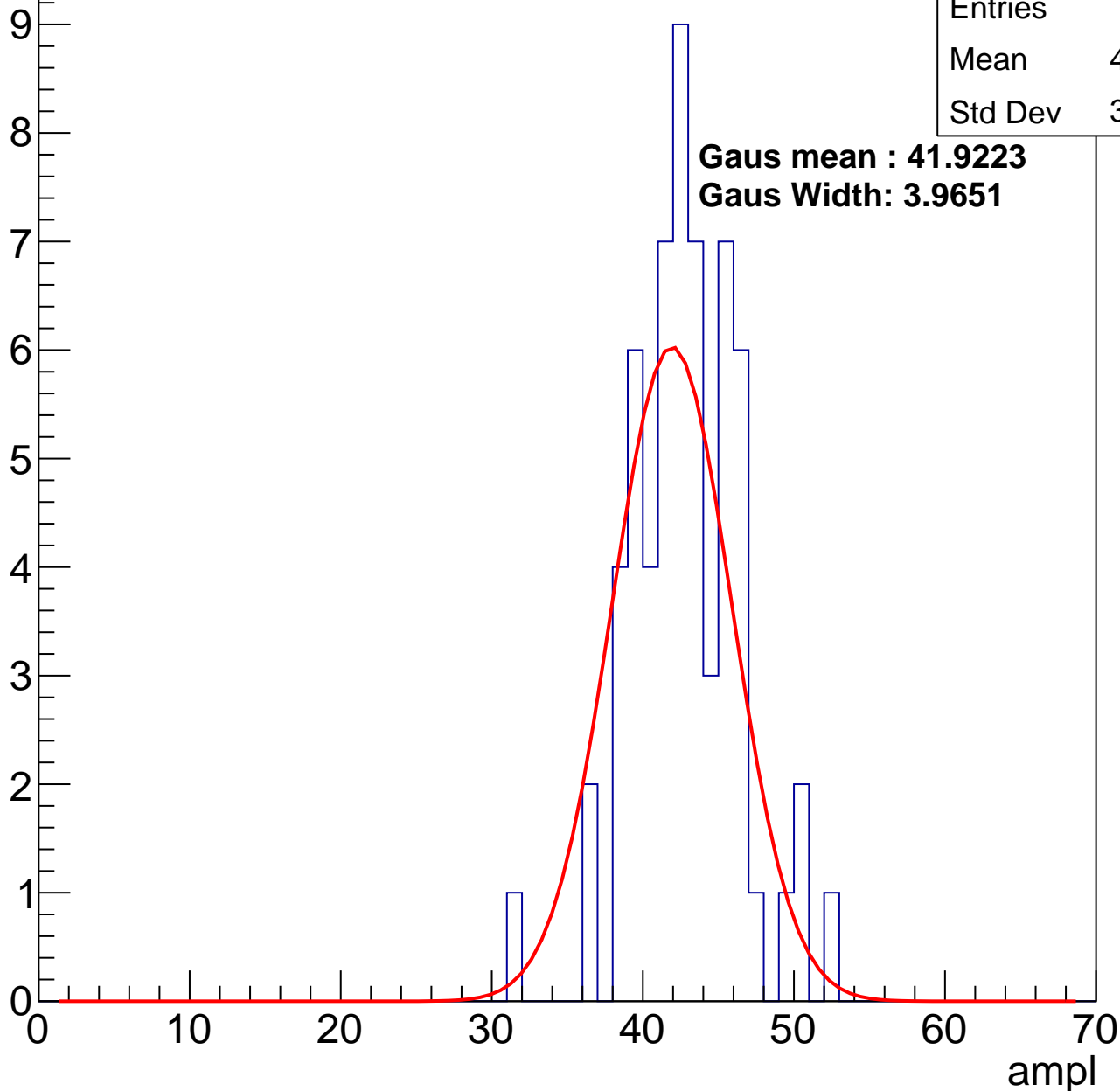
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.39
Std Dev	3.632

**Gaus mean : 41.9223**

**Gaus Width: 3.9651**

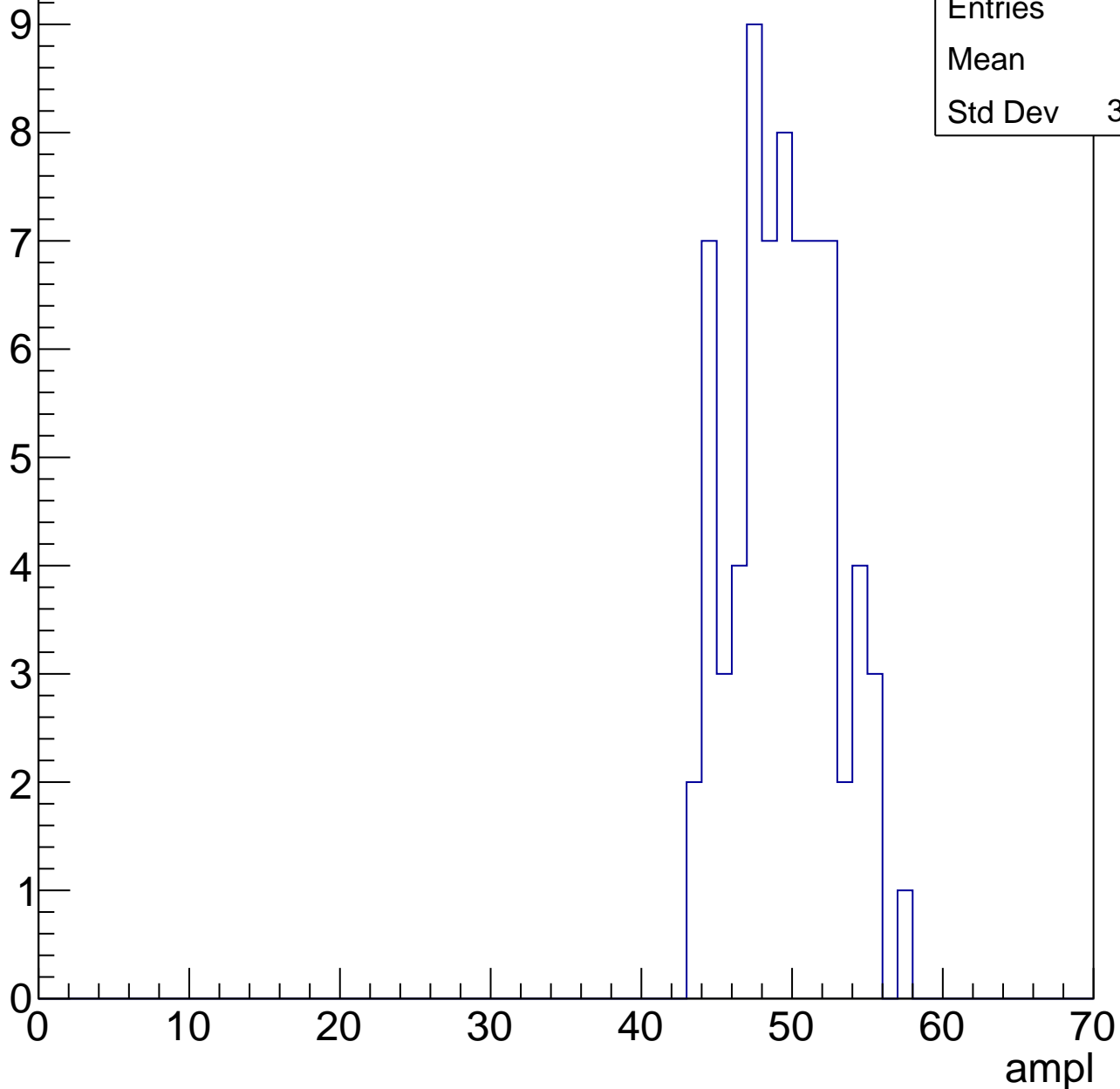


# B0L000S, U10-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	49
Std Dev	3.306

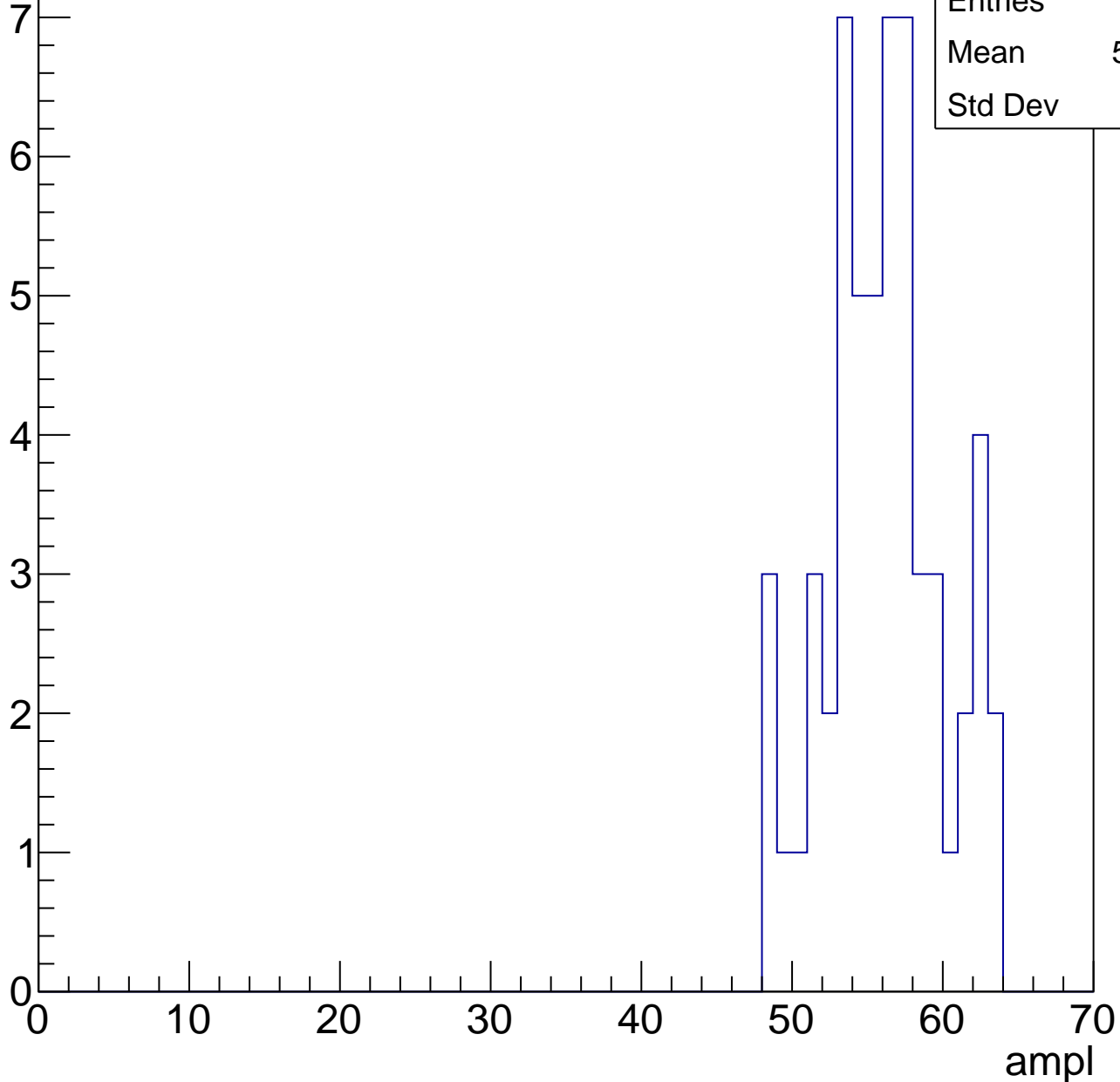


# B0L000S, U10-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.61
Std Dev	3.82



# B0L000S, U10-ch116, adc5

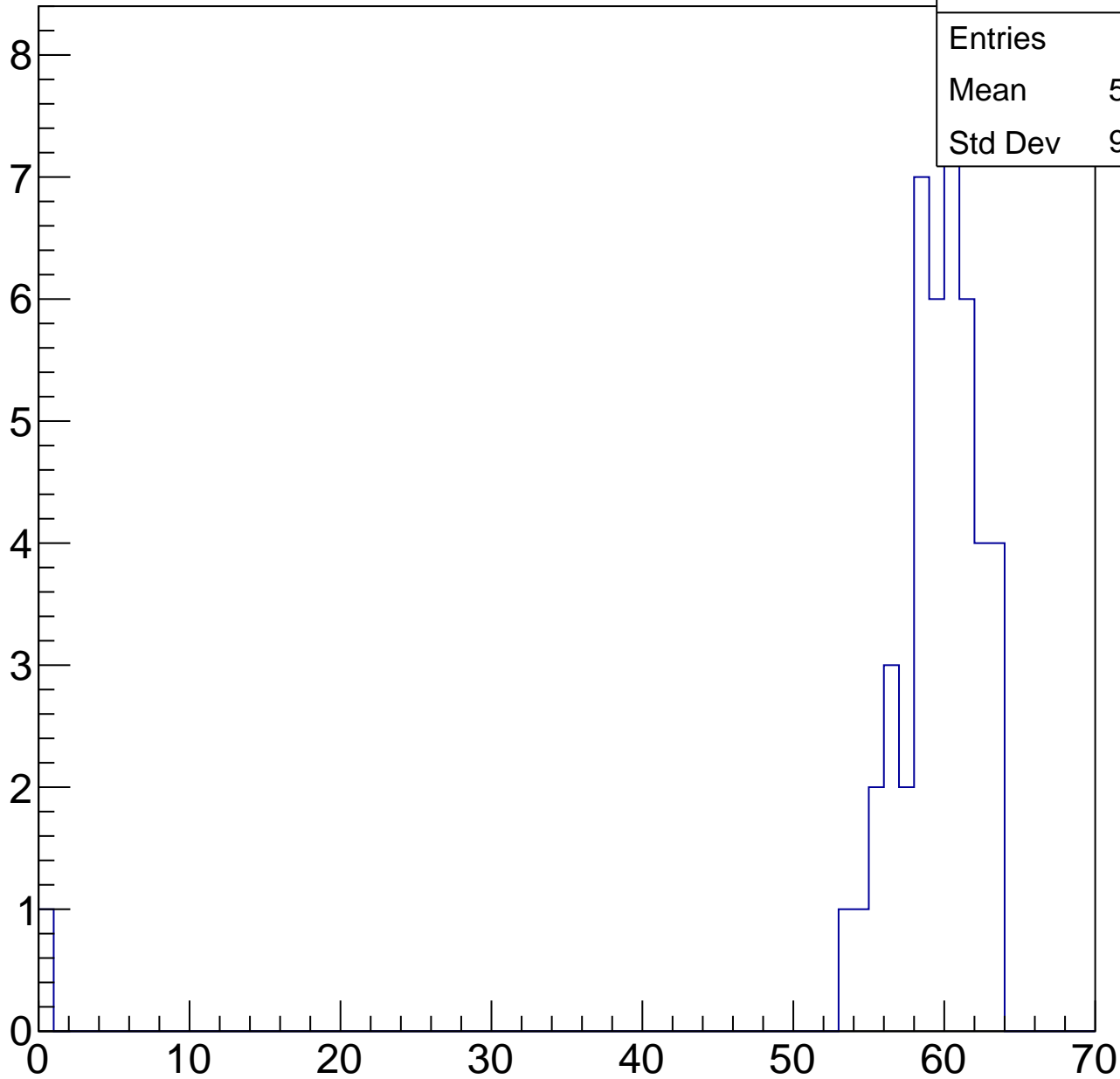
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	57.89
Std Dev	9.058

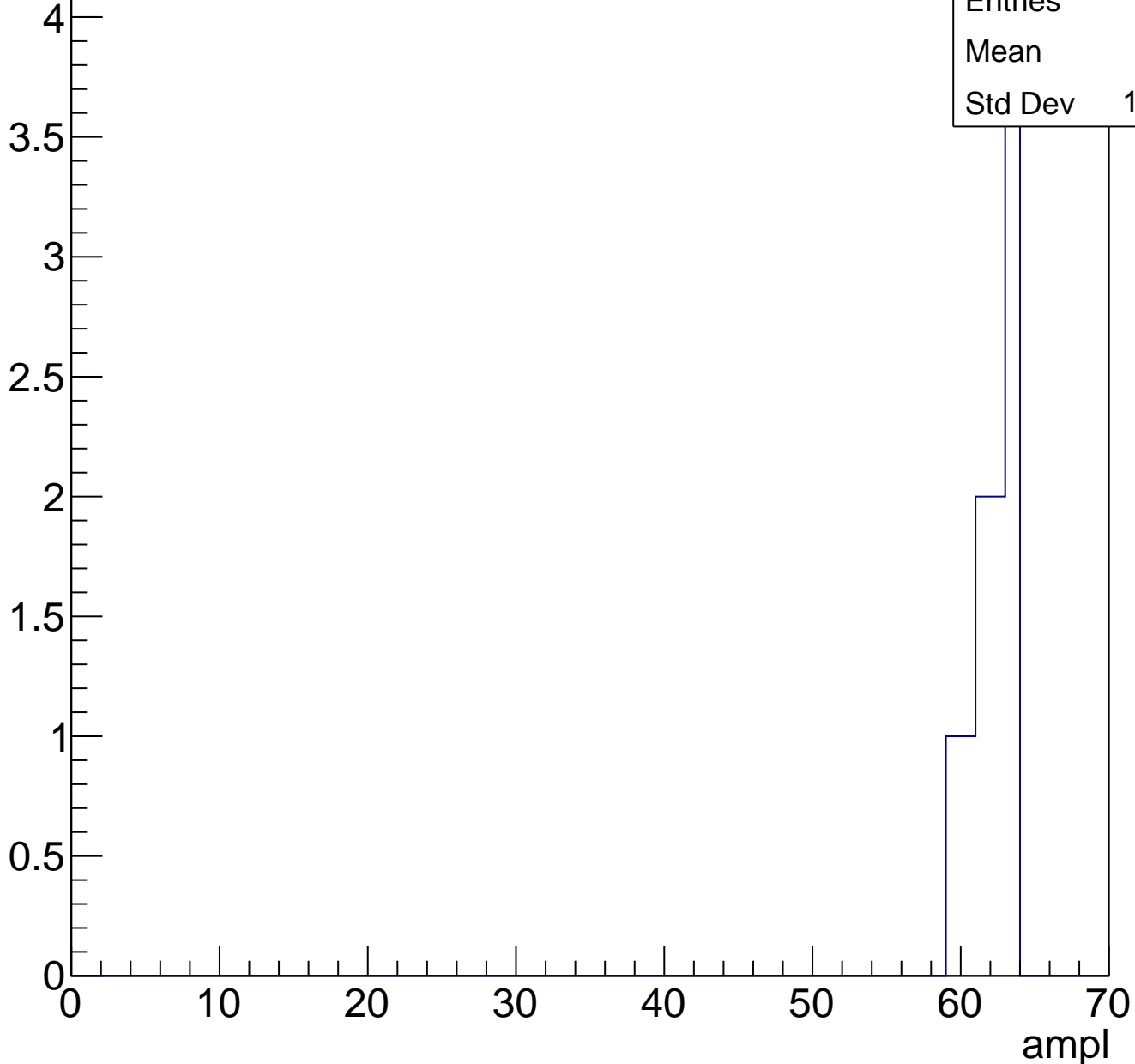
ampl



# B0L000S, U10-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



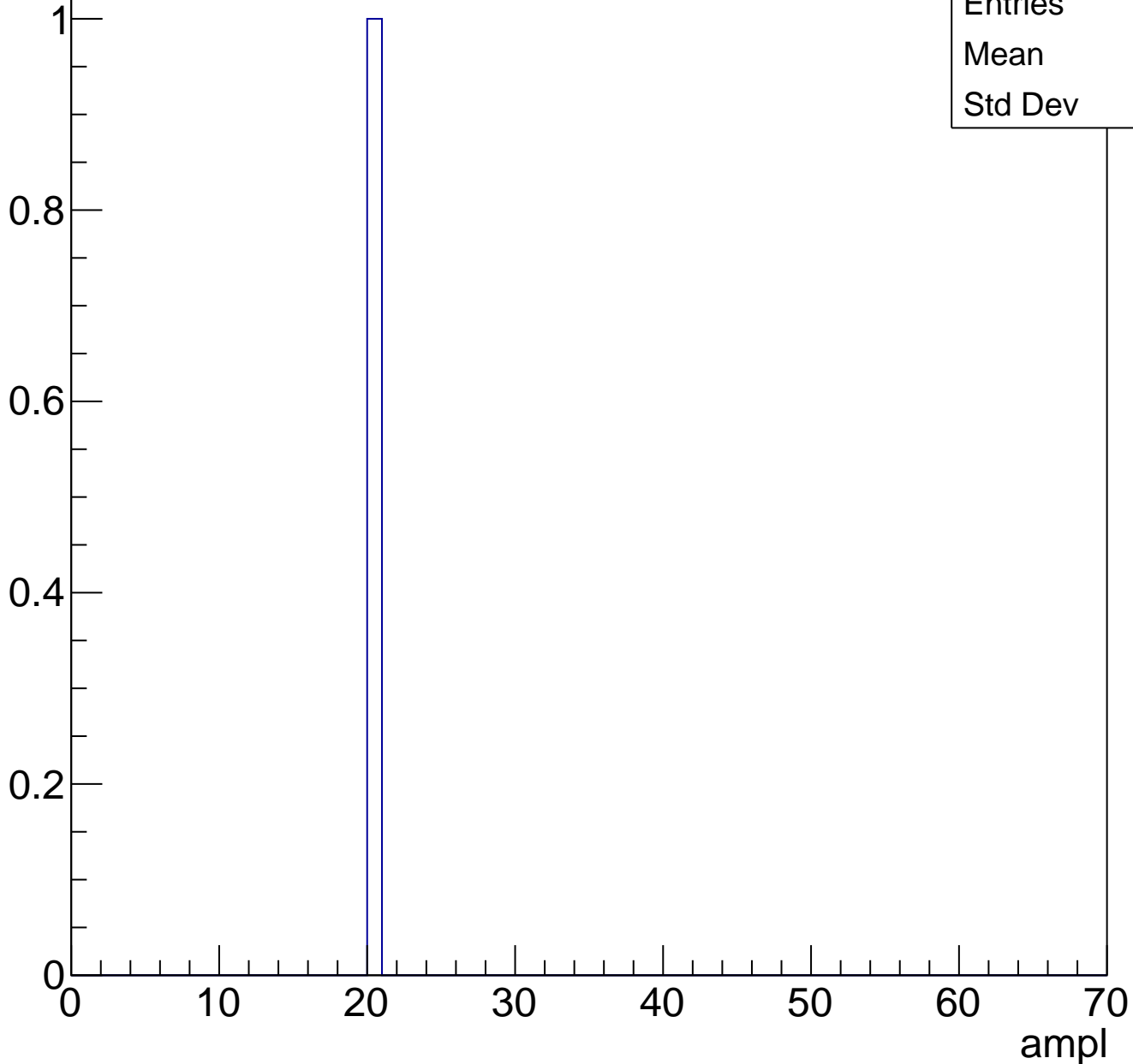
Entries	10
Mean	61.7
Std Dev	1.345



# B0L000S, U10-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch117, adc0

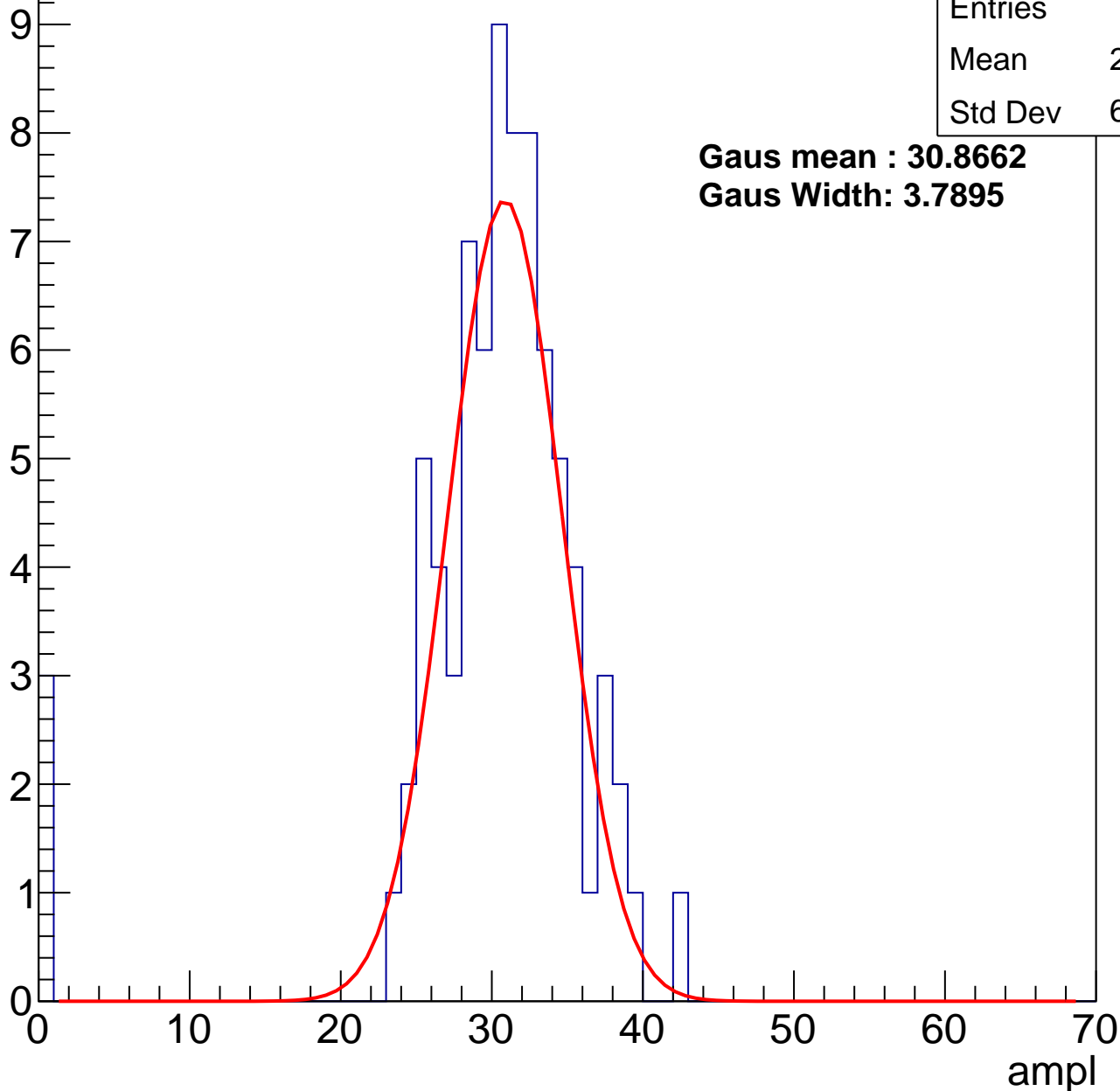
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	29.58
Std Dev	6.997

**Gaus mean : 30.8662**

**Gaus Width: 3.7895**



# B0L000S, U10-ch117, adc1

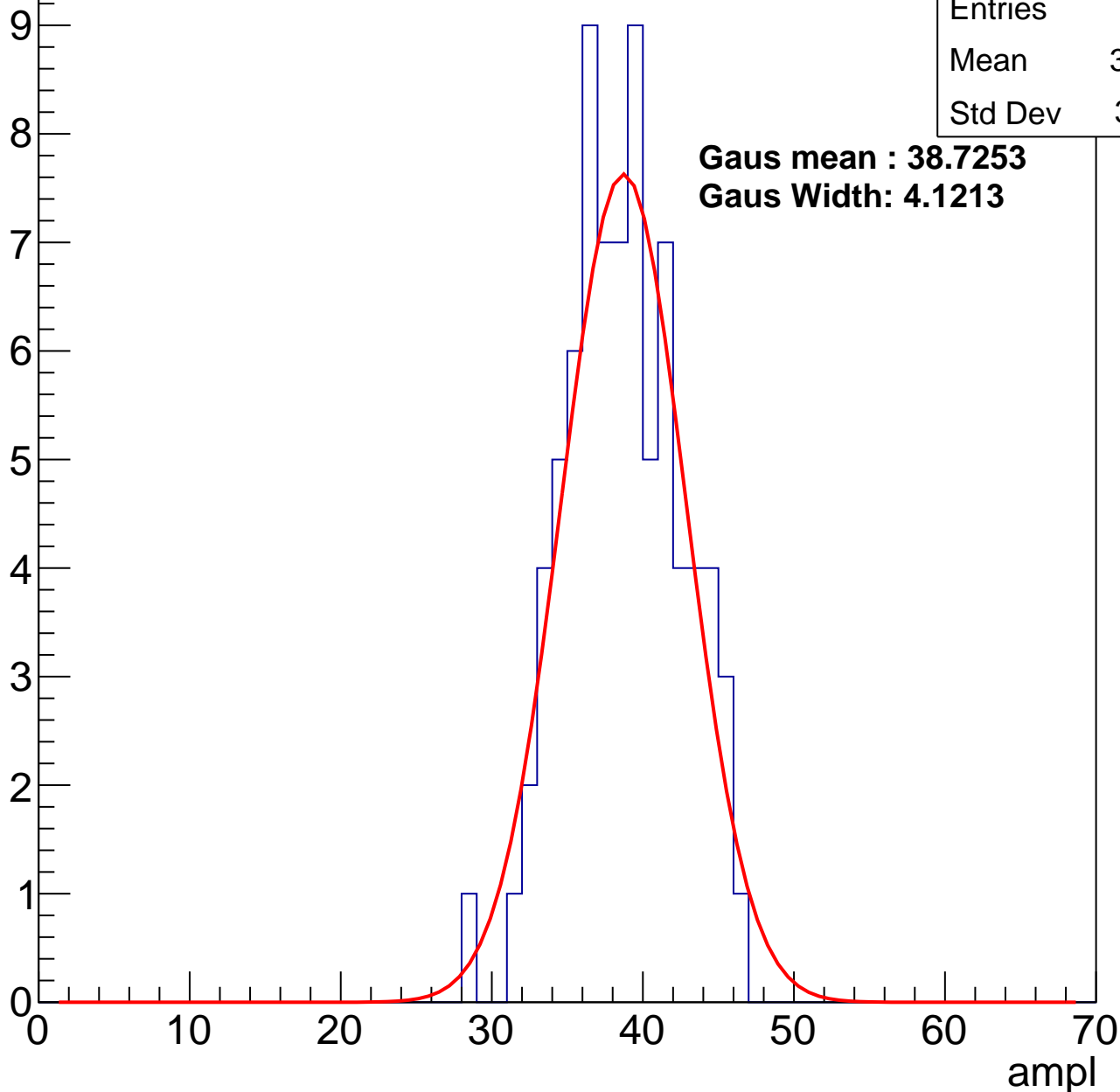
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	38.22
Std Dev	3.741

**Gaus mean : 38.7253**

**Gaus Width: 4.1213**



# B0L000S, U10-ch117, adc2

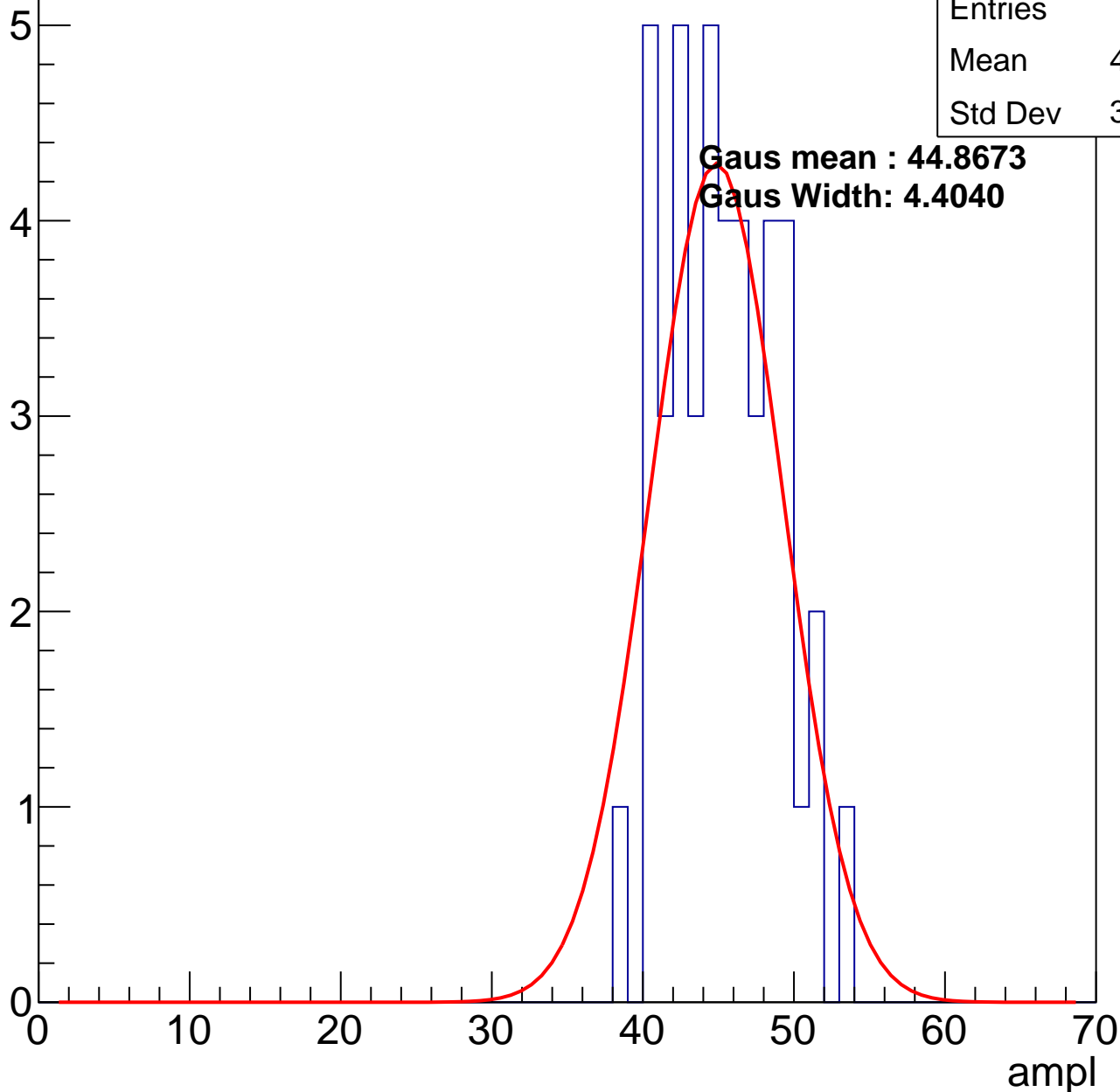
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	44.84
Std Dev	3.527

**Gaus mean : 44.8673**

**Gaus Width: 4.4040**

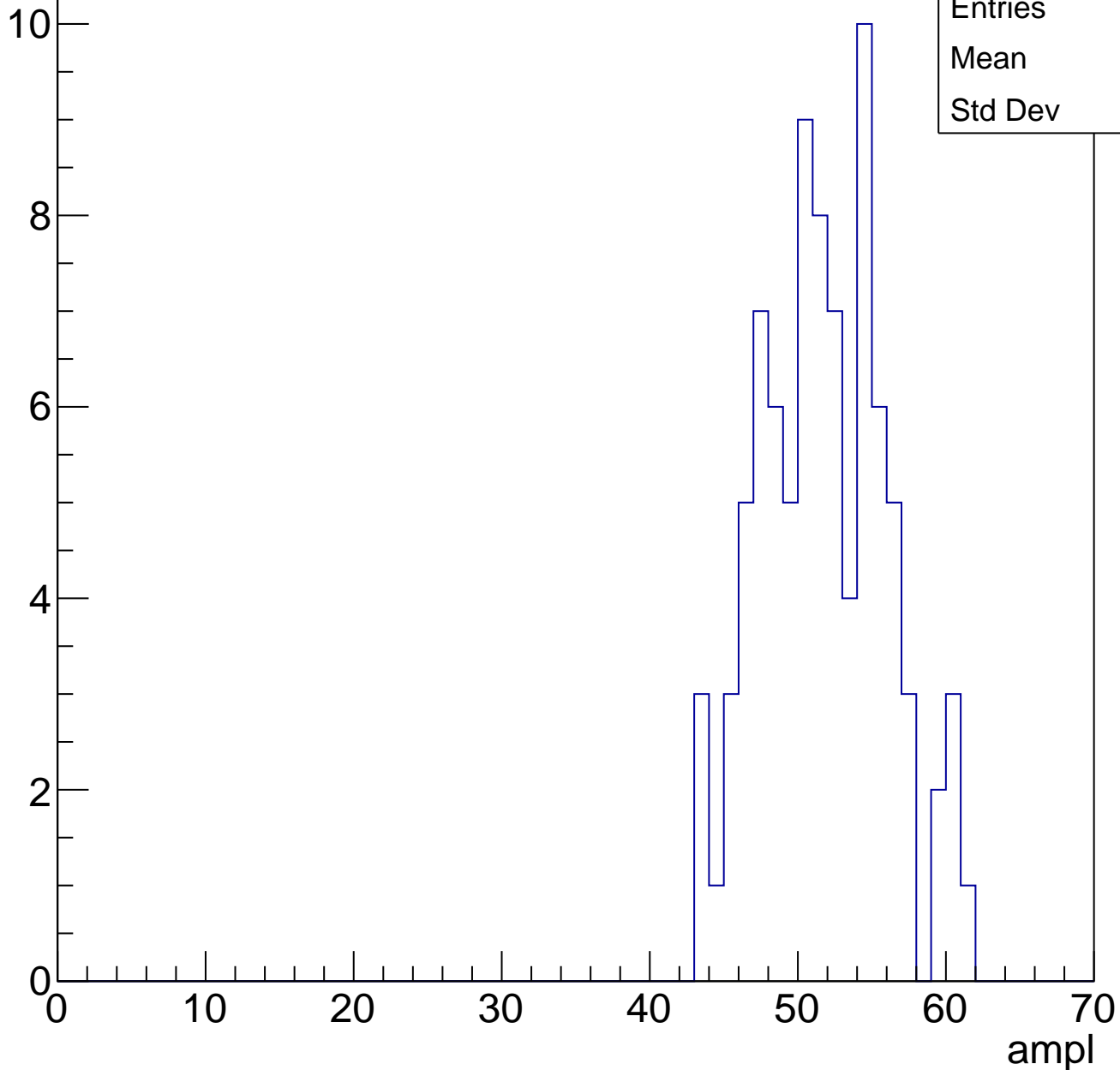


# B0L000S, U10-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	51.3
Std Dev	4.23

Entry

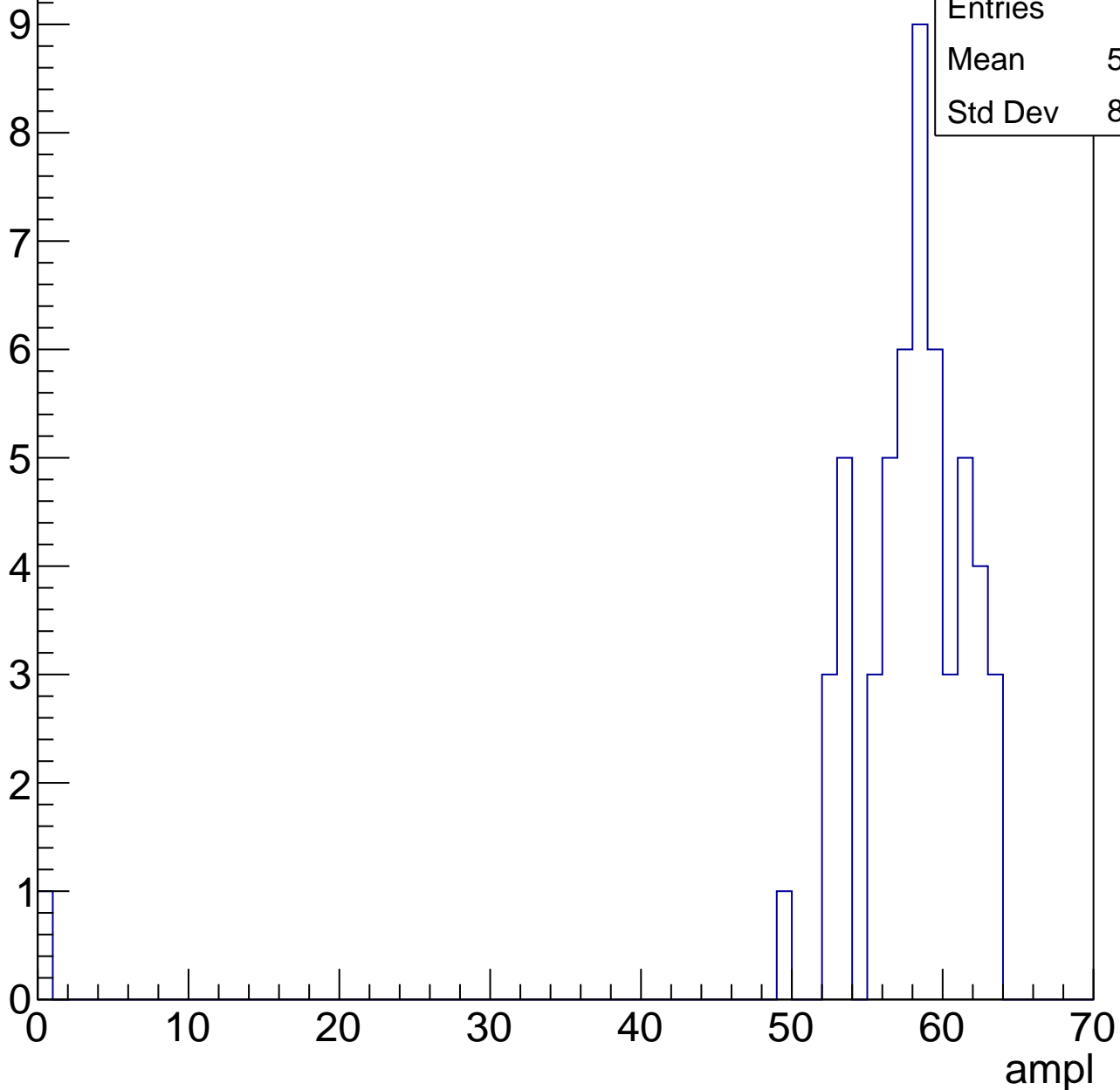


# B0L000S, U10-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	56.57
Std Dev	8.414

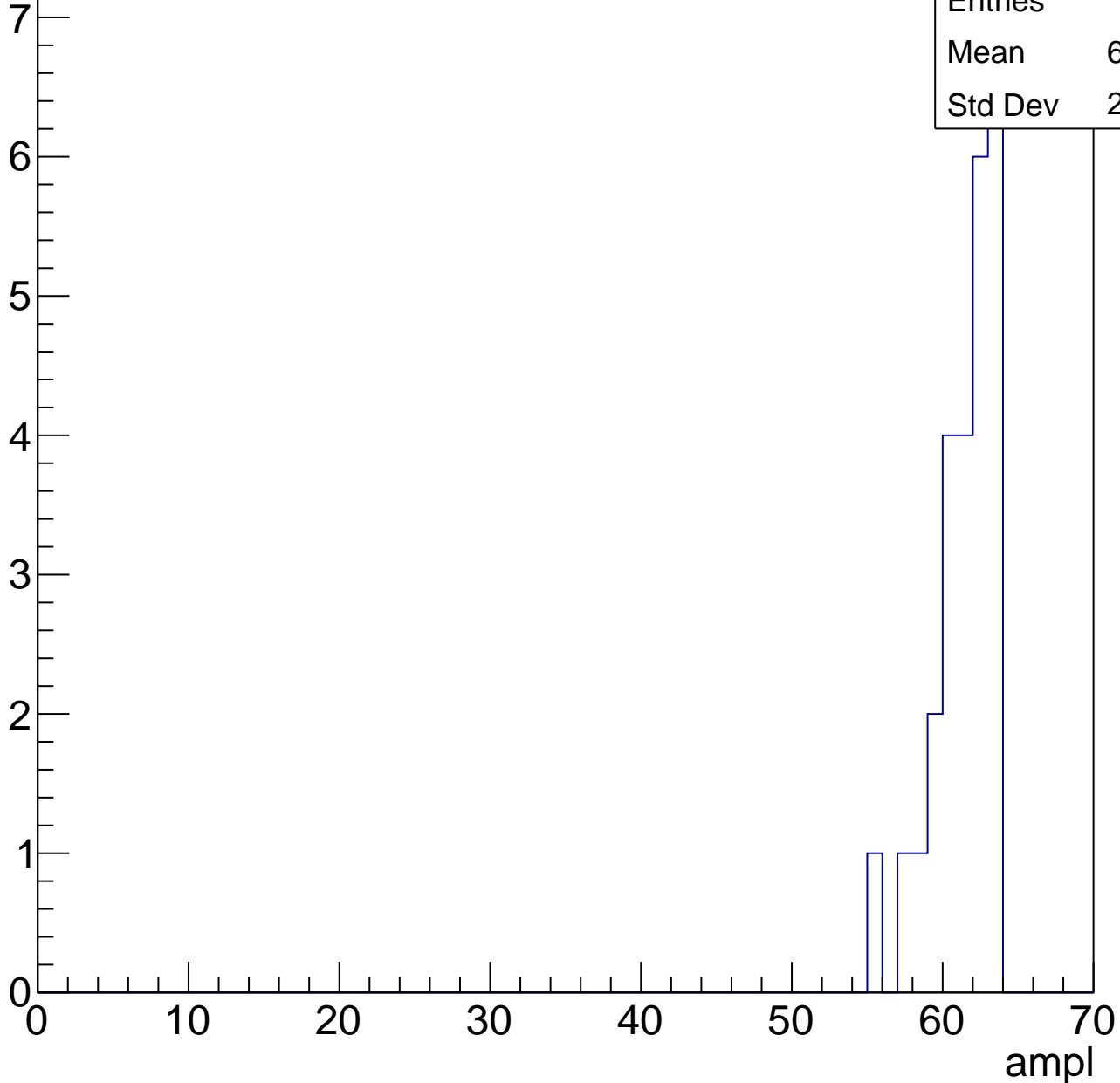


# B0L000S, U10-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	60.96
Std Dev	2.028



# B0L000S, U10-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U10-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl

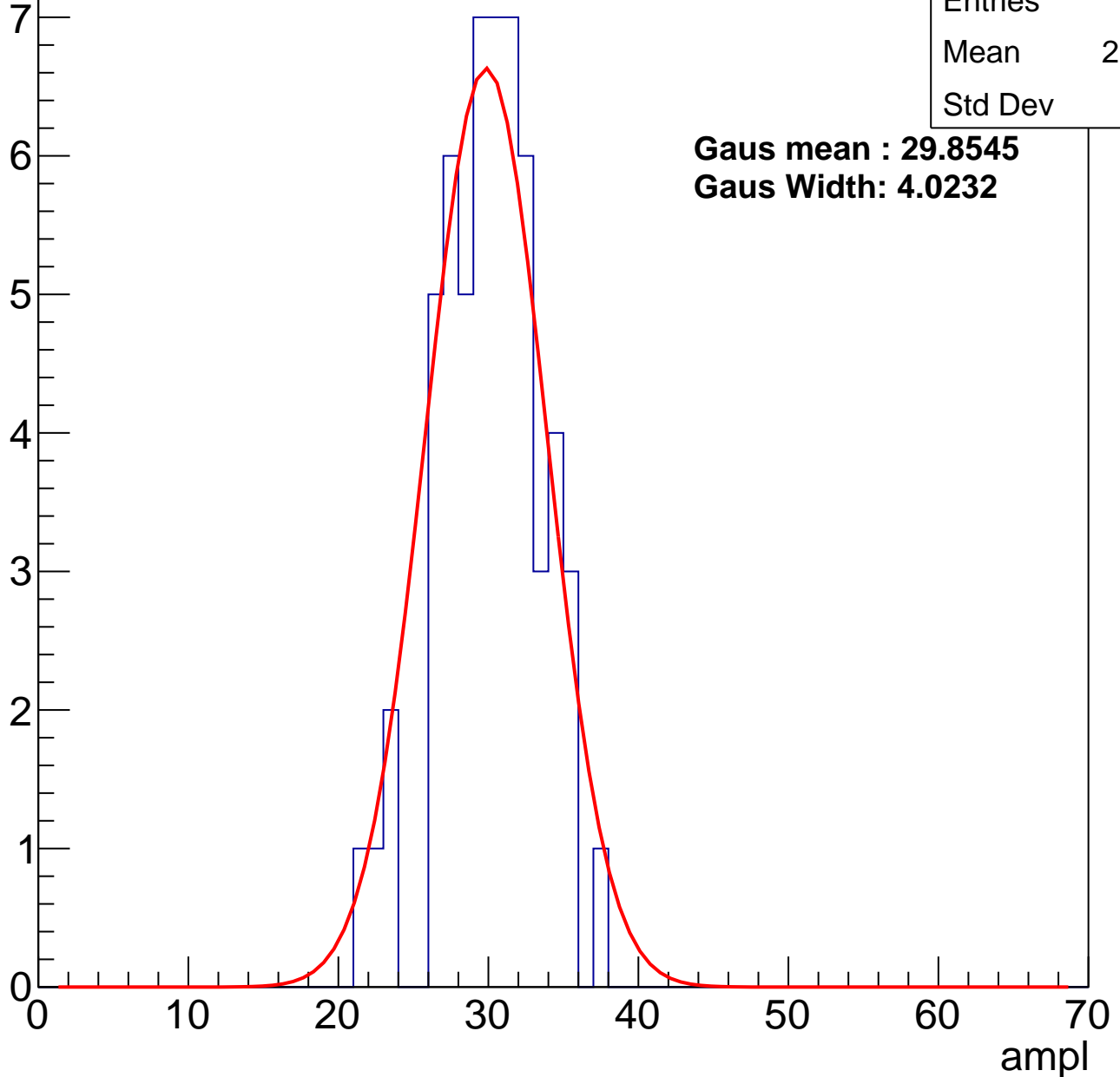
# B0L000S, U10-ch118, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.66
Std Dev	3.33

**Gaus mean : 29.8545**  
**Gaus Width: 4.0232**



# B0L000S, U10-ch118, adc1

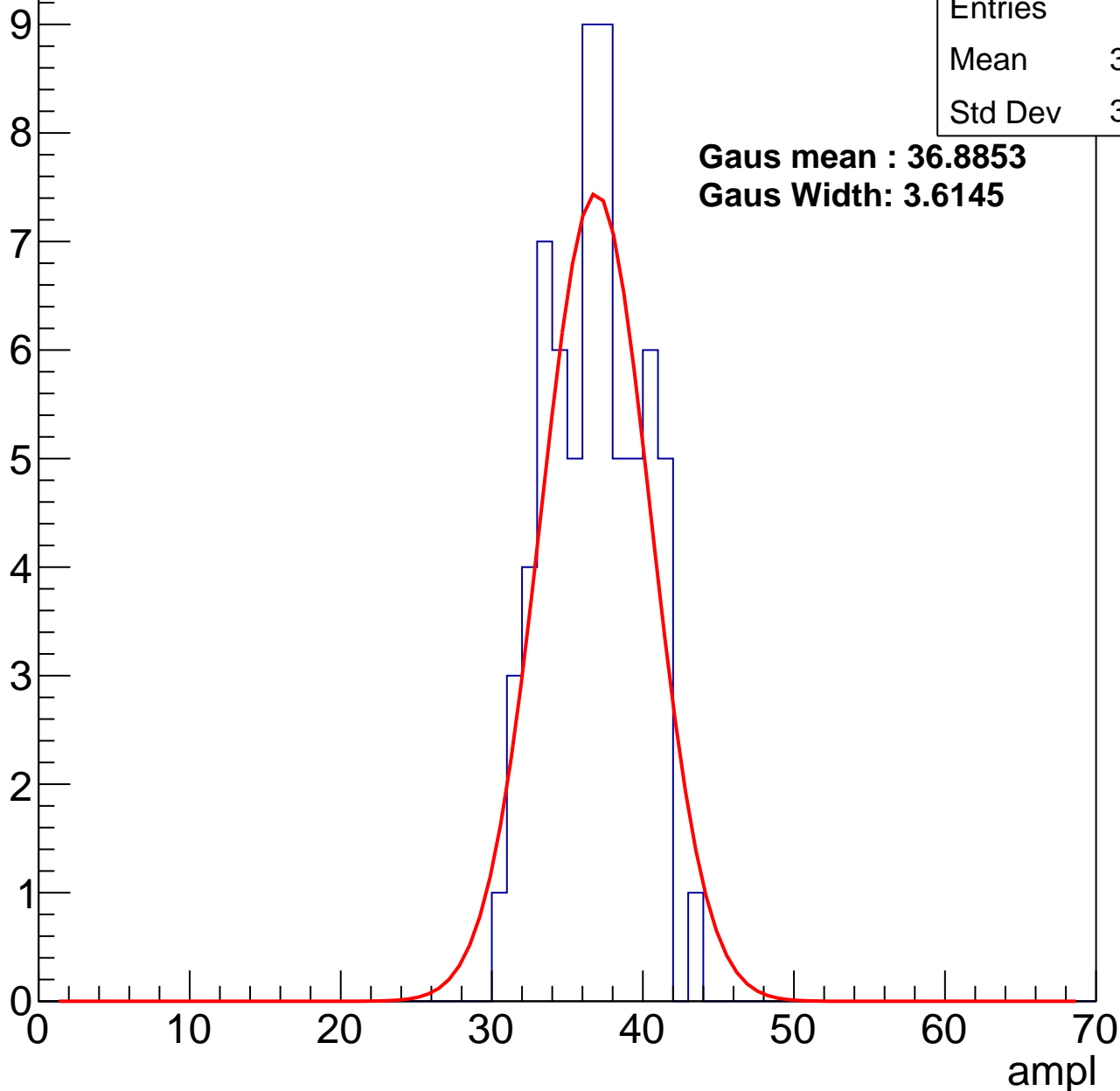
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.23
Std Dev	3.034

**Gaus mean : 36.8853**

**Gaus Width: 3.6145**



# B0L000S, U10-ch118, adc2

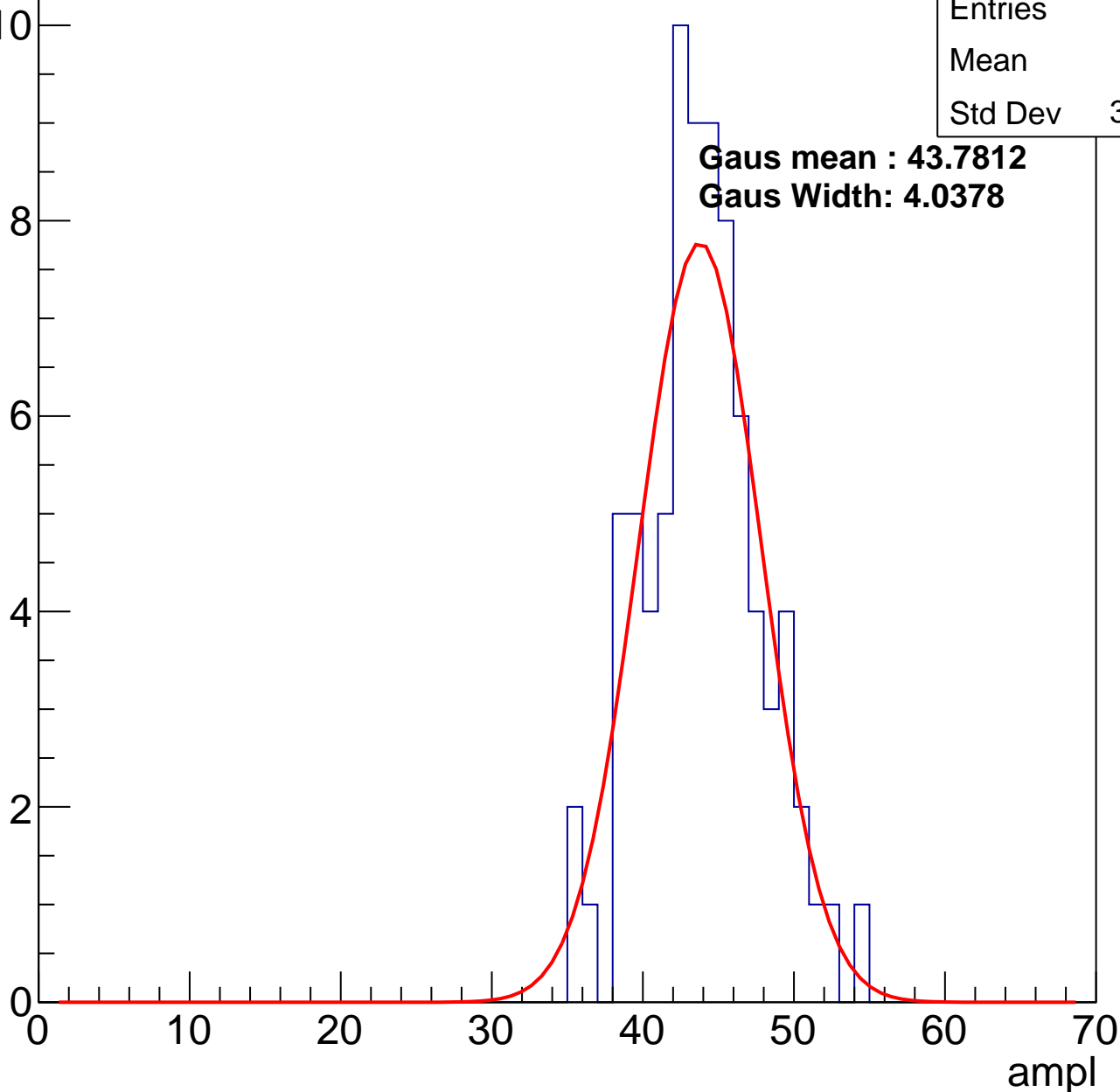
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	43.5
Std Dev	3.824

**Gaus mean : 43.7812**

**Gaus Width: 4.0378**

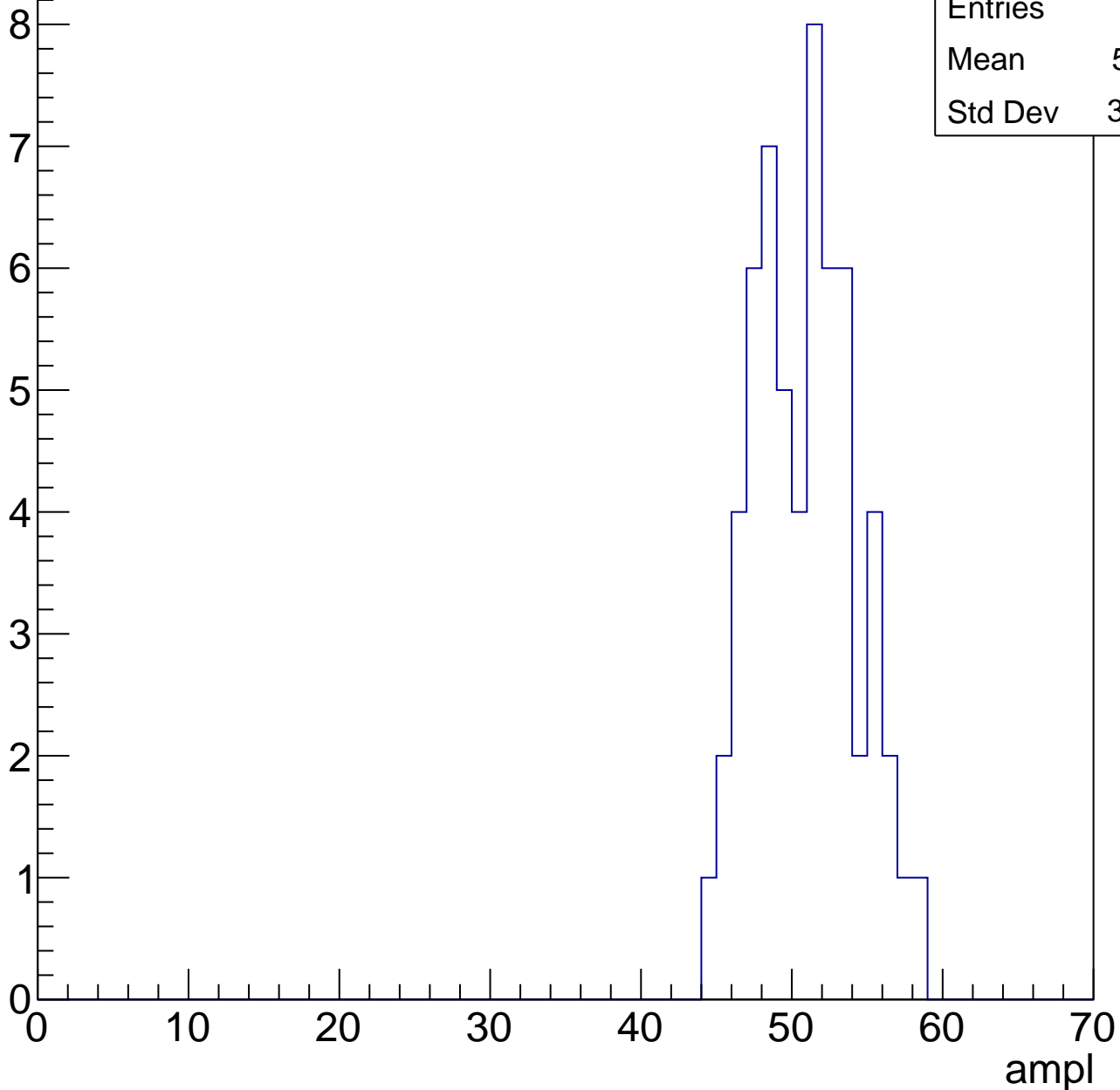


# B0L000S, U10-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.41
Std Dev	3.268

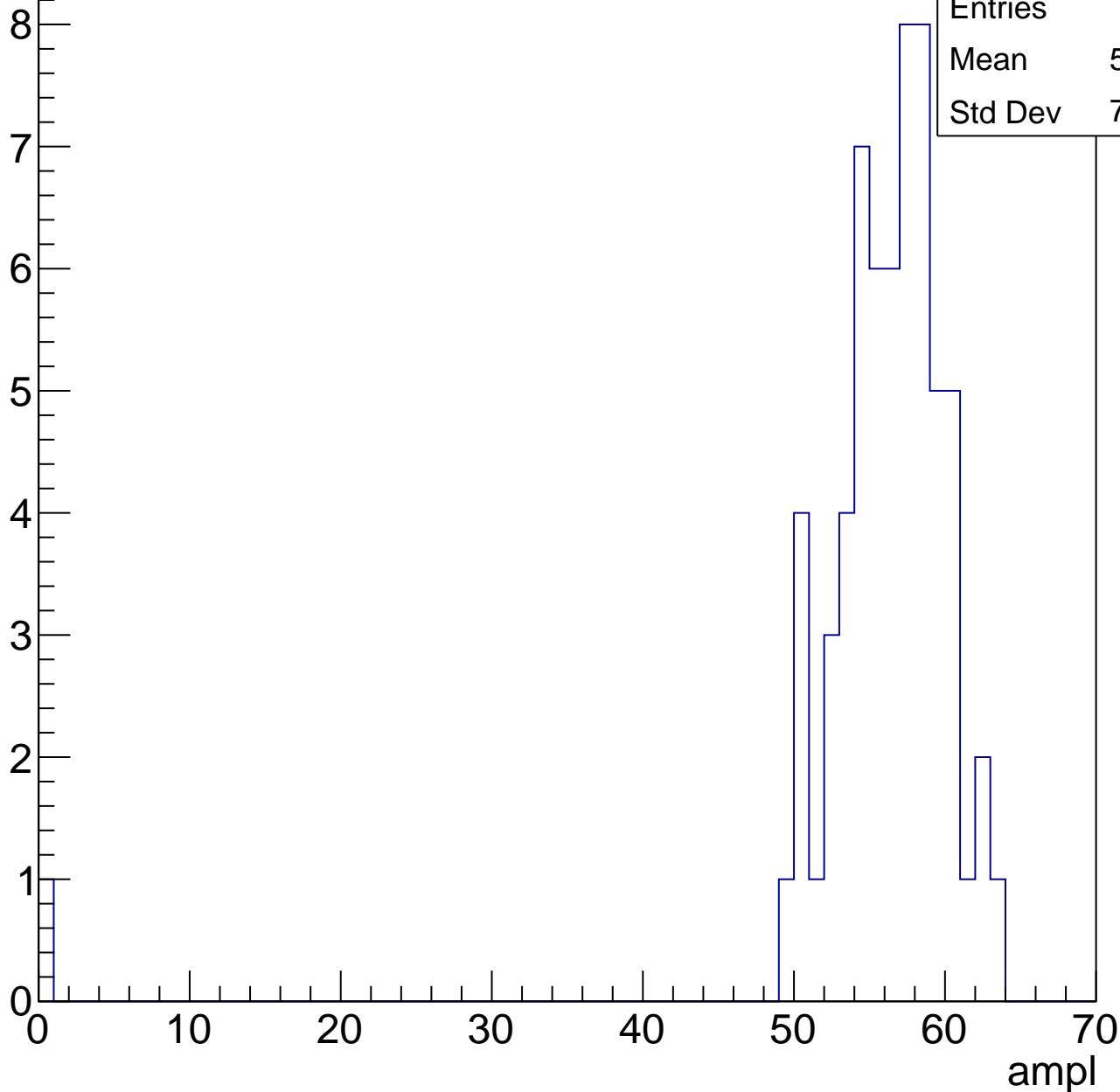


# B0L000S, U10-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	55.16
Std Dev	7.703

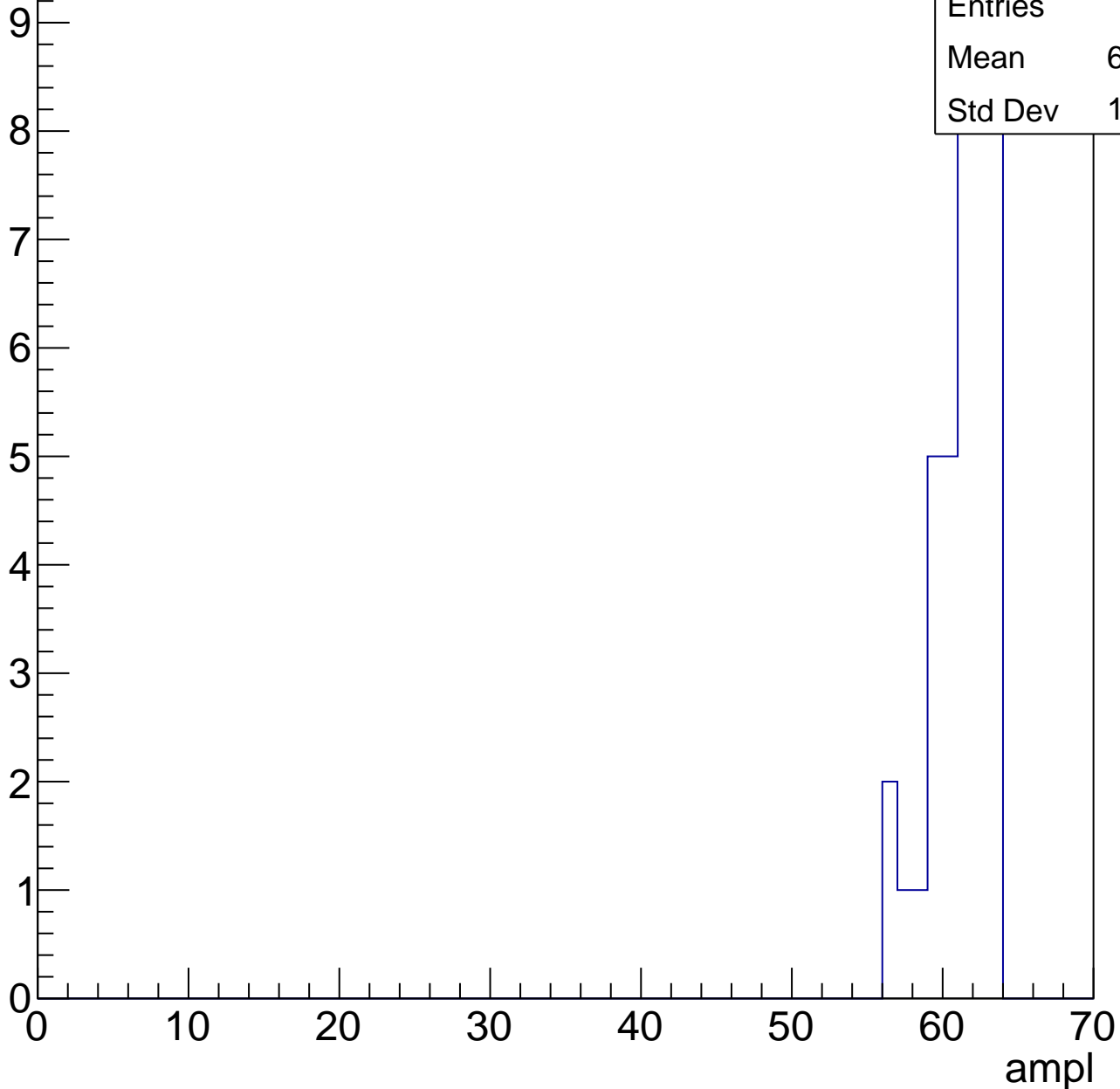


# B0L000S, U10-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	60.85
Std Dev	1.915



# B0L000S, U10-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch119, adc0

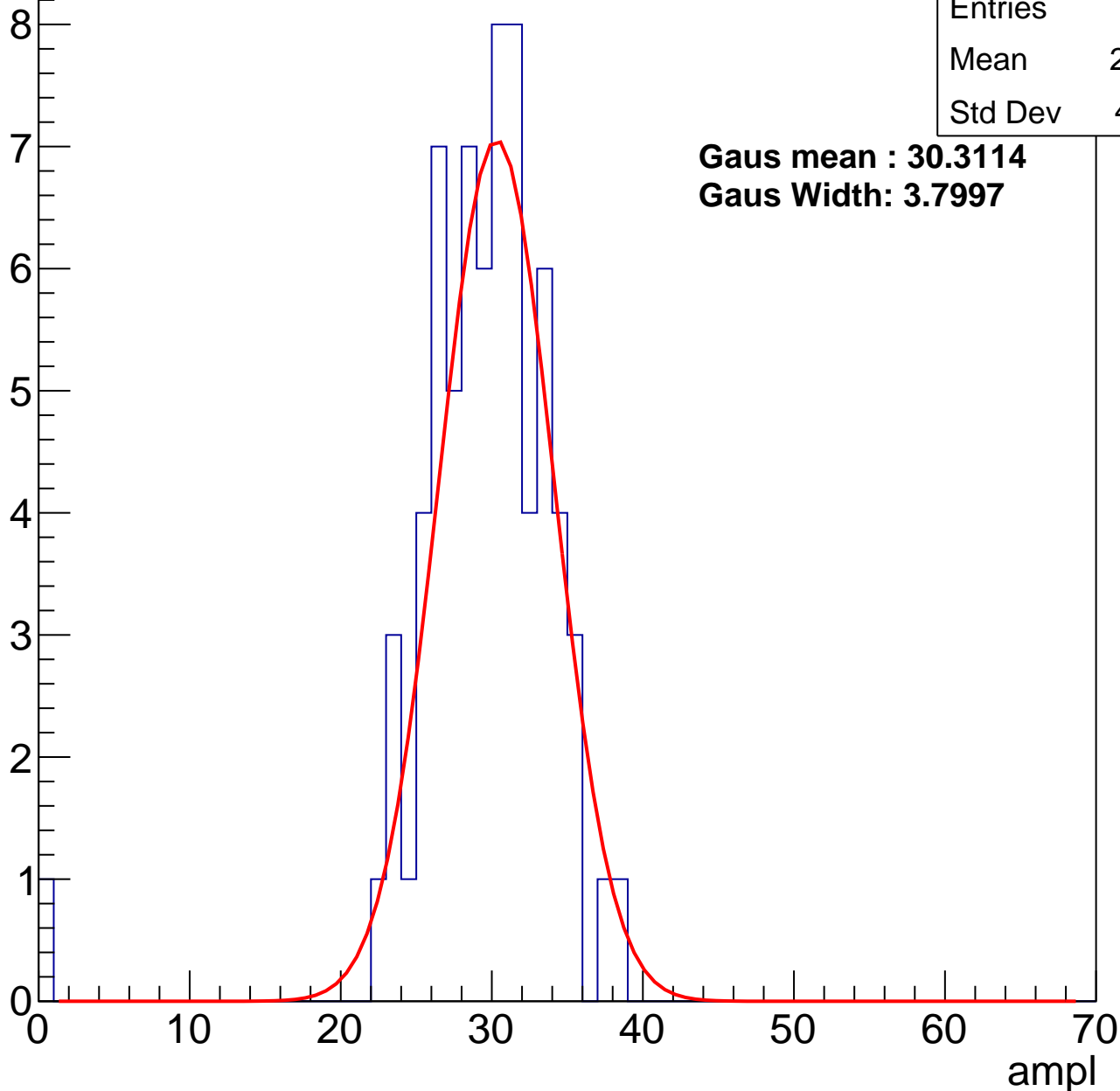
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.03
Std Dev	4.931

**Gaus mean : 30.3114**

**Gaus Width: 3.7997**



# B0L000S, U10-ch119, adc1

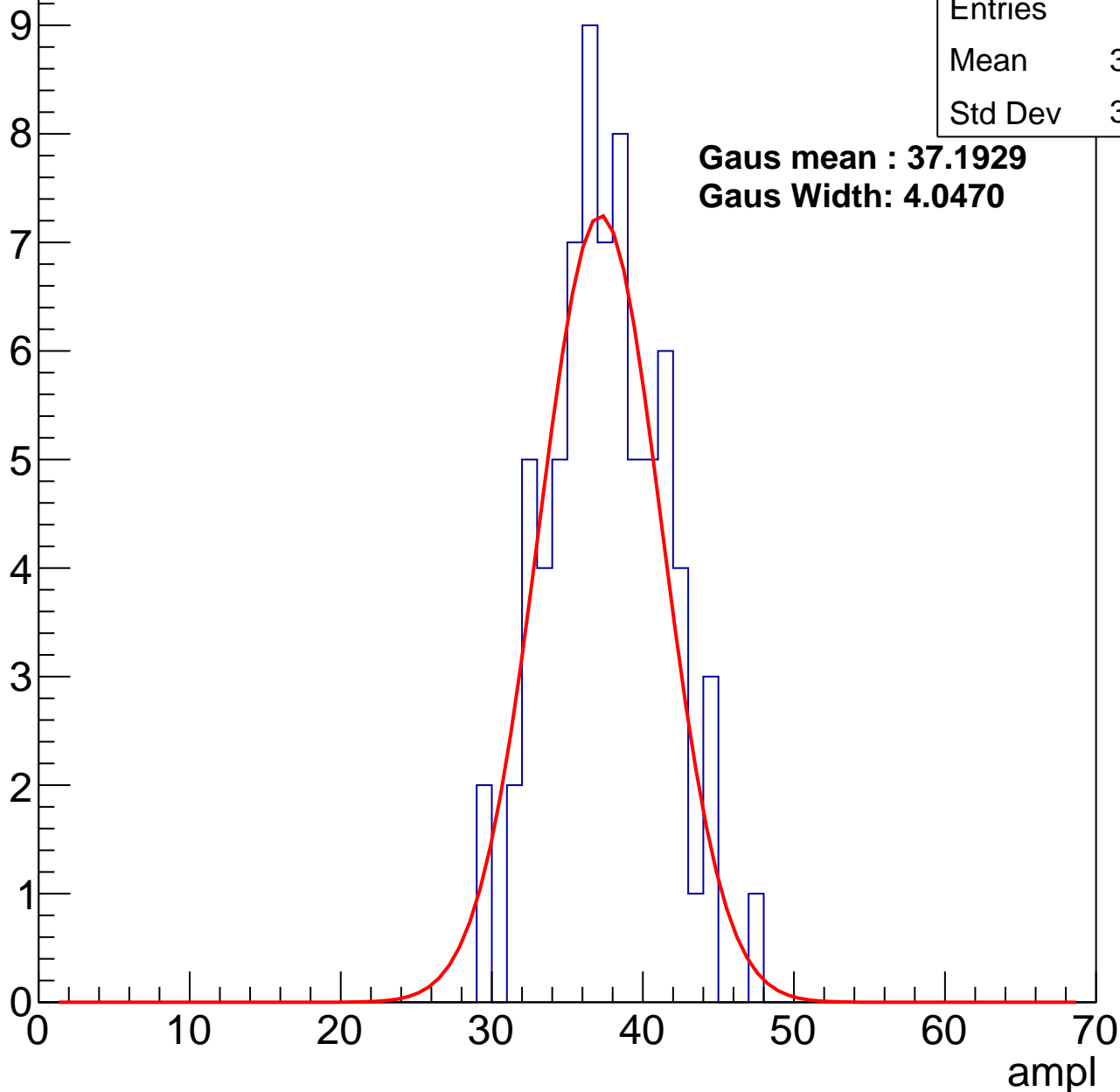
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.09
Std Dev	3.717

**Gaus mean : 37.1929**

**Gaus Width: 4.0470**



# B0L000S, U10-ch119, adc2

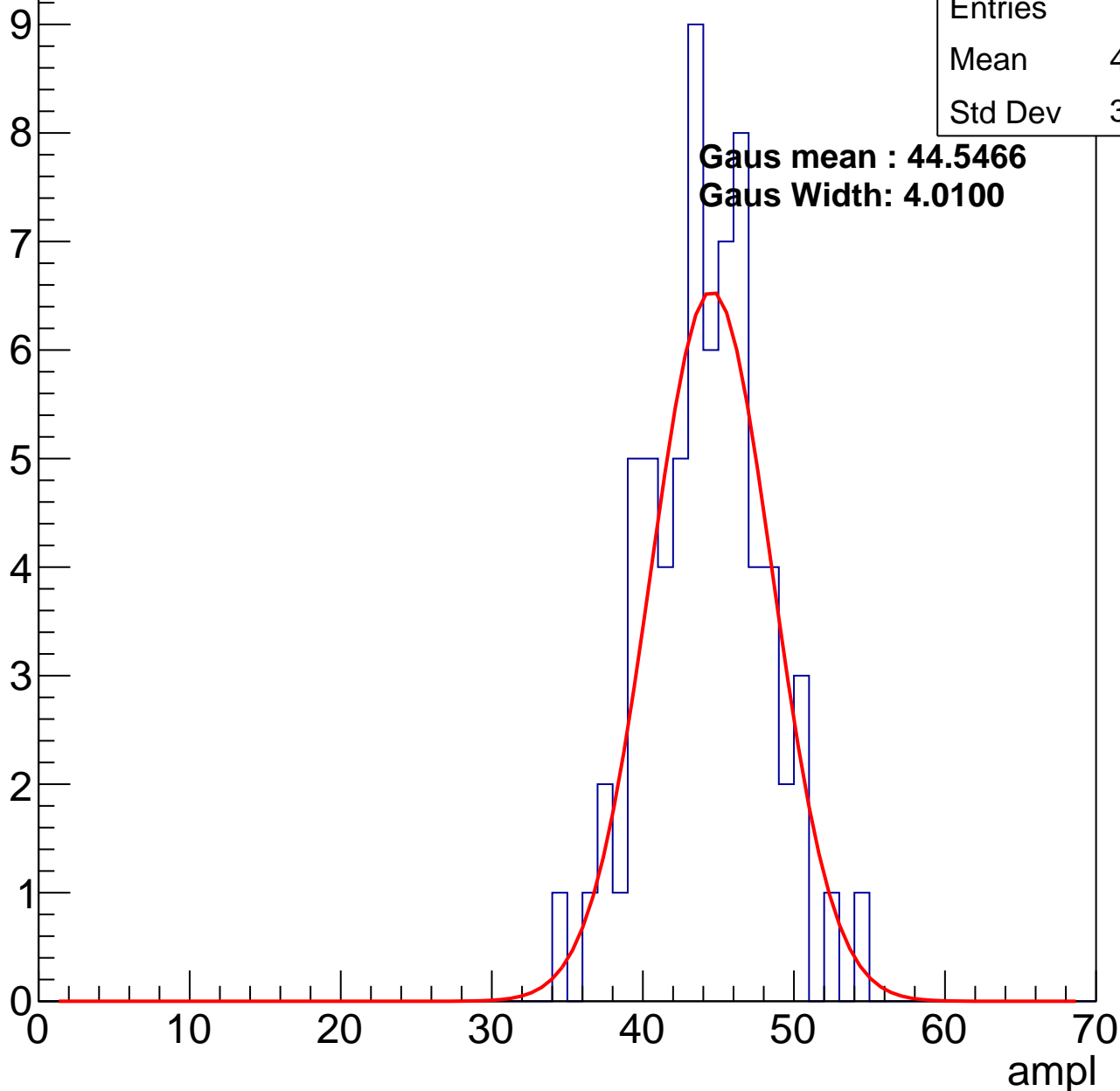
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.75
Std Dev	3.854

**Gaus mean : 44.5466**

**Gaus Width: 4.0100**

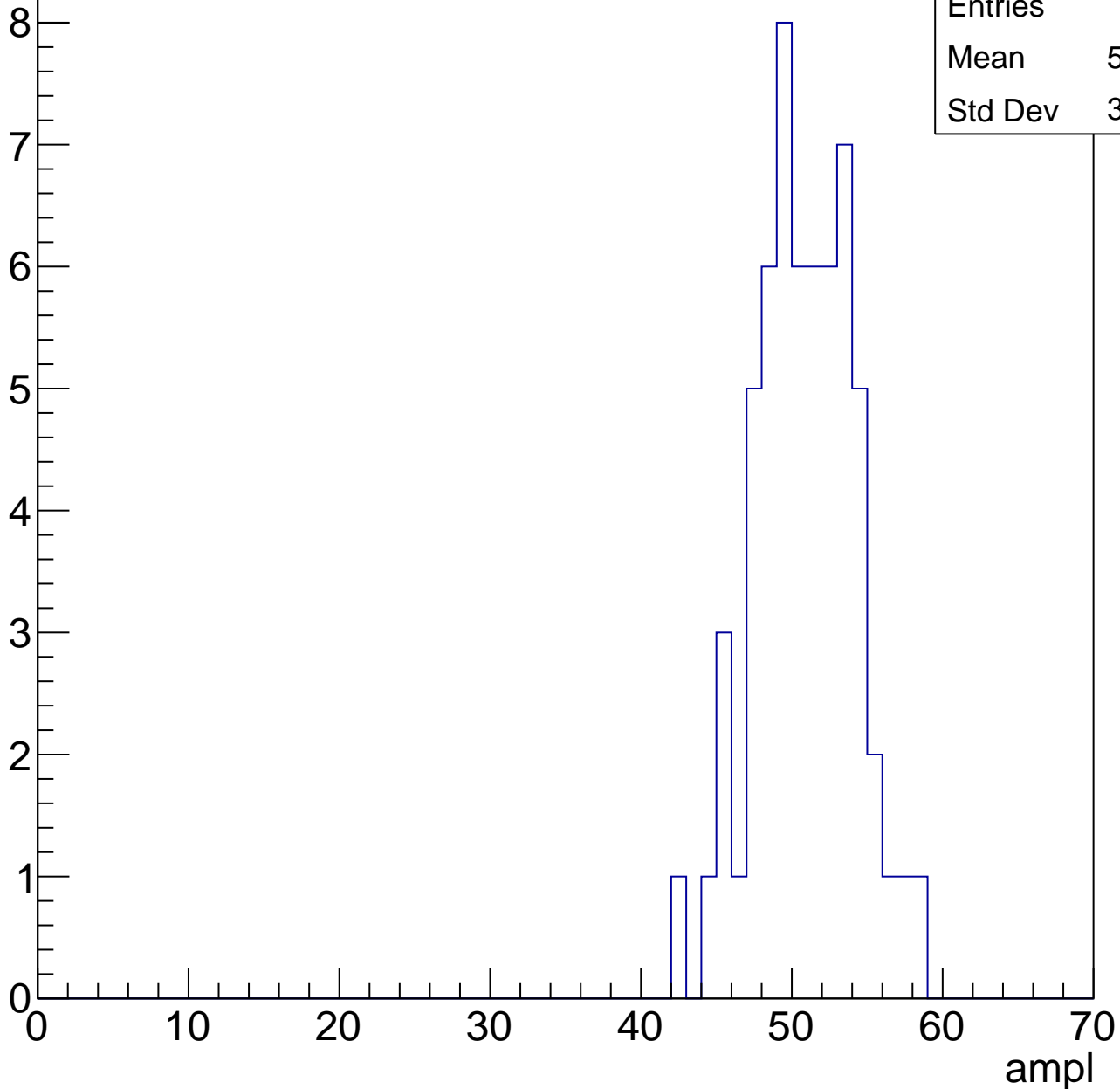


# B0L000S, U10-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.37
Std Dev	3.245

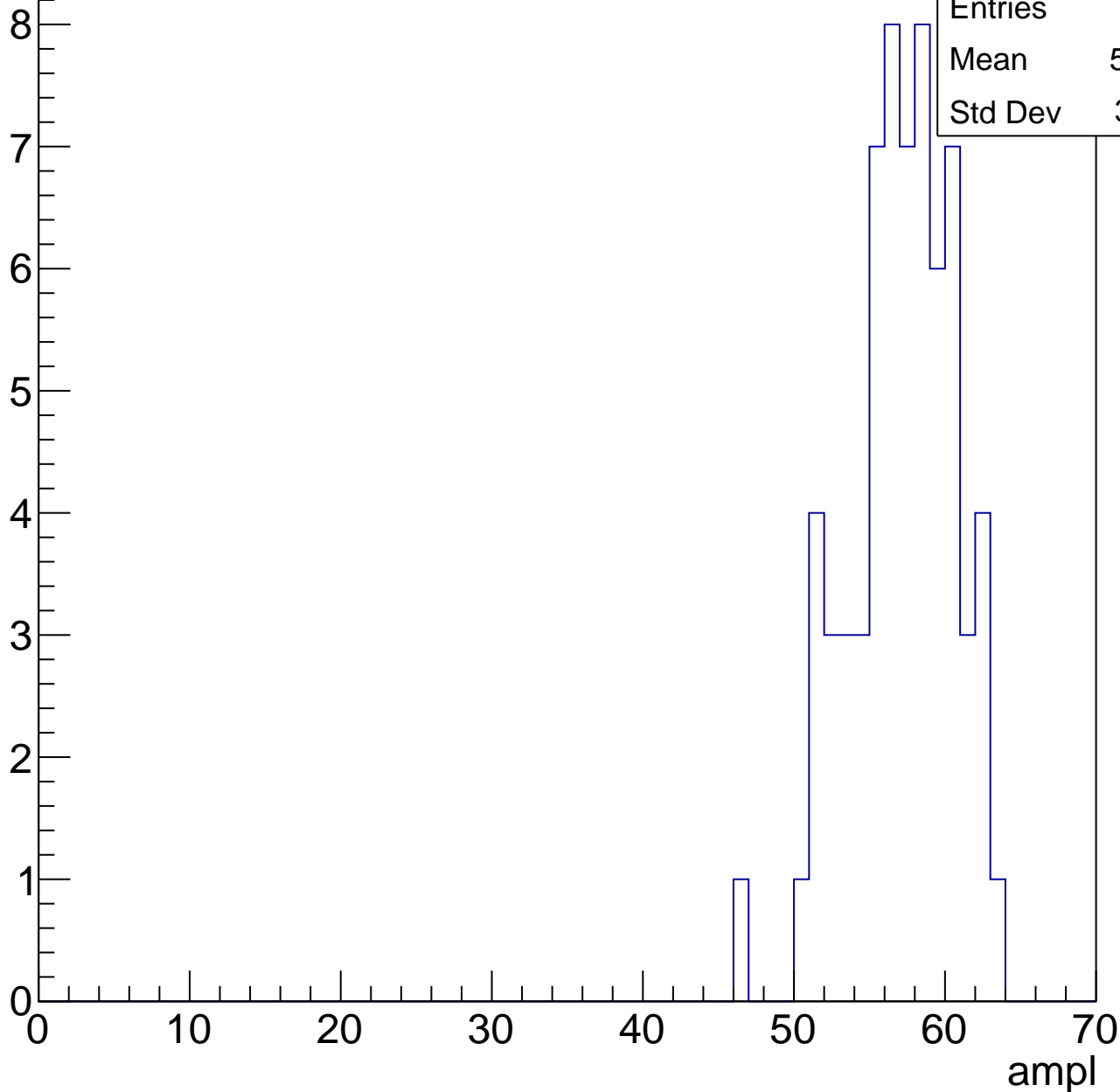


# B0L000S, U10-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.68
Std Dev	3.421

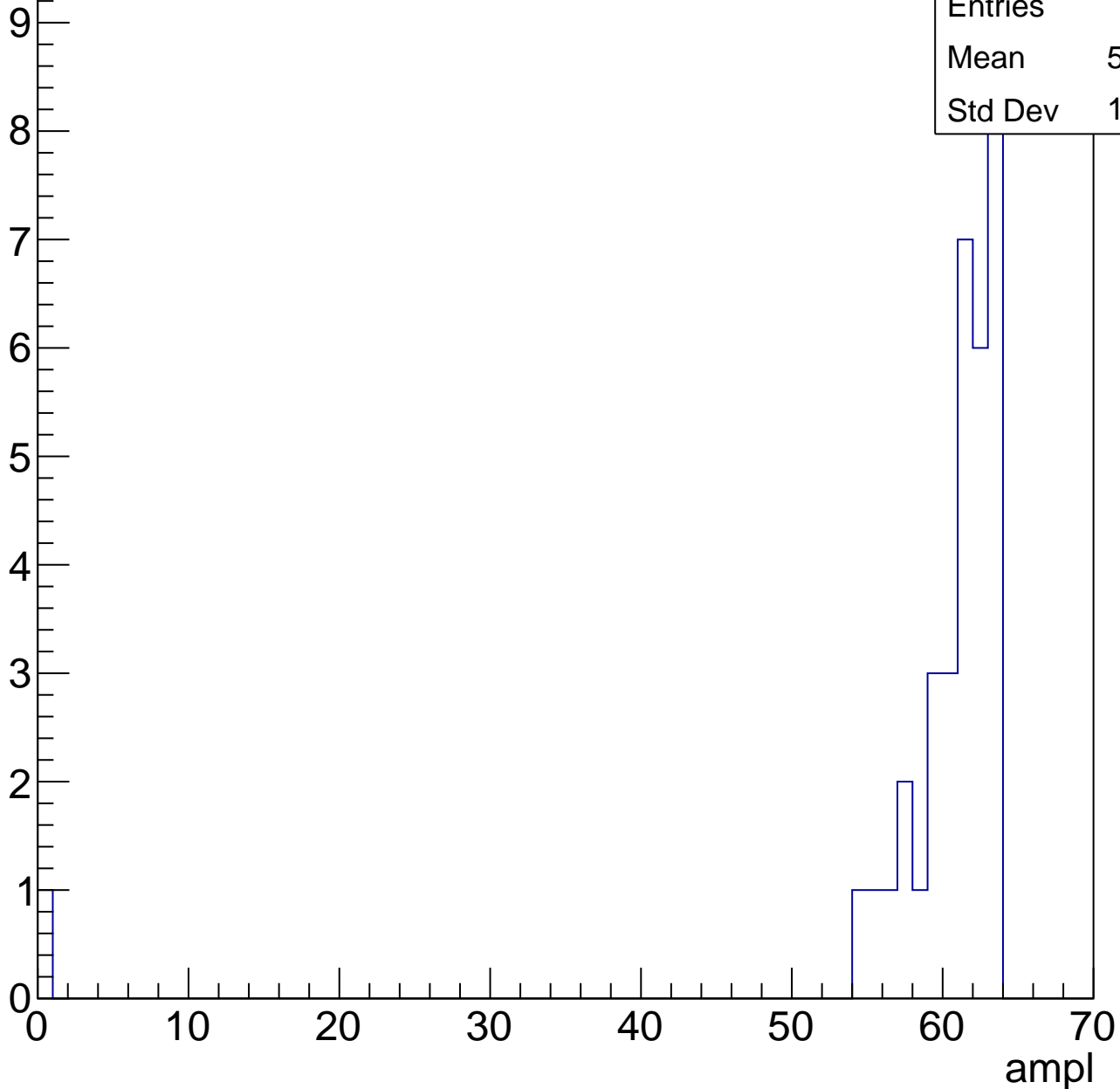


# B0L000S, U10-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.86
Std Dev	10.38



# B0L000S, U10-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch120, adc0

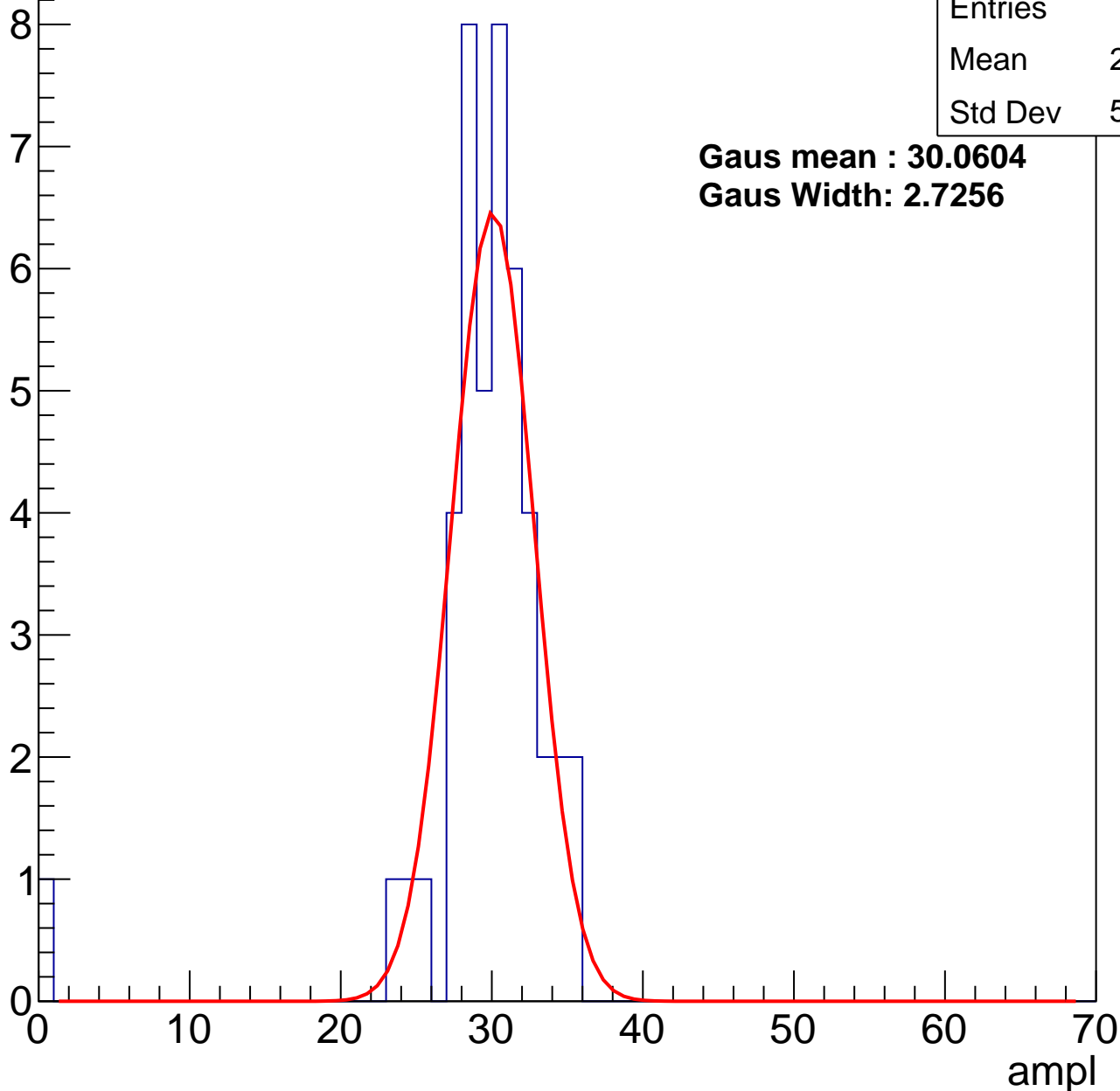
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	29.04
Std Dev	5.086

**Gaus mean : 30.0604**

**Gaus Width: 2.7256**



# B0L000S, U10-ch120, adc1

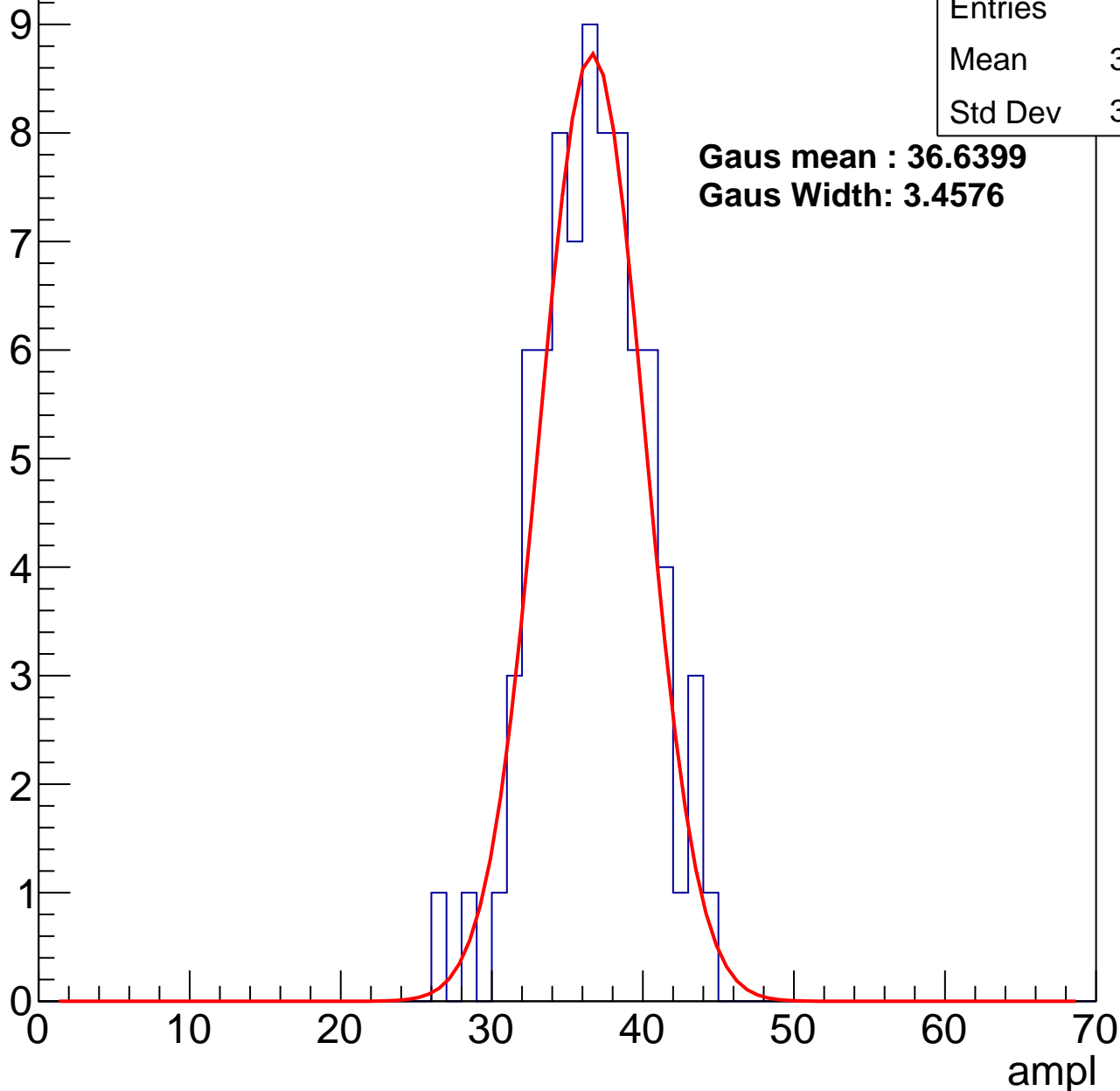
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	36.22
Std Dev	3.553

**Gaus mean : 36.6399**

**Gaus Width: 3.4576**

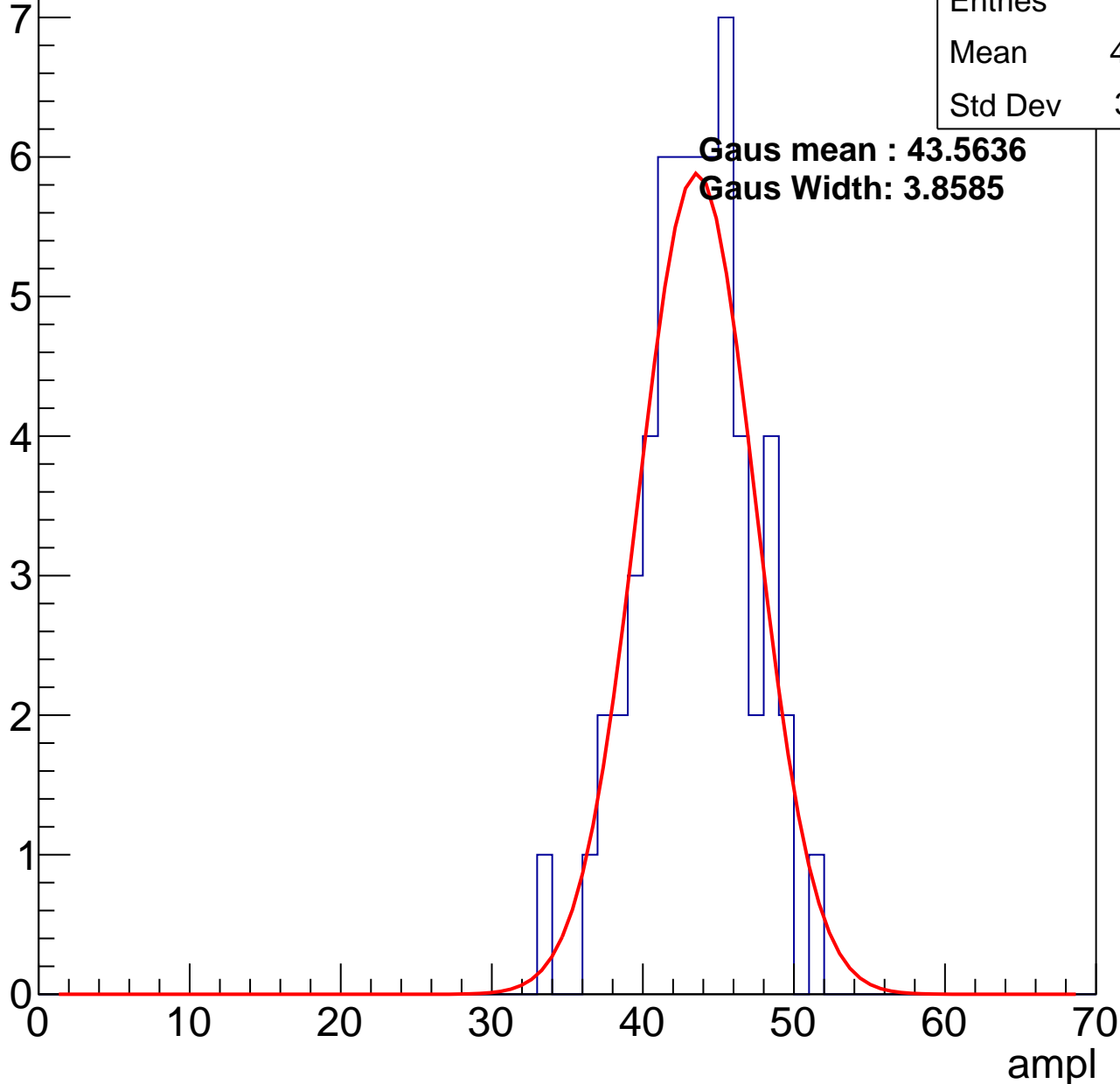


# B0L000S, U10-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.98
Std Dev	3.561

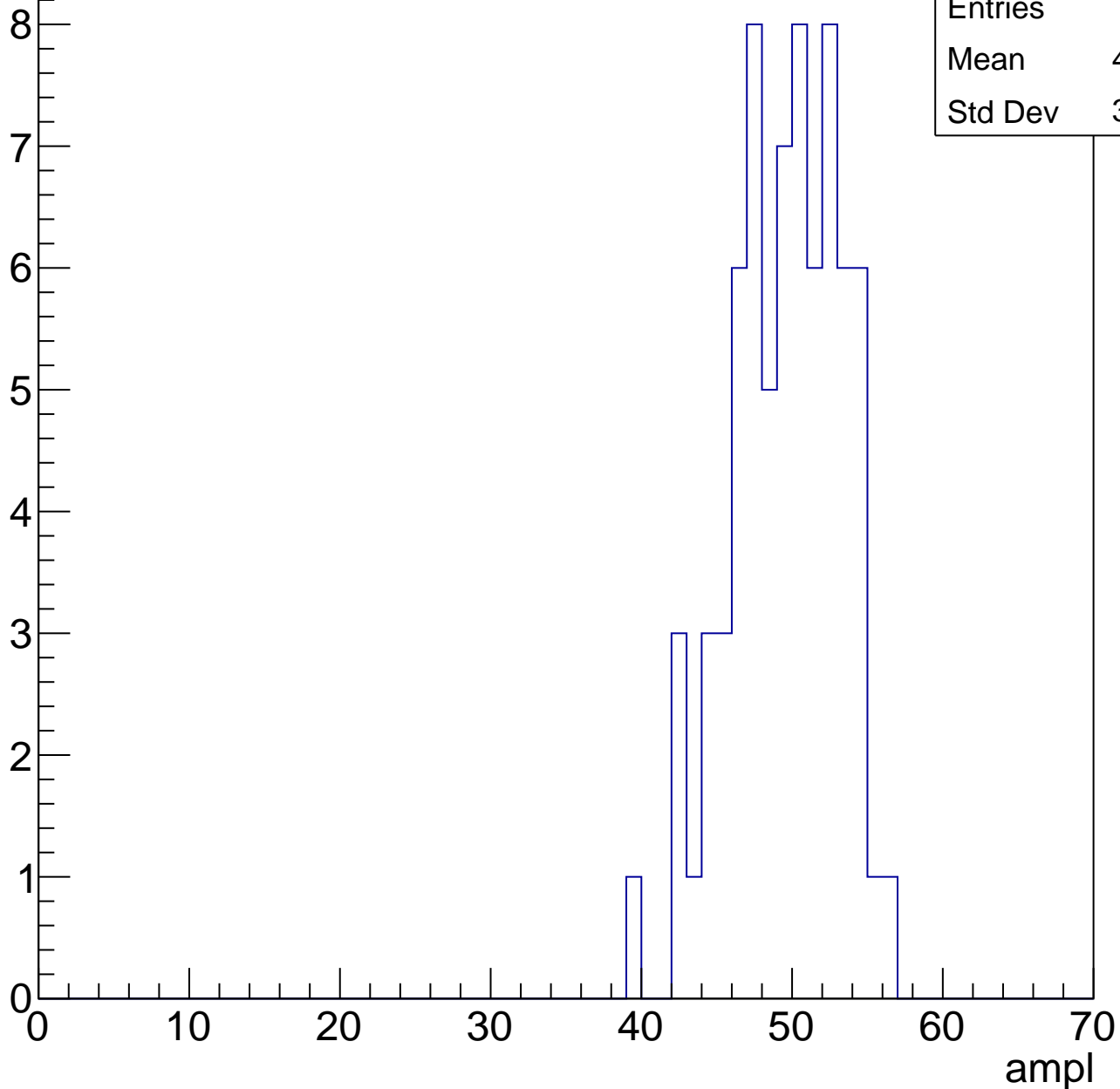


# B0L000S, U10-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	49.11
Std Dev	3.571

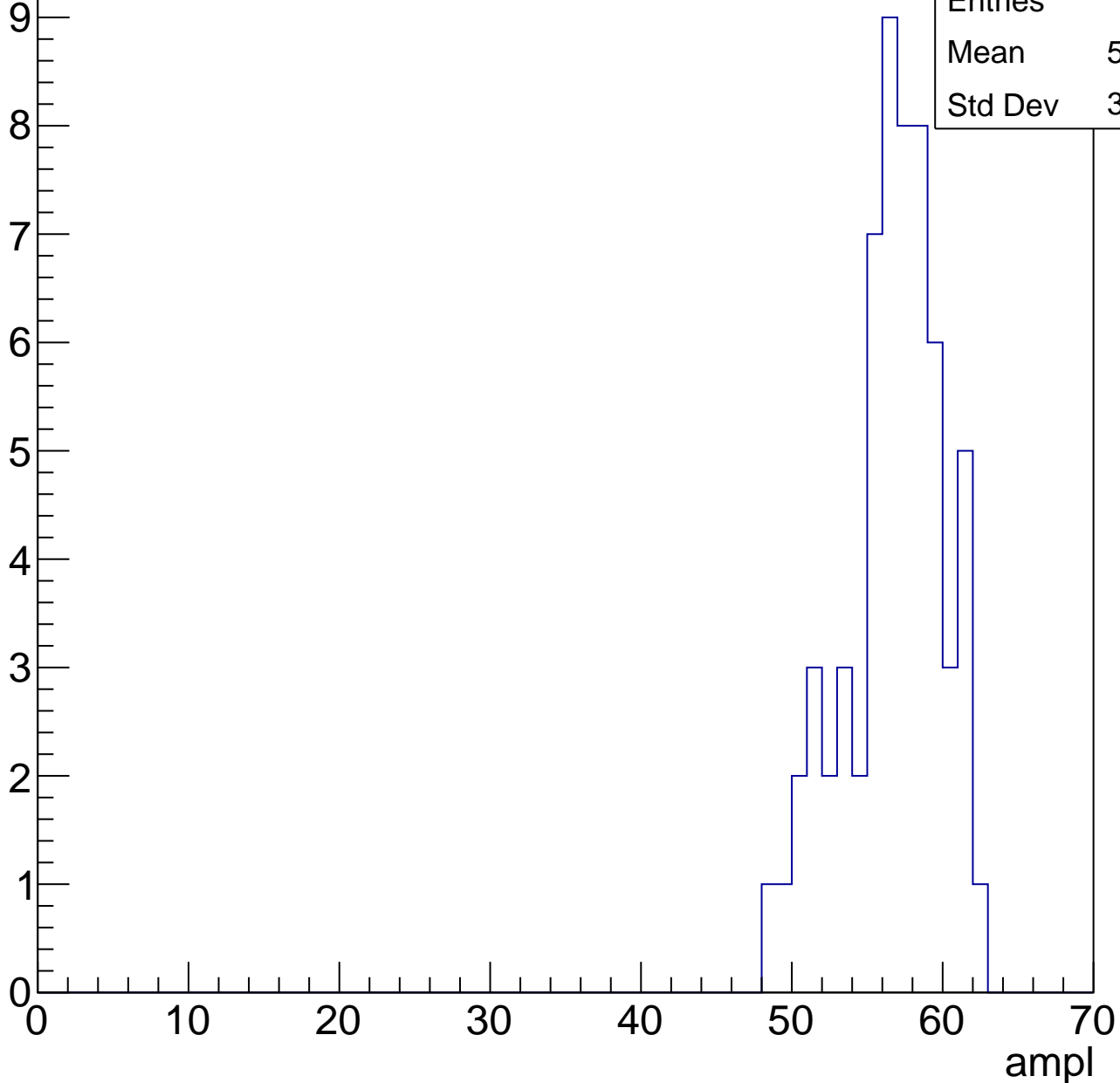


# B0L000S, U10-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

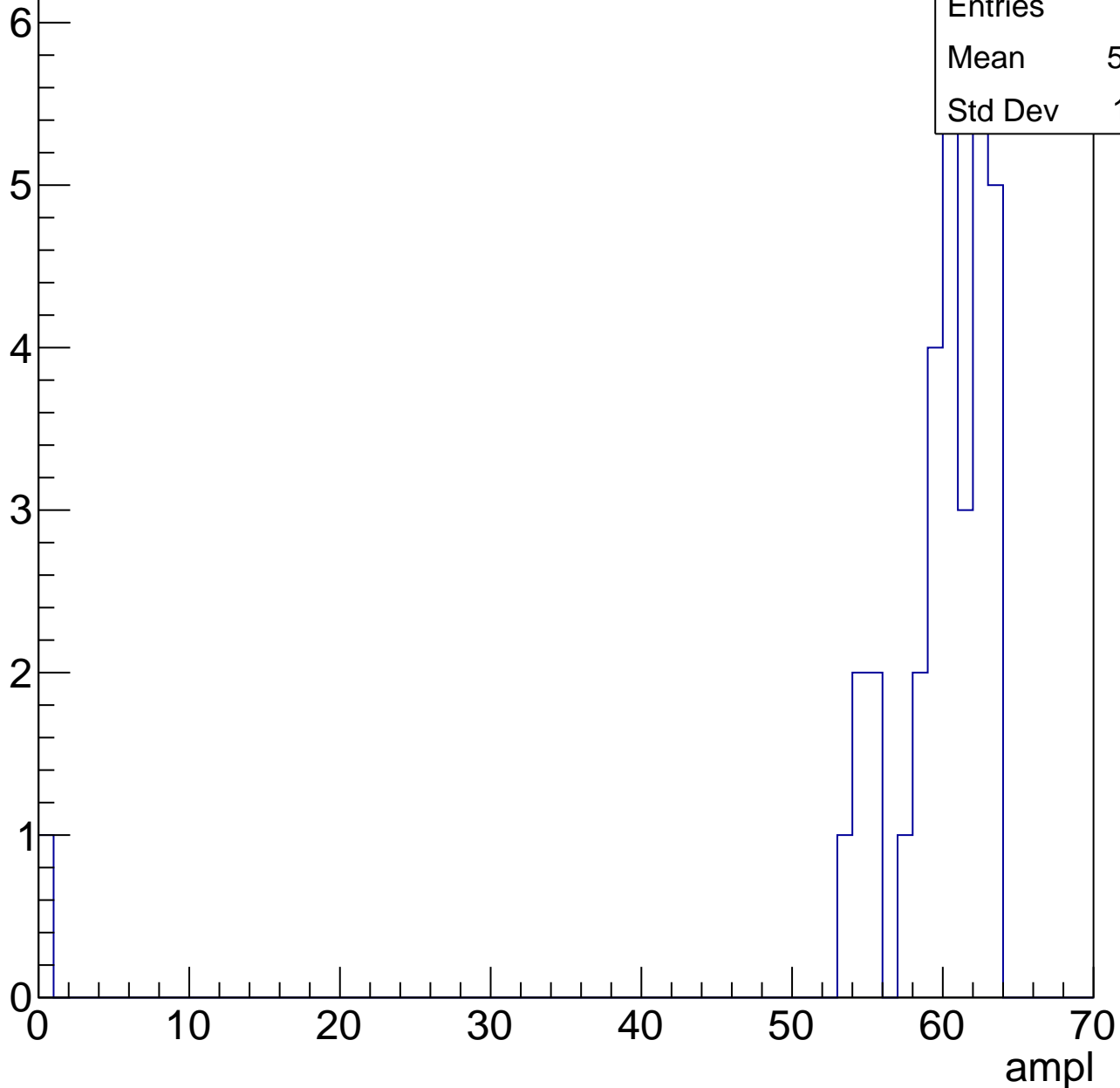
Entries	61
Mean	56.25
Std Dev	3.222



# B0L000S, U10-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

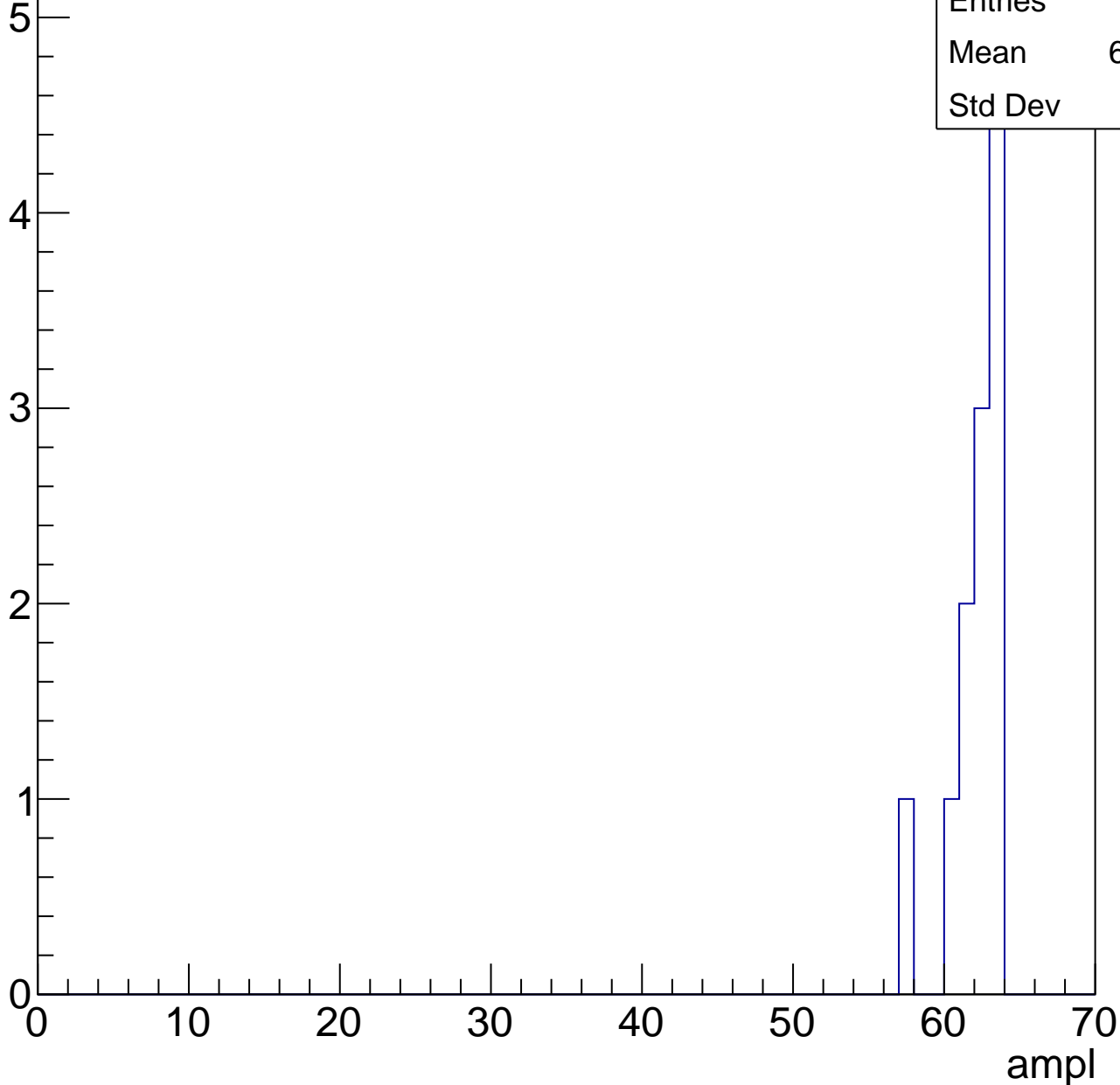


# B0L000S, U10-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	61.67
Std Dev	1.7





# B0L000S, U10-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch121, adc0

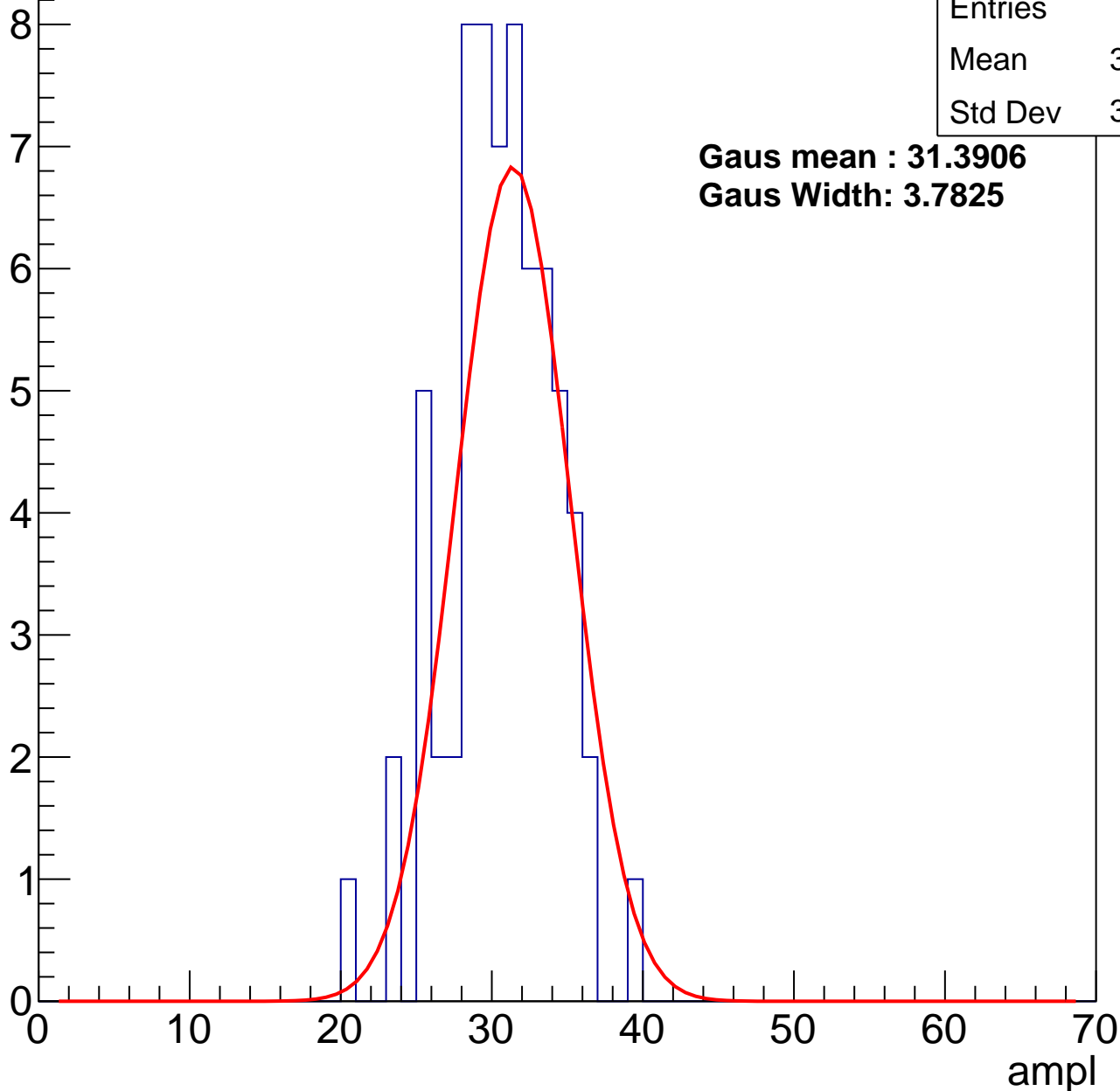
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.18
Std Dev	3.519

**Gaus mean : 31.3906**

**Gaus Width: 3.7825**



# B0L000S, U10-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	36.44
Std Dev	3.453

**Gaus mean : 37.0639**

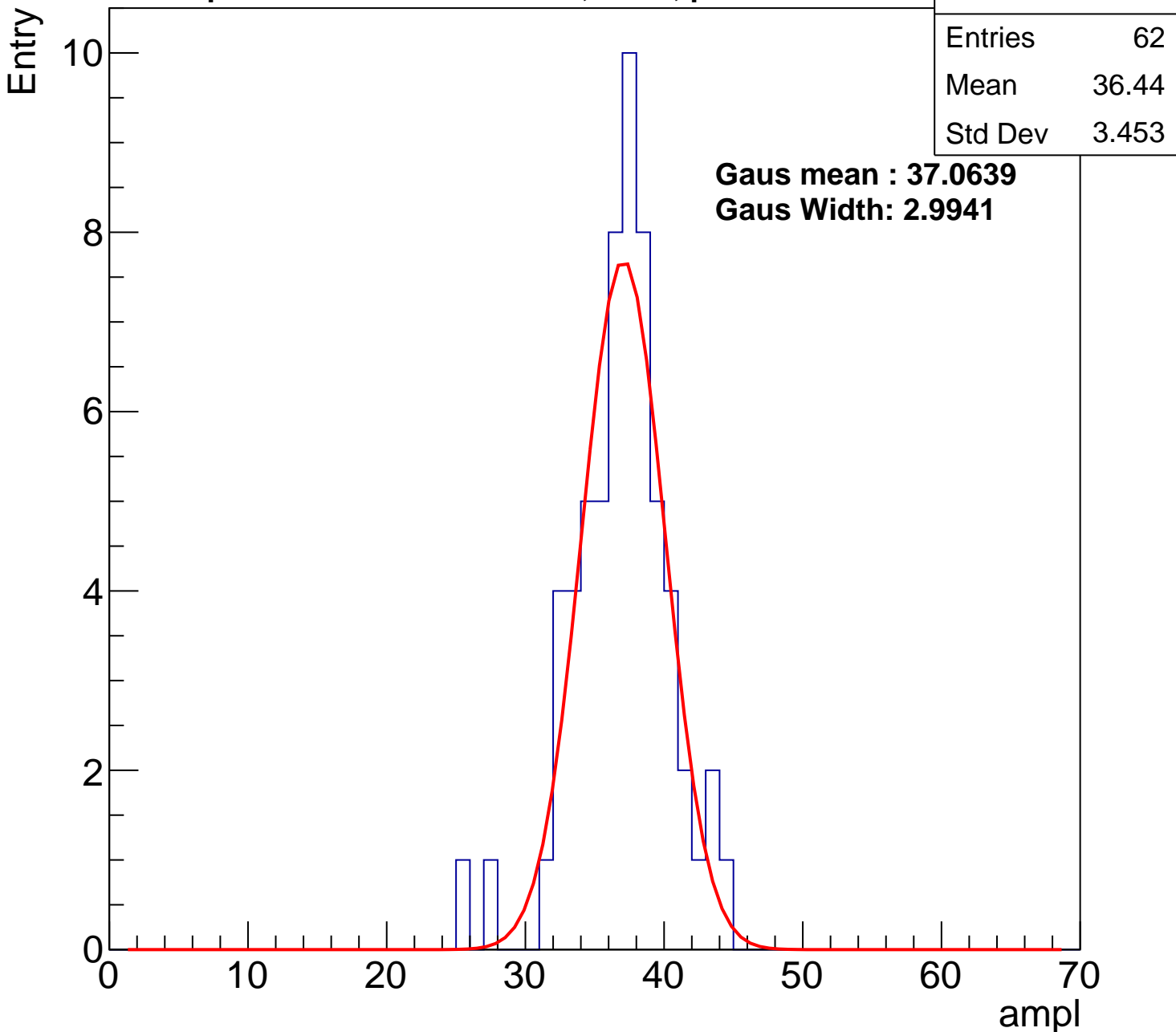
**Gaus Width: 2.9941**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch121, adc2

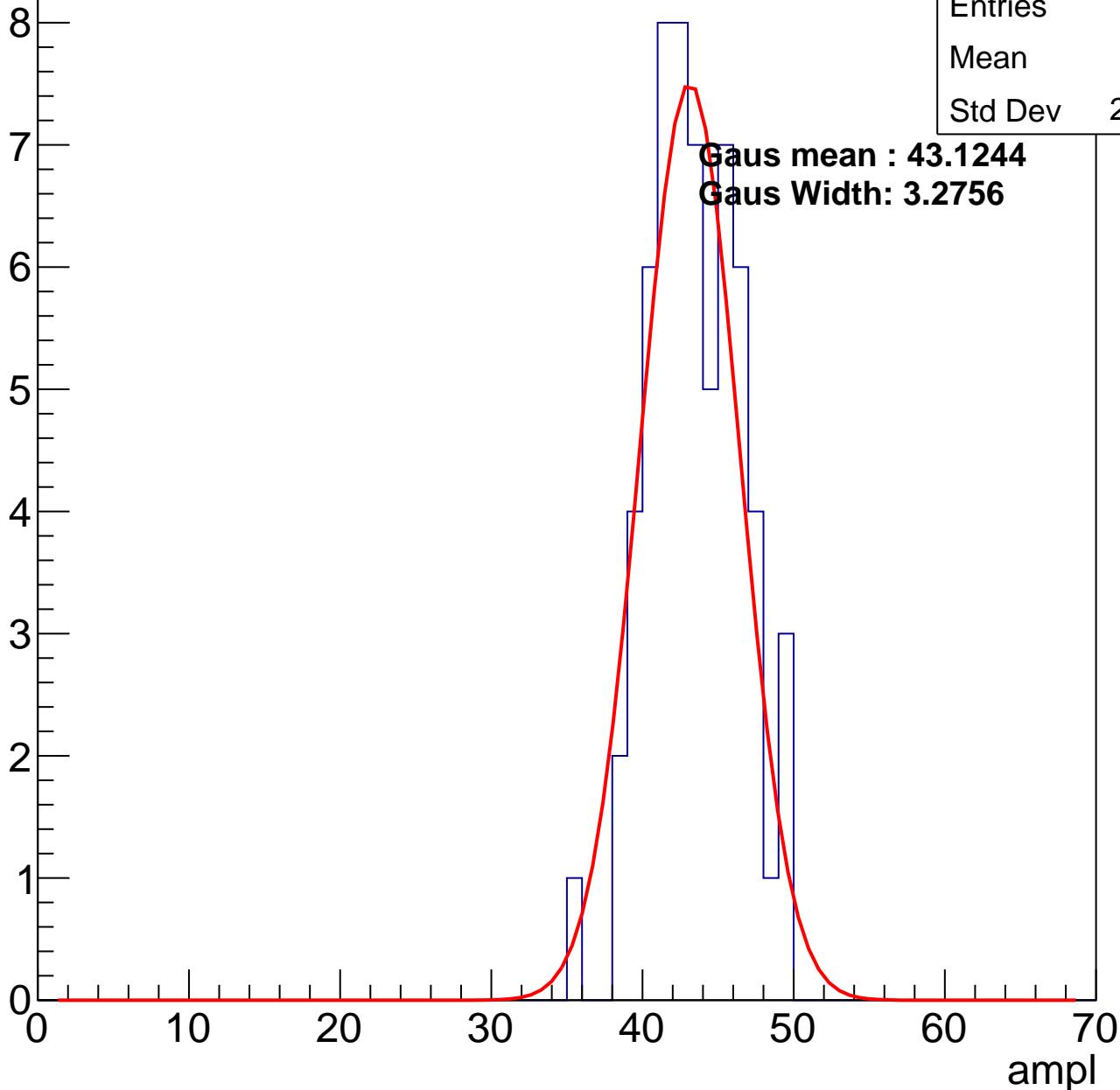
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43
Std Dev	2.995

**Gaus mean : 43.1244**

**Gaus Width: 3.2756**

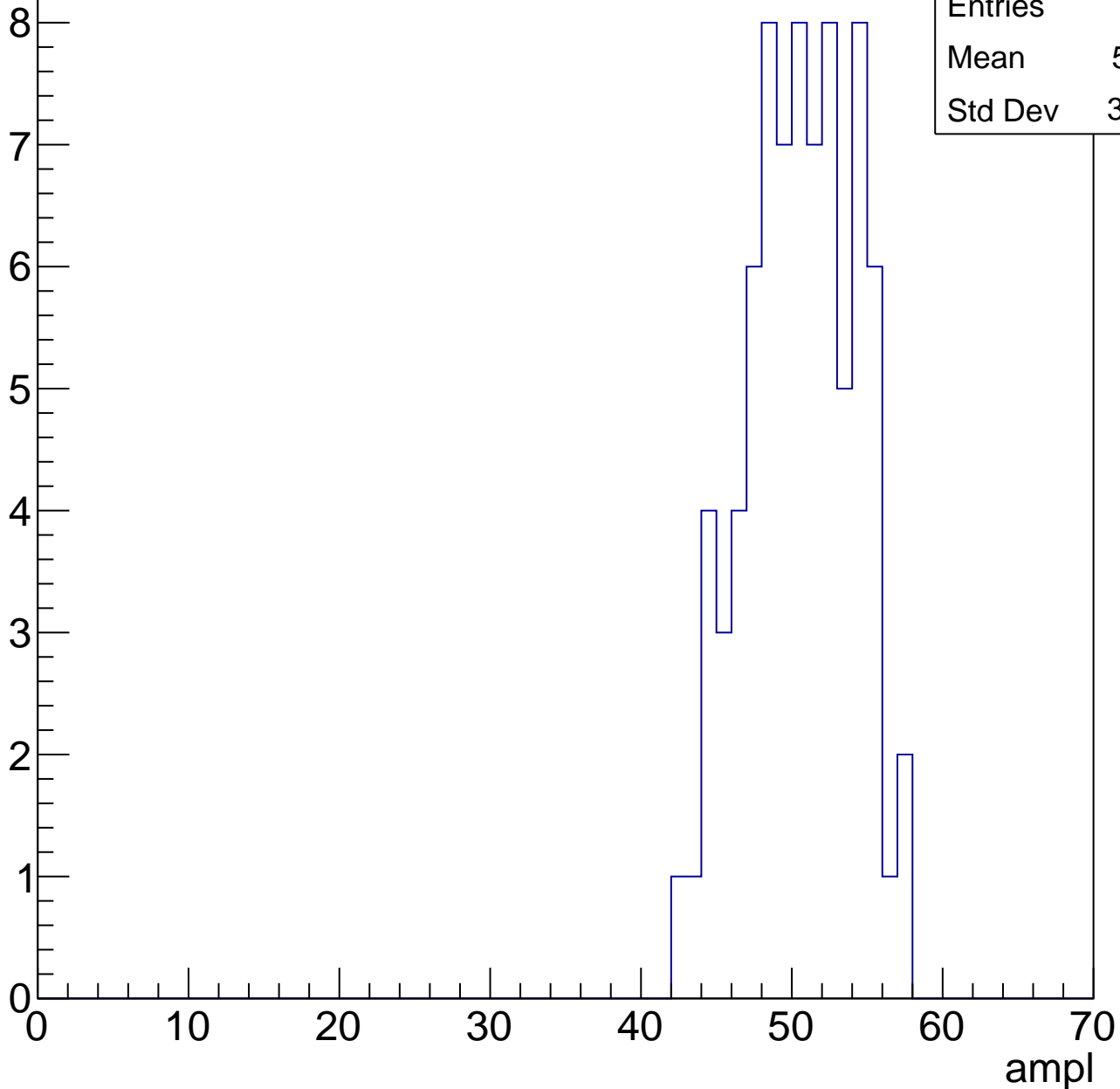


# B0L000S, U10-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	50.11
Std Dev	3.529

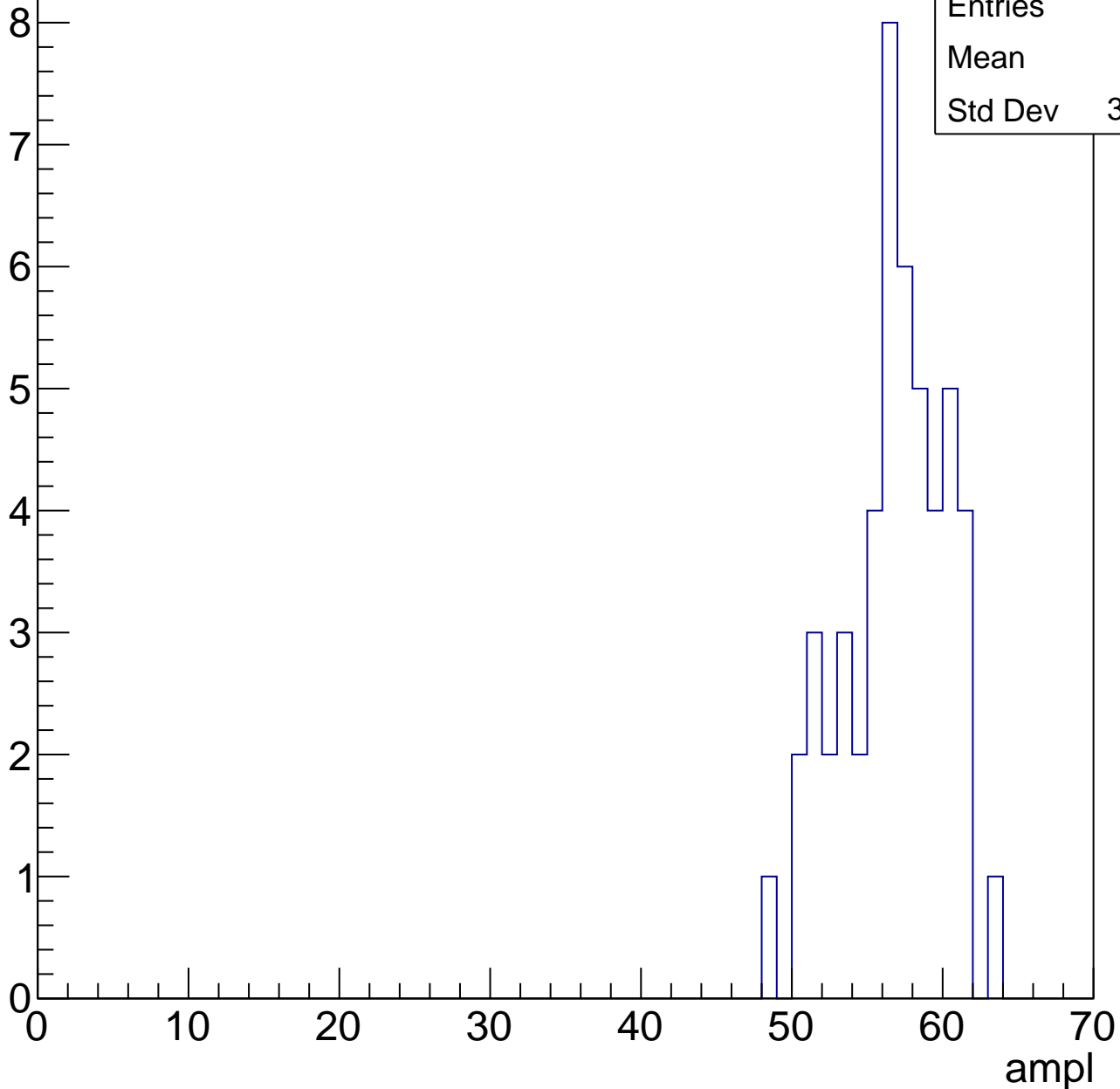


# B0L000S, U10-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.3
Std Dev	3.372

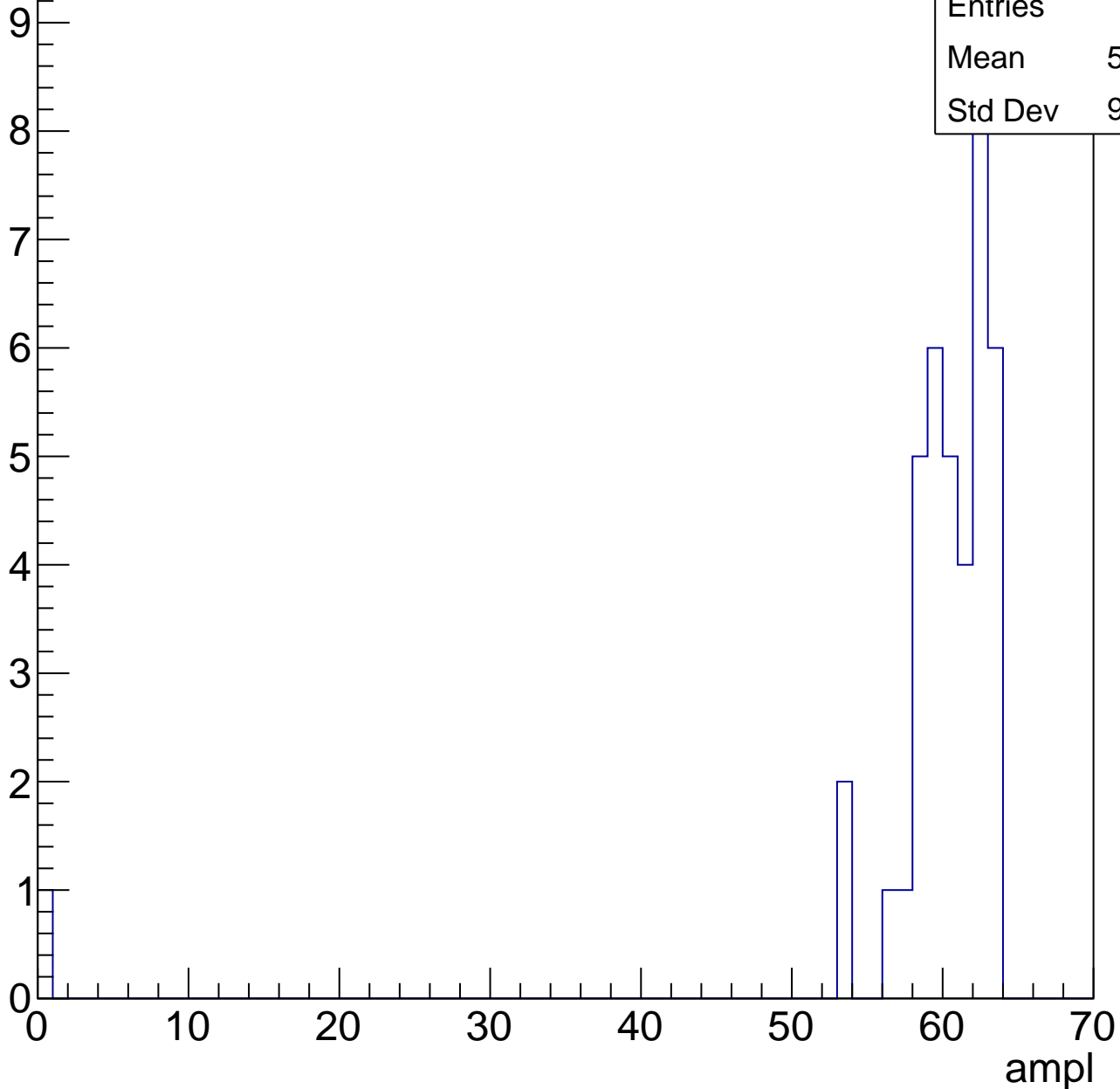


# B0L000S, U10-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

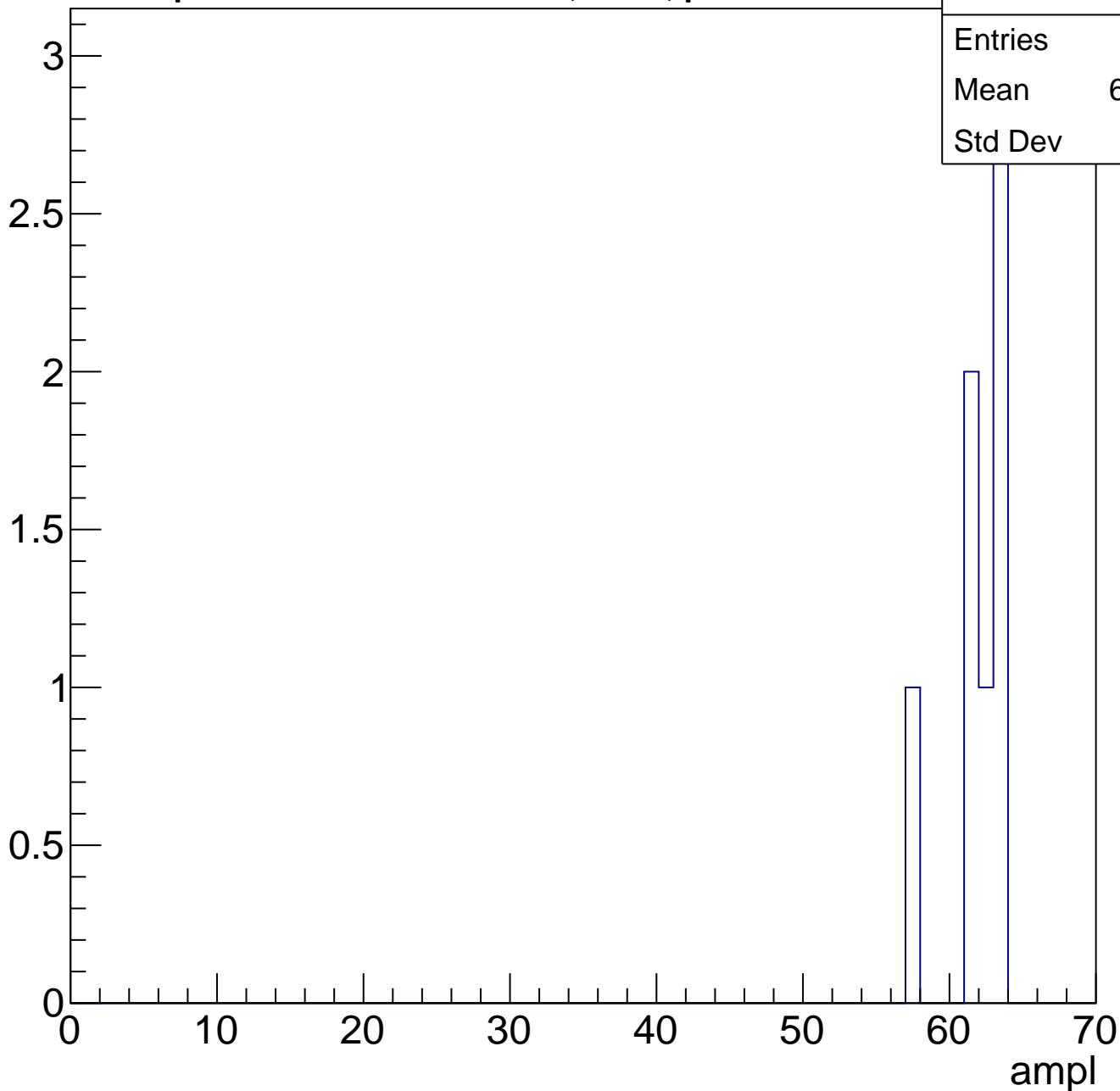
Entries	40
Mean	58.58
Std Dev	9.698



# B0L000S, U10-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch122, adc0

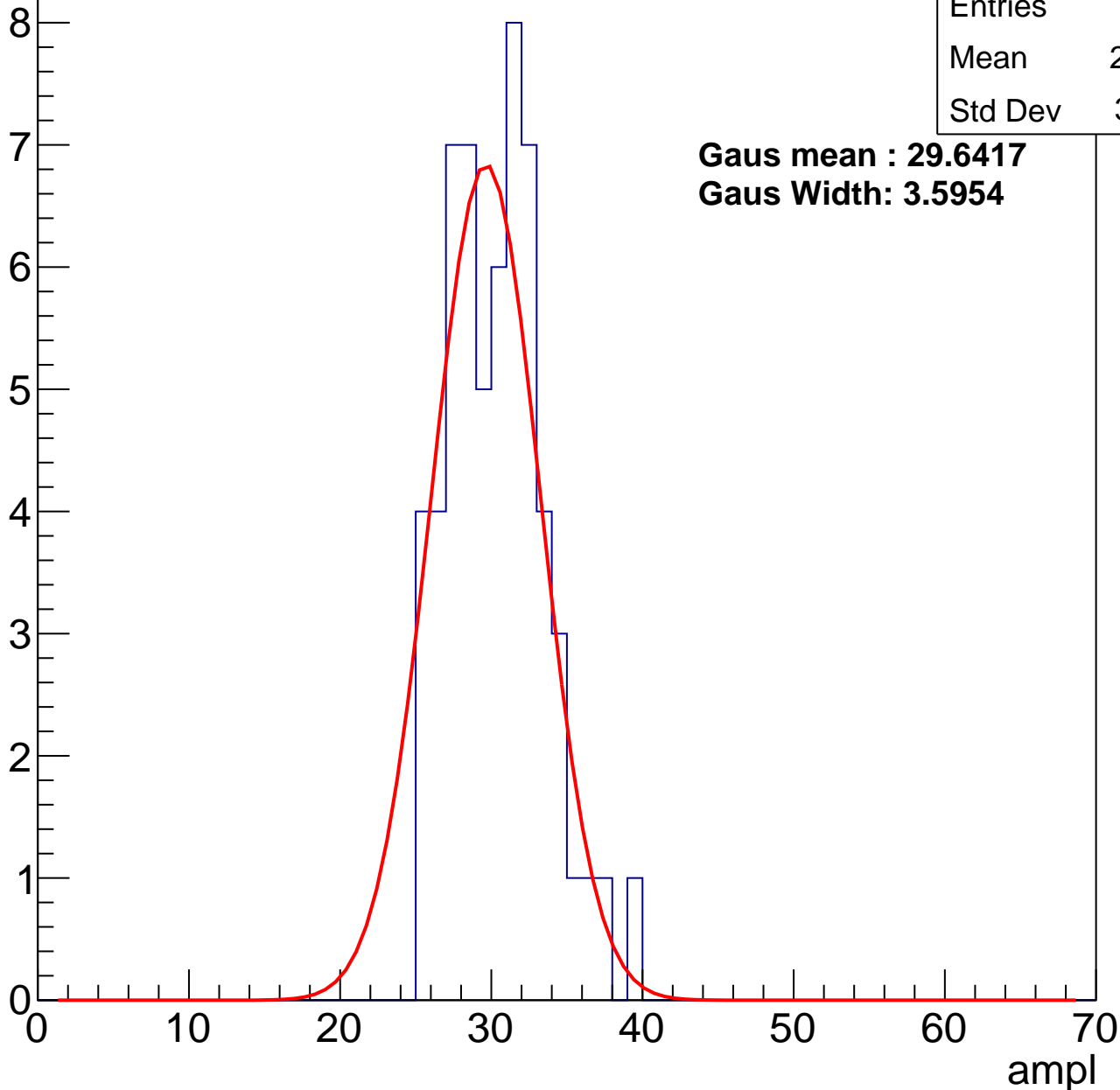
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.95
Std Dev	3.111

**Gaus mean : 29.6417**

**Gaus Width: 3.5954**



# B0L000S, U10-ch122, adc1

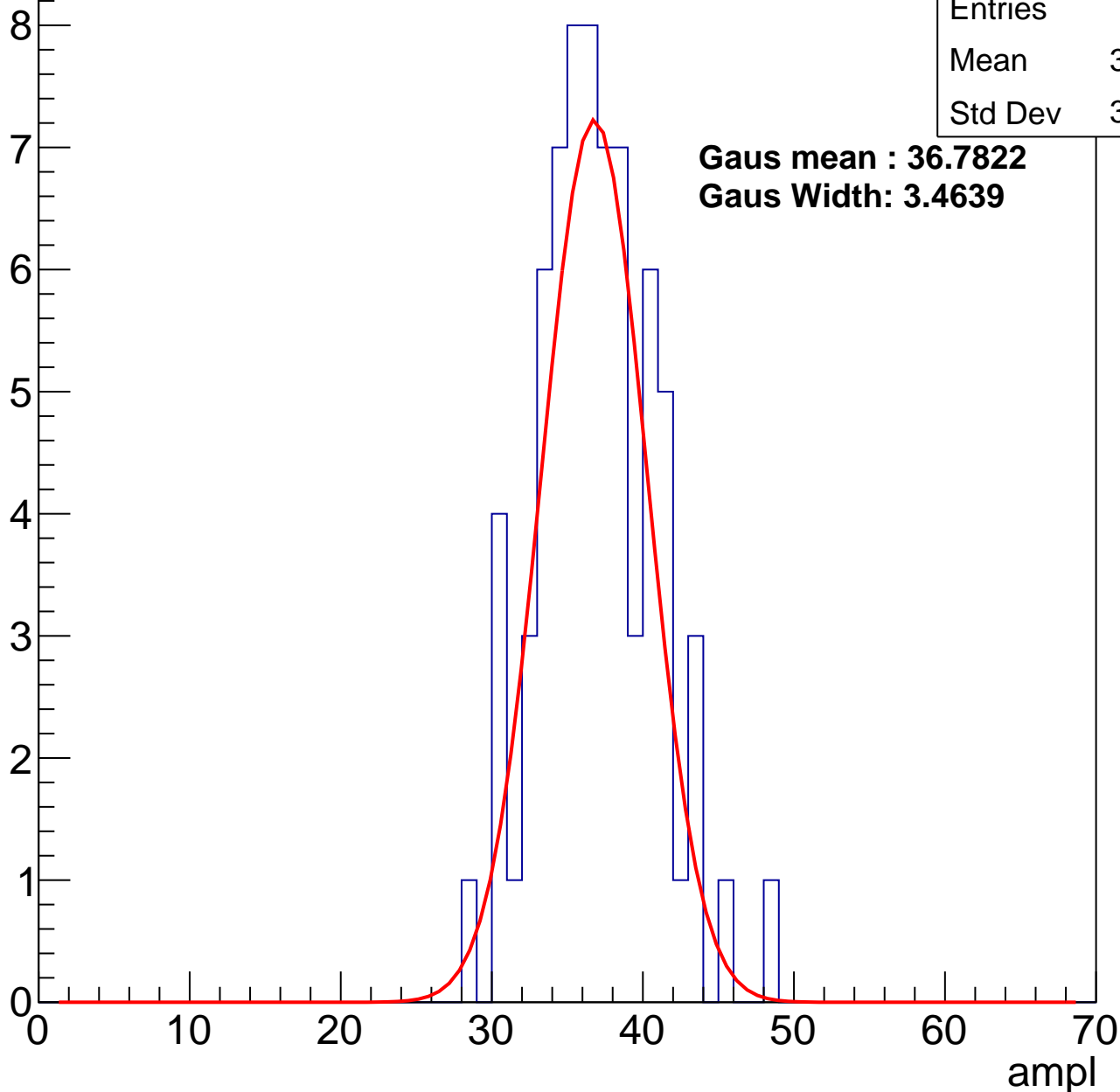
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.53
Std Dev	3.804

**Gaus mean : 36.7822**

**Gaus Width: 3.4639**



# B0L000S, U10-ch122, adc2

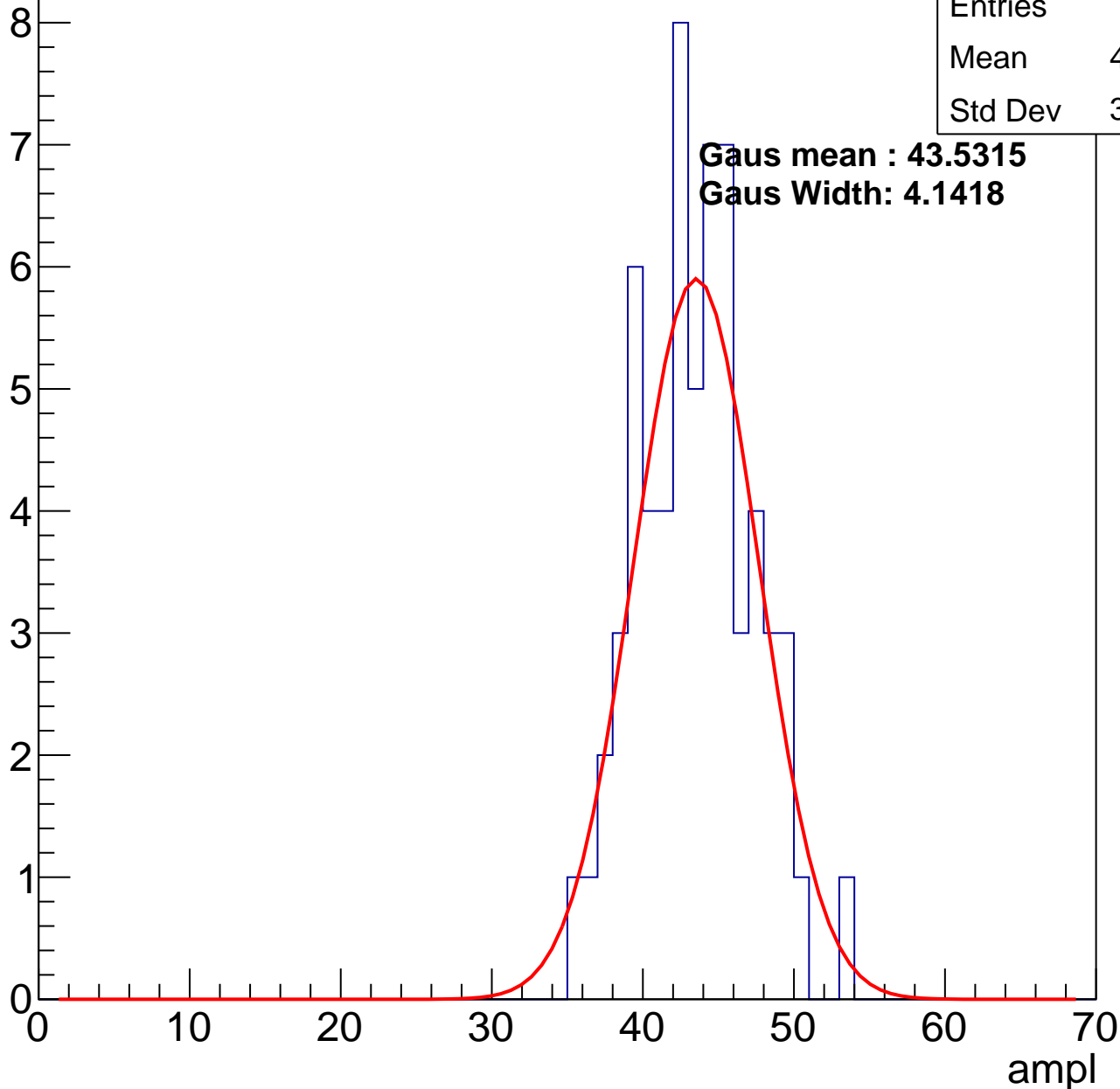
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	43.03
Std Dev	3.725

**Gaus mean : 43.5315**

**Gaus Width: 4.1418**

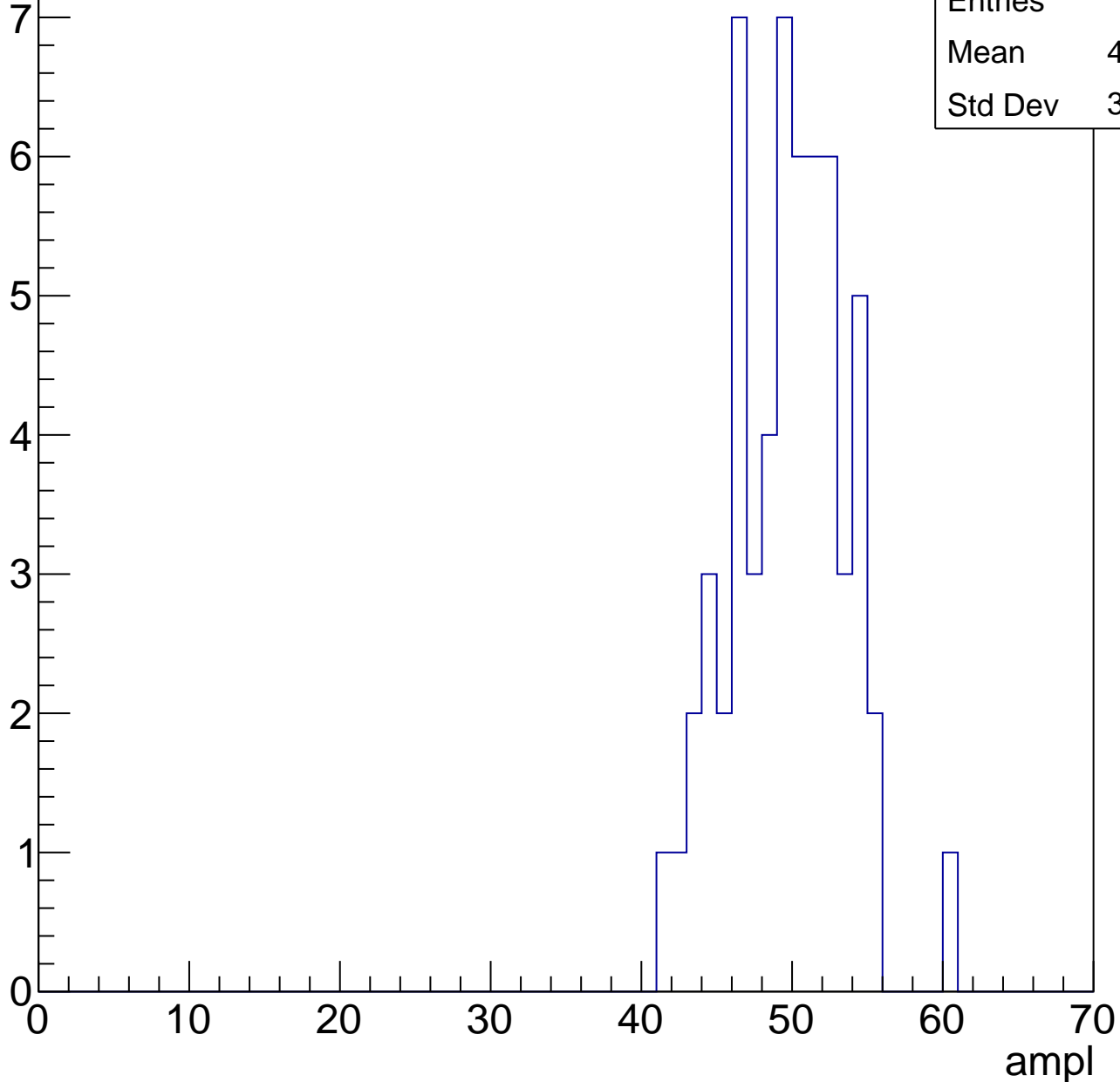


# B0L000S, U10-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.25
Std Dev	3.722

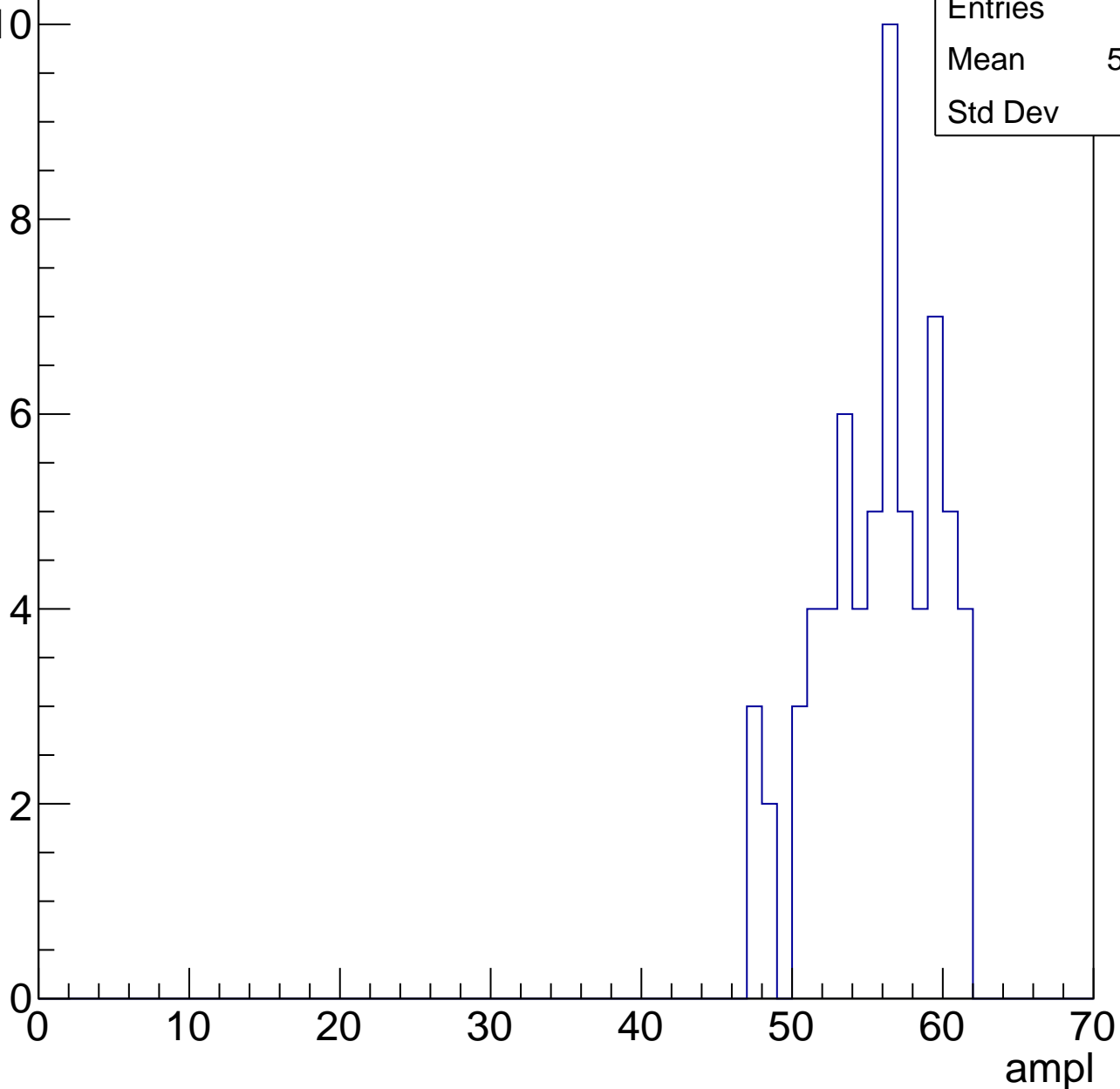


# B0L000S, U10-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	55.18
Std Dev	3.77

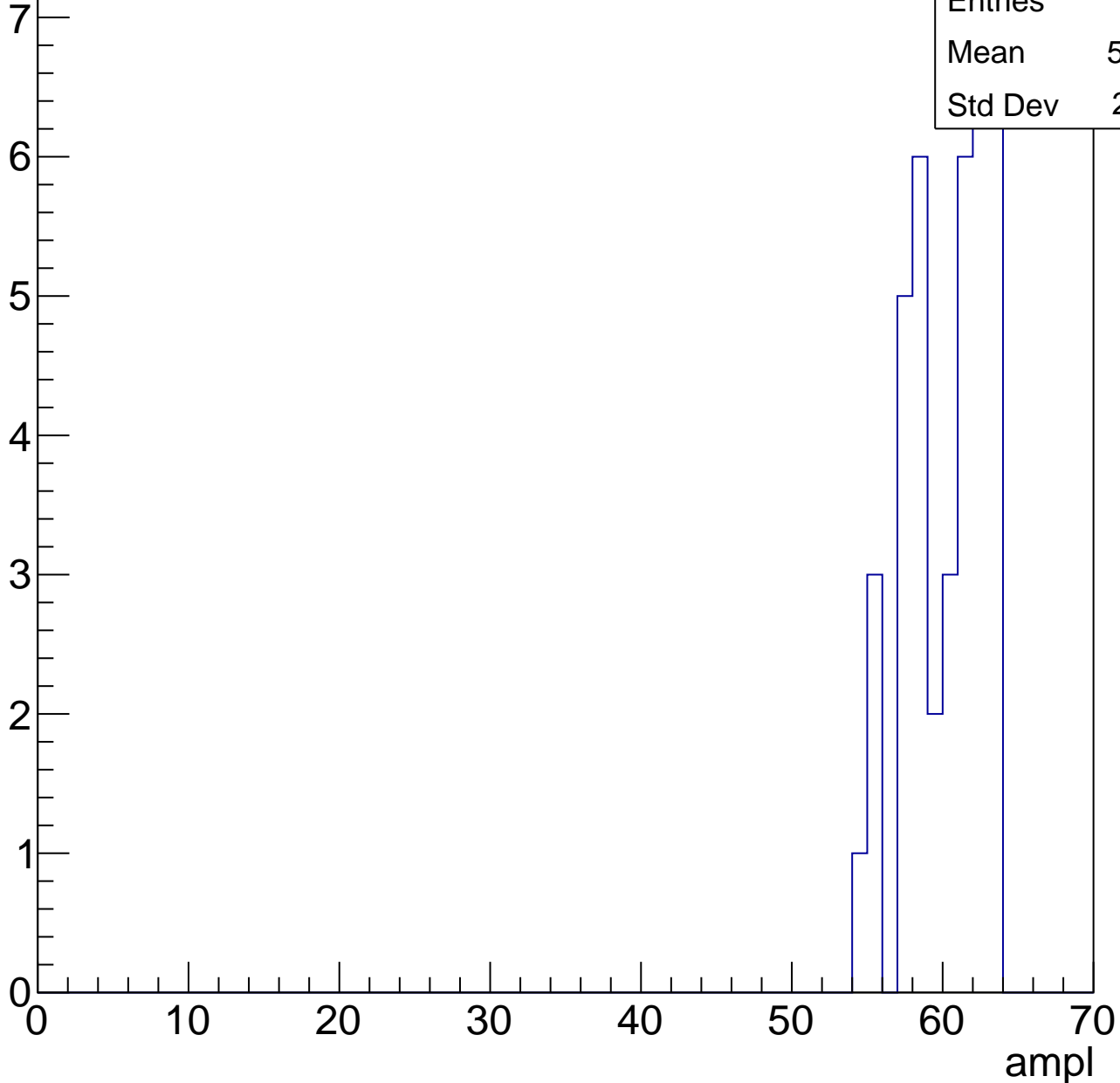


# B0L000S, U10-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	59.77
Std Dev	2.631



# B0L000S, U10-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

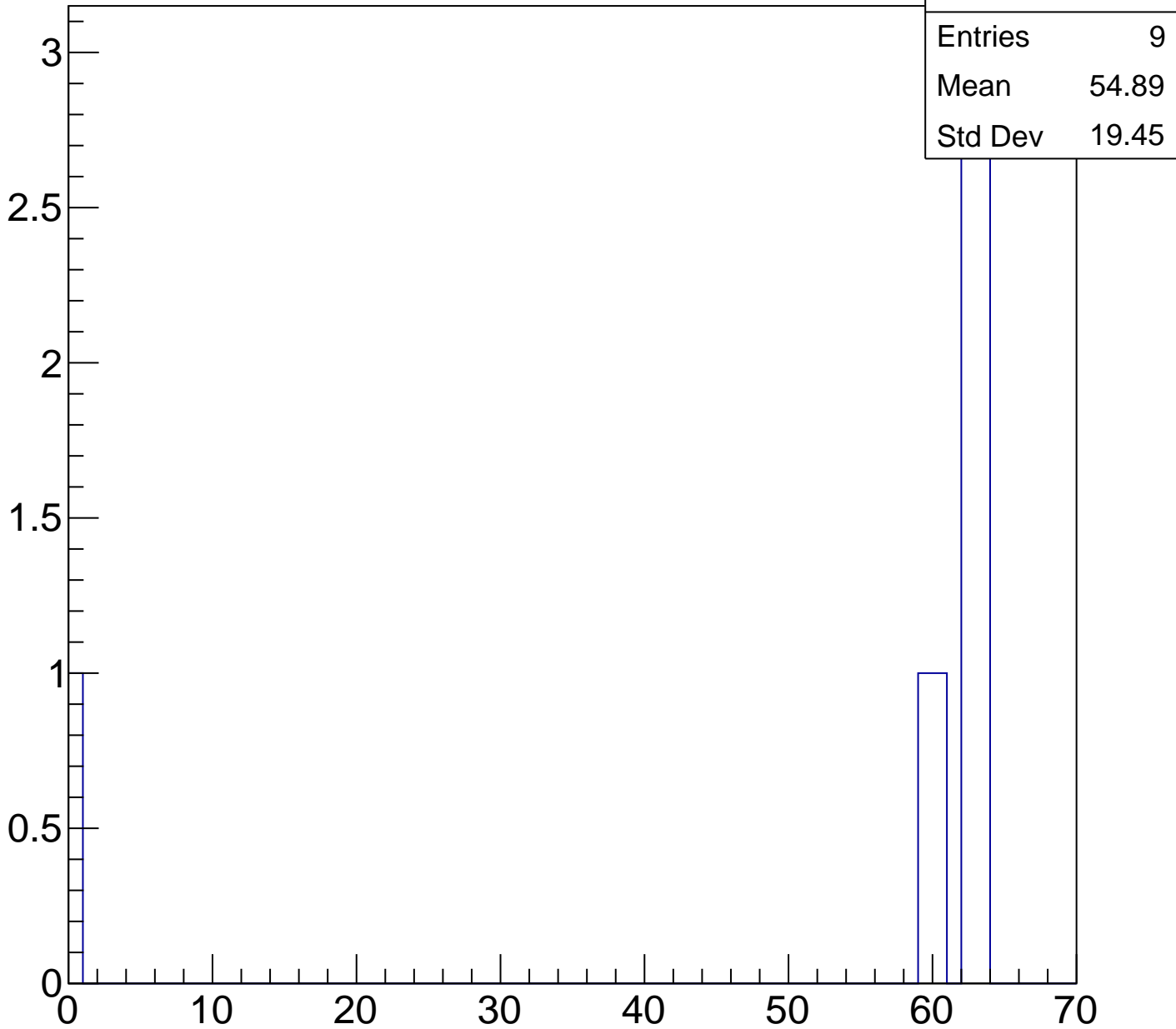
1

0.5

0

ampl

Entries	9
Mean	54.89
Std Dev	19.45





# B0L000S, U10-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U10-ch123, adc0

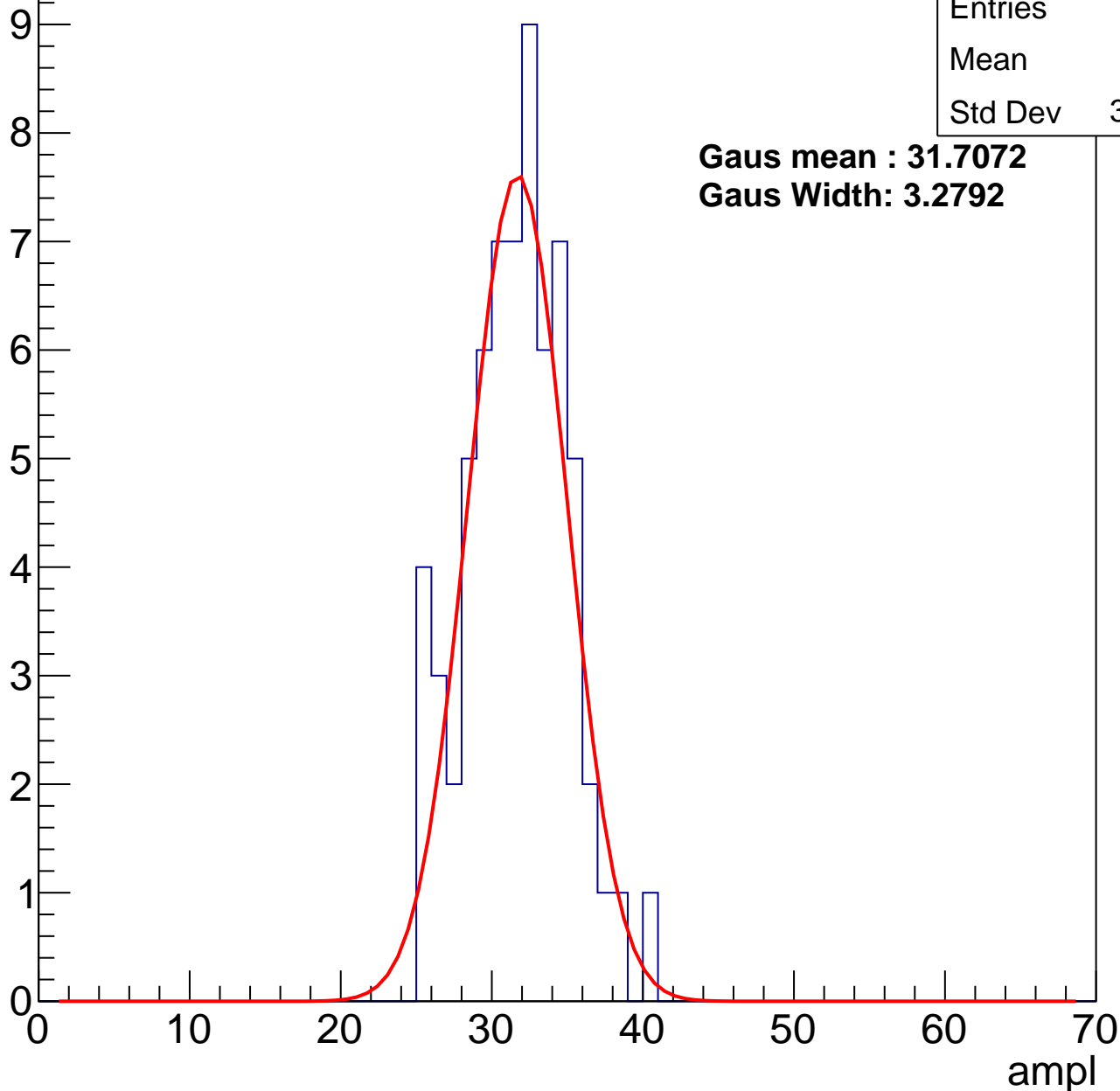
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	31.2
Std Dev	3.295

**Gaus mean : 31.7072**

**Gaus Width: 3.2792**



# B0L000S, U10-ch123, adc1

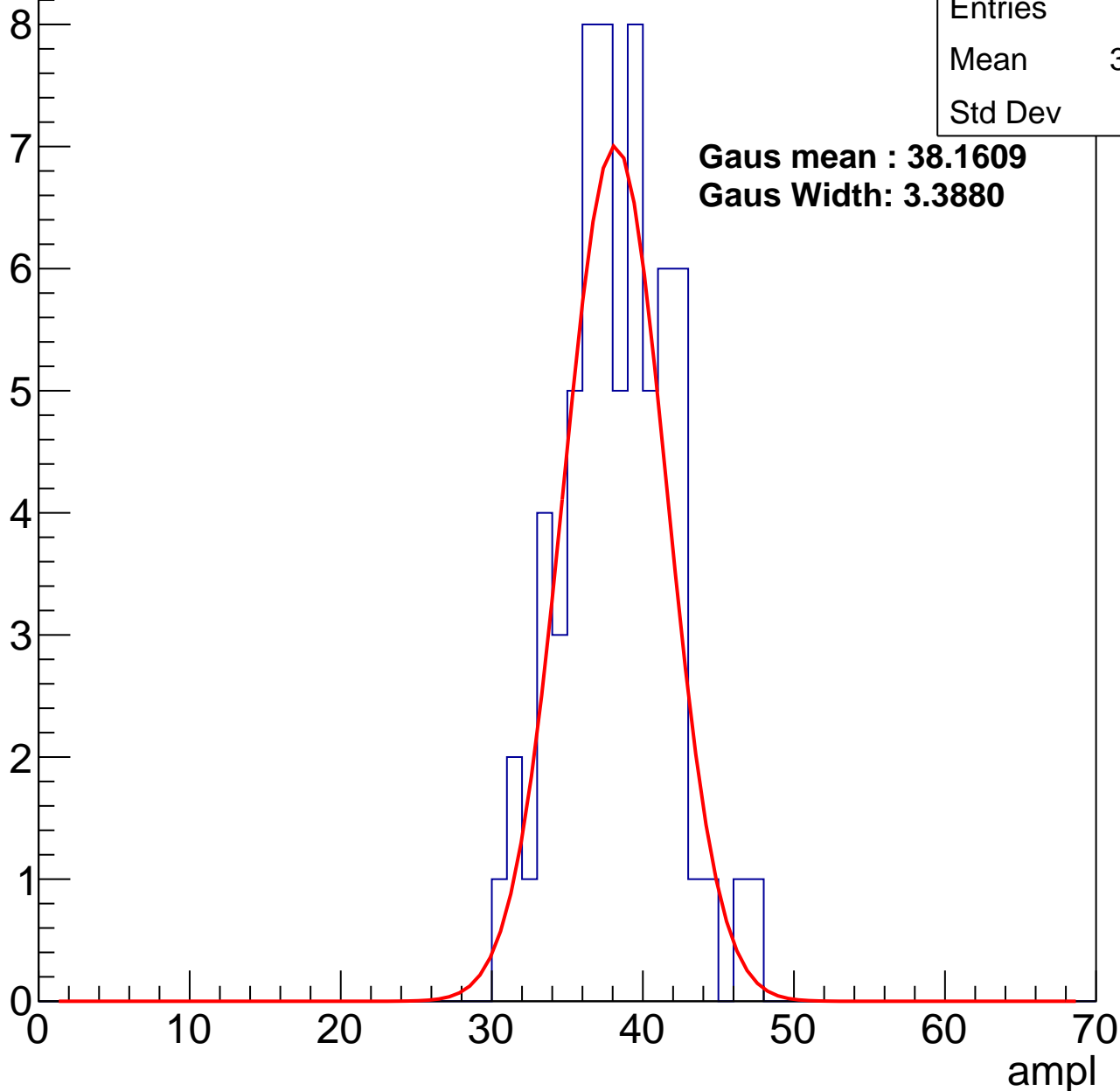
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.83
Std Dev	3.51

**Gaus mean : 38.1609**

**Gaus Width: 3.3880**



# B0L000S, U10-ch123, adc2

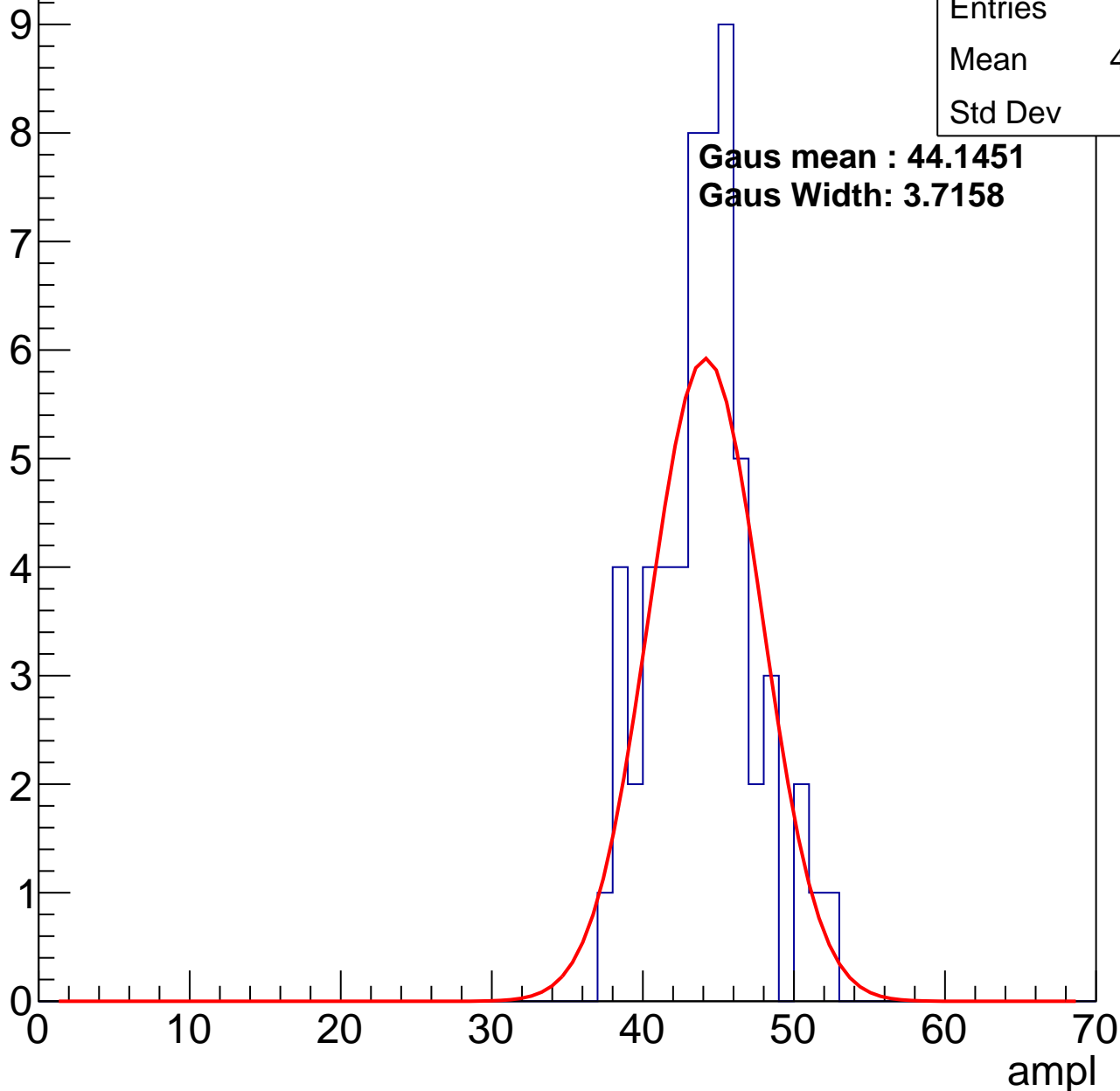
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.64
Std Dev	3.32

**Gaus mean : 44.1451**

**Gaus Width: 3.7158**

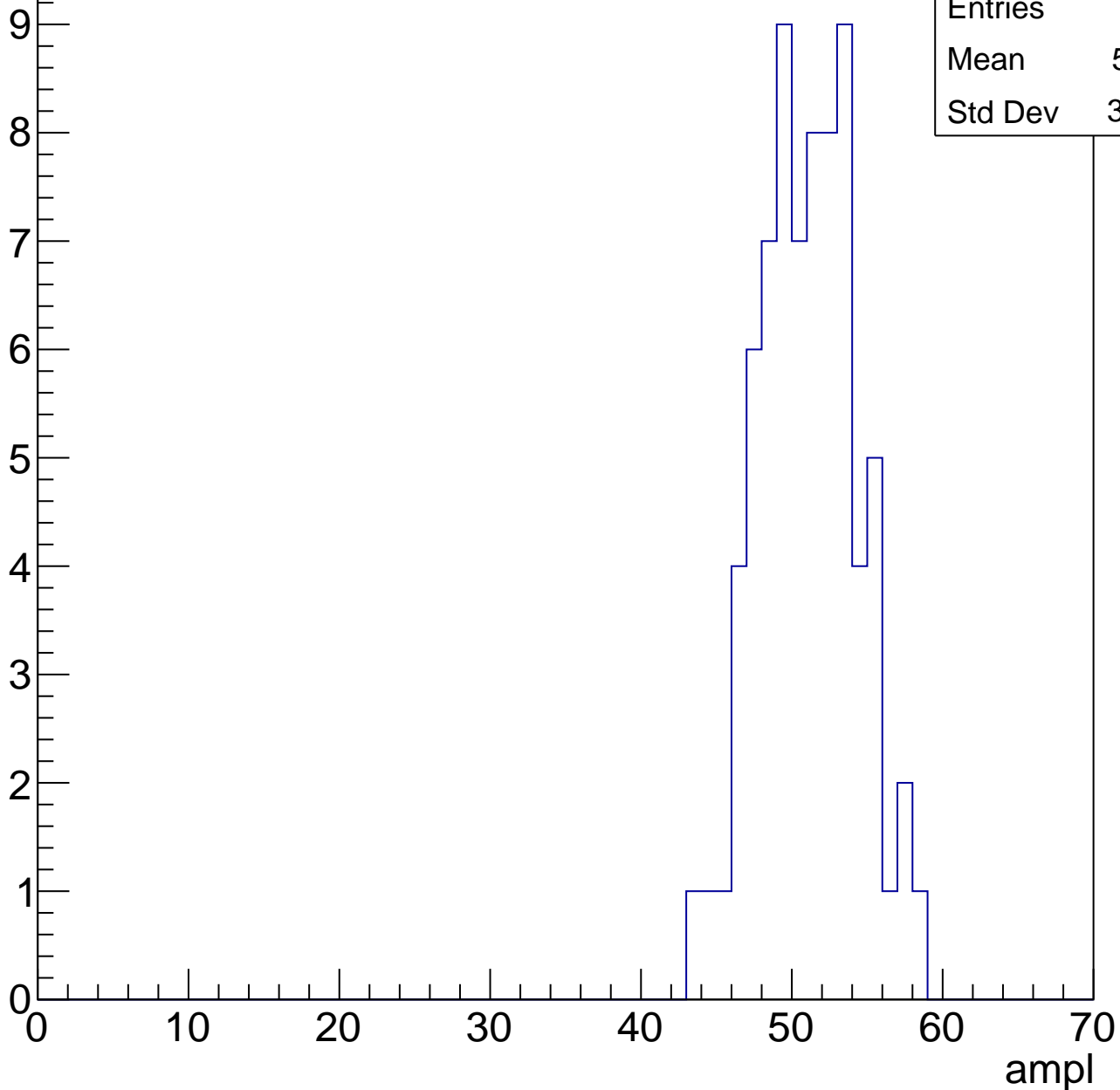


# B0L000S, U10-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	50.61
Std Dev	3.174

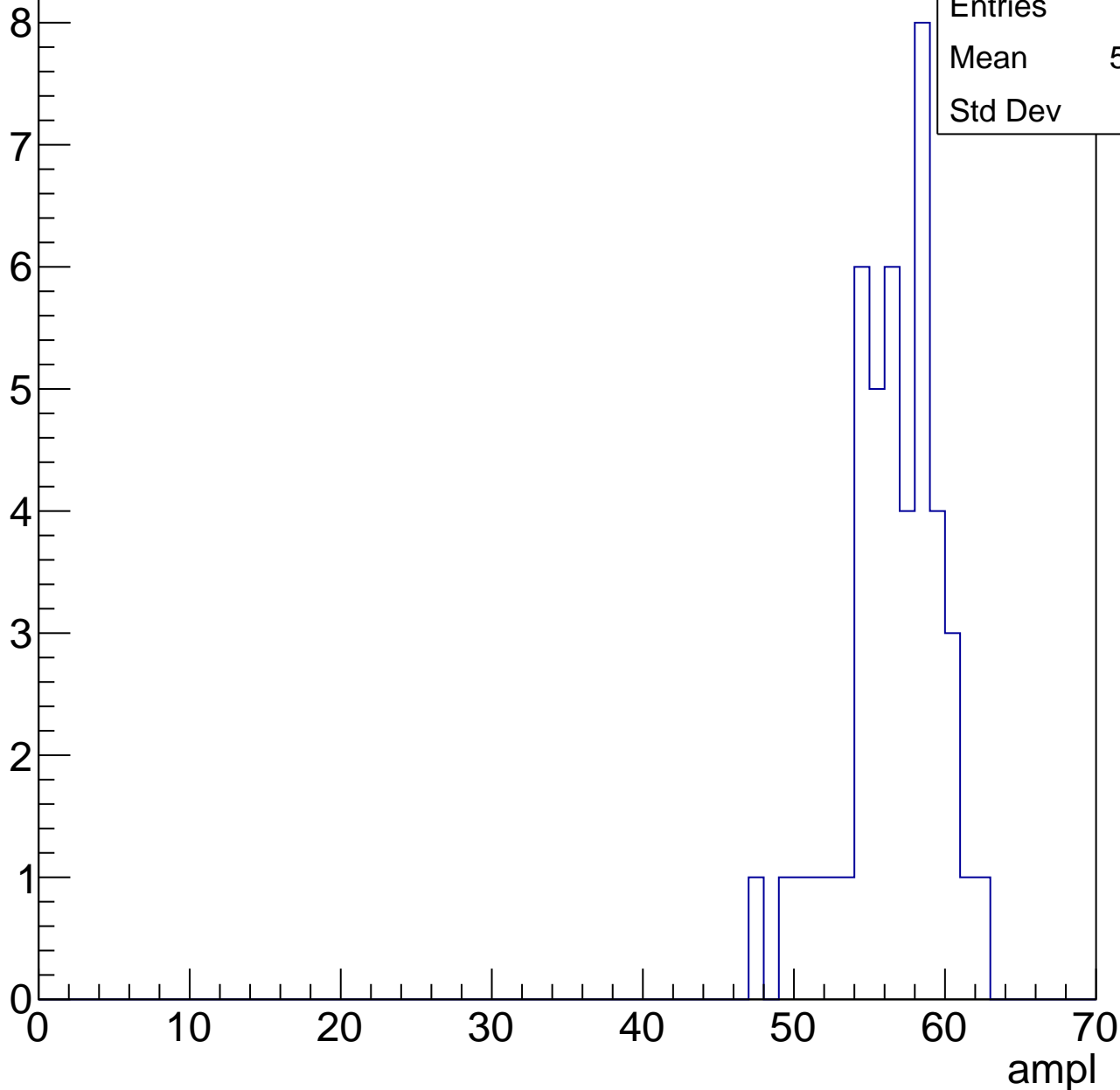


# B0L000S, U10-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	56.09
Std Dev	3.11

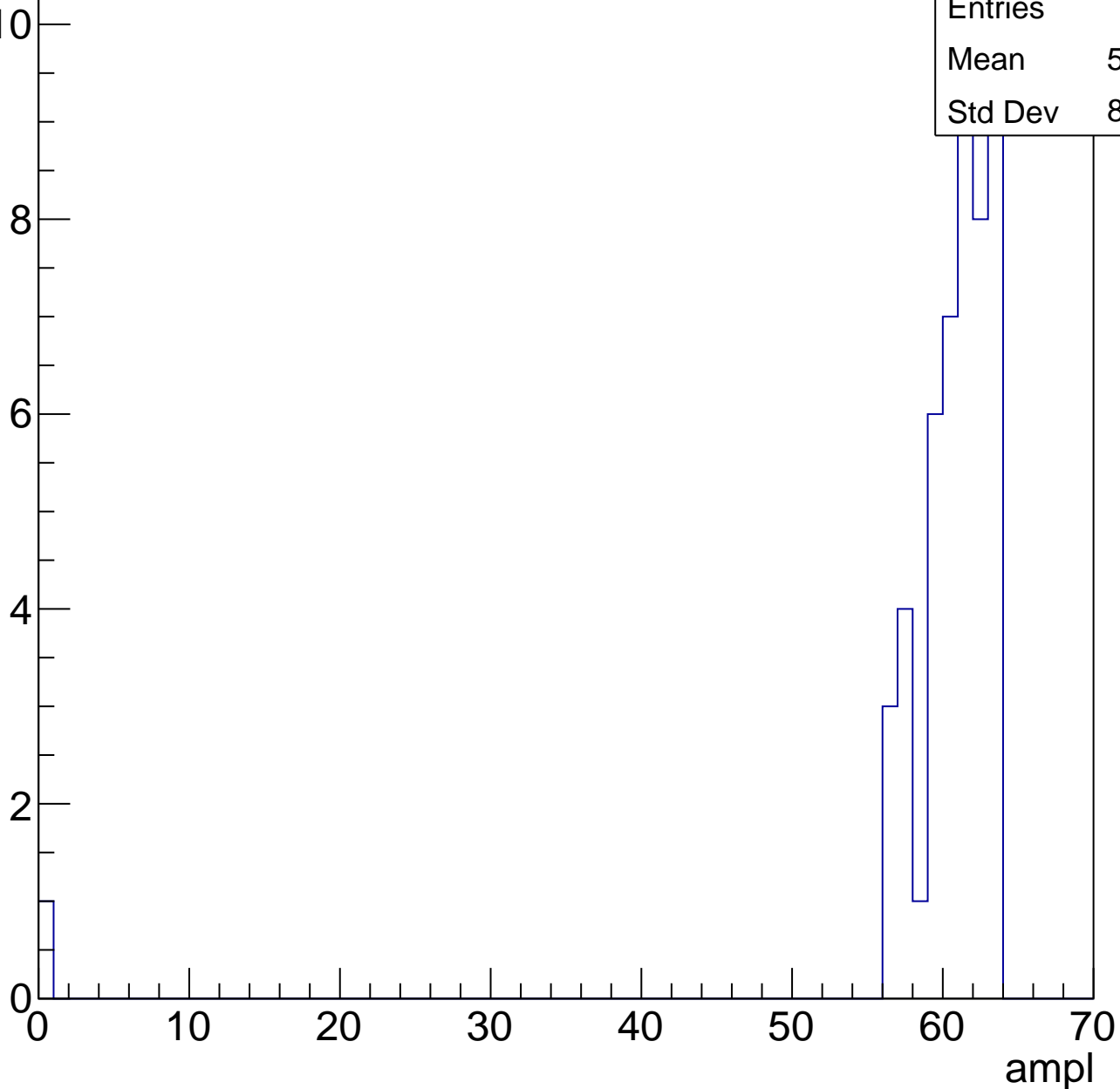


# B0L000S, U10-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	59.24
Std Dev	8.803



# B0L000S, U10-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U10-ch124, adc0

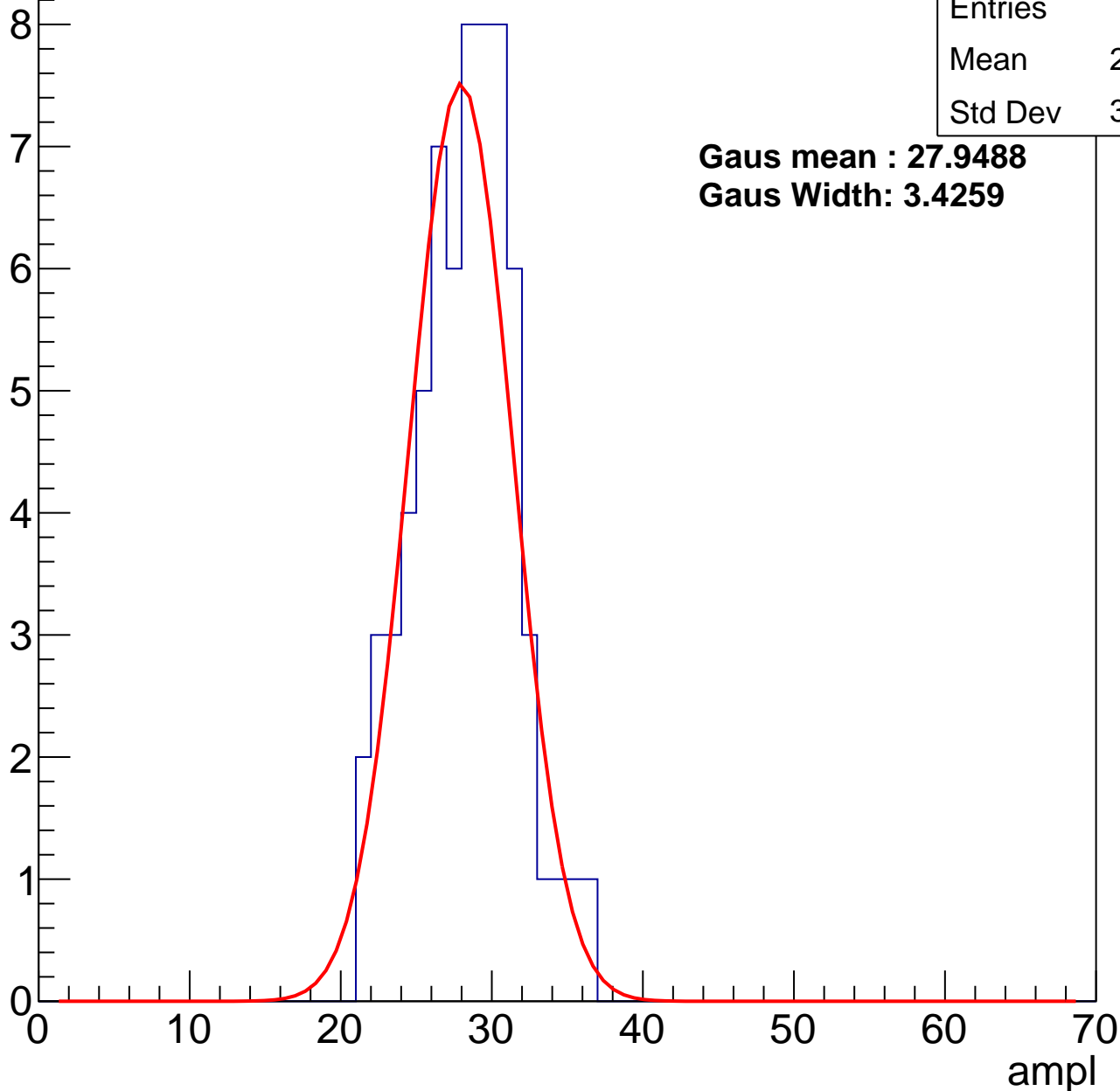
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	27.73
Std Dev	3.312

**Gaus mean : 27.9488**

**Gaus Width: 3.4259**



# B0L000S, U10-ch124, adc1

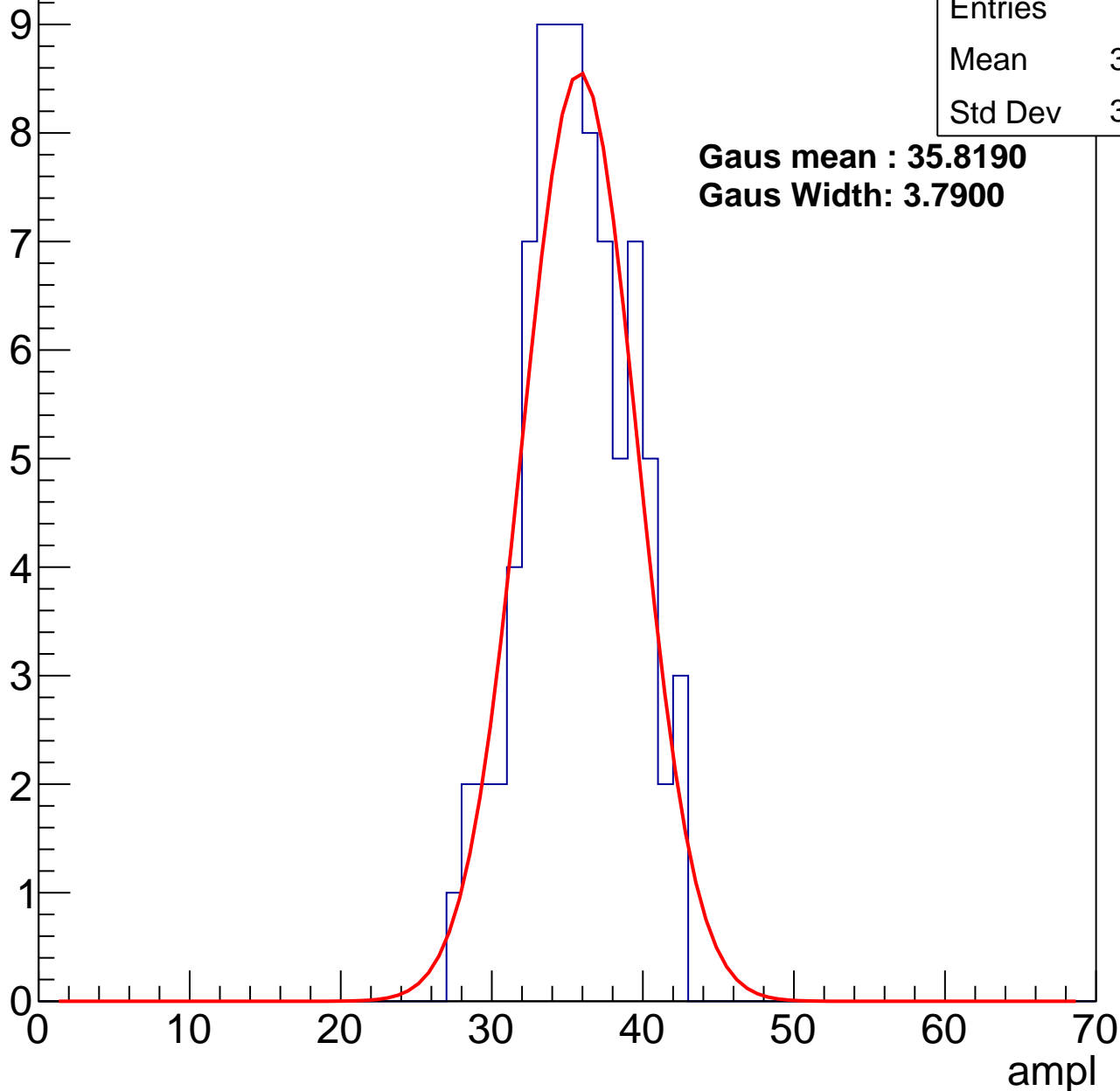
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	35.18
Std Dev	3.475

**Gaus mean : 35.8190**

**Gaus Width: 3.7900**



# B0L000S, U10-ch124, adc2

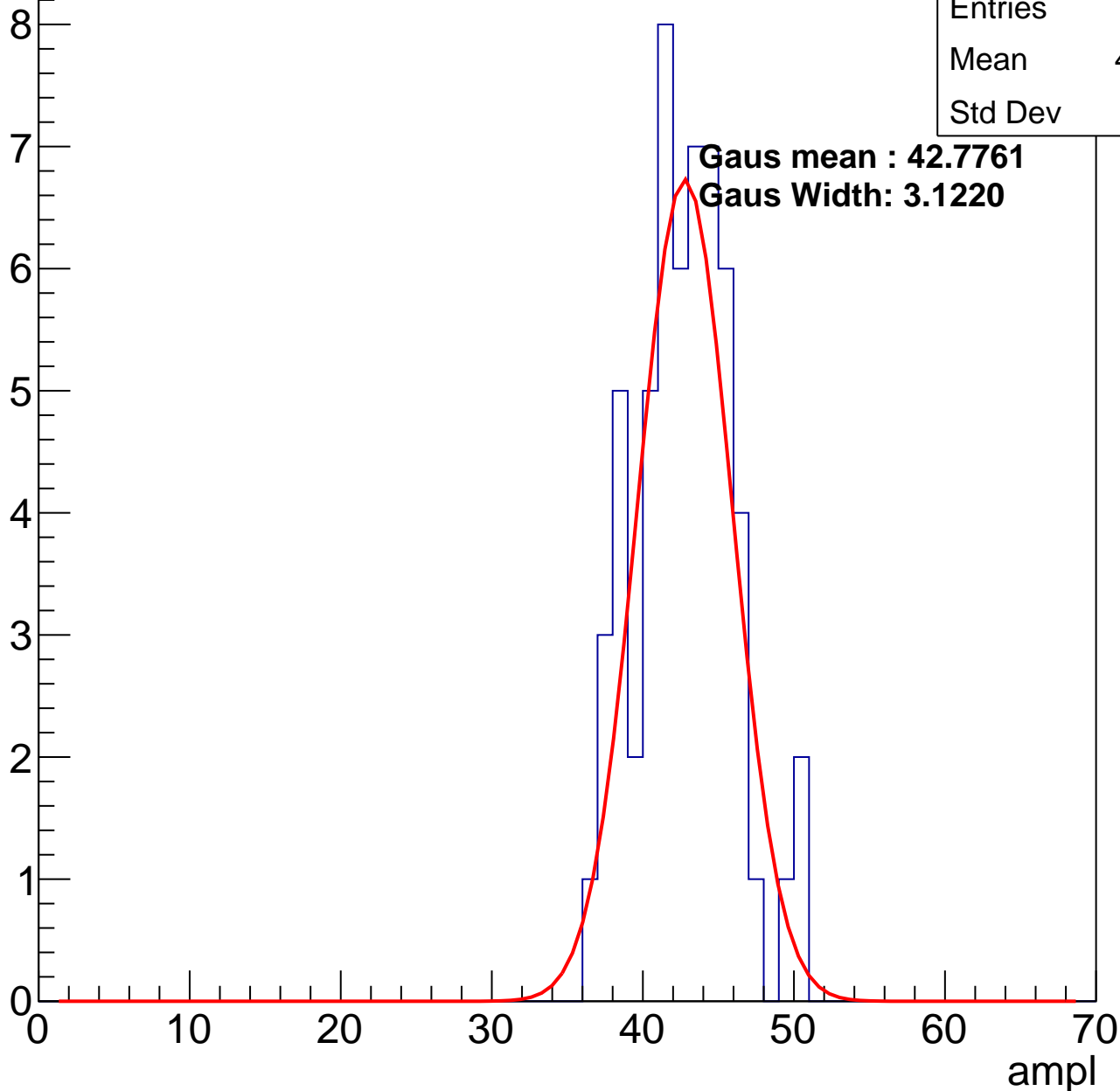
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.31
Std Dev	3.18

**Gaus mean : 42.7761**

**Gaus Width: 3.1220**

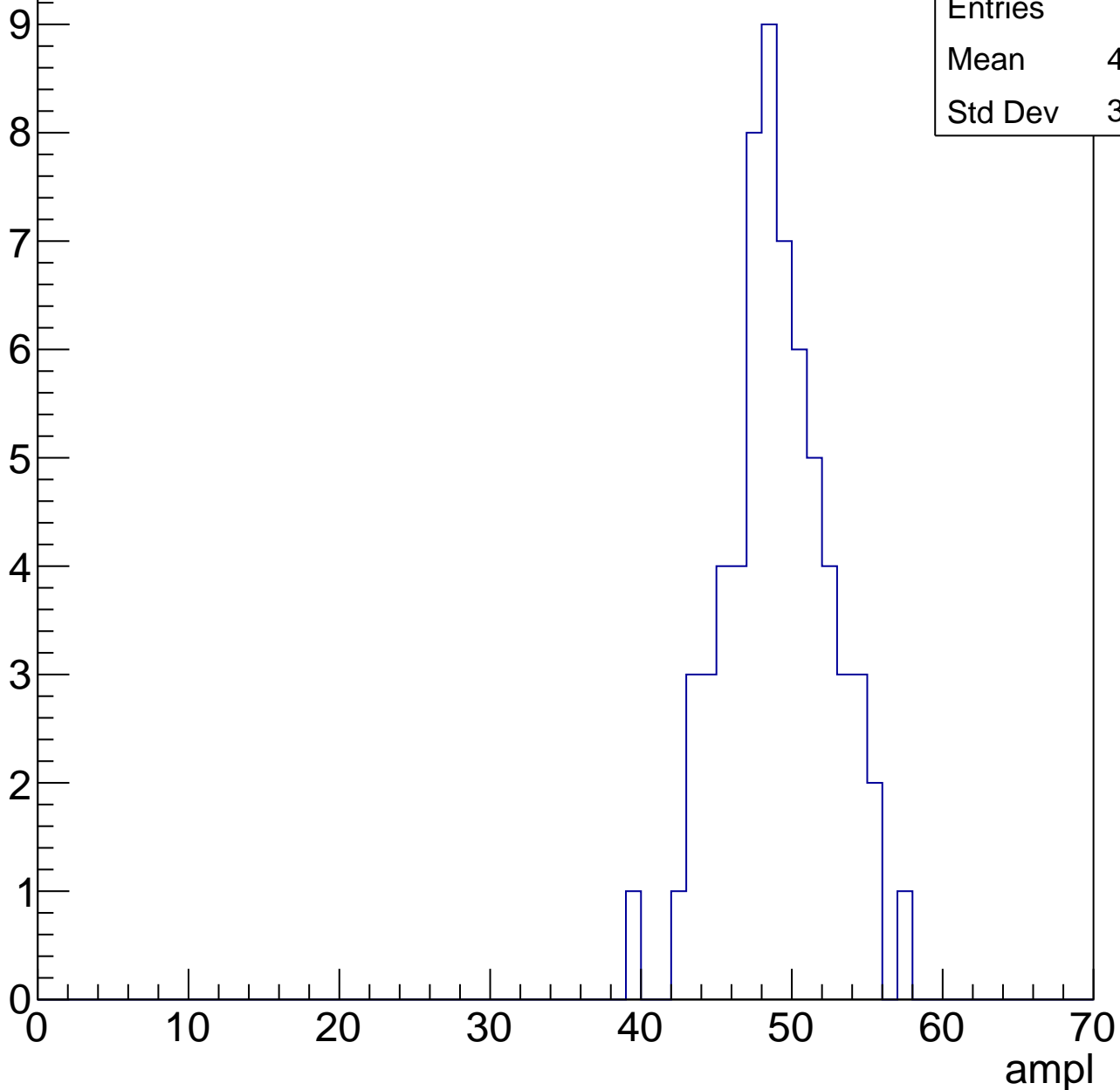


# B0L000S, U10-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.56
Std Dev	3.495

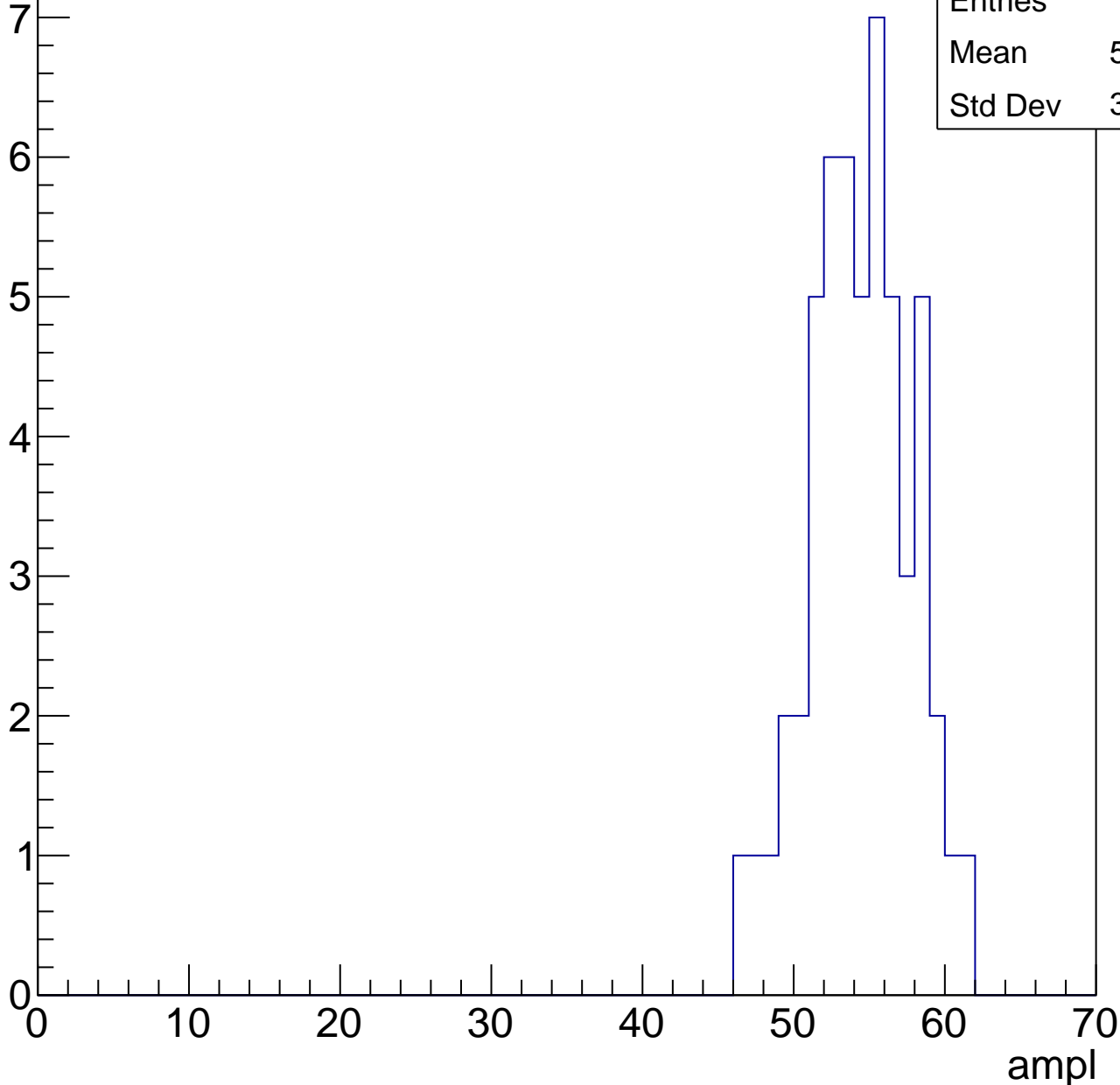


# B0L000S, U10-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	53.94
Std Dev	3.293

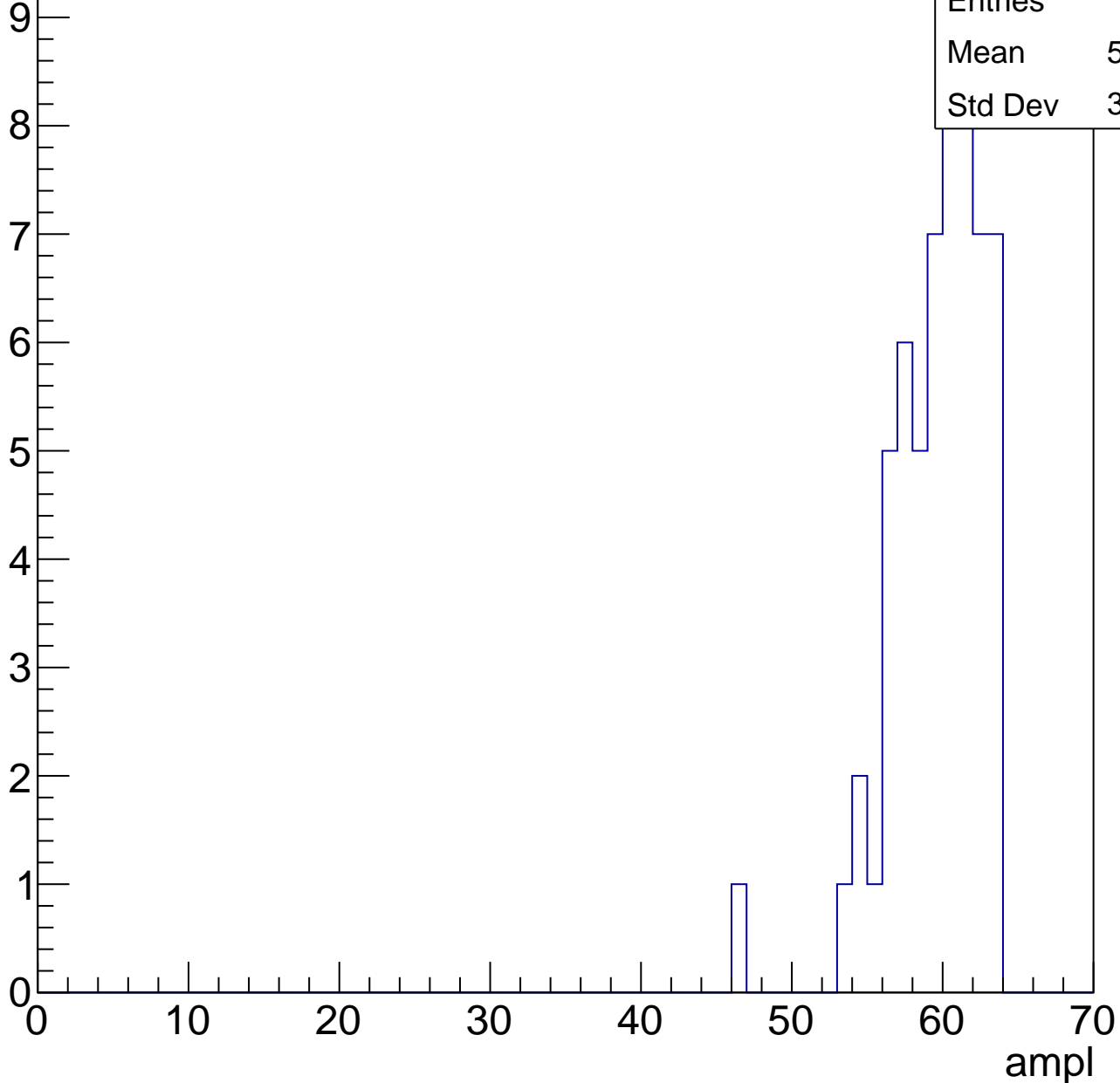


# B0L000S, U10-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	59.17
Std Dev	3.087



# B0L000S, U10-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

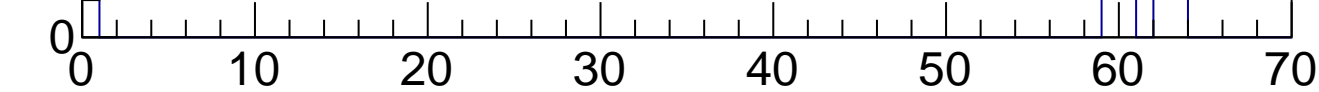
1

0.5

0

ampl

Entries	9
Mean	54.89
Std Dev	19.45





# B0L000S, U10-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch125, adc0

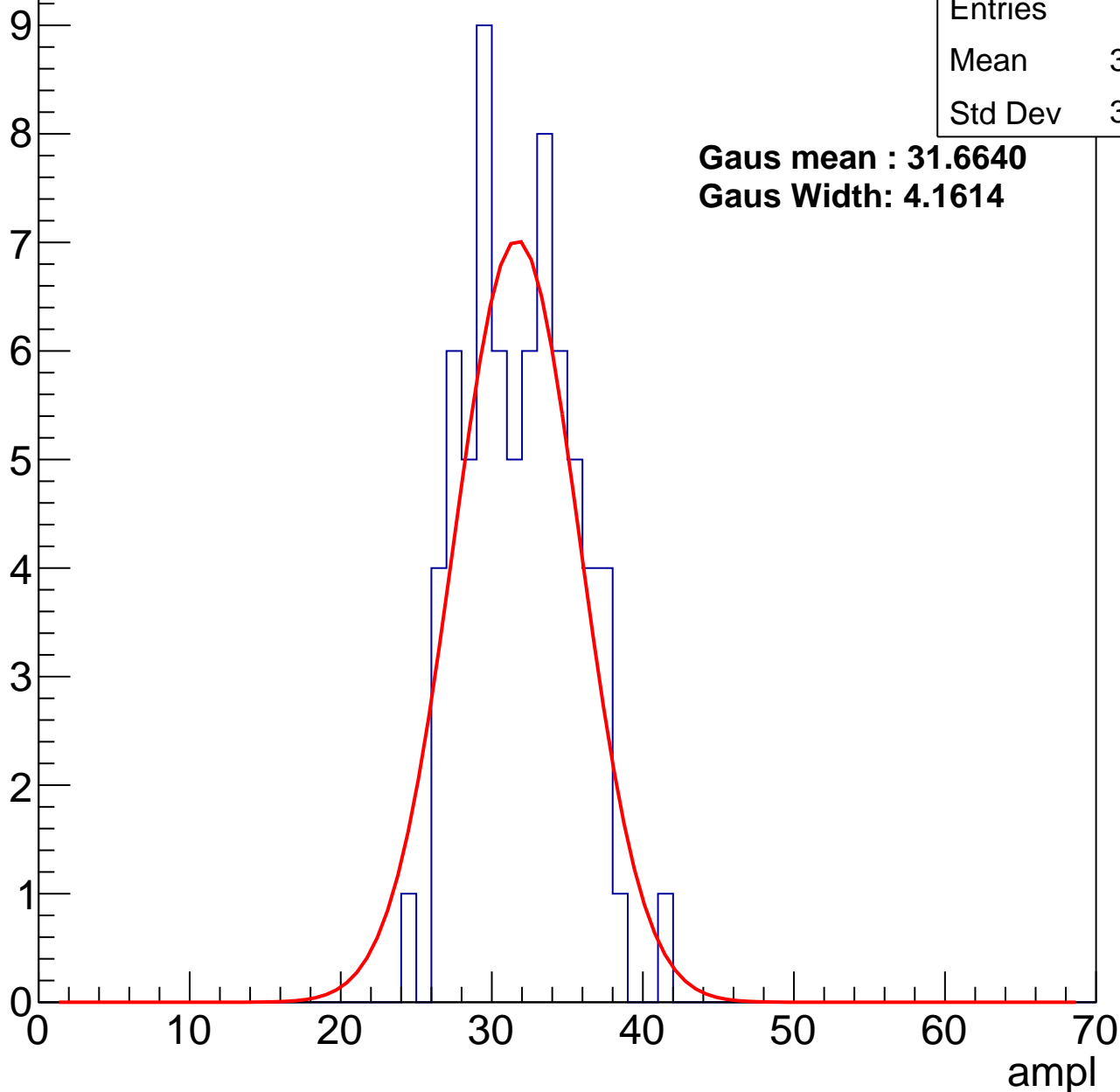
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	31.44
Std Dev	3.527

**Gaus mean : 31.6640**

**Gaus Width: 4.1614**



# B0L000S, U10-ch125, adc1

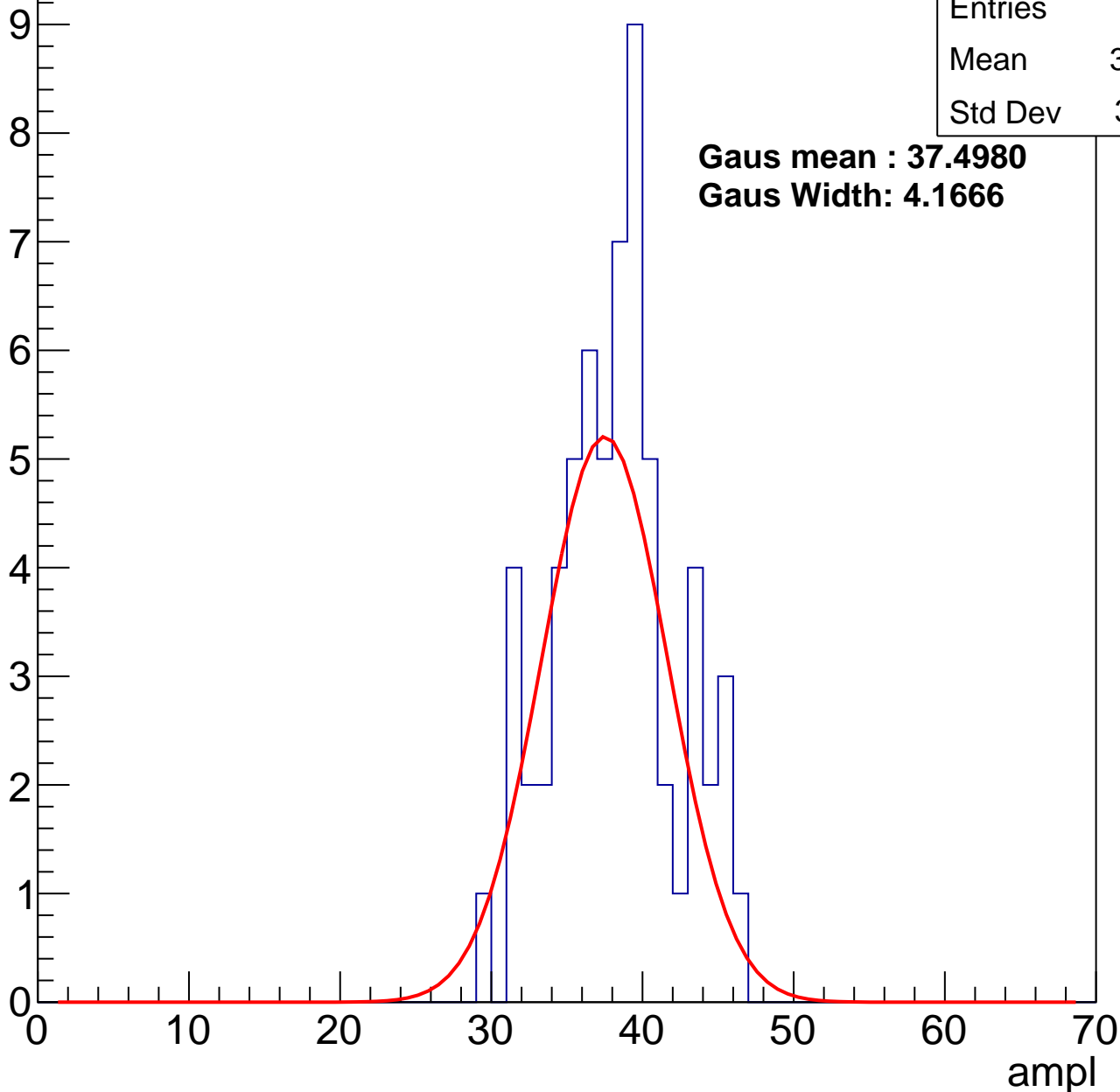
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.73
Std Dev	3.941

**Gaus mean : 37.4980**

**Gaus Width: 4.1666**



# B0L000S, U10-ch125, adc2

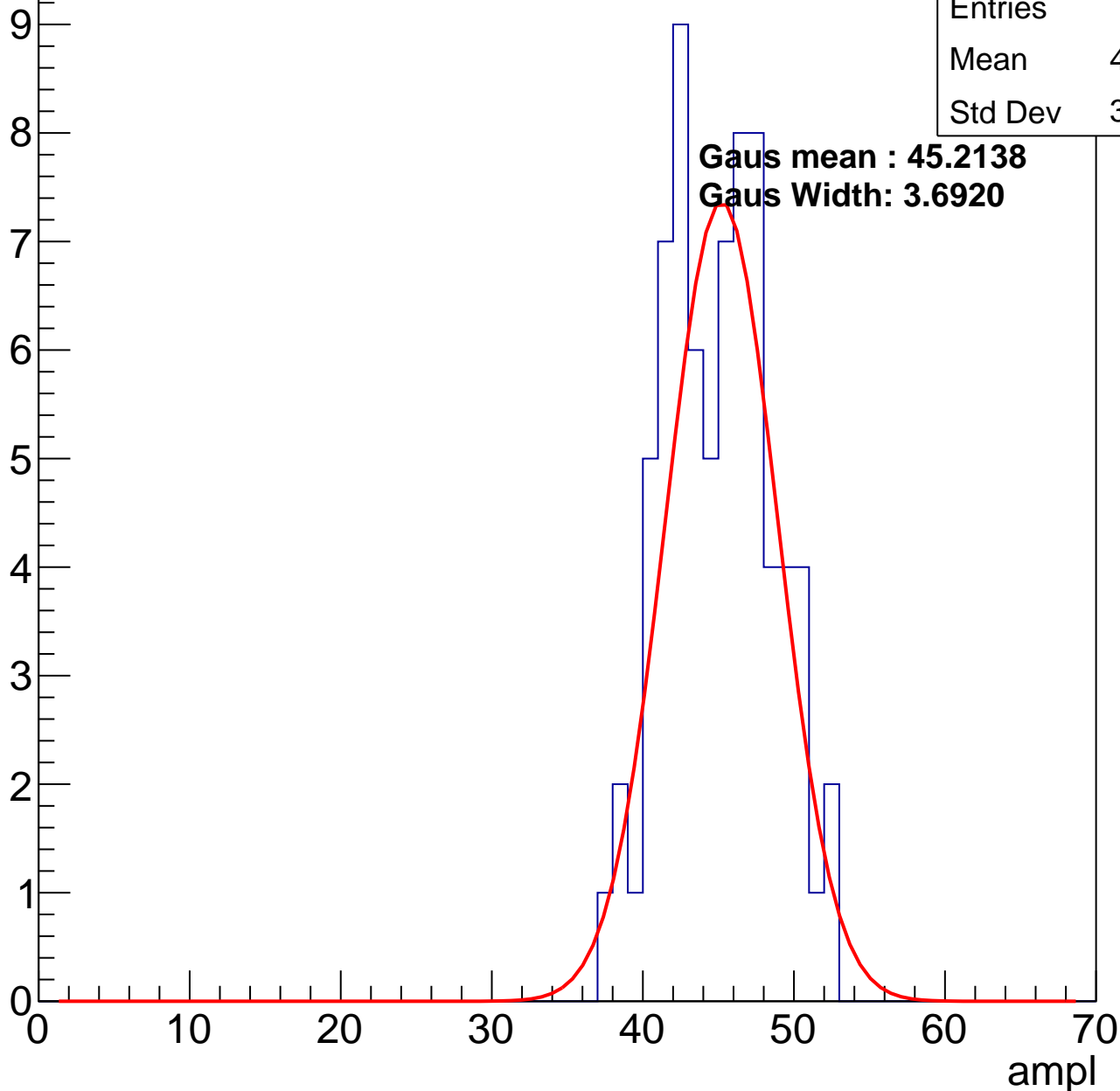
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	44.55
Std Dev	3.496

**Gaus mean : 45.2138**

**Gaus Width: 3.6920**

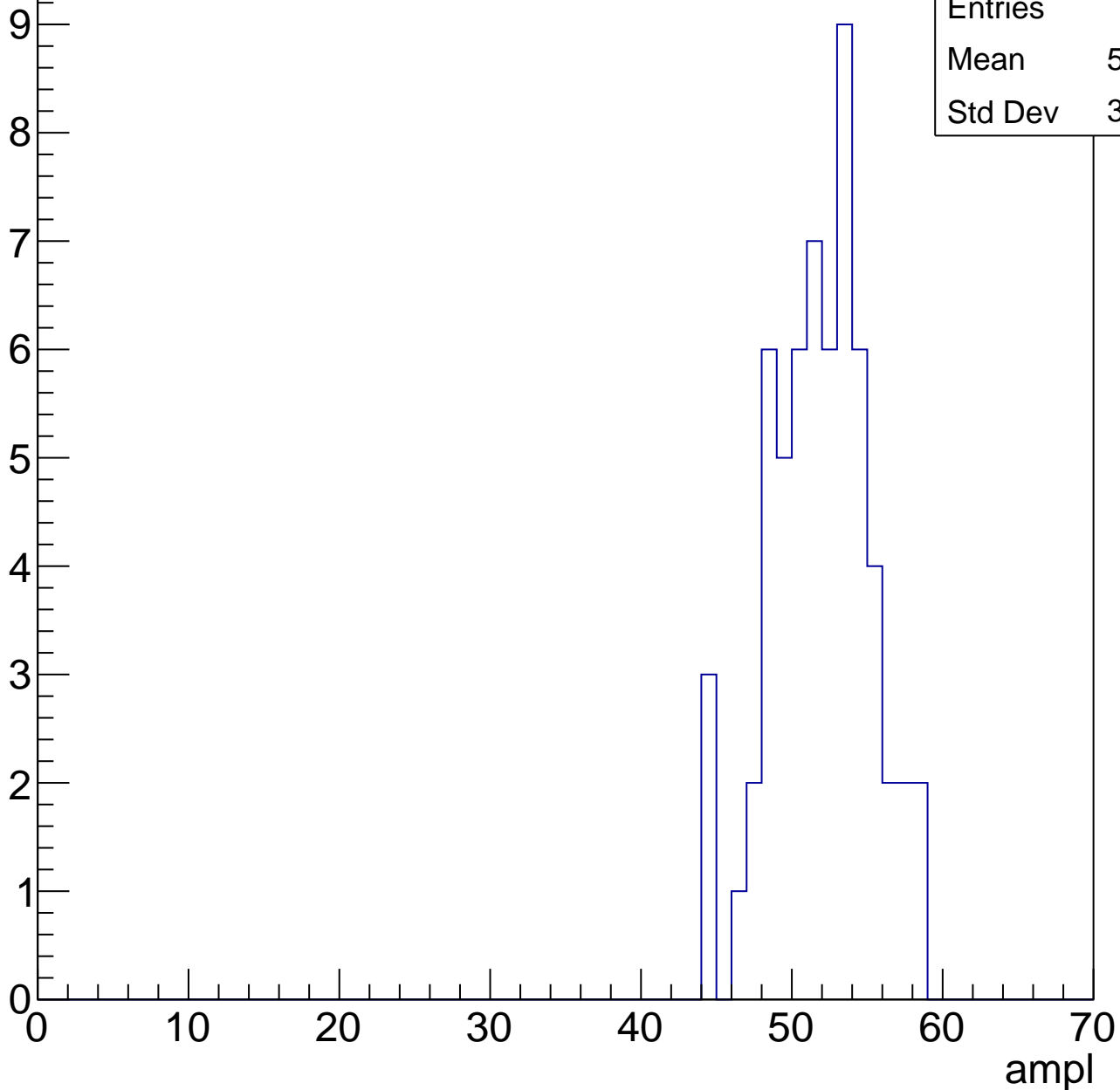


# B0L000S, U10-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	51.43
Std Dev	3.272

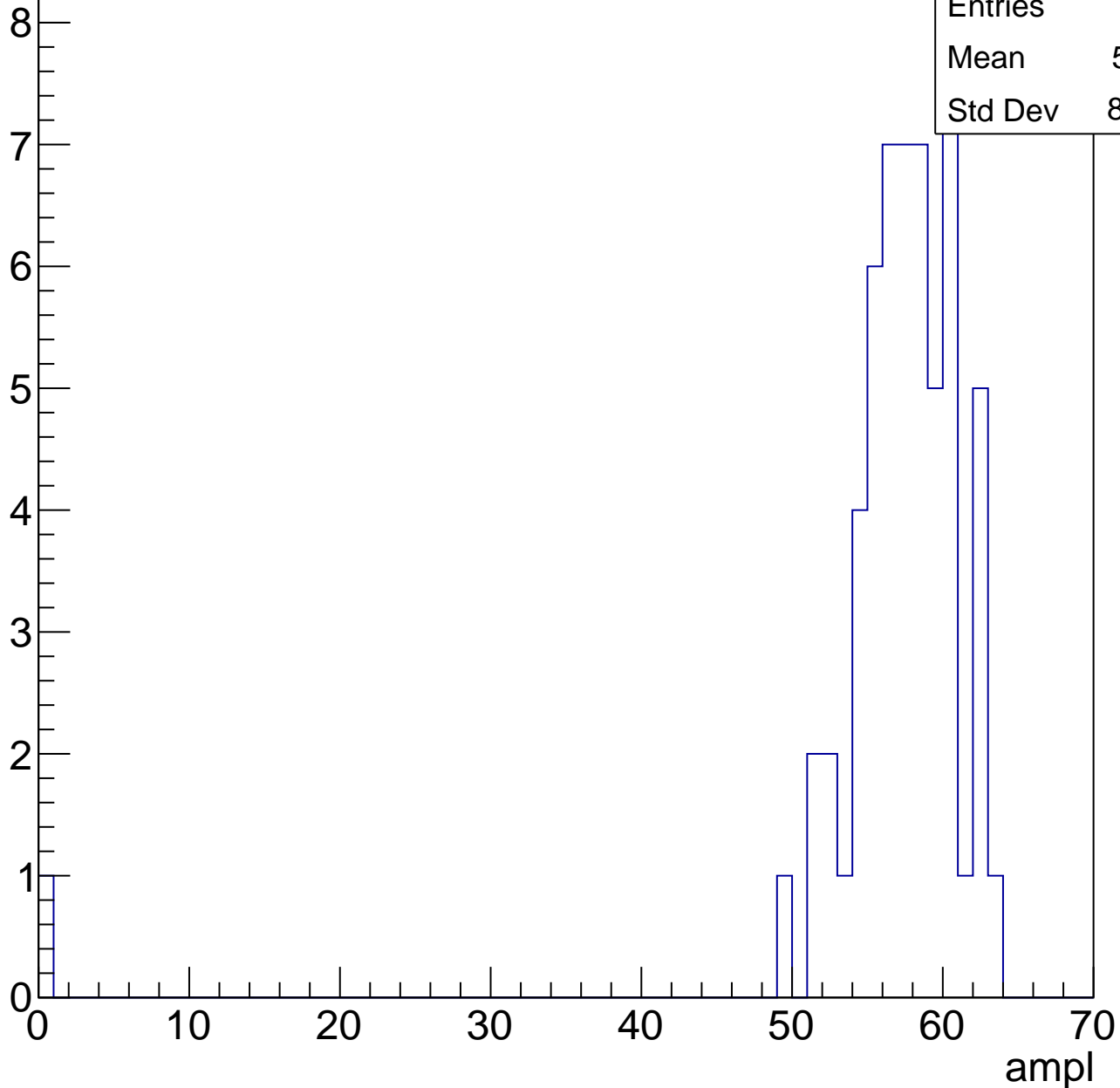


# B0L000S, U10-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	56.21
Std Dev	8.049

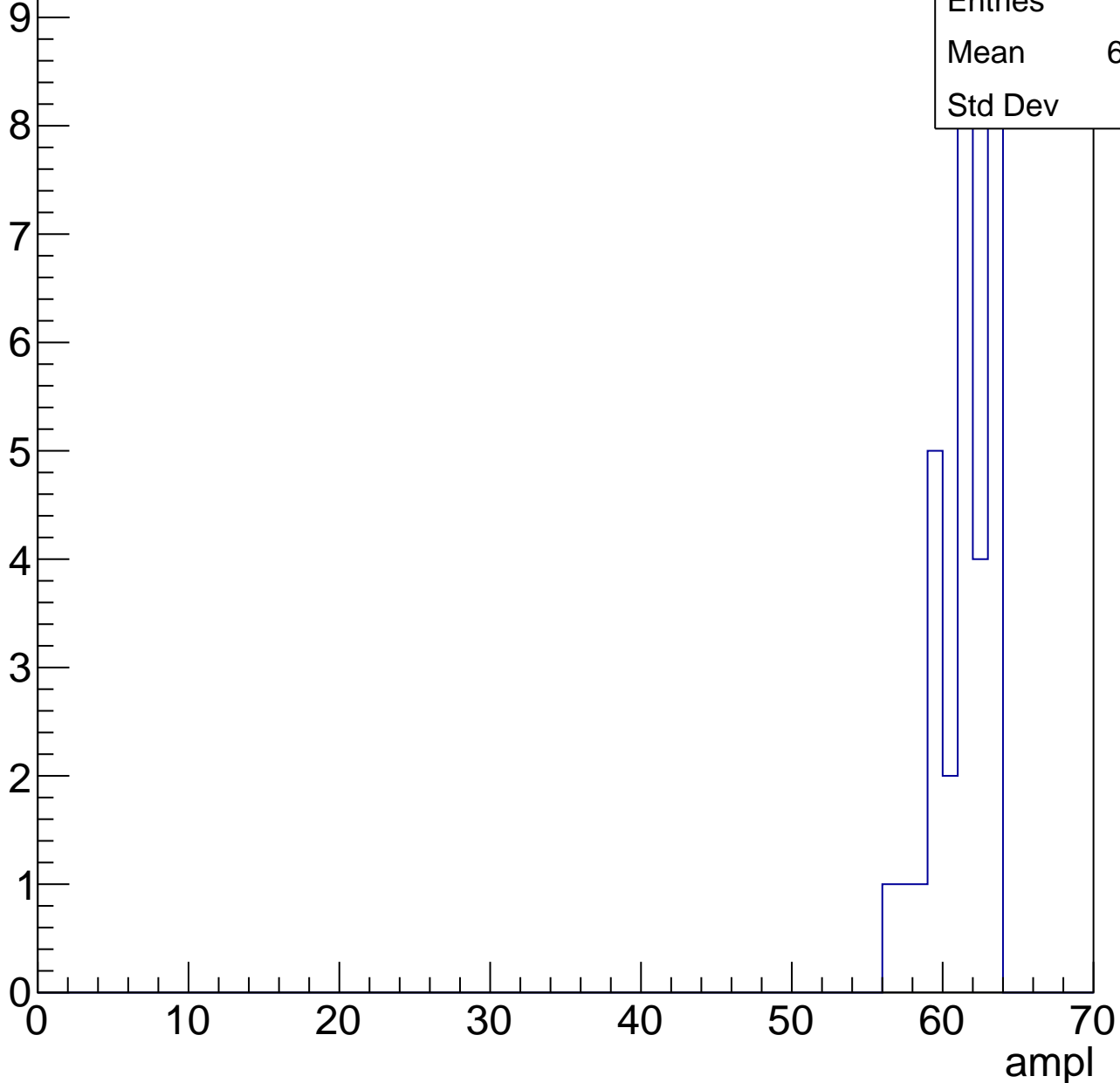


# B0L000S, U10-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

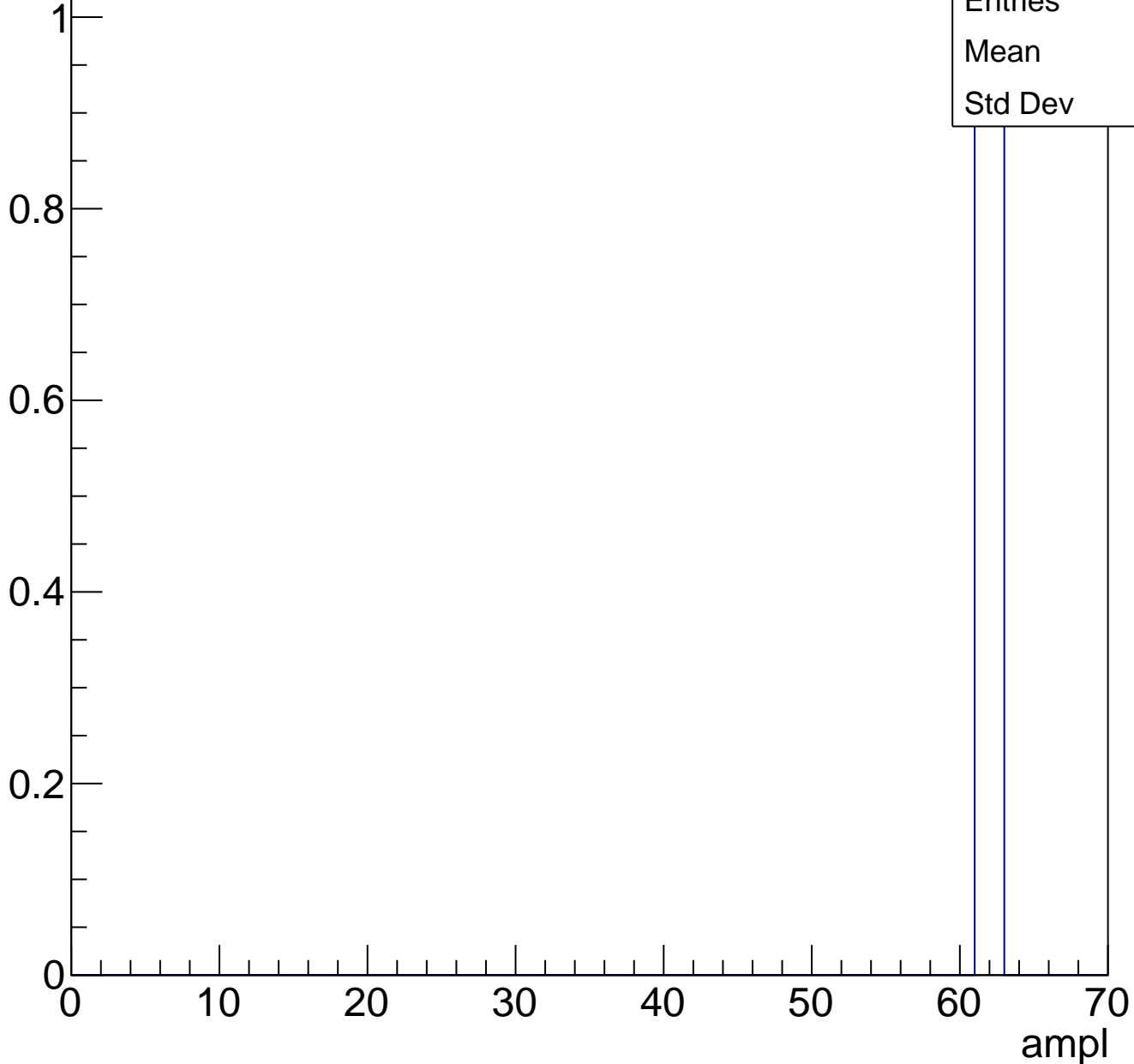
Entries	31
Mean	60.94
Std Dev	1.9



# B0L000S, U10-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U10-ch126, adc0

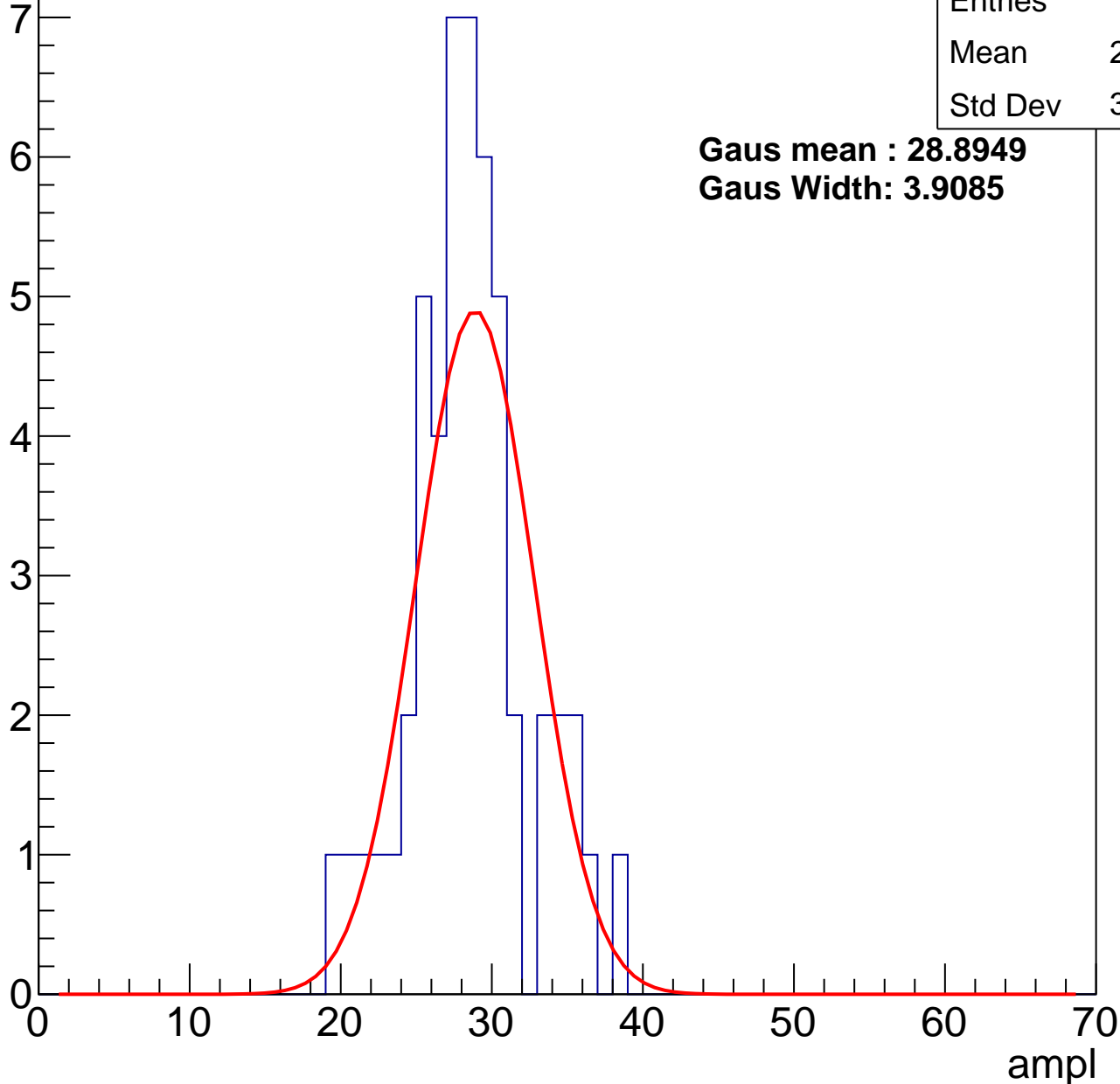
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	28.06
Std Dev	3.918

**Gaus mean : 28.8949**

**Gaus Width: 3.9085**



# B0L000S, U10-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	35.15
Std Dev	3.635

**Gaus mean : 35.5505**

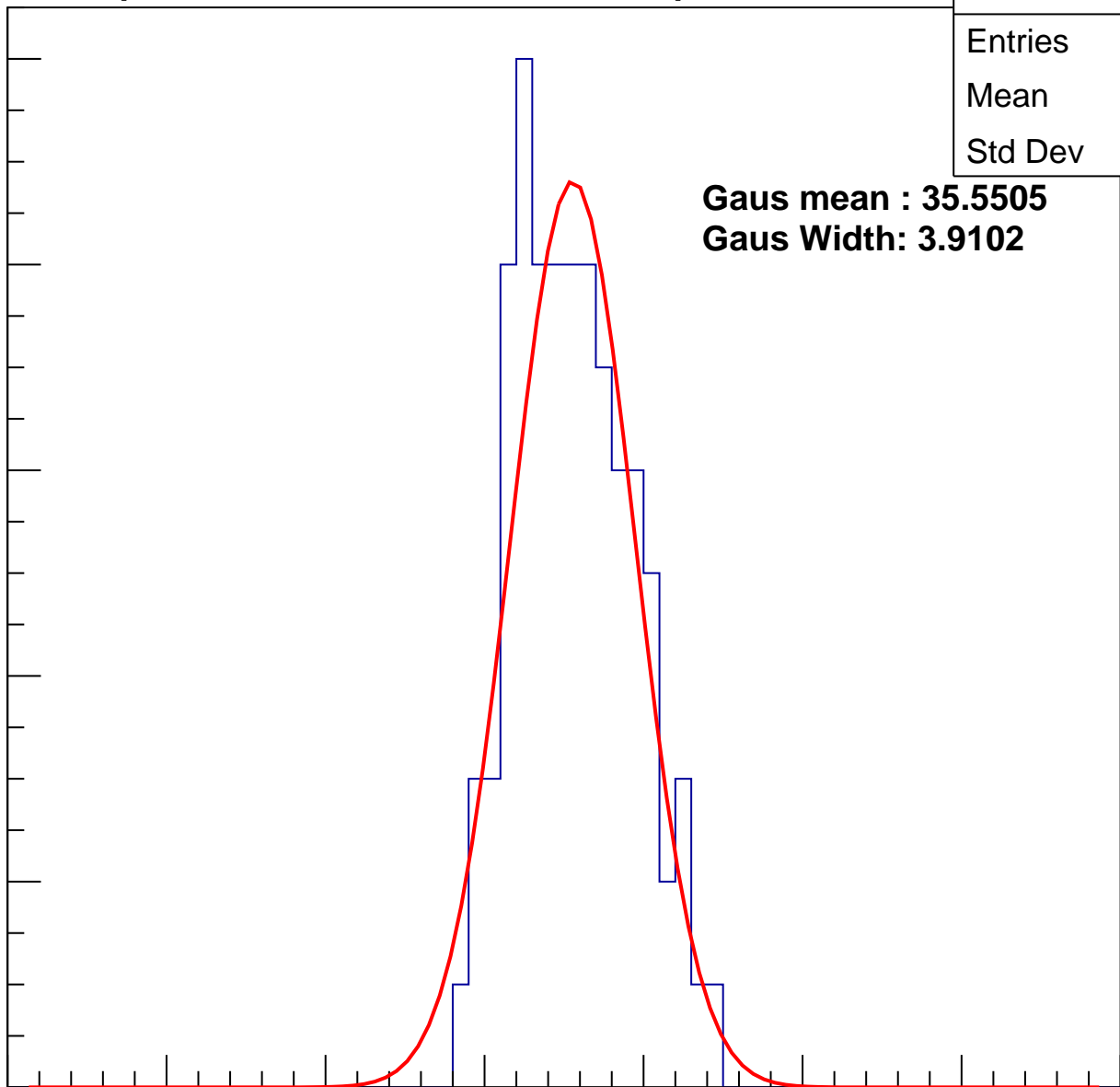
**Gaus Width: 3.9102**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U10-ch126, adc2

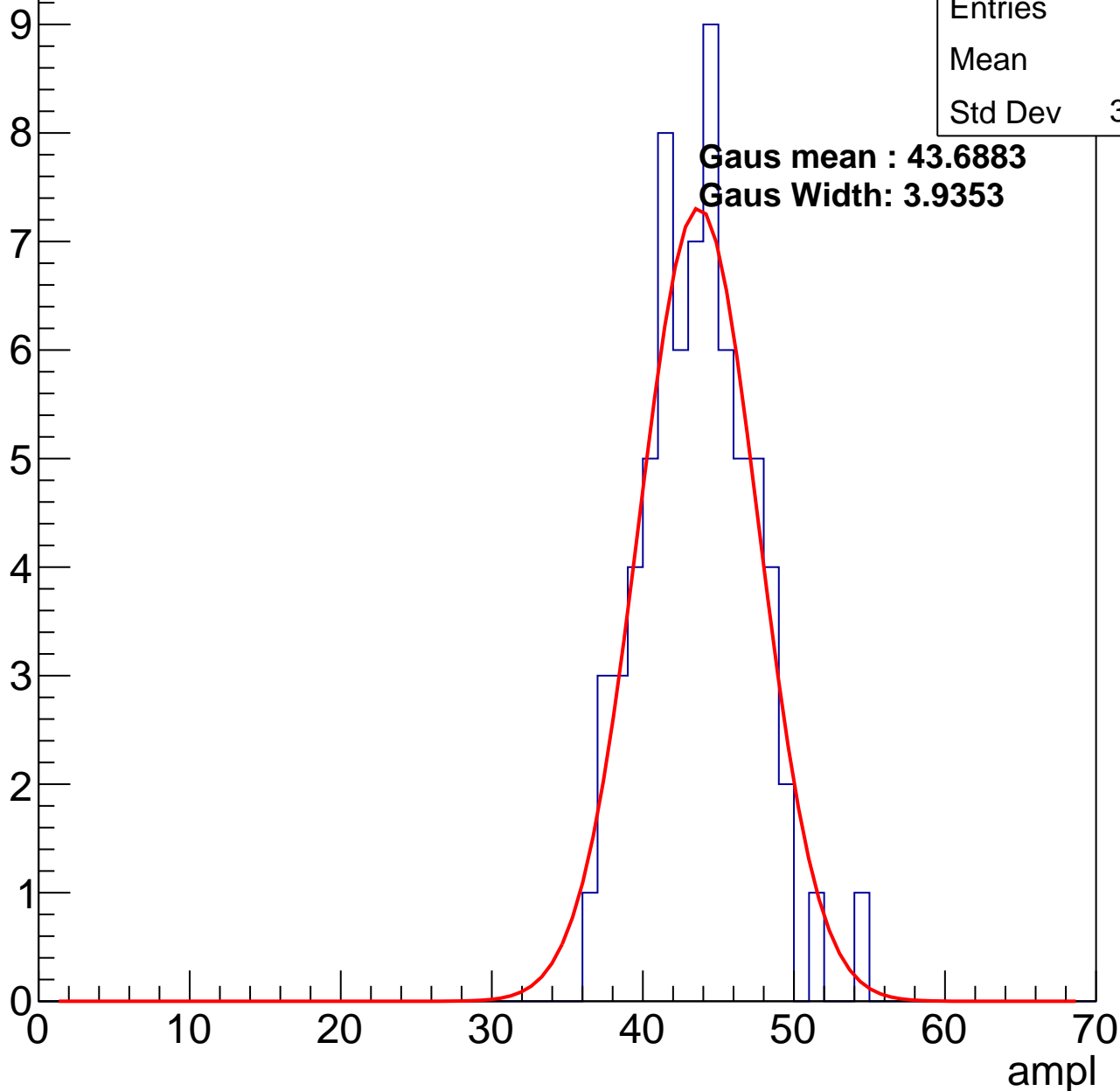
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.2
Std Dev	3.568

**Gaus mean : 43.6883**

**Gaus Width: 3.9353**

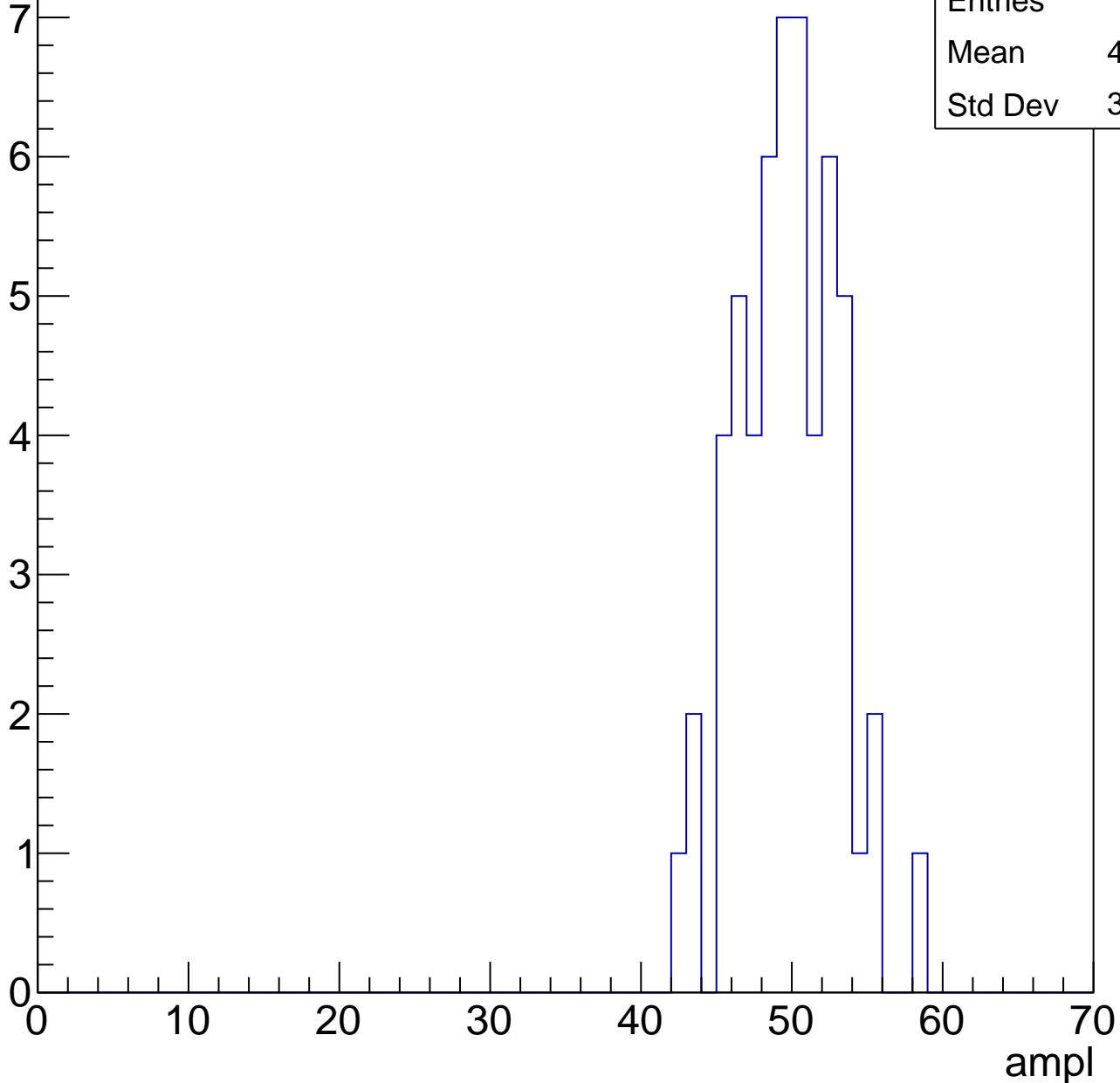


# B0L000S, U10-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.27
Std Dev	3.256

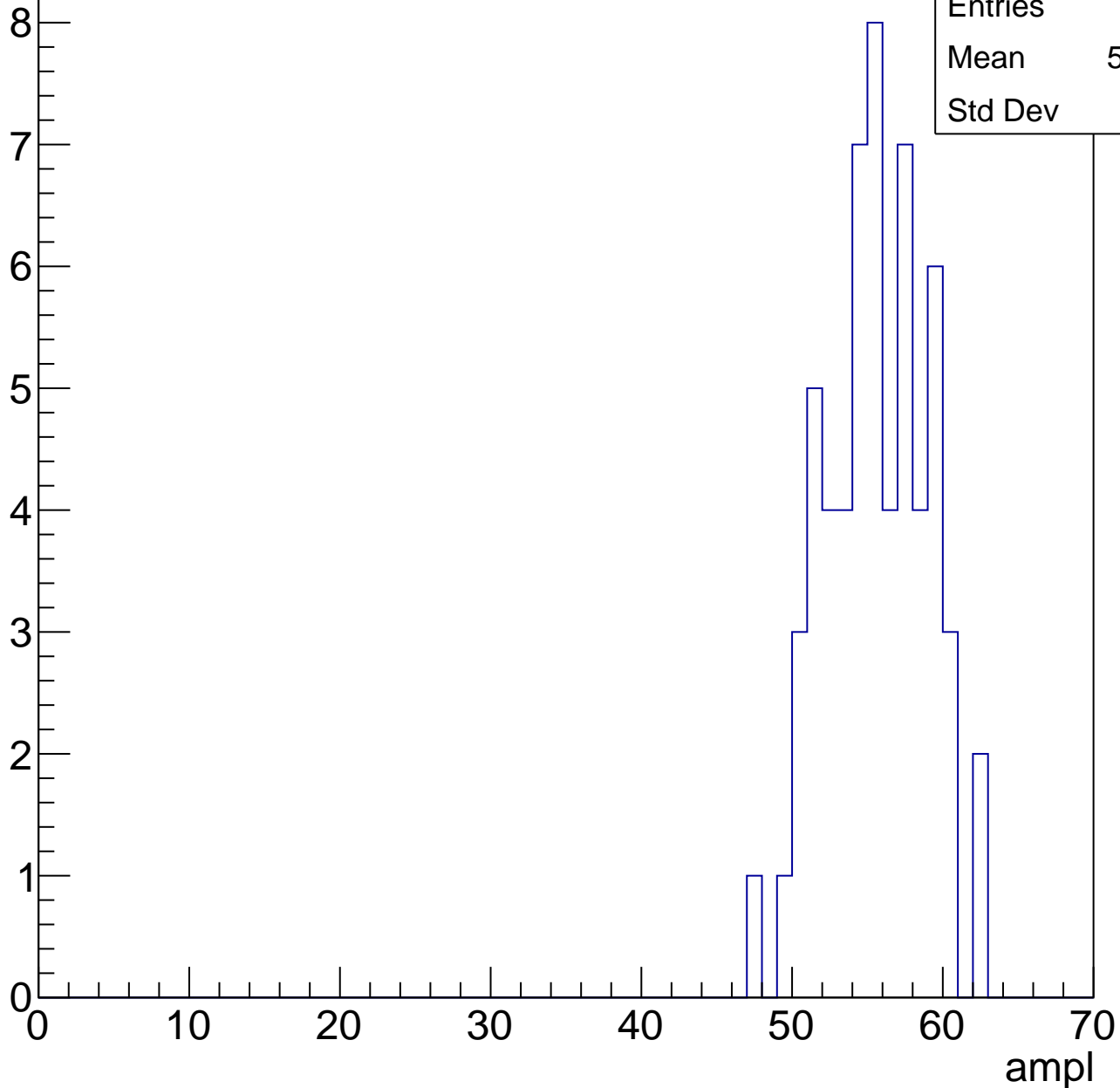


# B0L000S, U10-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.12
Std Dev	3.32

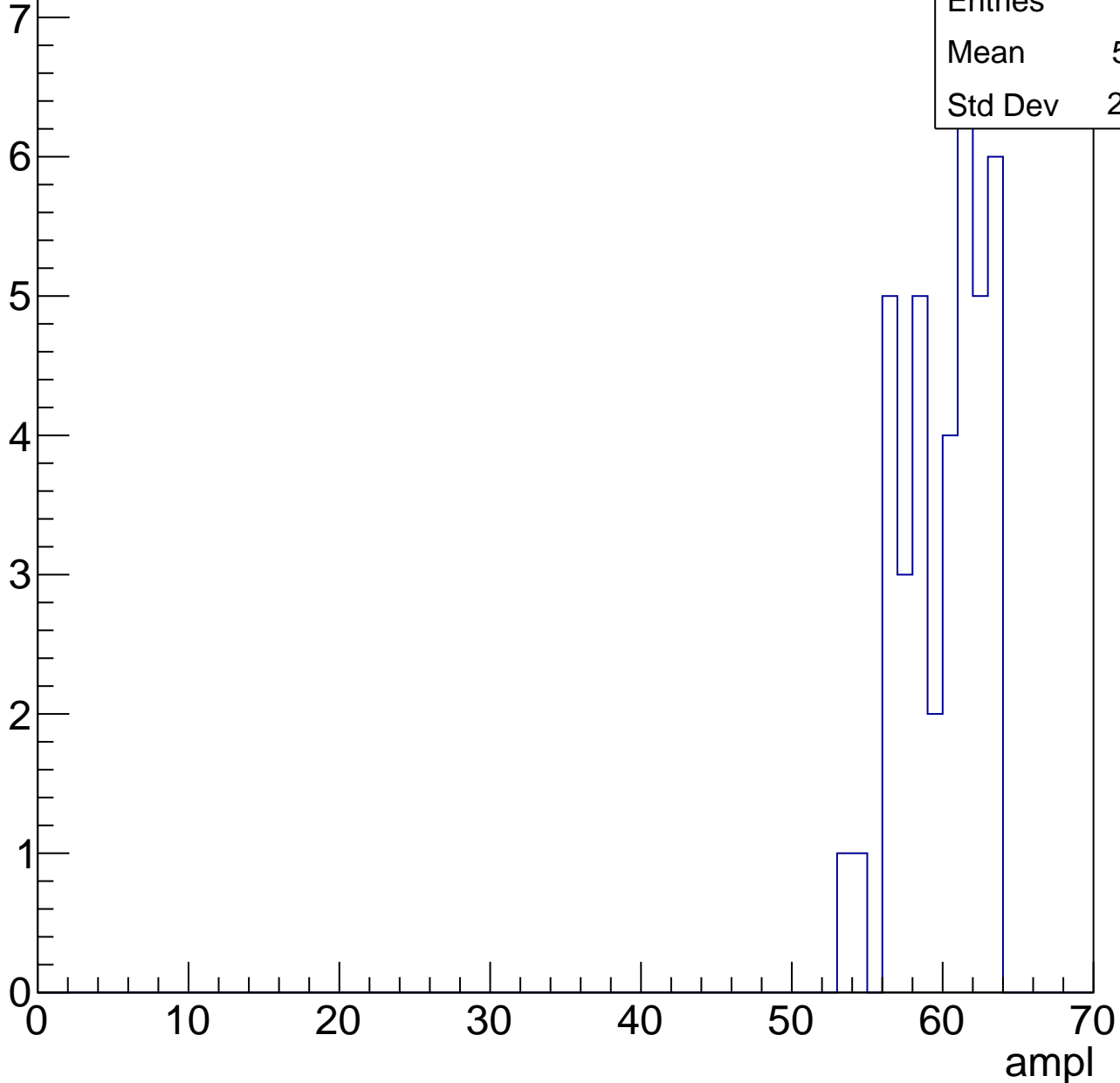


# B0L000S, U10-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

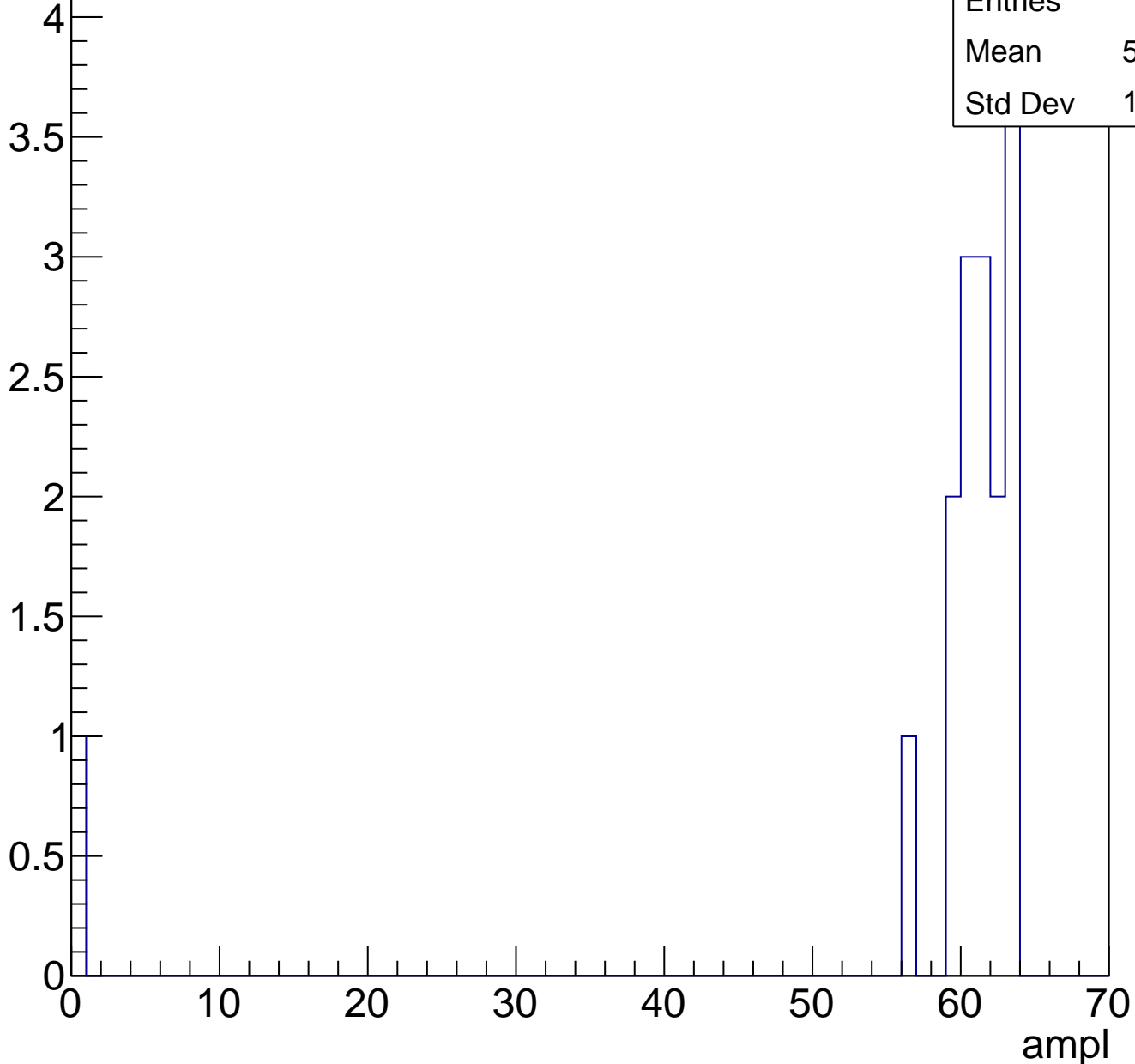
Entries	39
Mean	59.51
Std Dev	2.707



# B0L000S, U10-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



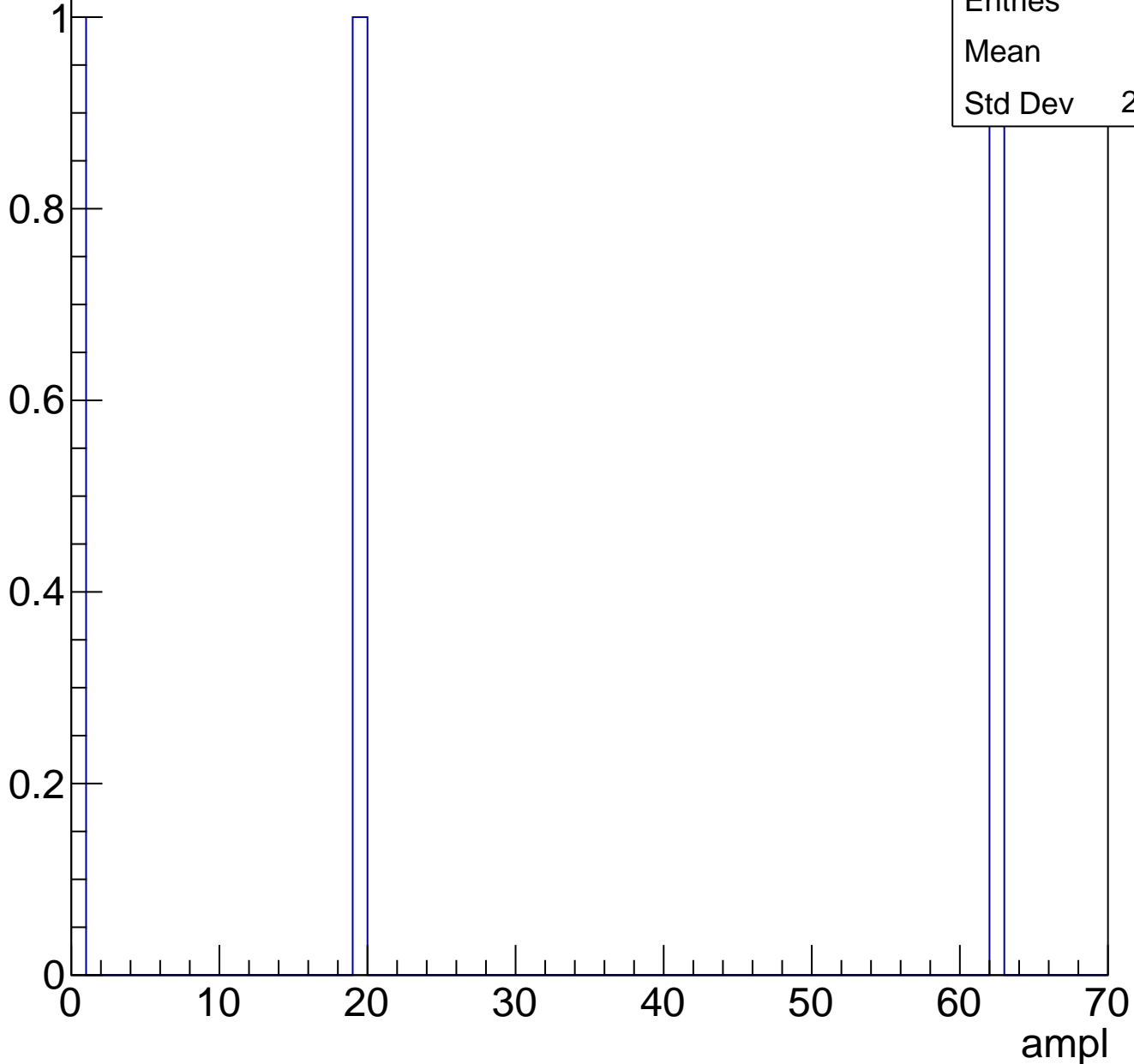
Entries	16
Mean	57.06
Std Dev	14.85



# B0L000S, U10-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	27
Std Dev	25.94

# B0L000S, U10-ch127, adc0

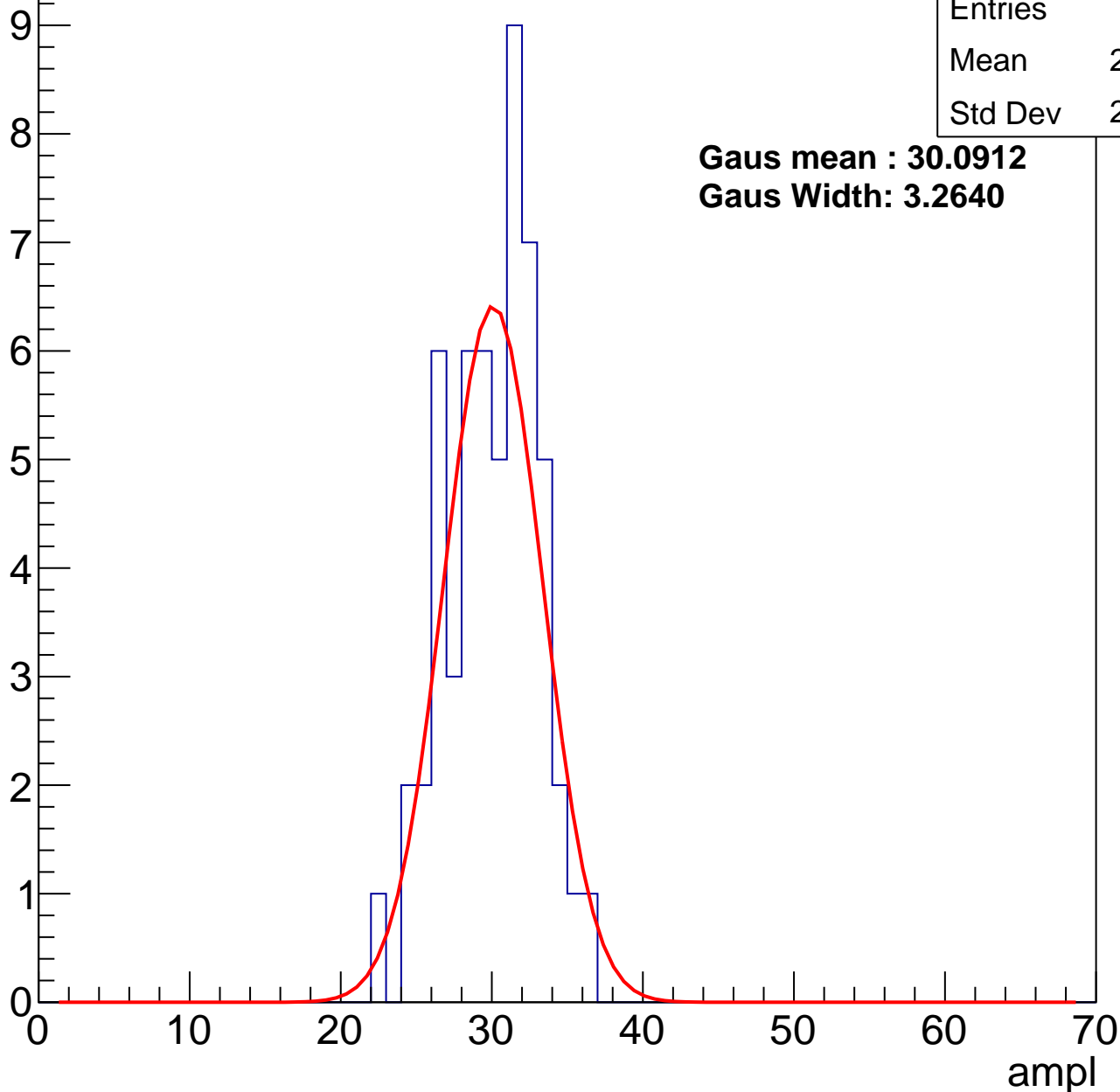
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.57
Std Dev	2.999

**Gaus mean : 30.0912**

**Gaus Width: 3.2640**



# B0L000S, U10-ch127, adc1

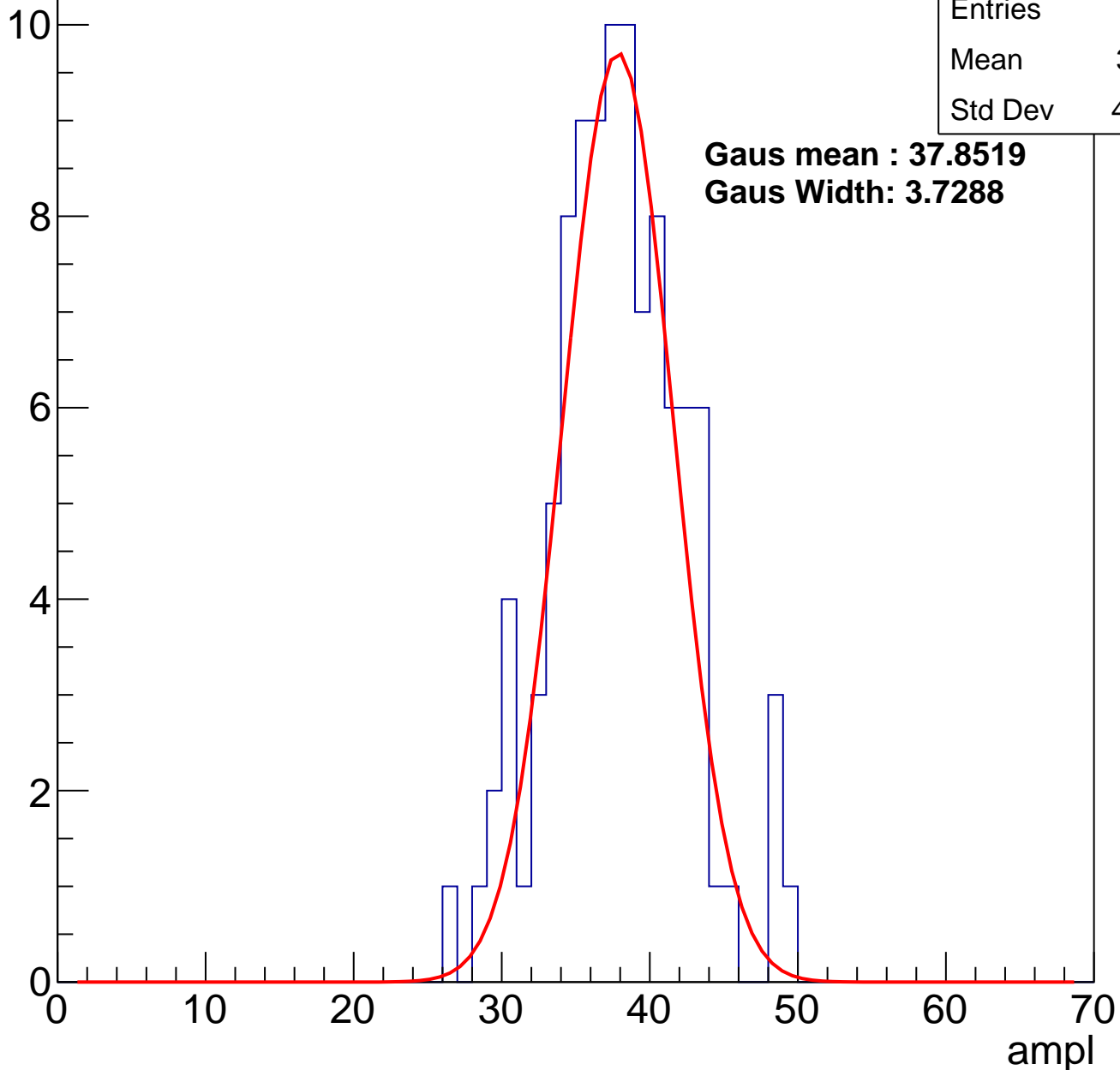
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	102
Mean	37.41
Std Dev	4.438

**Gaus mean : 37.8519**

**Gaus Width: 3.7288**

Entry



# B0L000S, U10-ch127, adc2

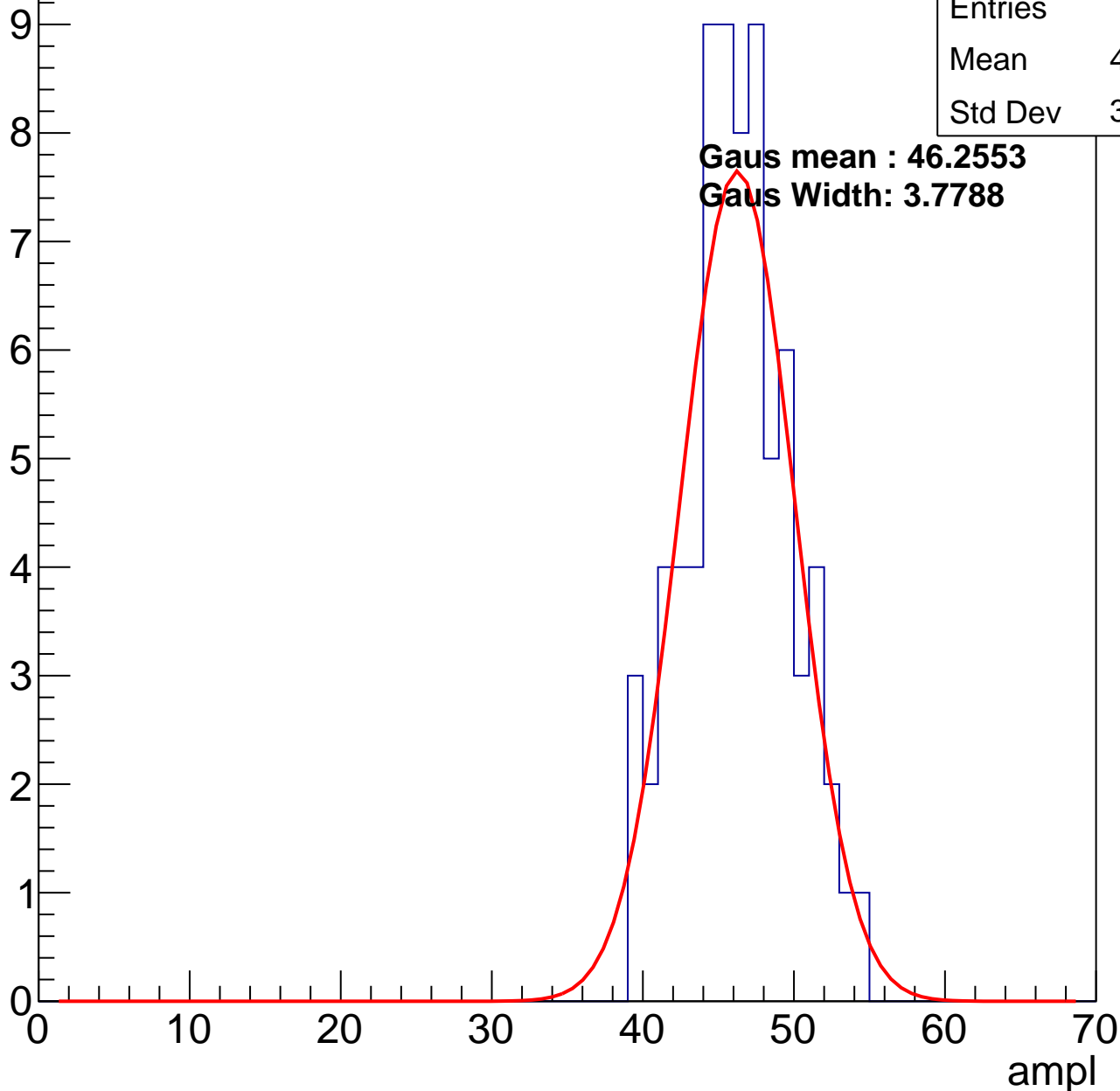
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	45.84
Std Dev	3.445

**Gaus mean : 46.2553**

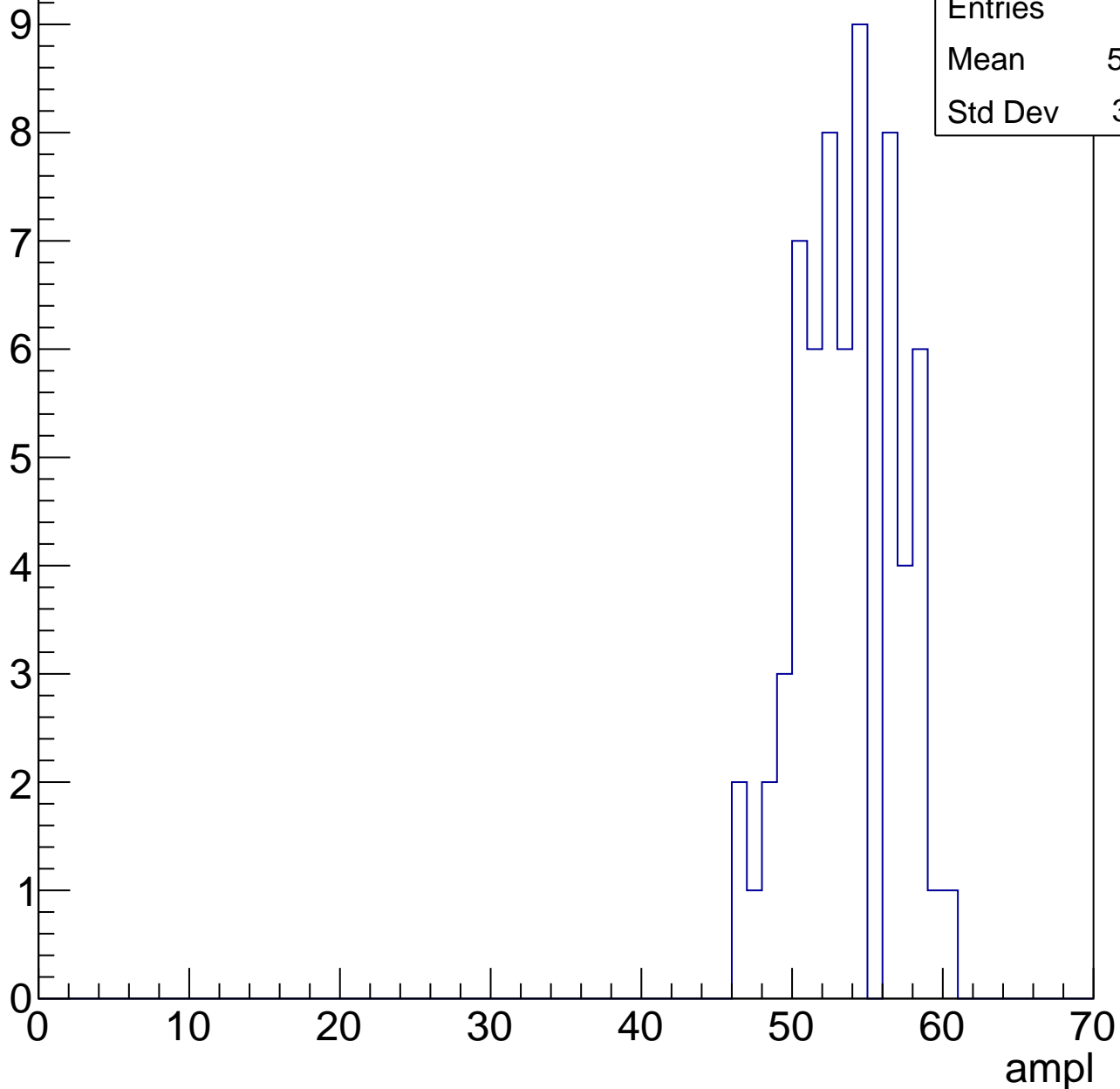
**Gaus Width: 3.7788**



# B0L000S, U10-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U10-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	50
Mean	58.3
Std Dev	2.879

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

10

8

6

4

2

0

0

10

20

30

40

50

60

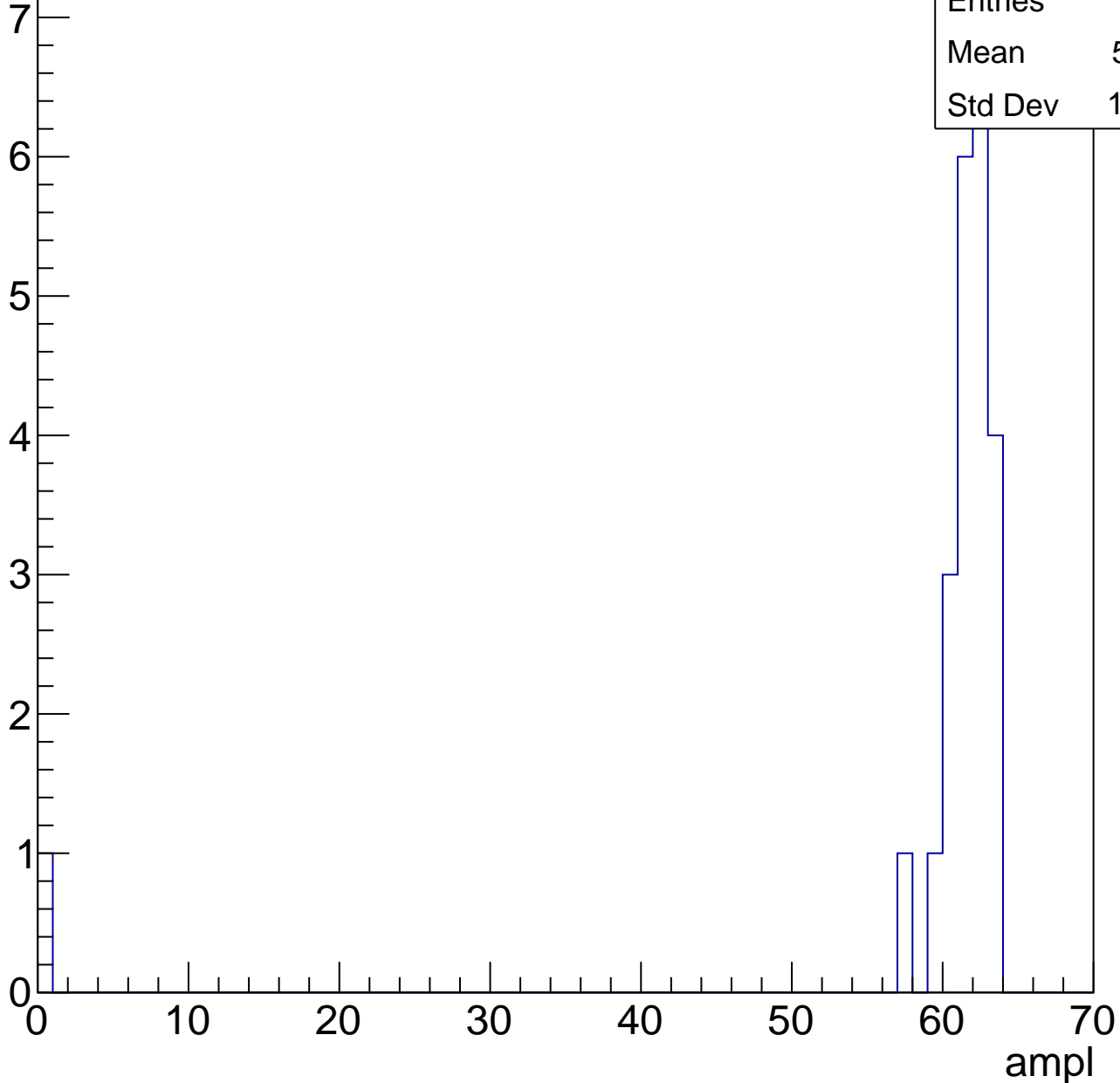
70

# B0L000S, U10-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.61
Std Dev	12.57



# B0L000S, U10-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U10-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U10-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0