

B0L101S, U25-ch0

calib_packv5_042523_0143.root, FC#1, port C1

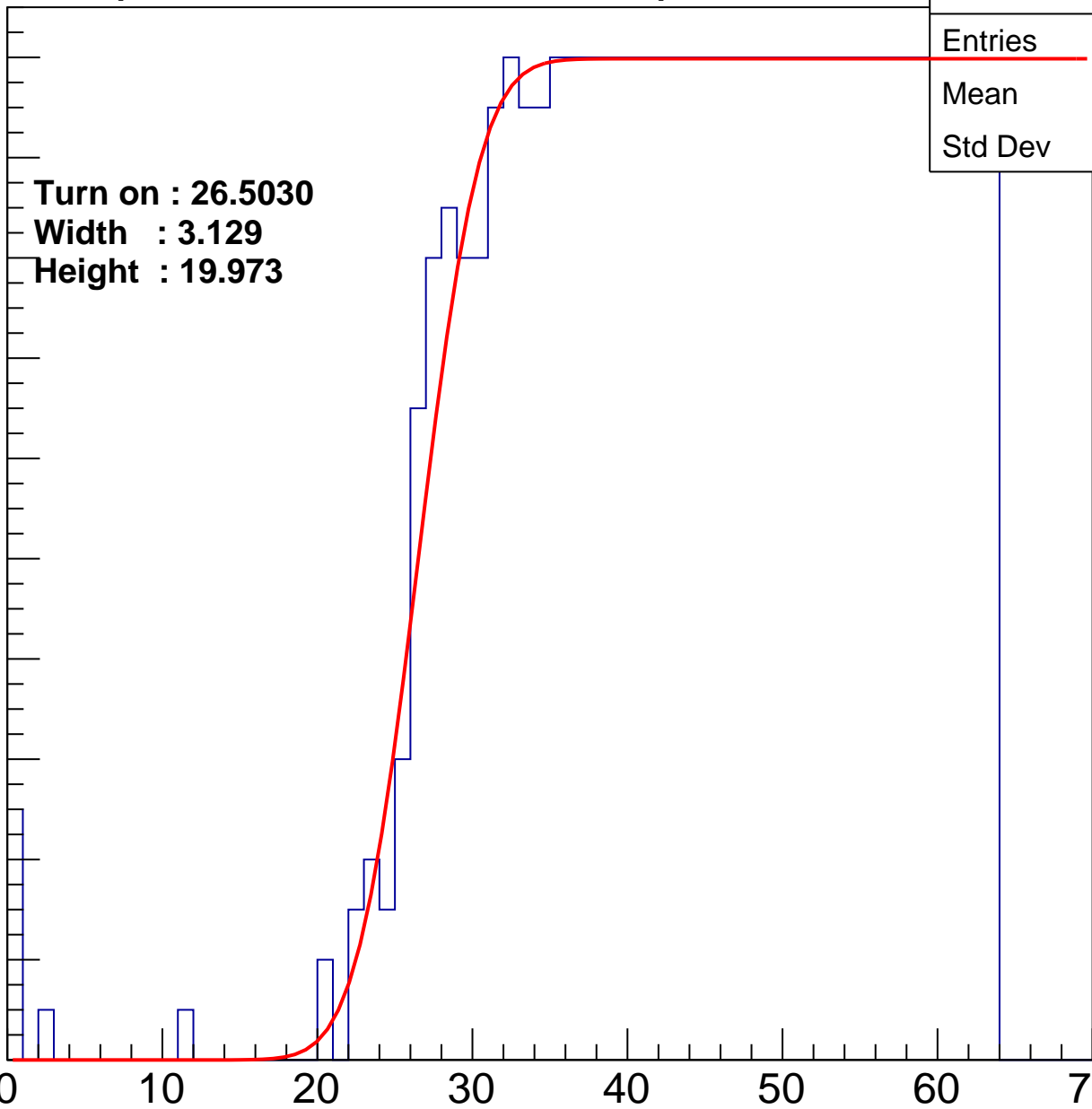
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5030
Width : 3.129
Height : 19.973

Entries	760
Mean	44.14
Std Dev	11.79

ampl



B0L101S, U25-ch1

calib_packv5_042523_0143.root, FC#1, port C1

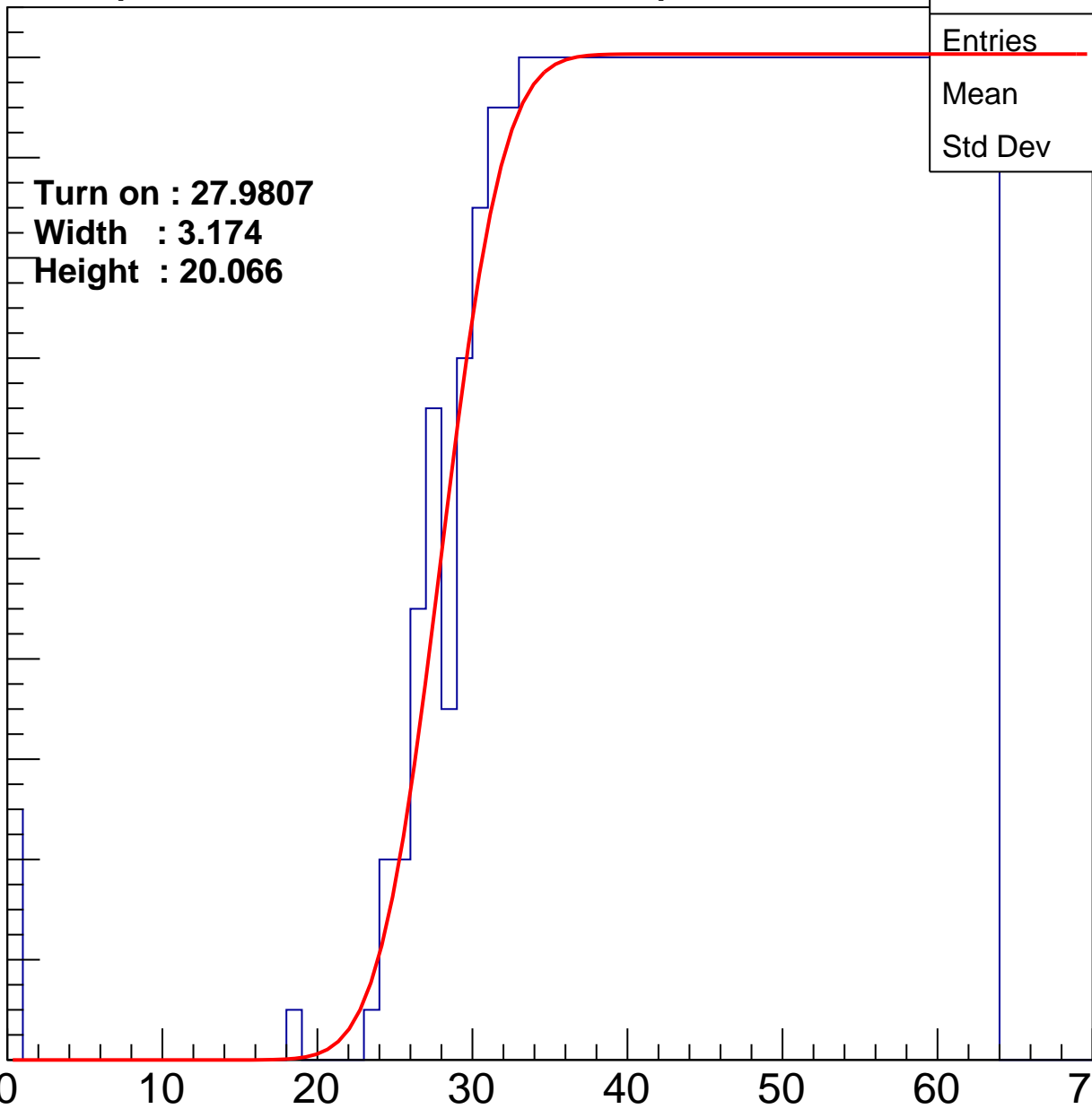
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9807
Width : 3.174
Height : 20.066

Entries	733
Mean	44.87
Std Dev	11.31

ampl



B0L101S, U25-ch2

calib_packv5_042523_0143.root, FC#1, port C1

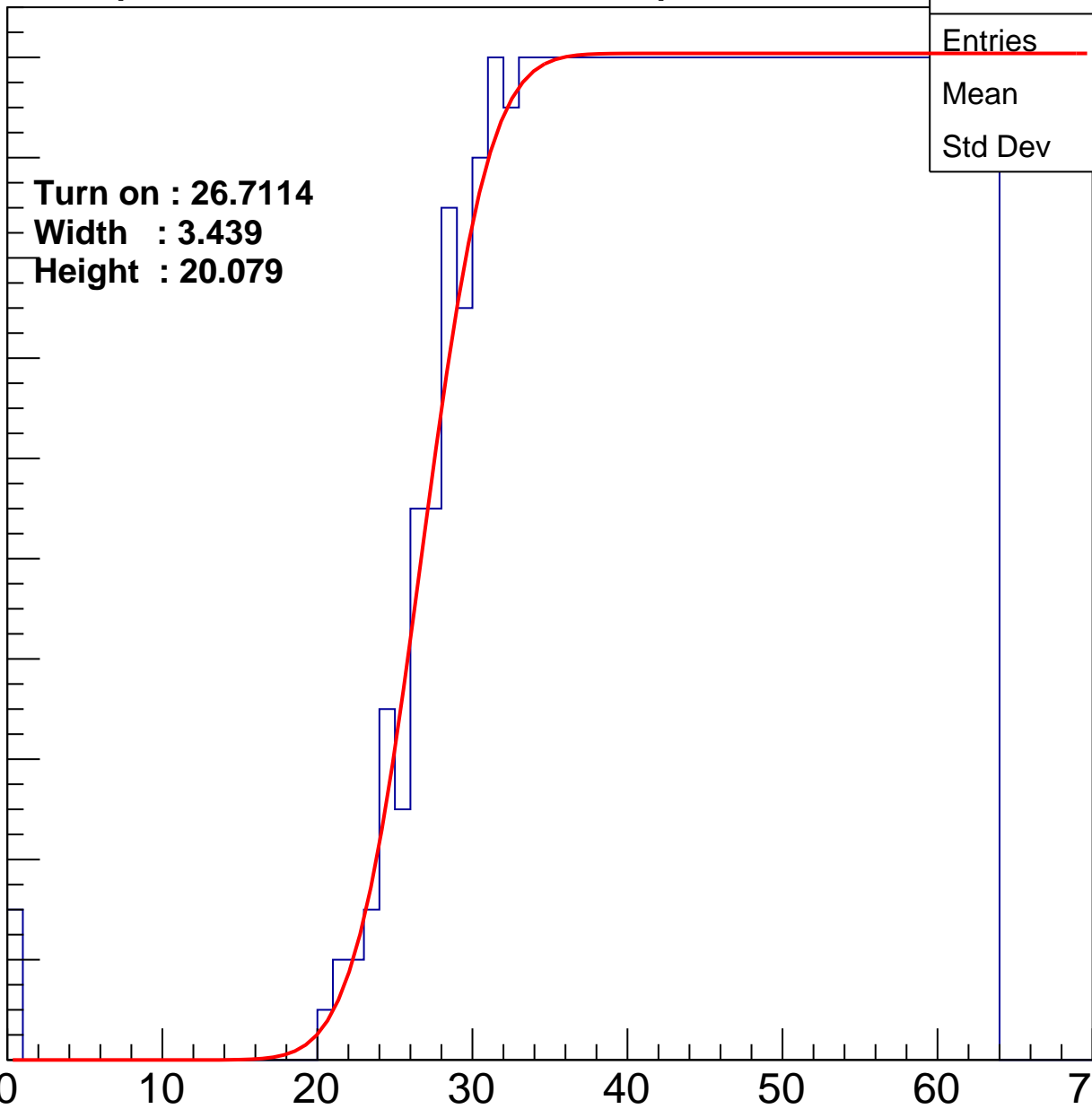
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7114
Width : 3.439
Height : 20.079

Entries	754
Mean	44.42
Std Dev	11.39

ampl



B0L101S, U25-ch3

calib_packv5_042523_0143.root, FC#1, port C1

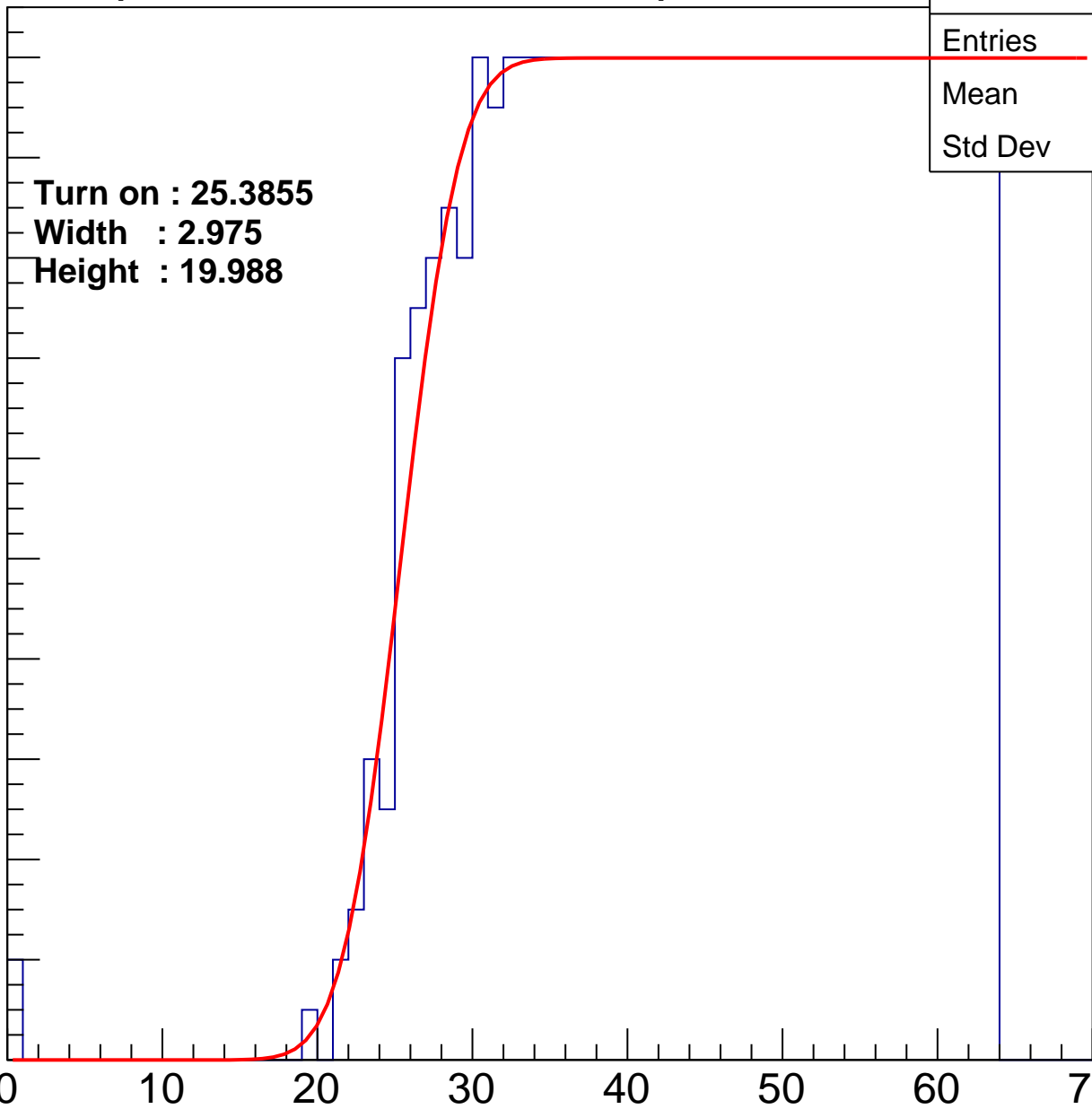
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3855
Width : 2.975
Height : 19.988

Entries	776
Mean	43.93
Std Dev	11.56

ampl



B0L101S, U25-ch4

calib_packv5_042523_0143.root, FC#1, port C1

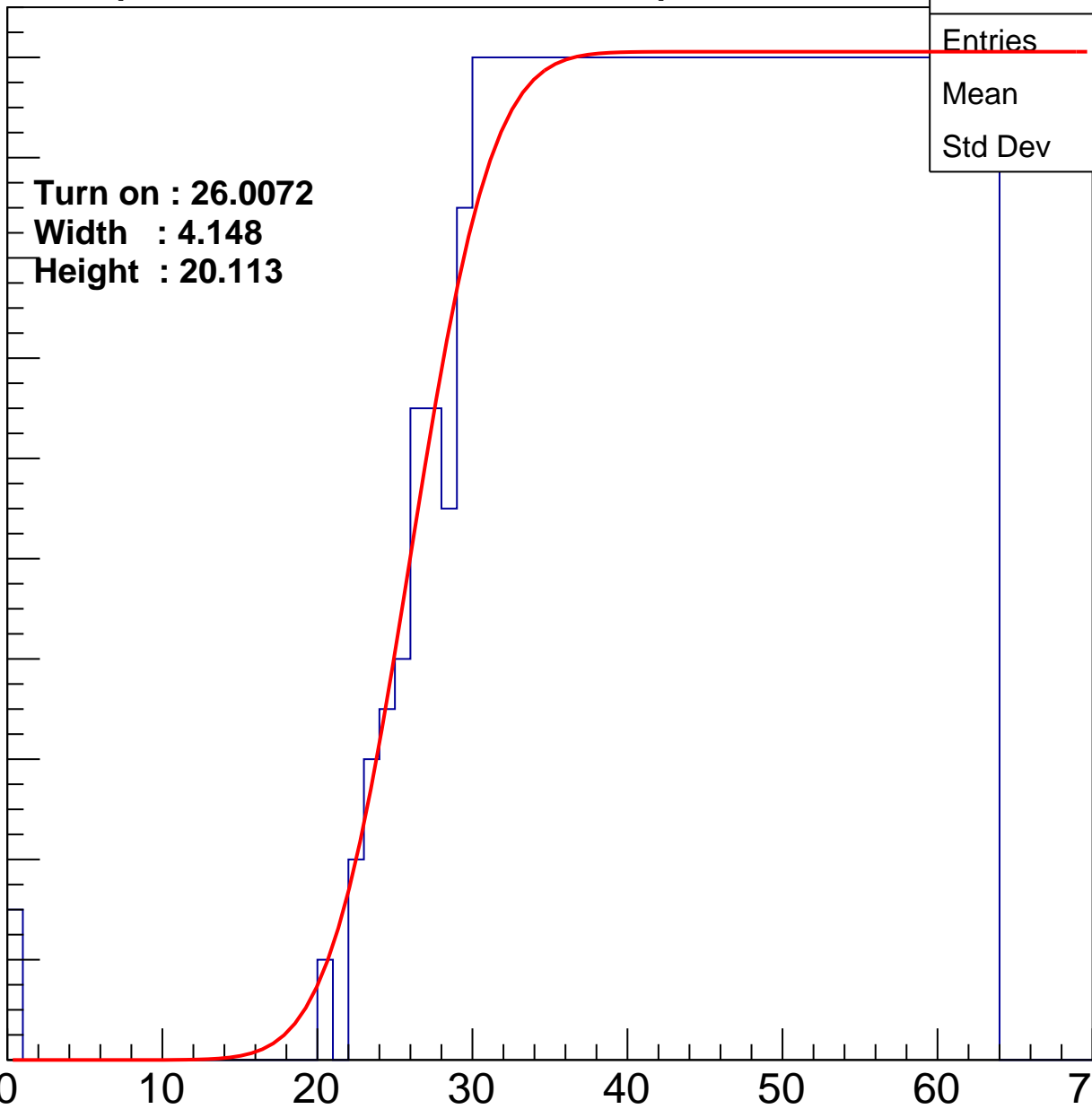
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0072
Width : 4.148
Height : 20.113

Entries	764
Mean	44.17
Std Dev	11.53

ampl



B0L101S, U25-ch5

calib_packv5_042523_0143.root, FC#1, port C1

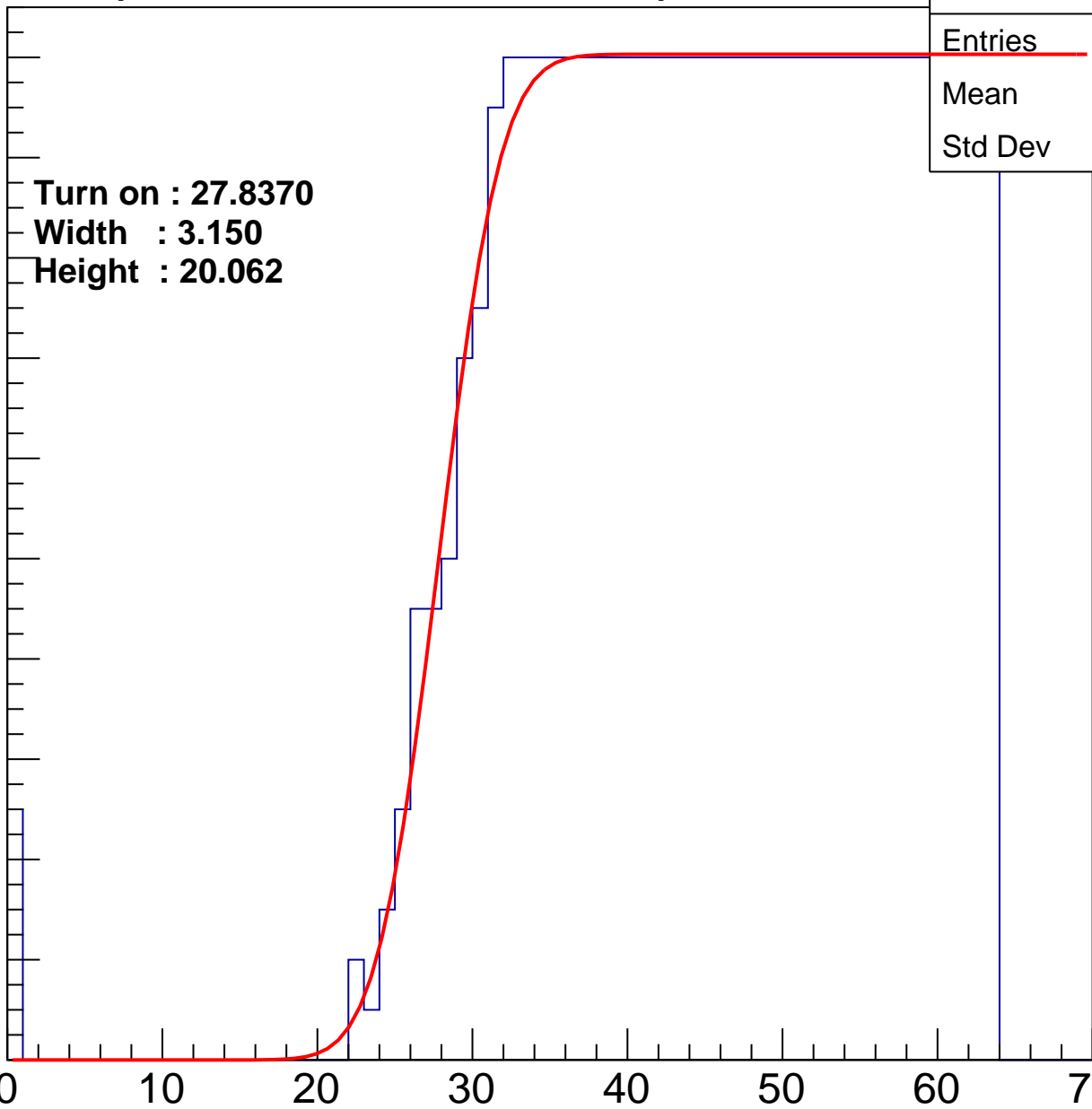
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8370
Width : 3.150
Height : 20.062

Entries	732
Mean	44.9
Std Dev	11.29

ampl



B0L101S, U25-ch6

calib_packv5_042523_0143.root, FC#1, port C1

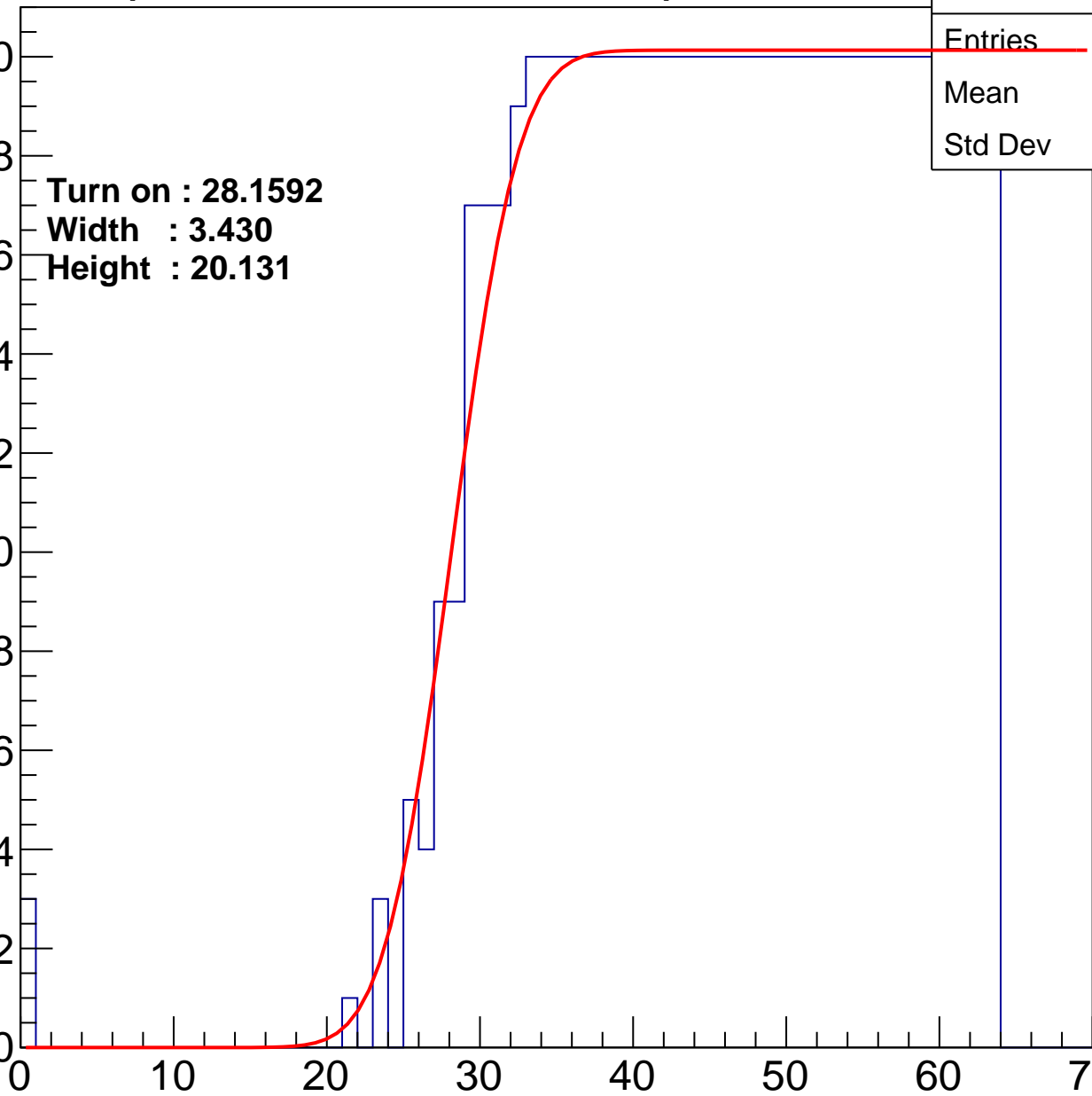
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1592
Width : 3.430
Height : 20.131

Entries	724
Mean	45.18
Std Dev	10.96

ampl



B0L101S, U25-ch7

calib_packv5_042523_0143.root, FC#1, port C1

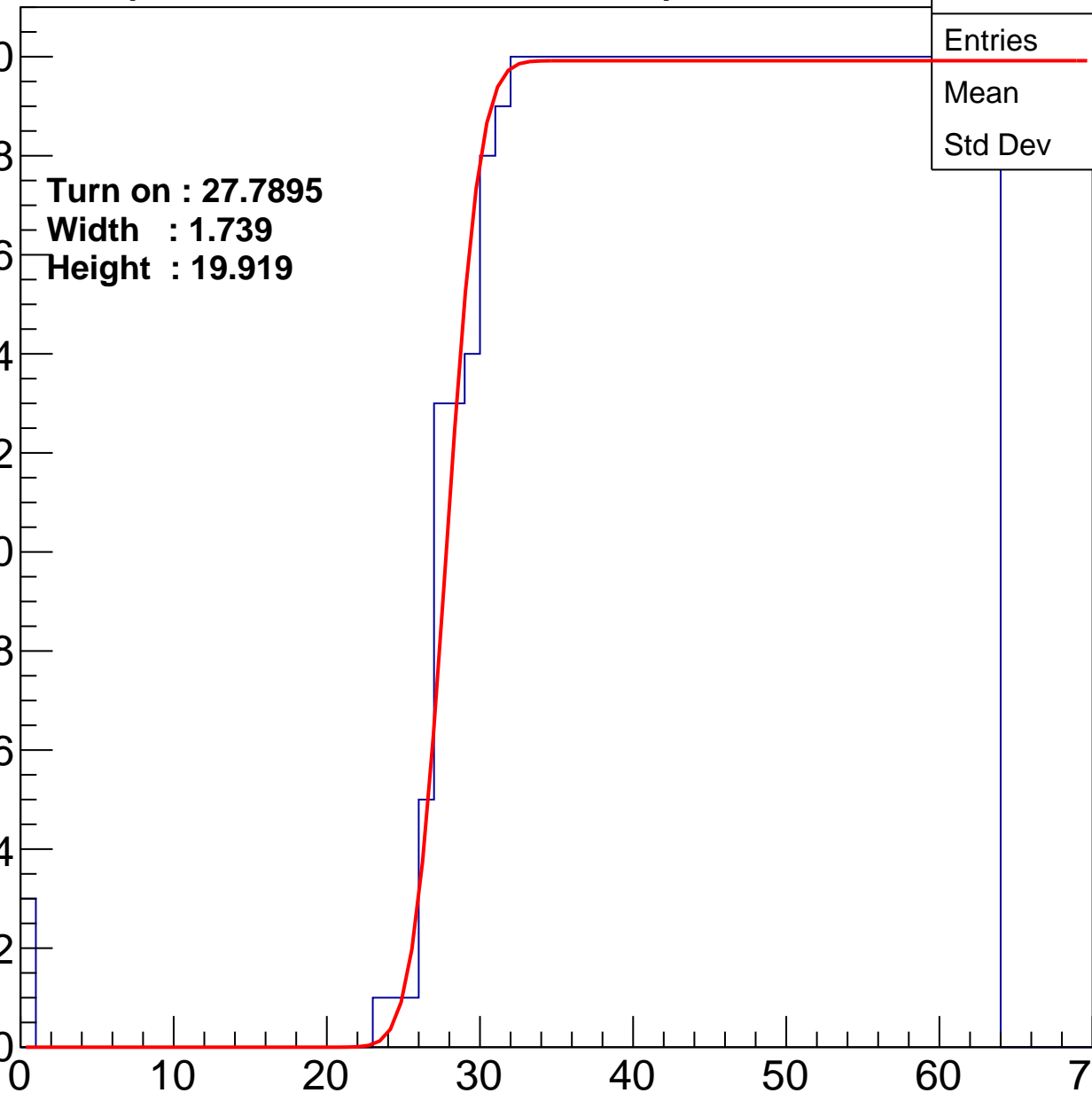
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7895
Width : 1.739
Height : 19.919

Entries	728
Mean	45.13
Std Dev	10.94

ampl



B0L101S, U25-ch8

calib_packv5_042523_0143.root, FC#1, port C1

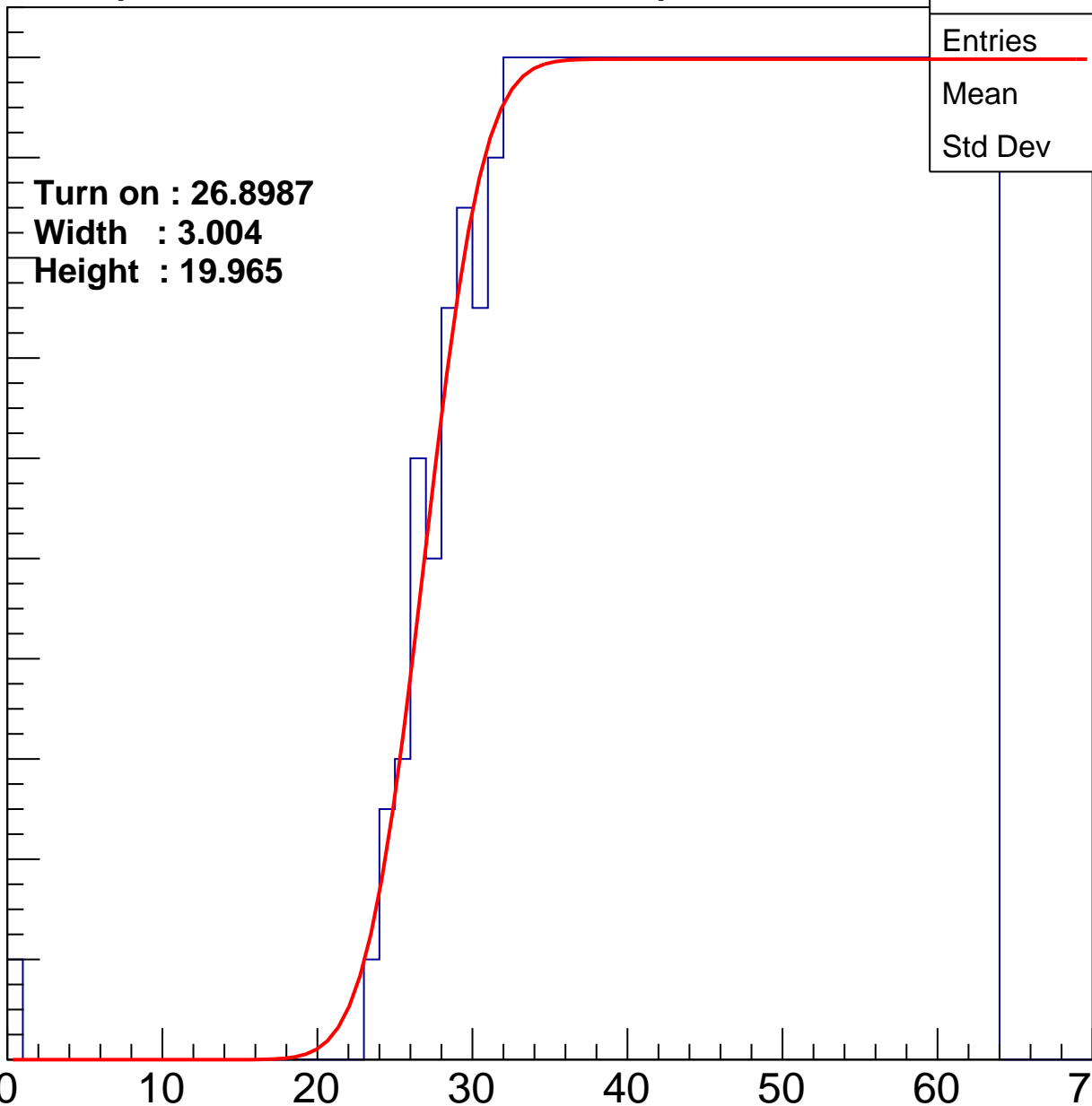
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8987
Width : 3.004
Height : 19.965

Entries	742
Mean	44.77
Std Dev	11.09

ampl



B0L101S, U25-ch9

calib_packv5_042523_0143.root, FC#1, port C1

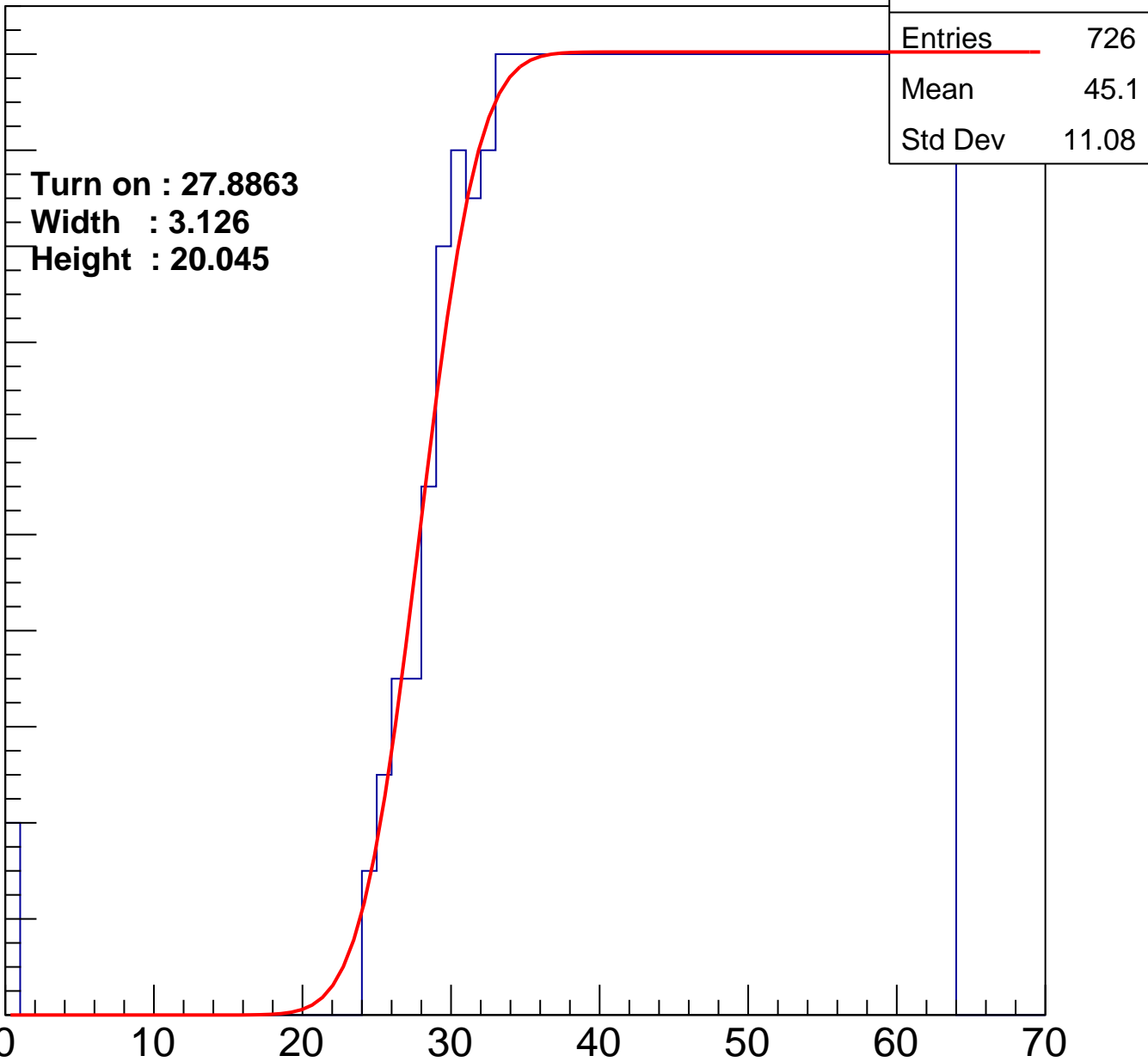
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8863
Width : 3.126
Height : 20.045

Entries	726
Mean	45.1
Std Dev	11.08

ampl



B0L101S, U25-ch10

calib_packv5_042523_0143.root, FC#1, port C1

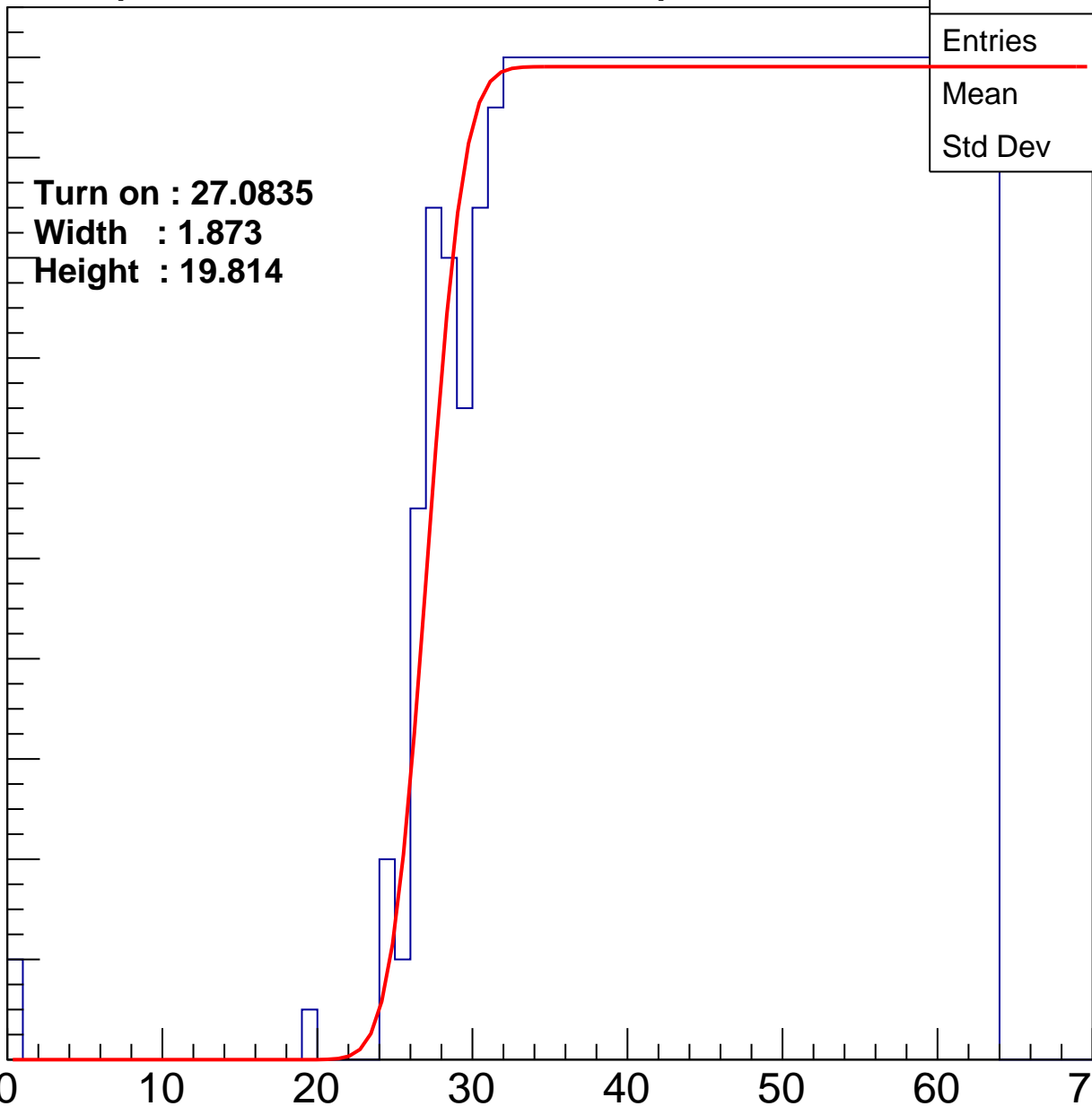
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0835
Width : 1.873
Height : 19.814

Entries	742
Mean	44.79
Std Dev	11.06

ampl



B0L101S, U25-ch11

calib_packv5_042523_0143.root, FC#1, port C1

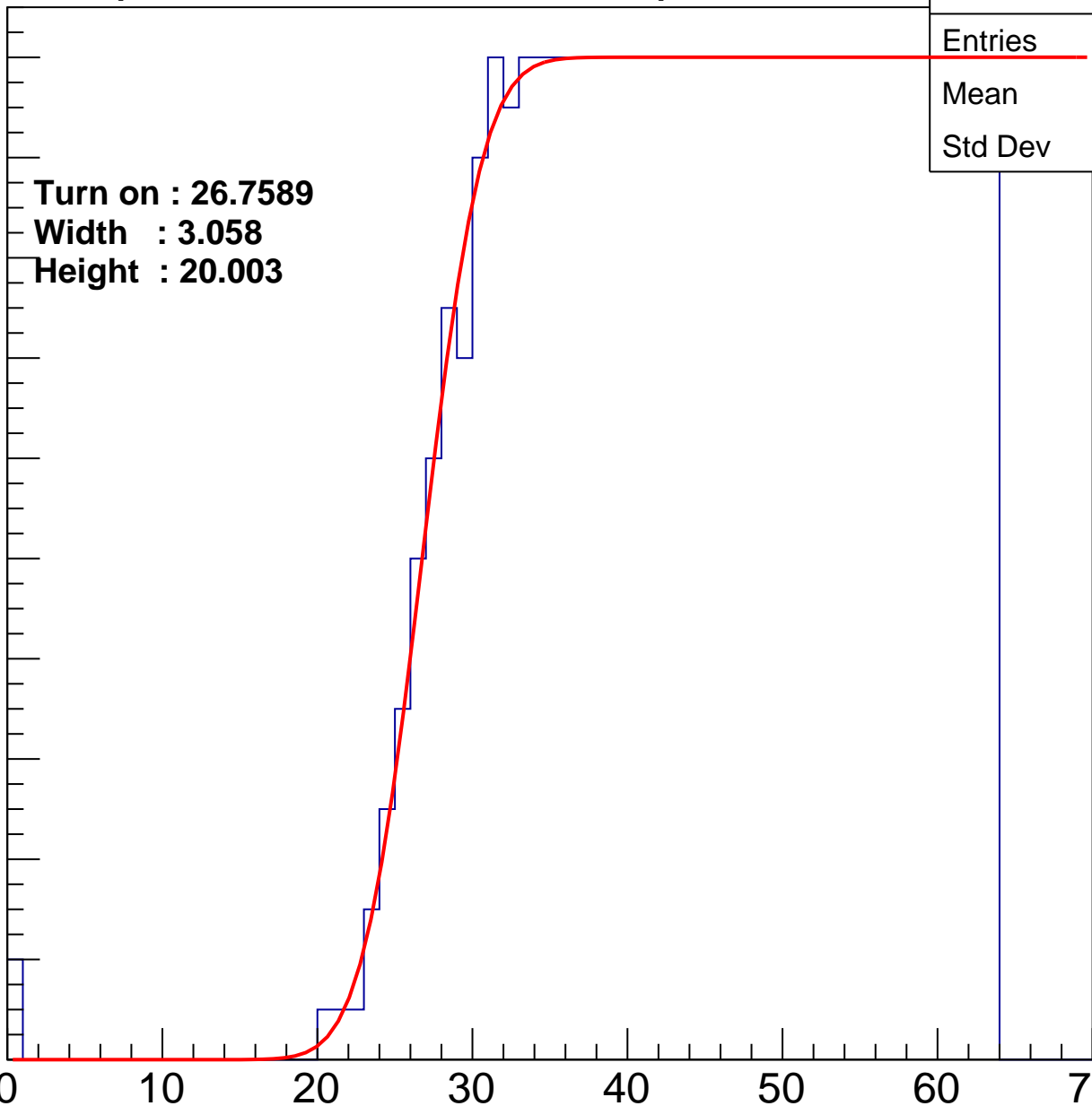
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7589
Width : 3.058
Height : 20.003

Entries	748
Mean	44.61
Std Dev	11.2

ampl



B0L101S, U25-ch12

calib_packv5_042523_0143.root, FC#1, port C1

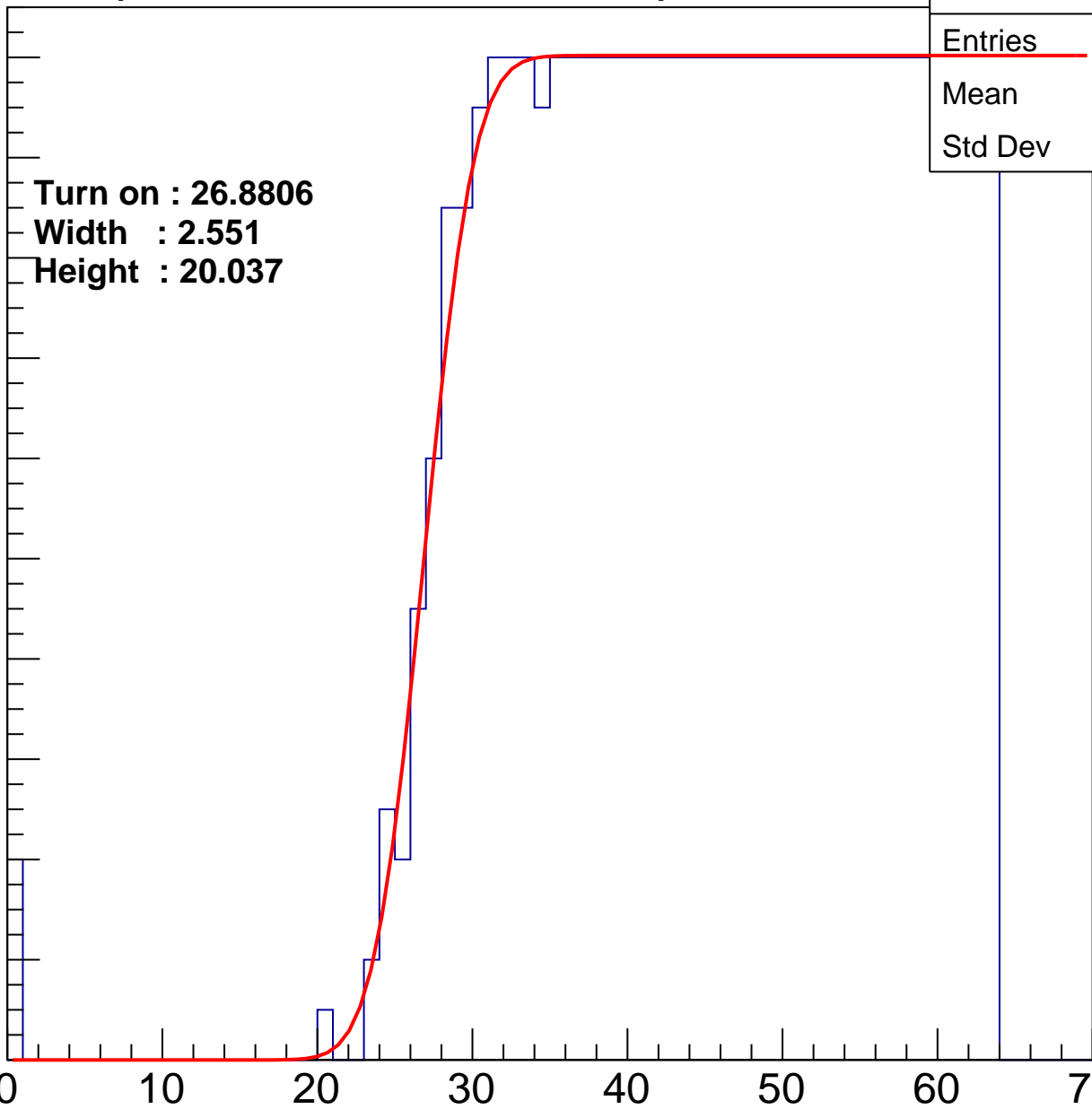
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8806
Width : 2.551
Height : 20.037

Entries	749
Mean	44.55
Std Dev	11.34

ampl



B0L101S, U25-ch13

calib_packv5_042523_0143.root, FC#1, port C1

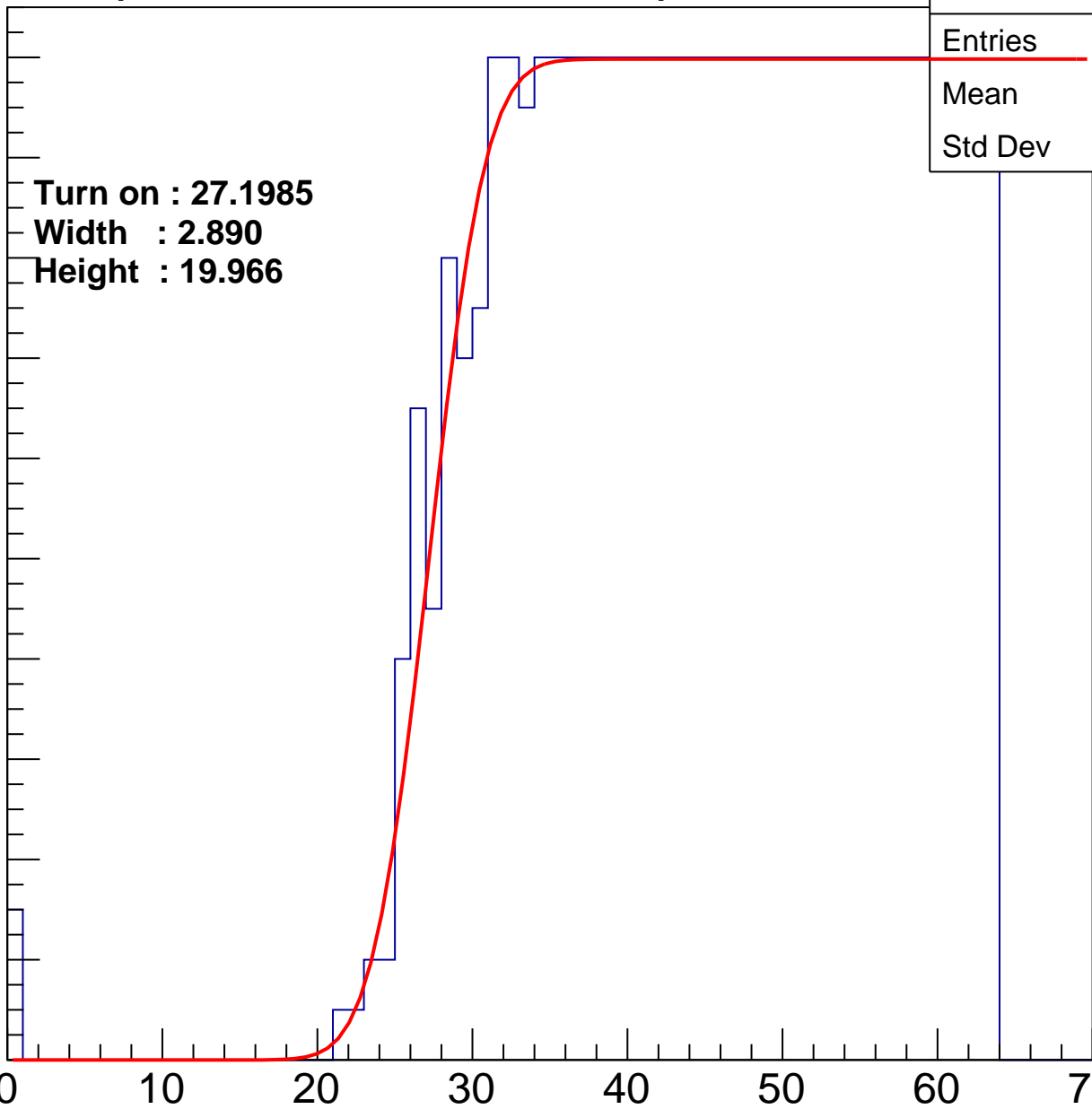
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1985
Width : 2.890
Height : 19.966

Entries	743
Mean	44.7
Std Dev	11.23

ampl



B0L101S, U25-ch14

calib_packv5_042523_0143.root, FC#1, port C1

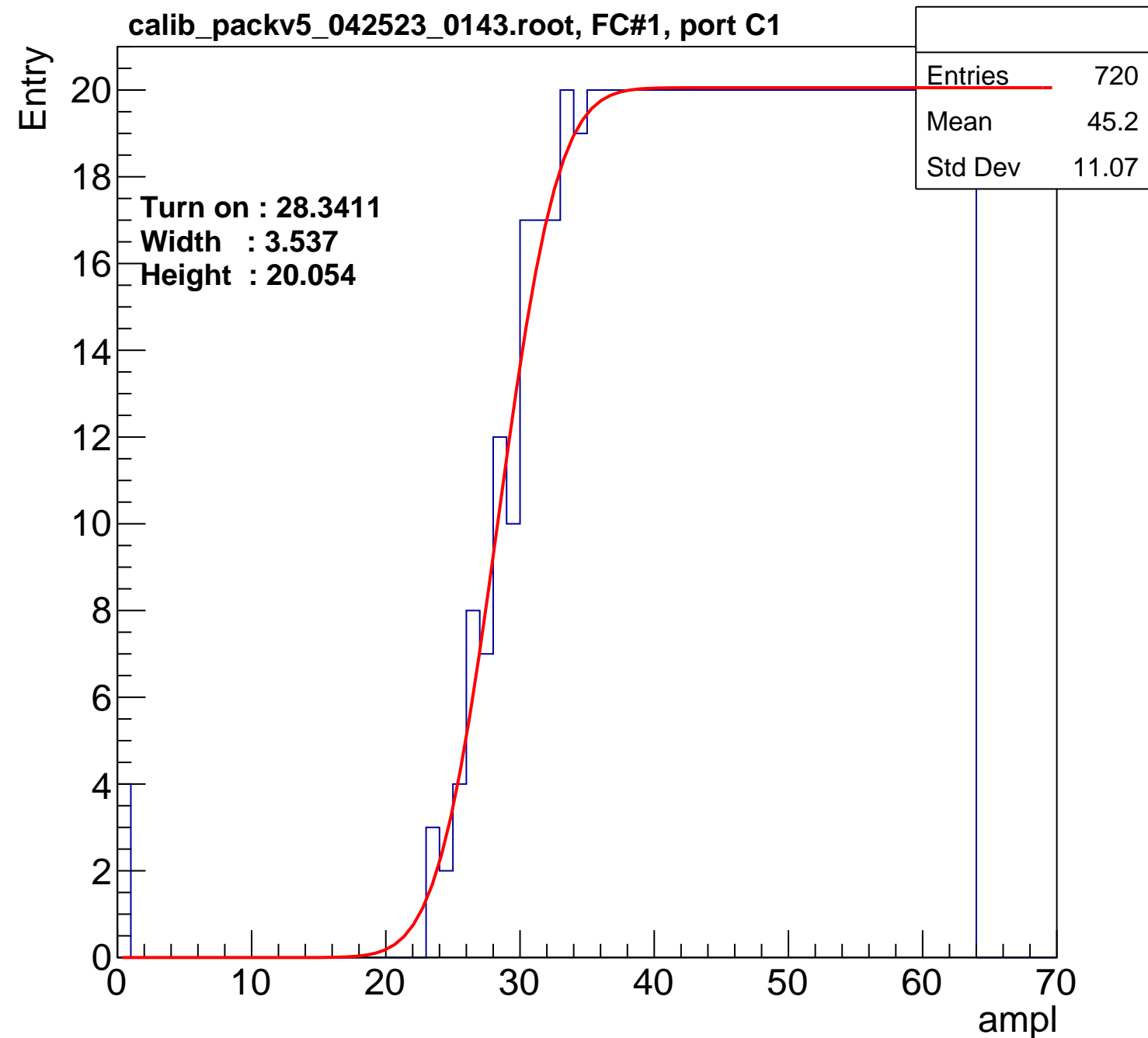
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3411
Width : 3.537
Height : 20.054

Entries	720
Mean	45.2
Std Dev	11.07

ampl



B0L101S, U25-ch15

calib_packv5_042523_0143.root, FC#1, port C1

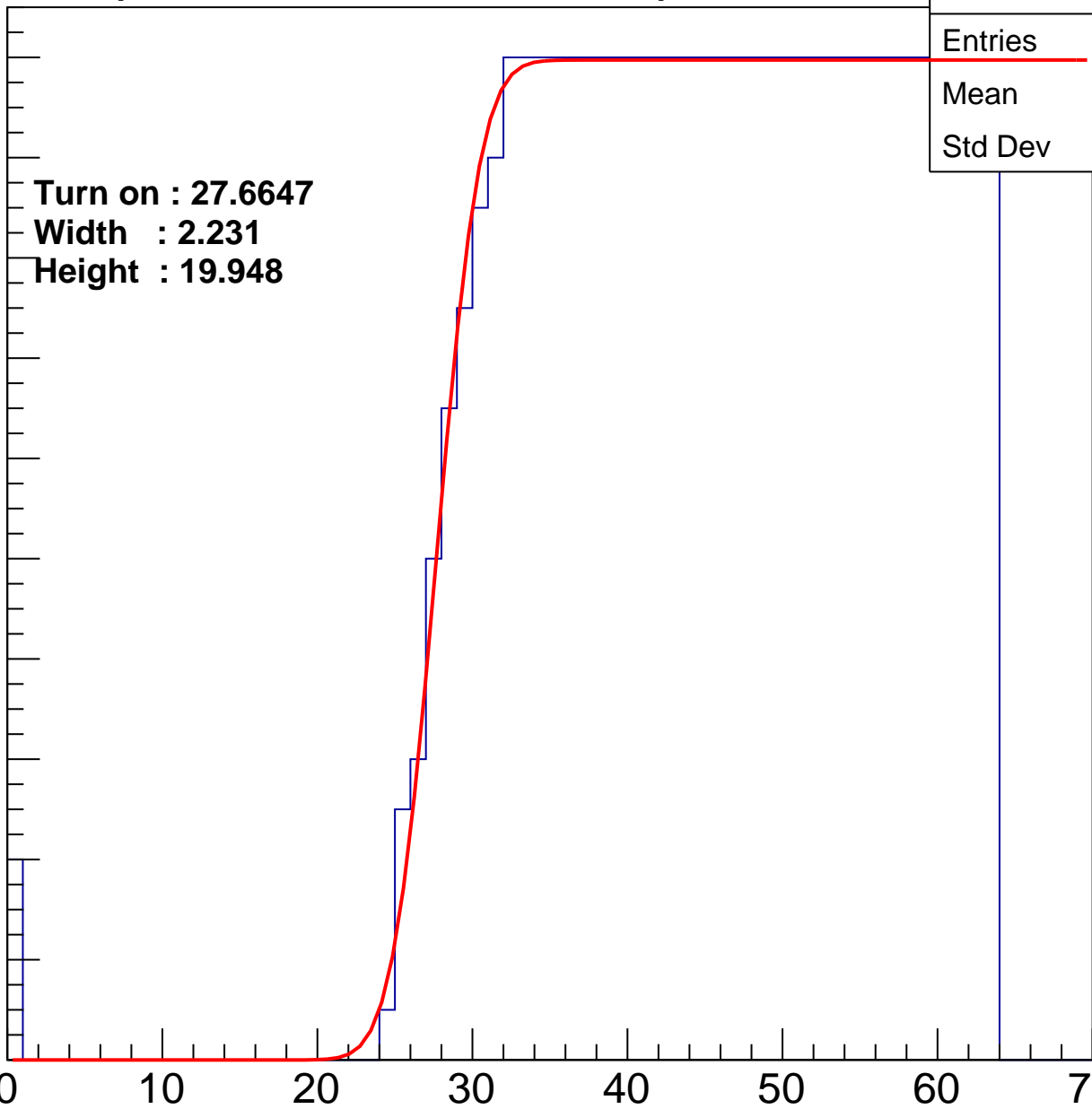
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6647
Width : 2.231
Height : 19.948

Entries	729
Mean	45.05
Std Dev	11.08

ampl



B0L101S, U25-ch16

calib_packv5_042523_0143.root, FC#1, port C1

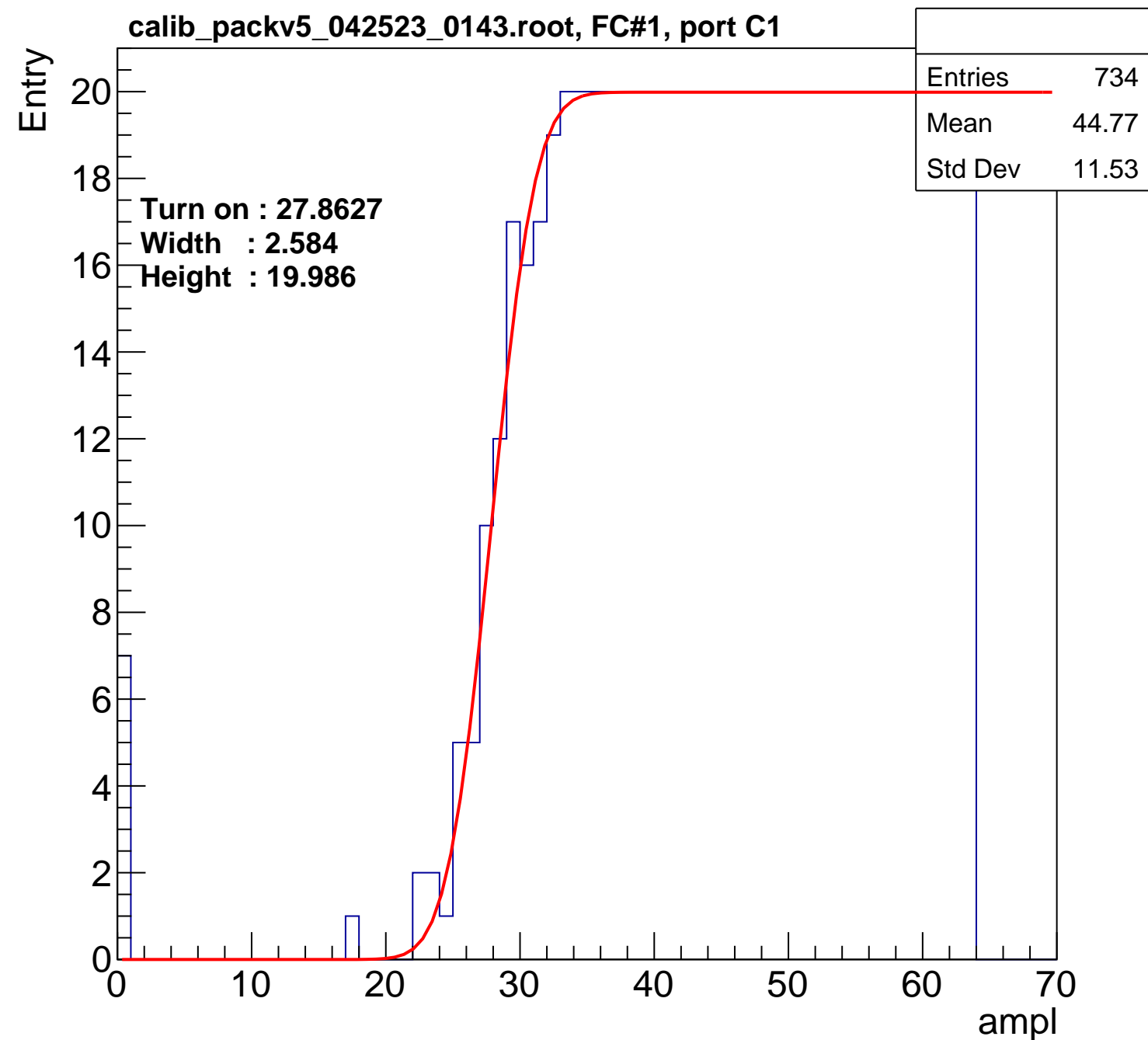
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8627
Width : 2.584
Height : 19.986

Entries	734
Mean	44.77
Std Dev	11.53

ampl



B0L101S, U25-ch17

calib_packv5_042523_0143.root, FC#1, port C1

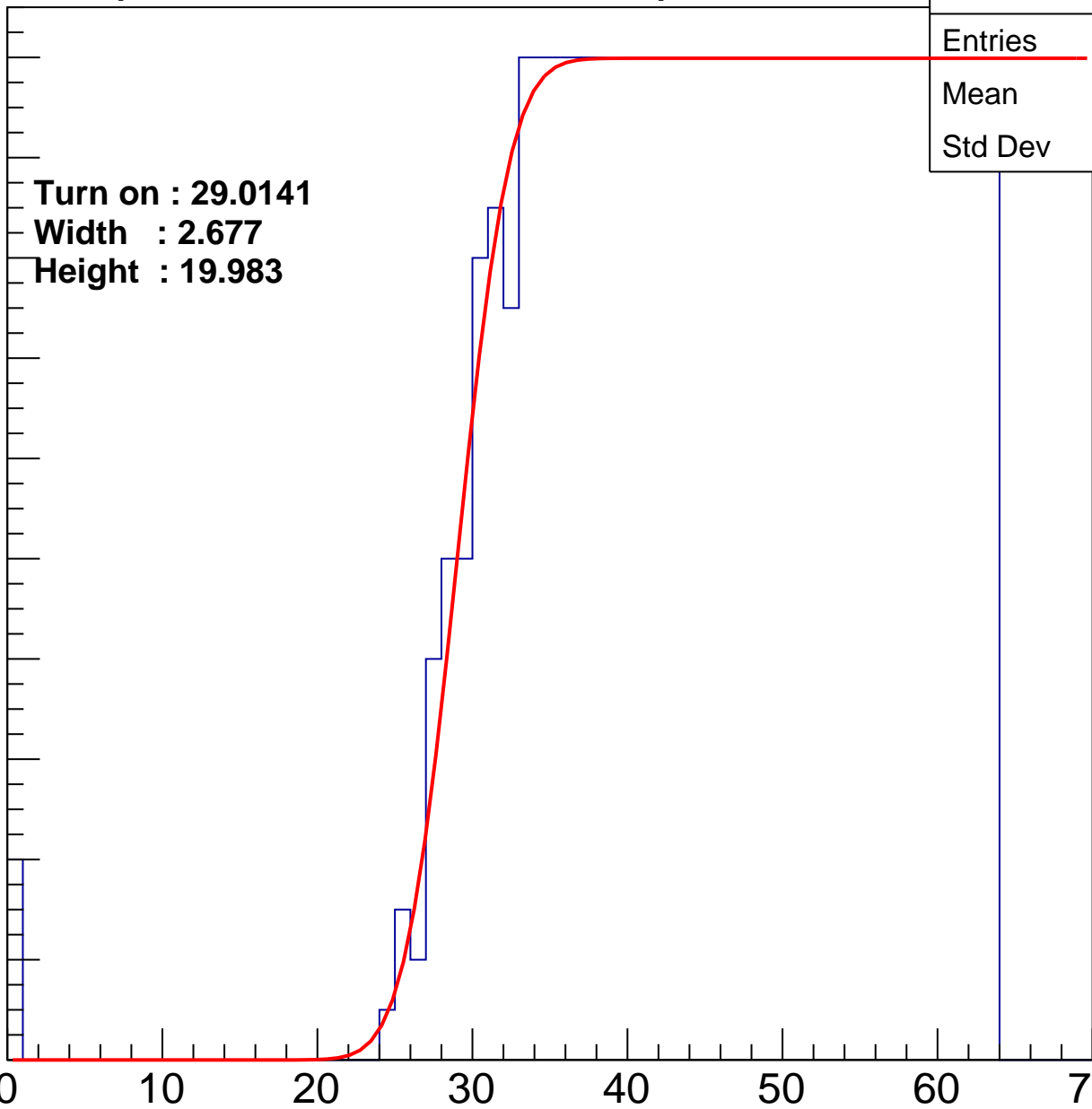
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0141
Width : 2.677
Height : 19.983

Entries	706
Mean	45.59
Std Dev	10.84

ampl



B0L101S, U25-ch18

calib_packv5_042523_0143.root, FC#1, port C1

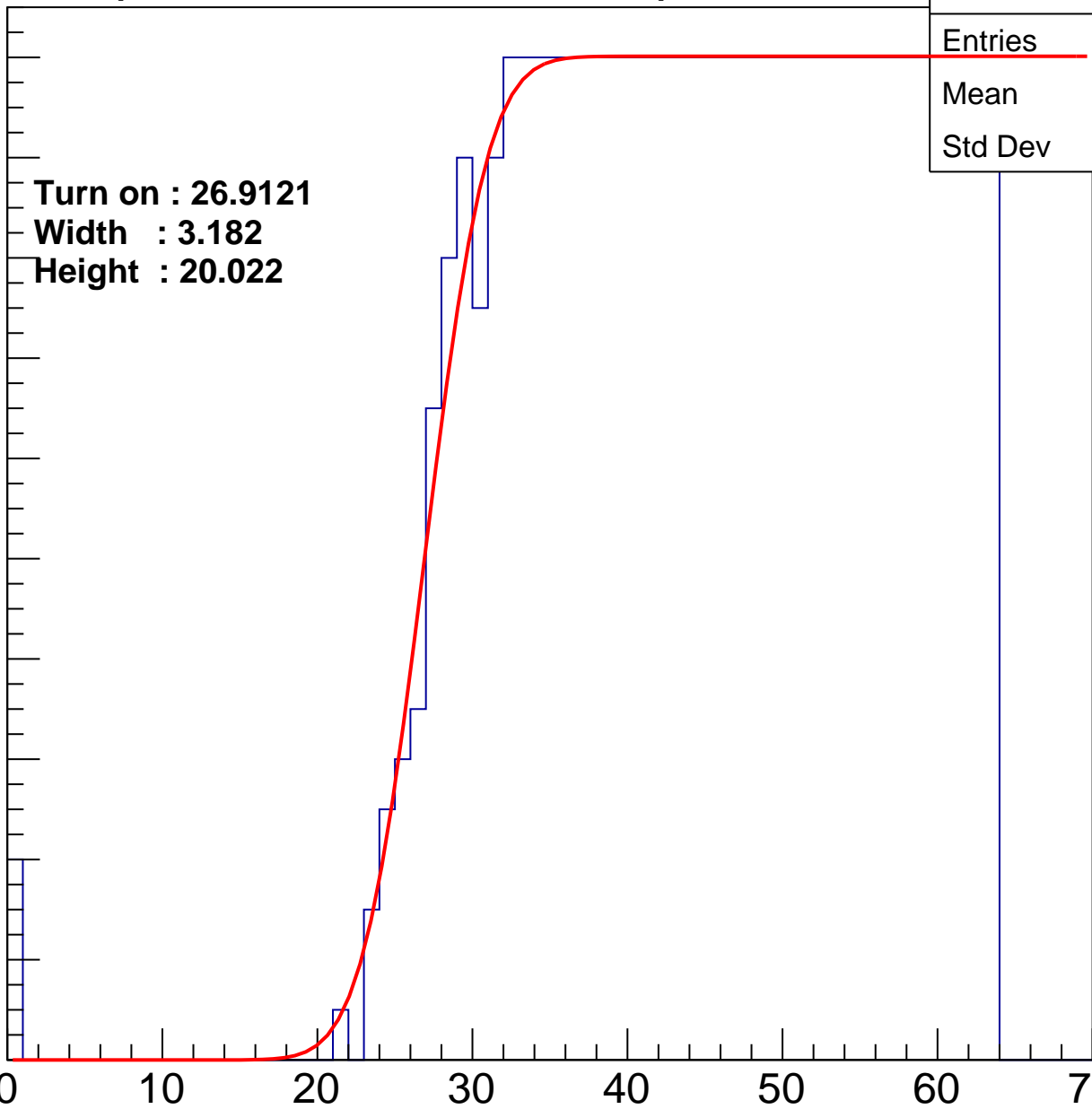
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9121
Width : 3.182
Height : 20.022

Entries	746
Mean	44.6
Std Dev	11.35

ampl



B0L101S, U25-ch19

calib_packv5_042523_0143.root, FC#1, port C1

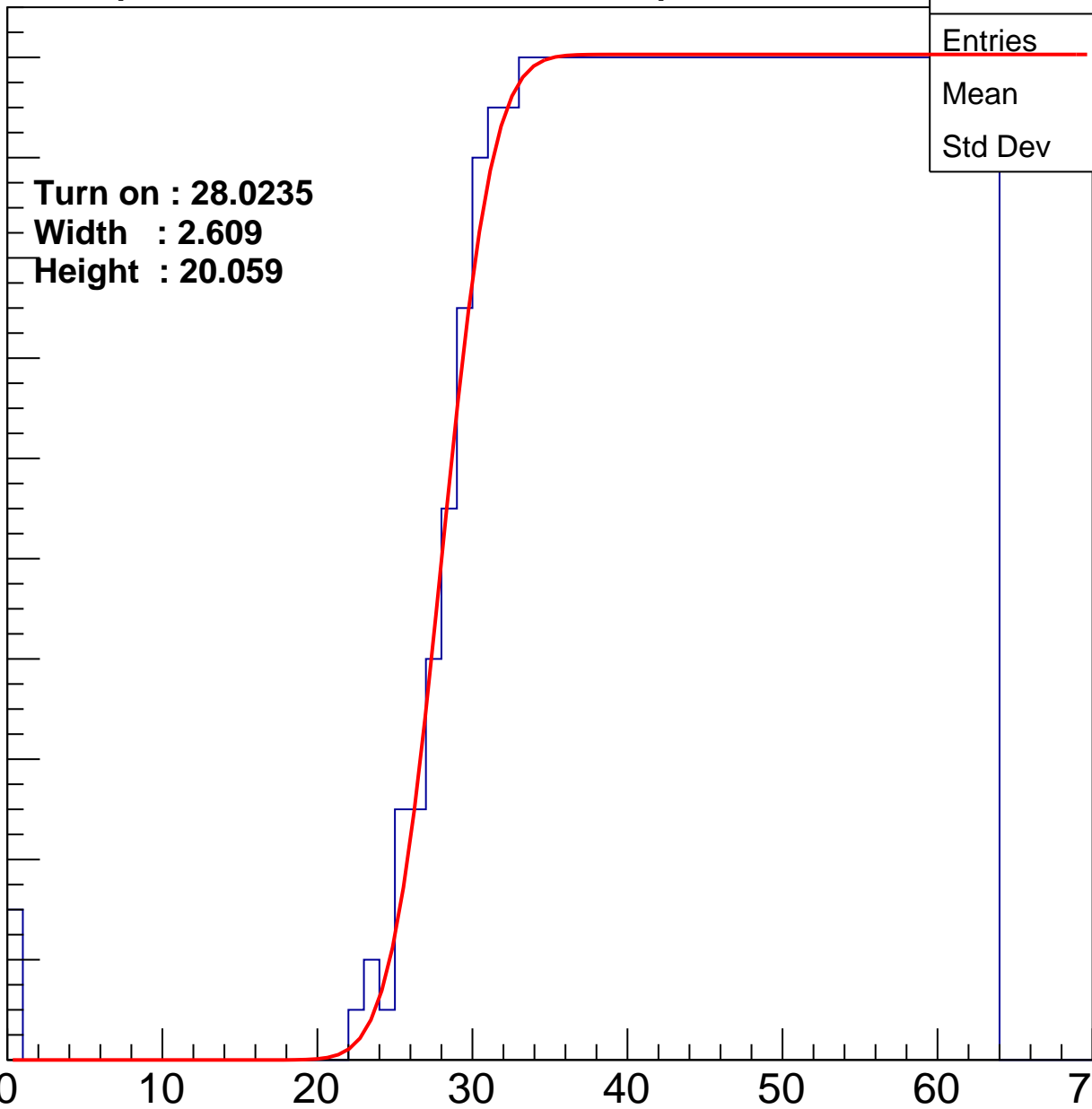
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0235
Width : 2.609
Height : 20.059

Entries	727
Mean	45.12
Std Dev	10.98

ampl



B0L101S, U25-ch20

calib_packv5_042523_0143.root, FC#1, port C1

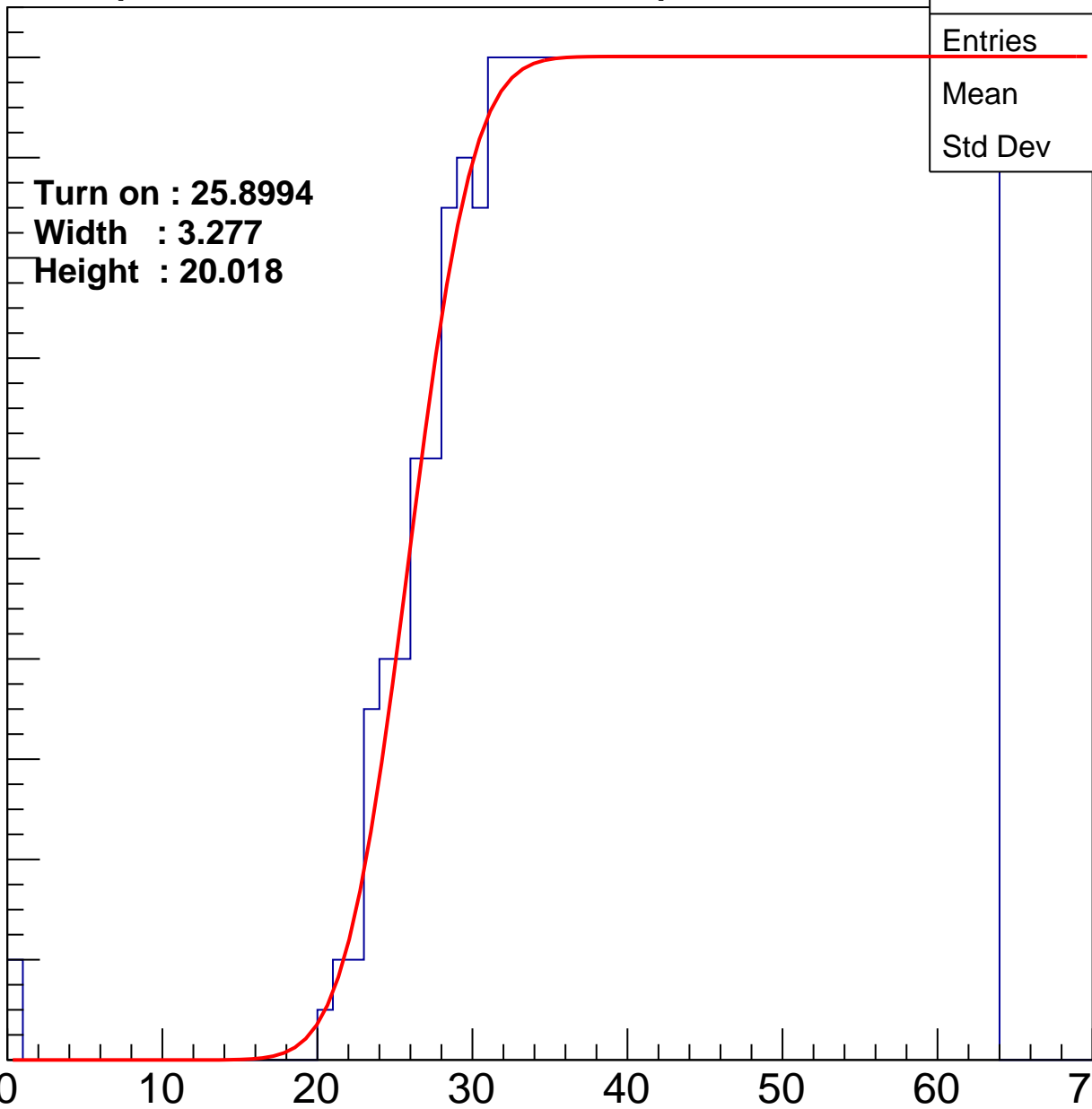
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8994
Width : 3.277
Height : 20.018

Entries	766
Mean	44.16
Std Dev	11.45

ampl



B0L101S, U25-ch21

calib_packv5_042523_0143.root, FC#1, port C1

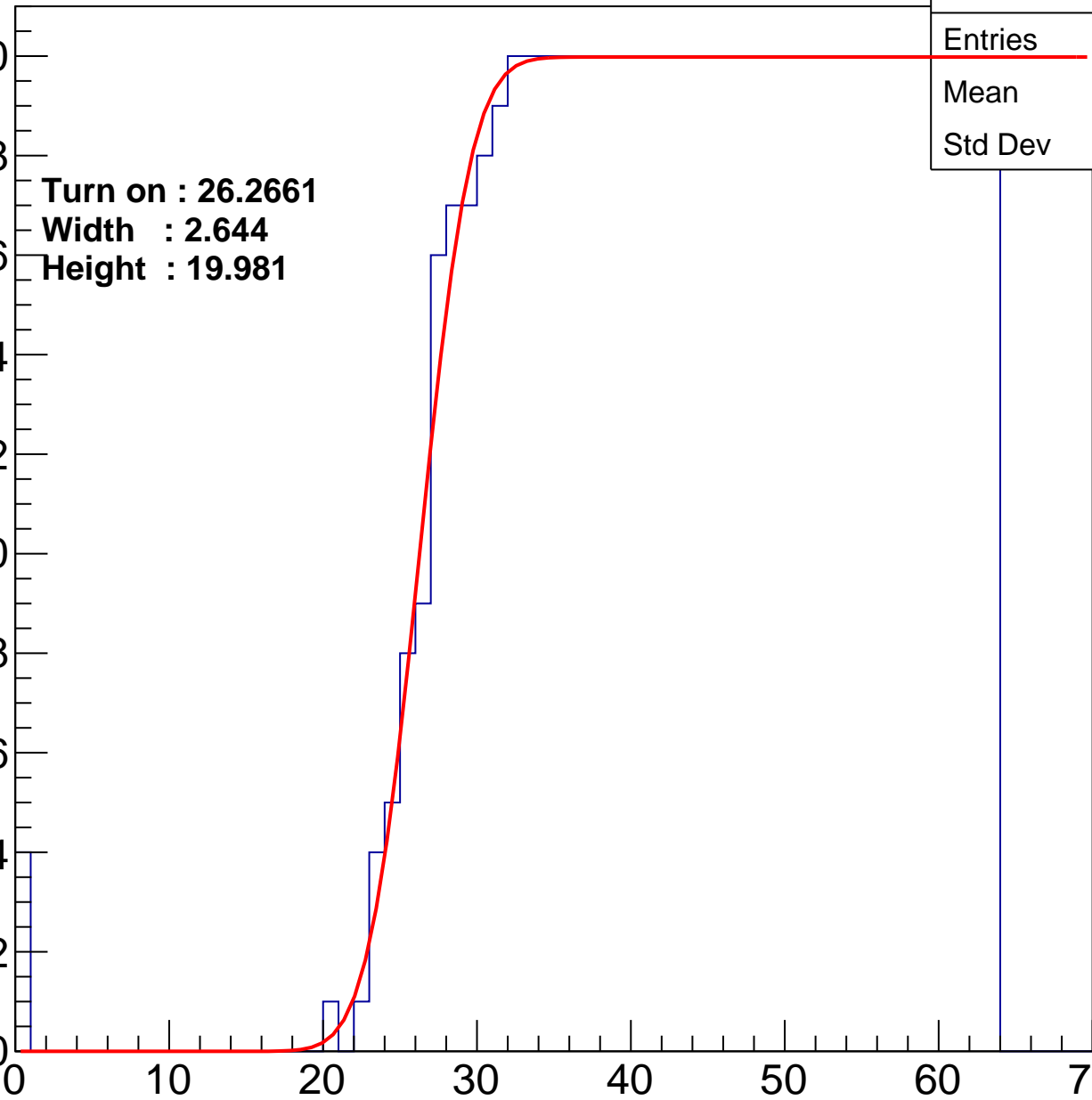
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2661
Width : 2.644
Height : 19.981

Entries	759
Mean	44.29
Std Dev	11.49

ampl



B0L101S, U25-ch22

calib_packv5_042523_0143.root, FC#1, port C1

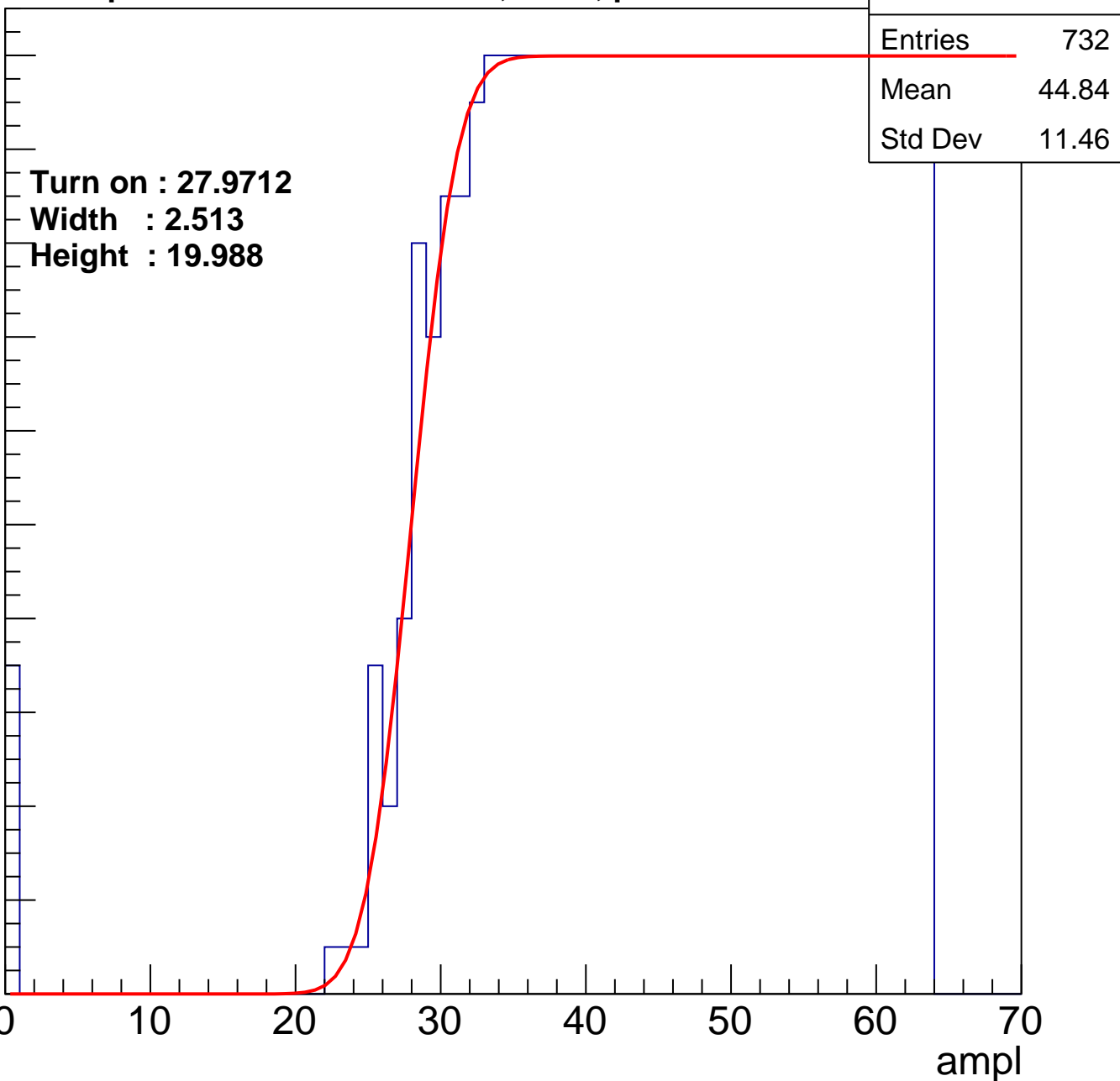
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9712
Width : 2.513
Height : 19.988

Entries	732
Mean	44.84
Std Dev	11.46

ampl



B0L101S, U25-ch23

calib_packv5_042523_0143.root, FC#1, port C1

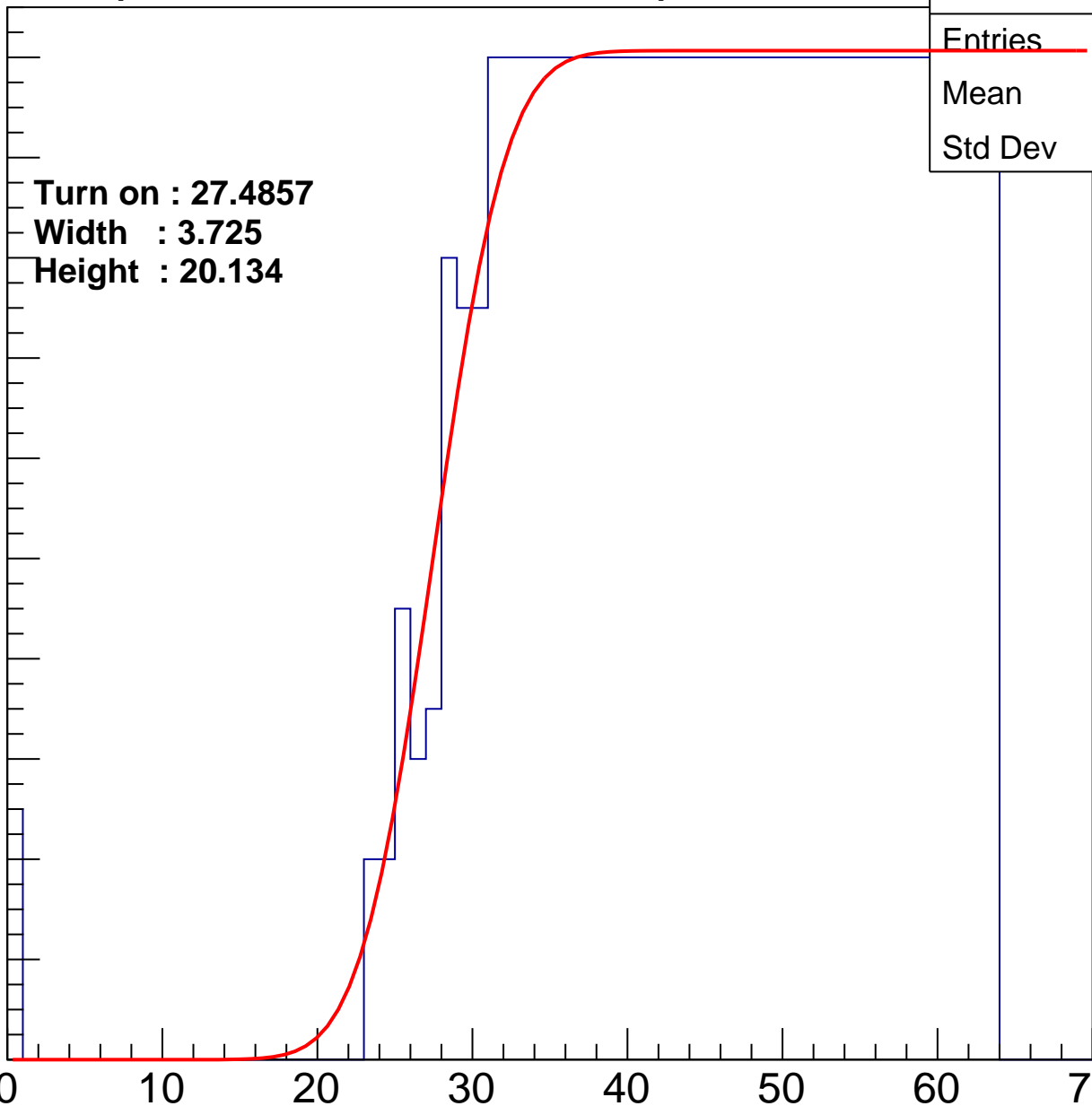
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4857
Width : 3.725
Height : 20.134

Entries	741
Mean	44.68
Std Dev	11.39

ampl



B0L101S, U25-ch24

calib_packv5_042523_0143.root, FC#1, port C1

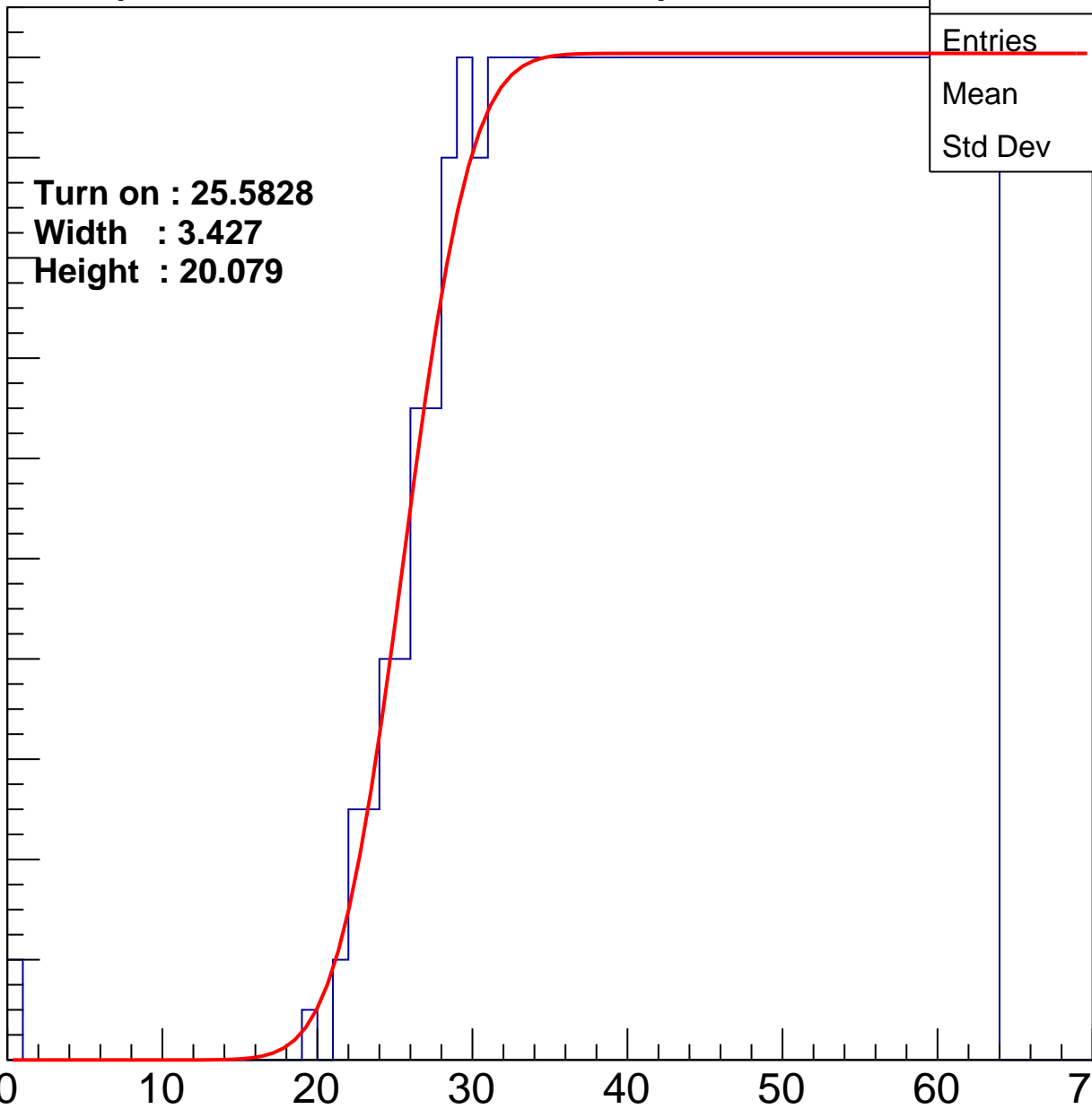
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5828
Width : 3.427
Height : 20.079

Entries	773
Mean	44
Std Dev	11.52

ampl



B0L101S, U25-ch25

calib_packv5_042523_0143.root, FC#1, port C1

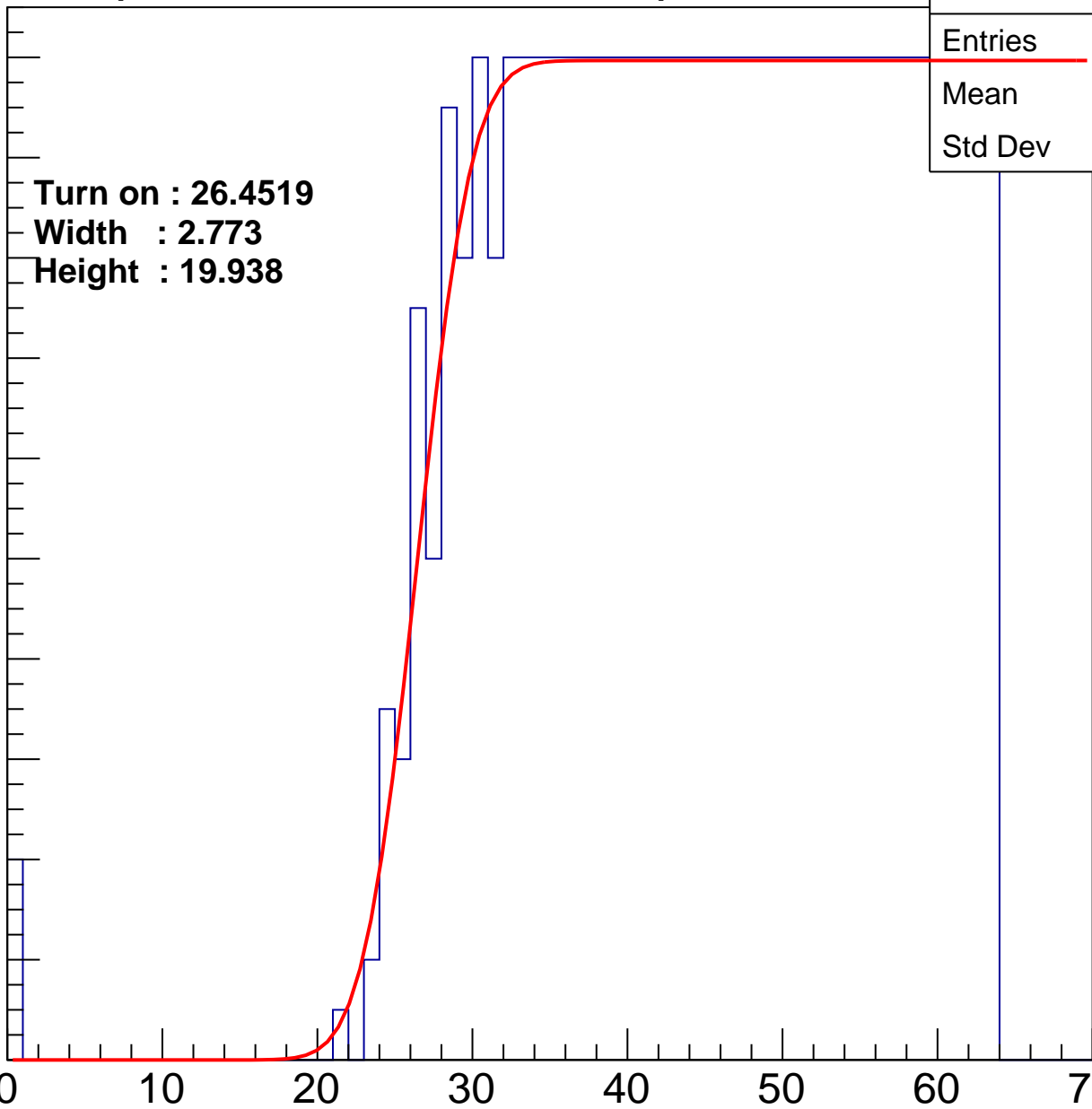
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4519
Width : 2.773
Height : 19.938

Entries	756
Mean	44.36
Std Dev	11.46

ampl



B0L101S, U25-ch26

calib_packv5_042523_0143.root, FC#1, port C1

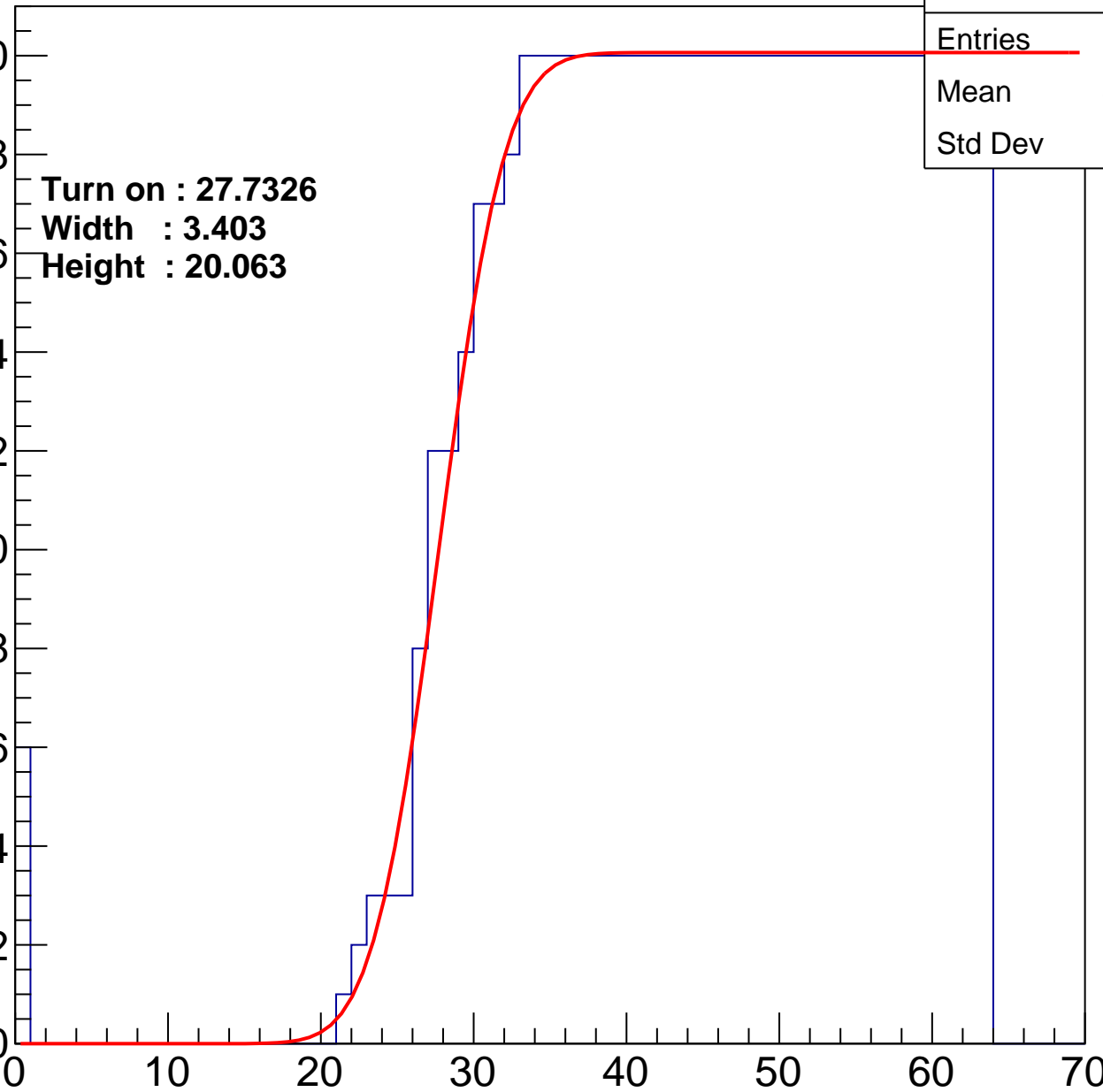
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7326
Width : 3.403
Height : 20.063

Entries	736
Mean	44.74
Std Dev	11.48

ampl



B0L101S, U25-ch27

calib_packv5_042523_0143.root, FC#1, port C1

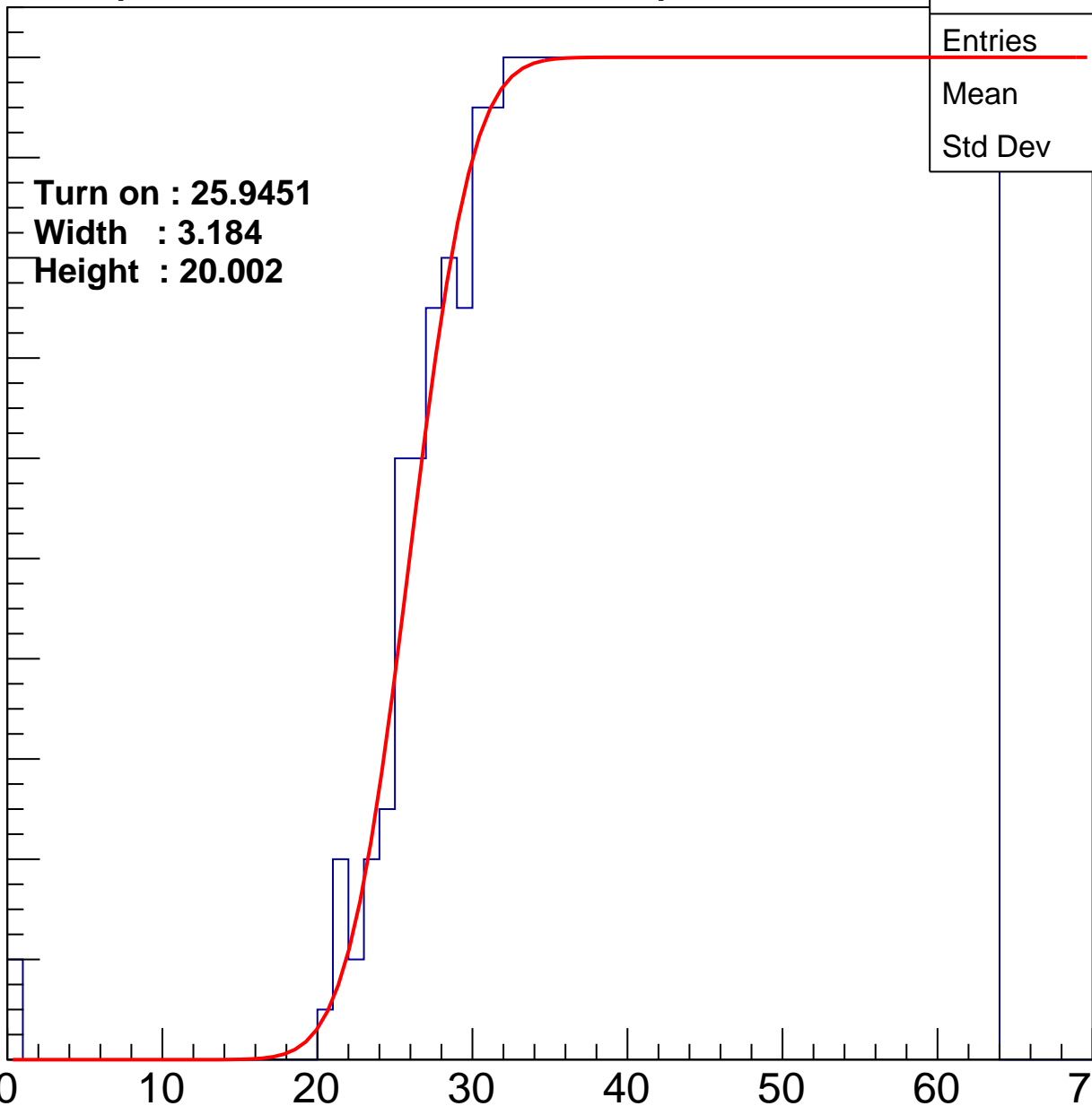
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9451
Width : 3.184
Height : 20.002

Entries	766
Mean	44.15
Std Dev	11.46

ampl



B0L101S, U25-ch28

calib_packv5_042523_0143.root, FC#1, port C1

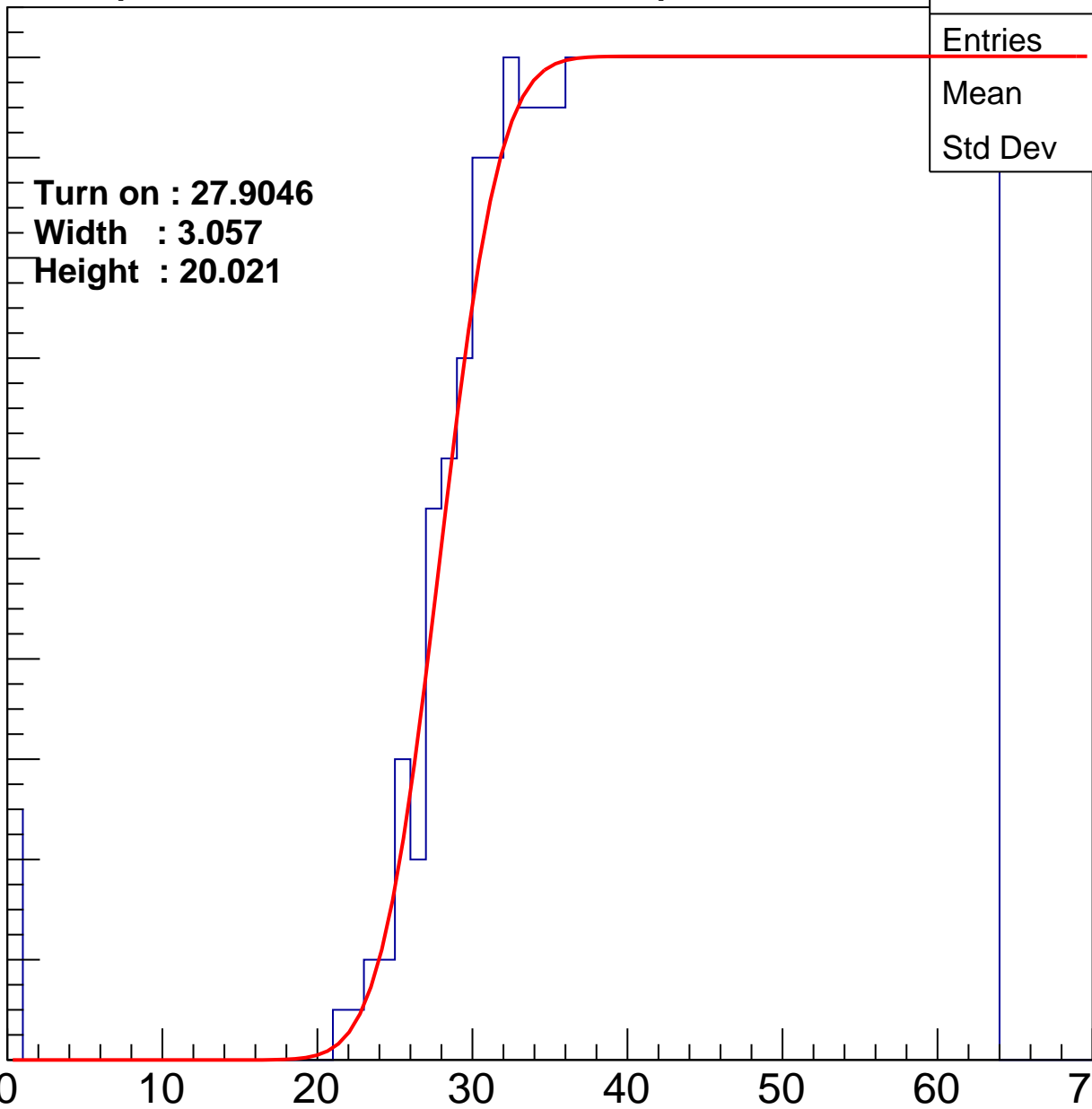
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9046
Width : 3.057
Height : 20.021

Entries	731
Mean	44.91
Std Dev	11.3

ampl



B0L101S, U25-ch29

calib_packv5_042523_0143.root, FC#1, port C1

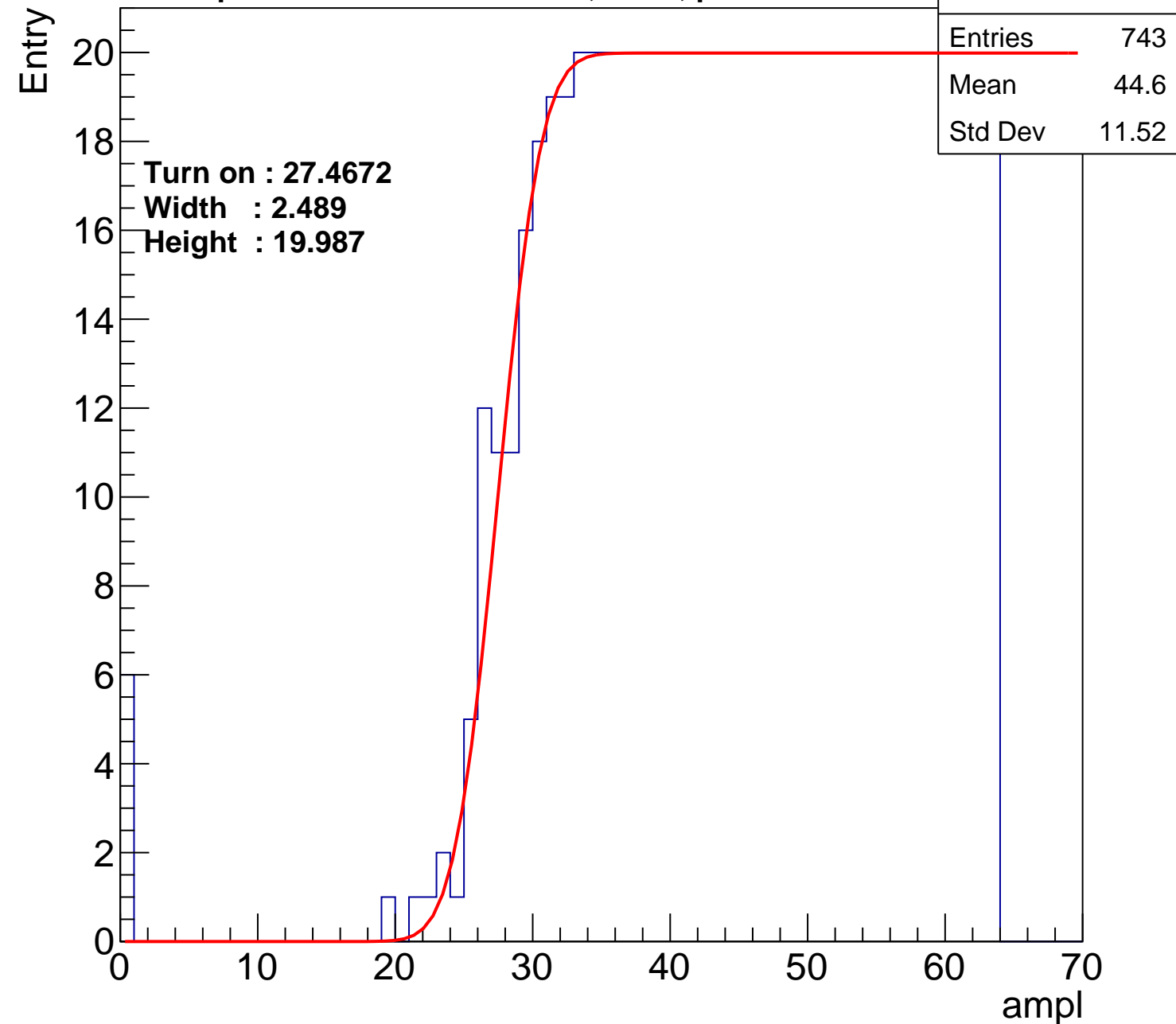
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4672
Width : 2.489
Height : 19.987

Entries	743
Mean	44.6
Std Dev	11.52

ampl



B0L101S, U25-ch30

calib_packv5_042523_0143.root, FC#1, port C1

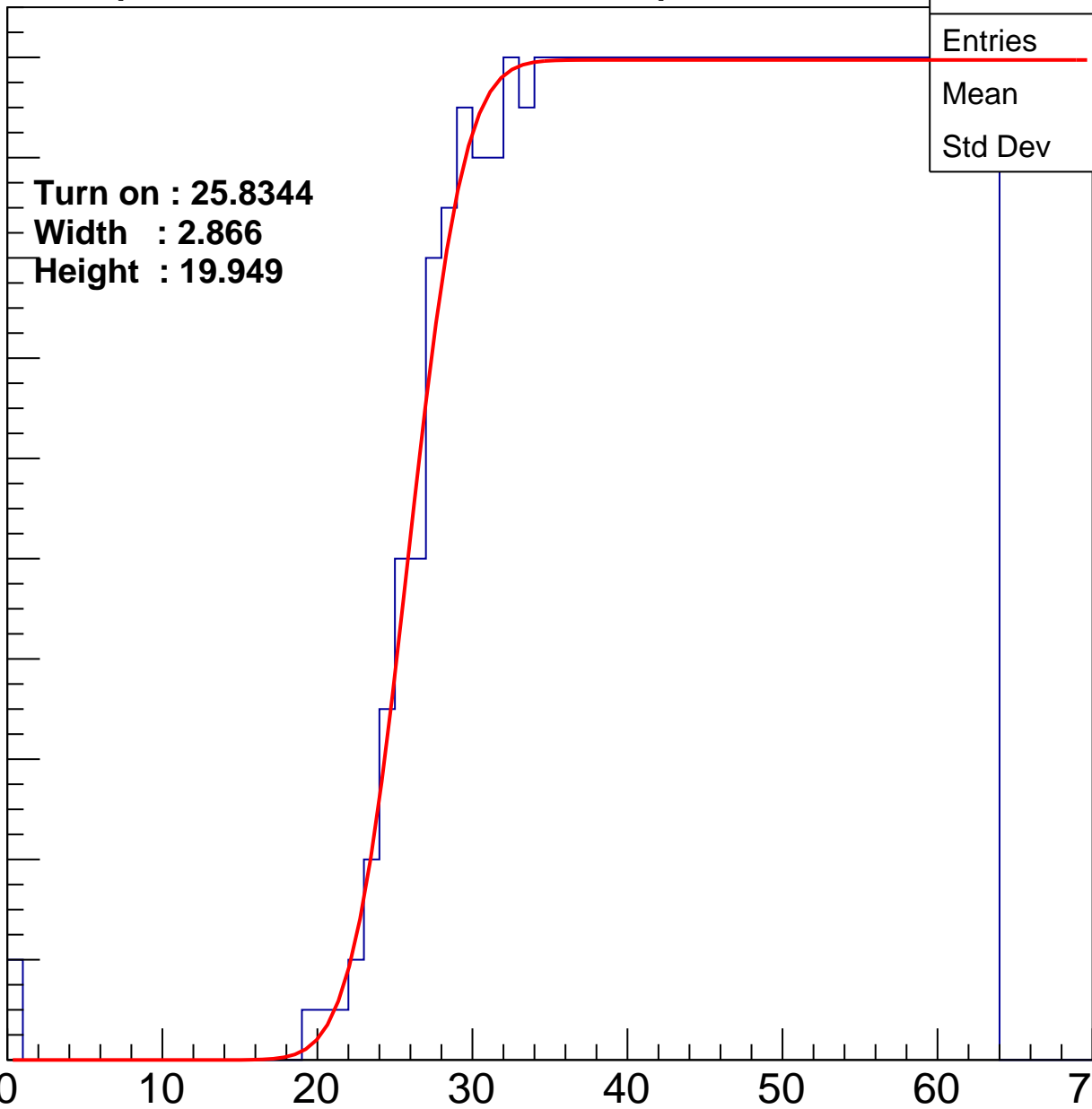
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8344
Width : 2.866
Height : 19.949

Entries	765
Mean	44.18
Std Dev	11.44

ampl



B0L101S, U25-ch31

calib_packv5_042523_0143.root, FC#1, port C1

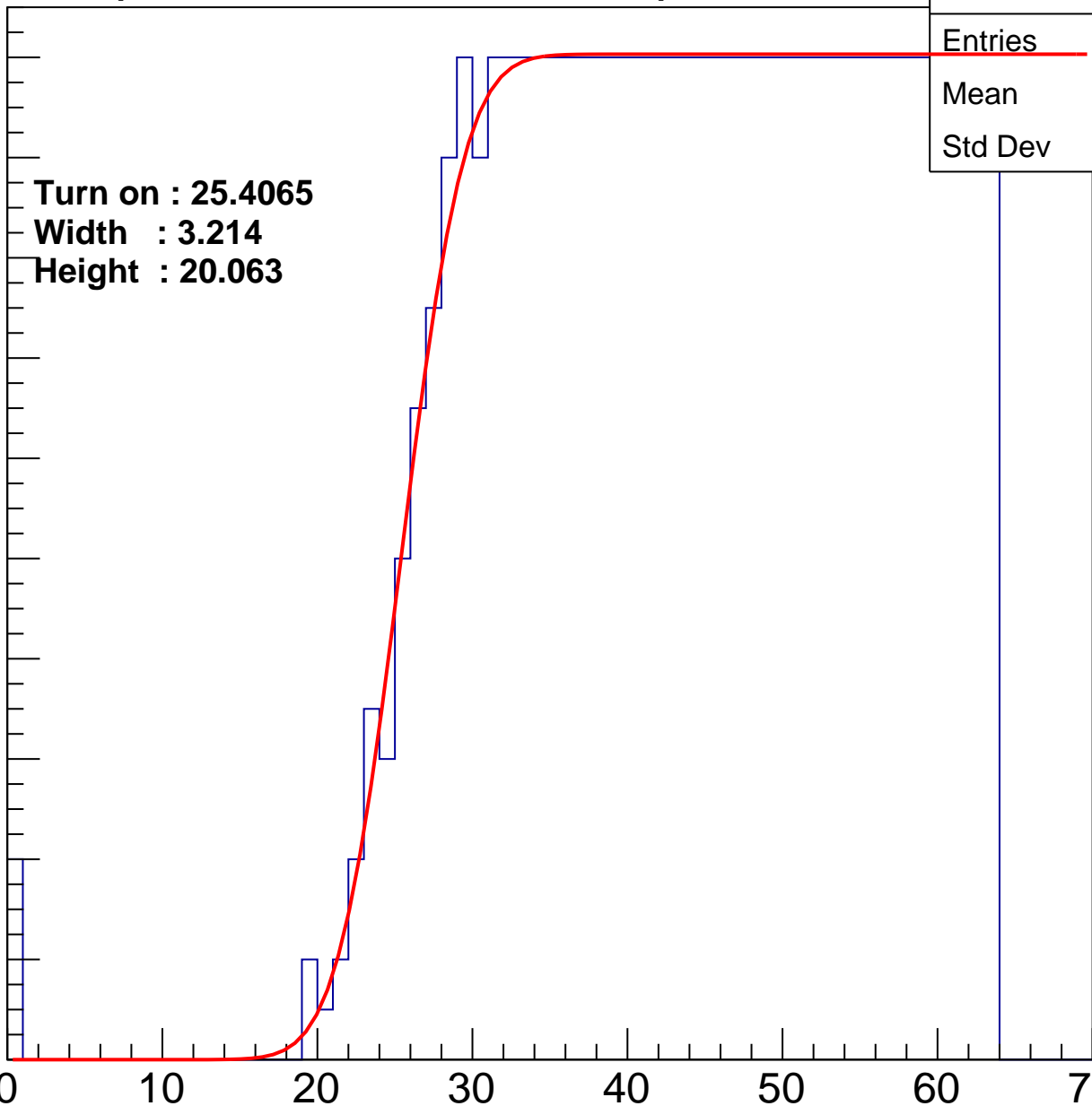
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4065
Width : 3.214
Height : 20.063

Entries	780
Mean	43.76
Std Dev	11.8

ampl



B0L101S, U25-ch32

calib_packv5_042523_0143.root, FC#1, port C1

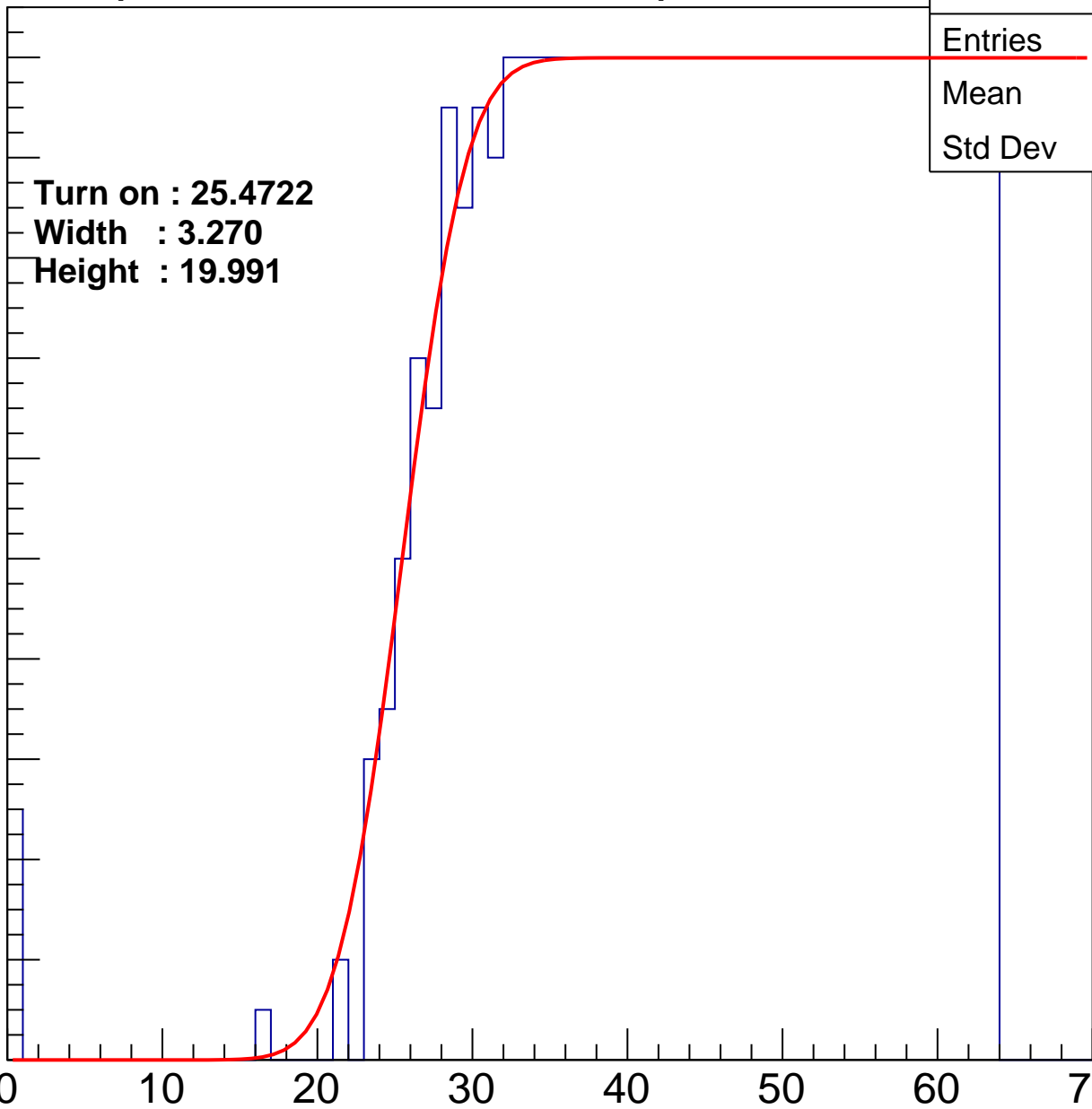
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4722
Width : 3.270
Height : 19.991

Entries	771
Mean	43.95
Std Dev	11.77

ampl



B0L101S, U25-ch33

calib_packv5_042523_0143.root, FC#1, port C1

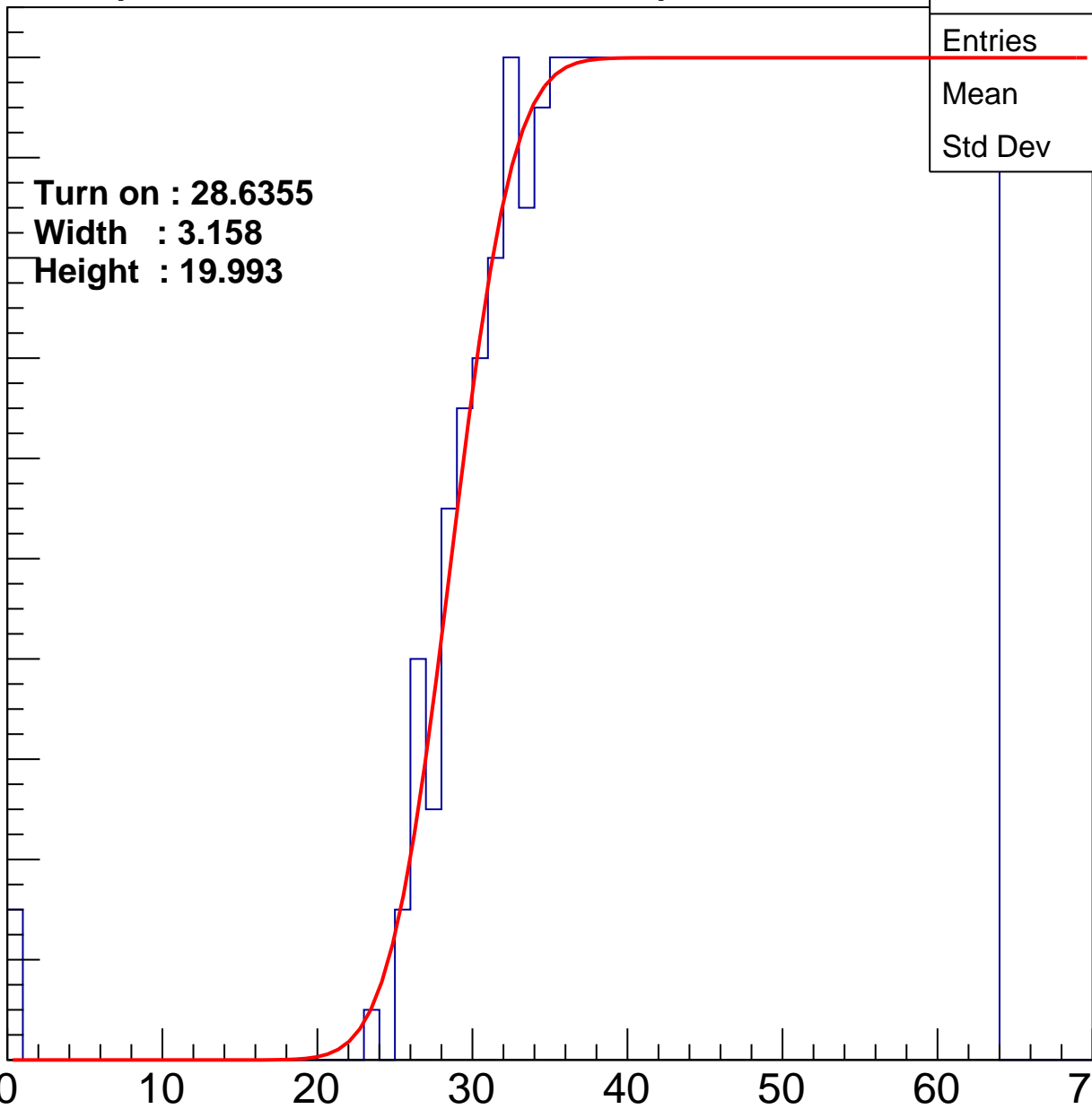
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6355
Width : 3.158
Height : 19.993

Entries	710
Mean	45.51
Std Dev	10.8

ampl



B0L101S, U25-ch34

calib_packv5_042523_0143.root, FC#1, port C1

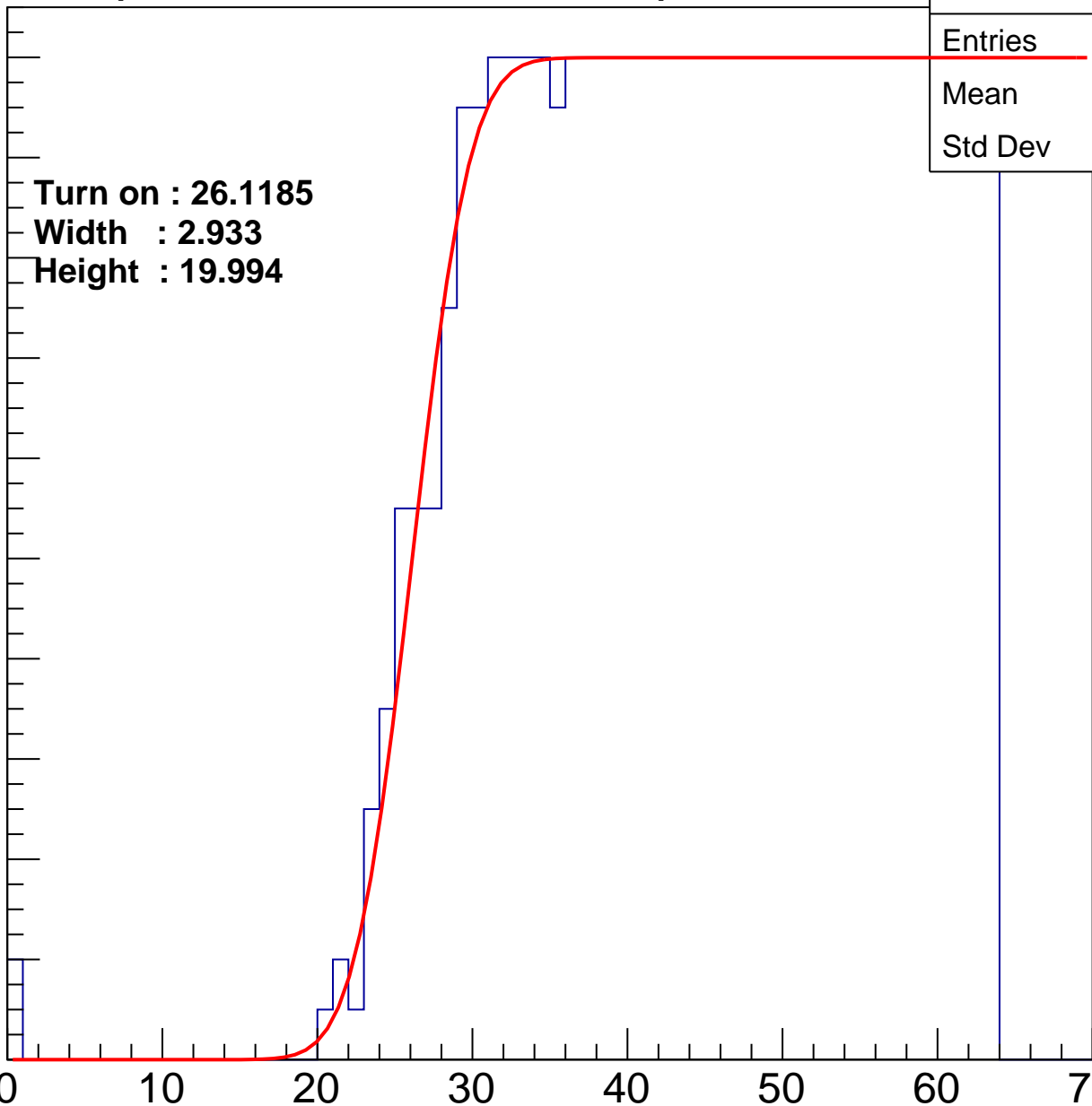
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1185
Width : 2.933
Height : 19.994

Entries	763
Mean	44.23
Std Dev	11.4

ampl



B0L101S, U25-ch35

calib_packv5_042523_0143.root, FC#1, port C1

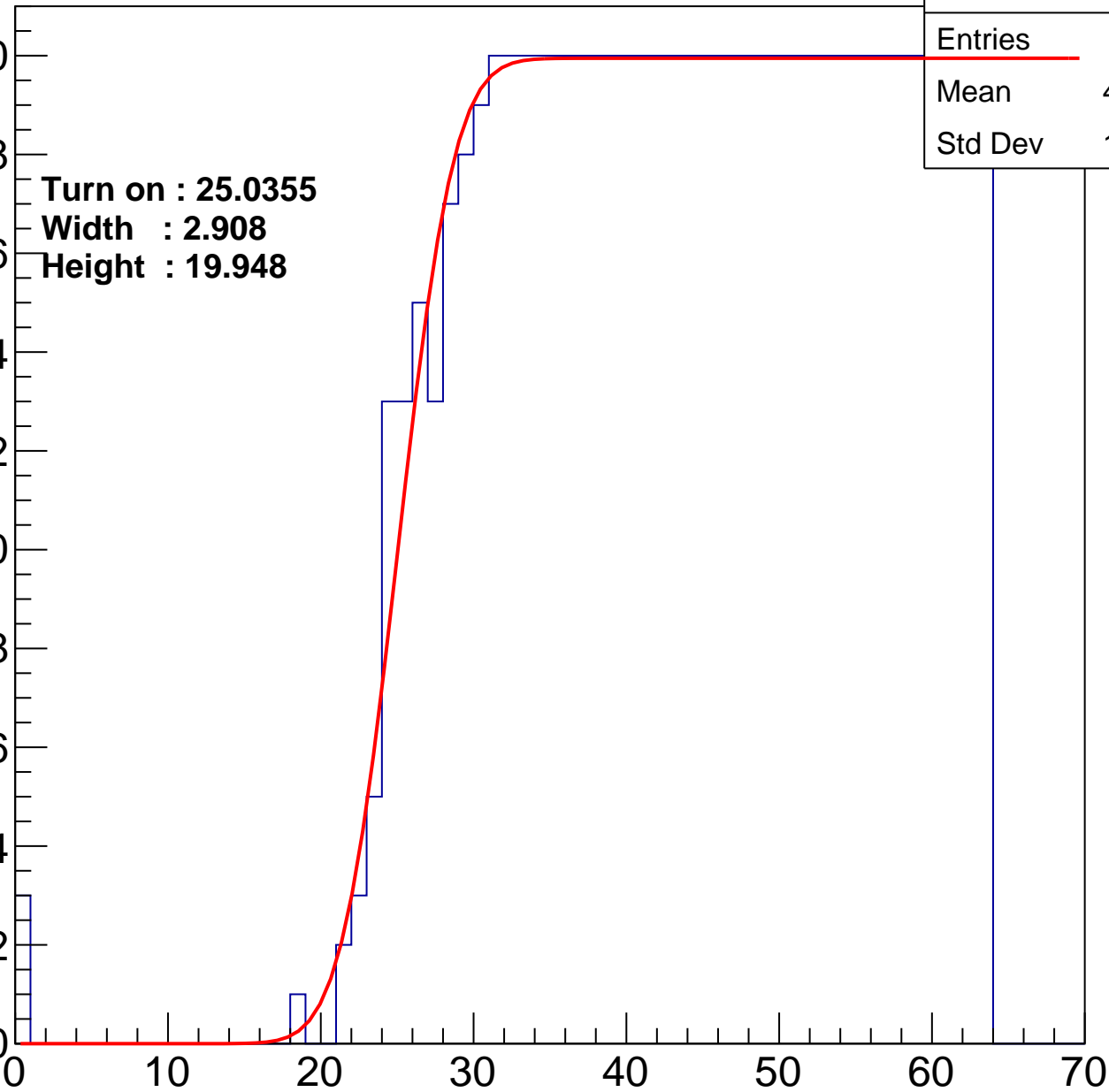
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0355
Width : 2.908
Height : 19.948

Entries	782
Mean	43.74
Std Dev	11.73

ampl



B0L101S, U25-ch36

calib_packv5_042523_0143.root, FC#1, port C1

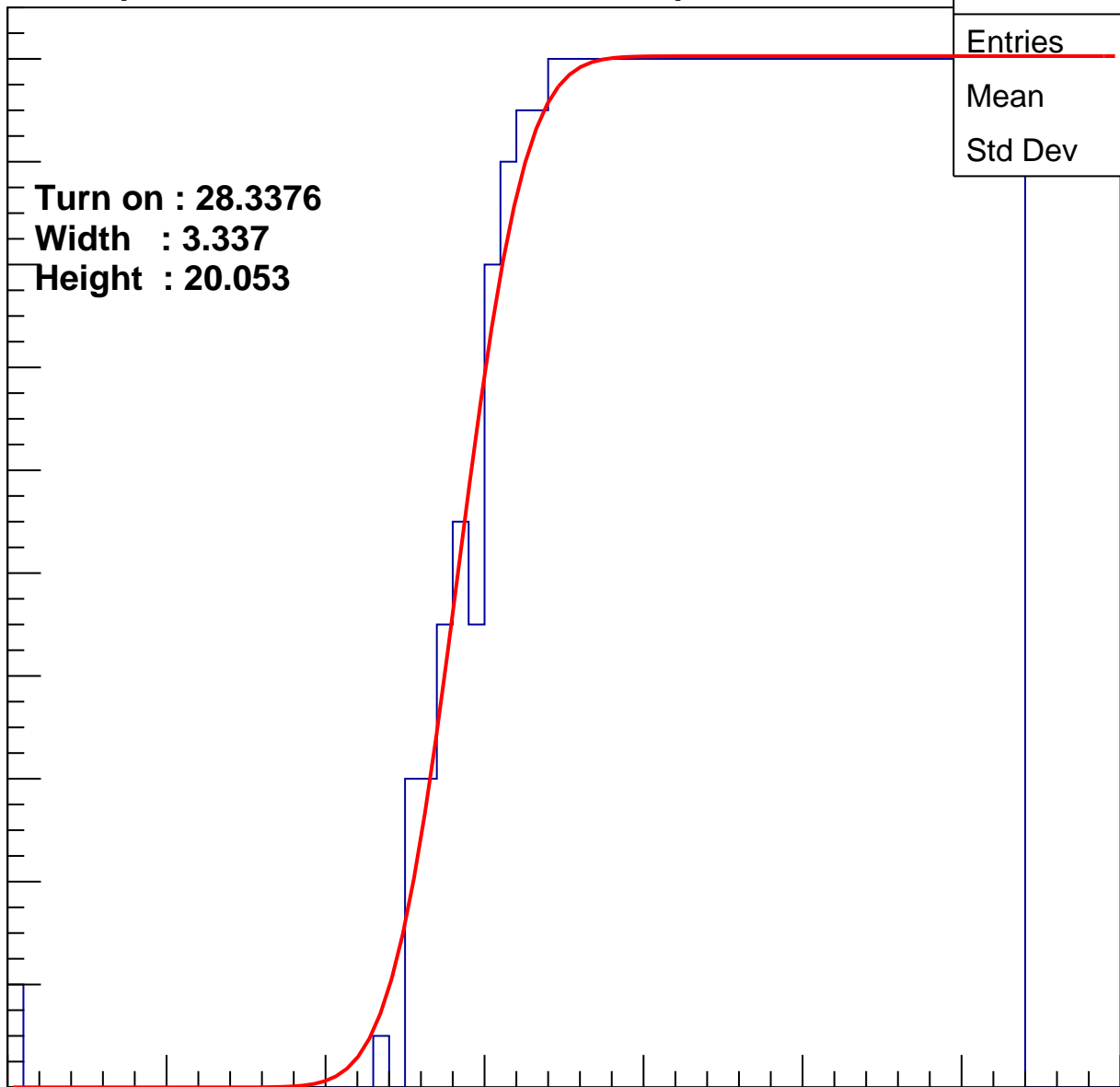
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3376
Width : 3.337
Height : 20.053

Entries	716
Mean	45.41
Std Dev	10.75

ampl



B0L101S, U25-ch37

calib_packv5_042523_0143.root, FC#1, port C1

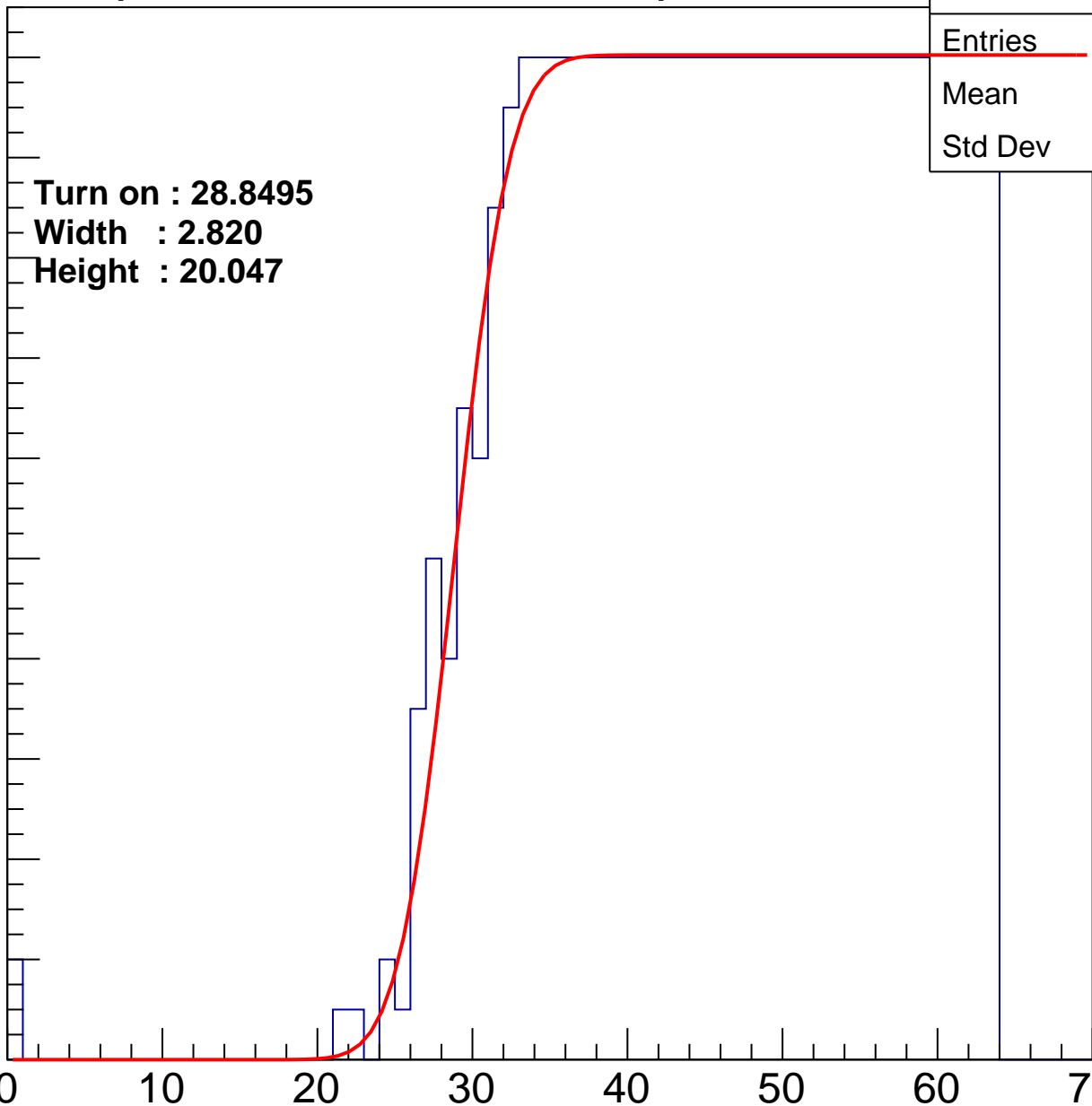
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8495
Width : 2.820
Height : 20.047

Entries	713
Mean	45.48
Std Dev	10.73

ampl



B0L101S, U25-ch38

calib_packv5_042523_0143.root, FC#1, port C1

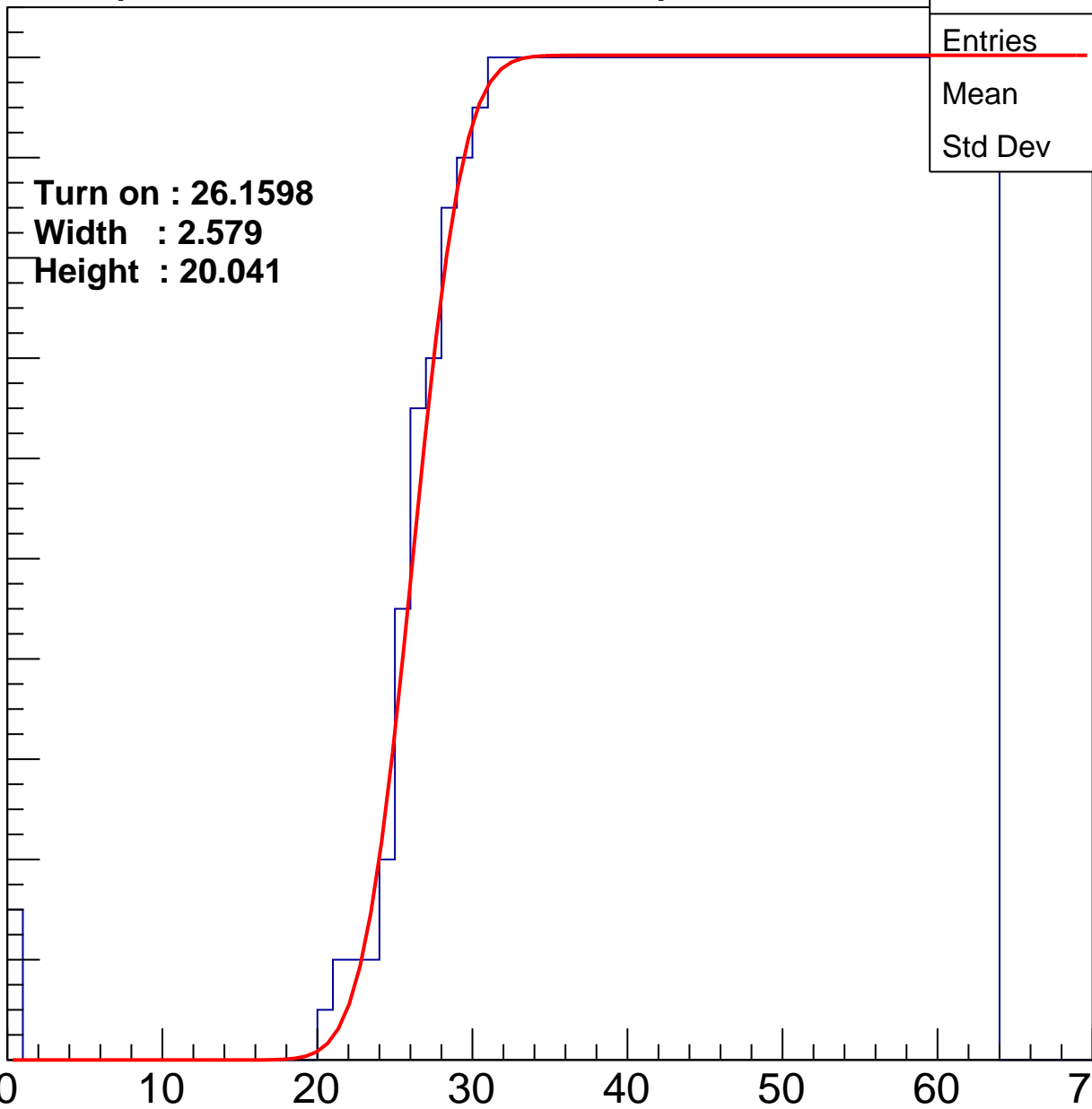
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1598
Width : 2.579
Height : 20.041

Entries	764
Mean	44.21
Std Dev	11.46

ampl



B0L101S, U25-ch39

calib_packv5_042523_0143.root, FC#1, port C1

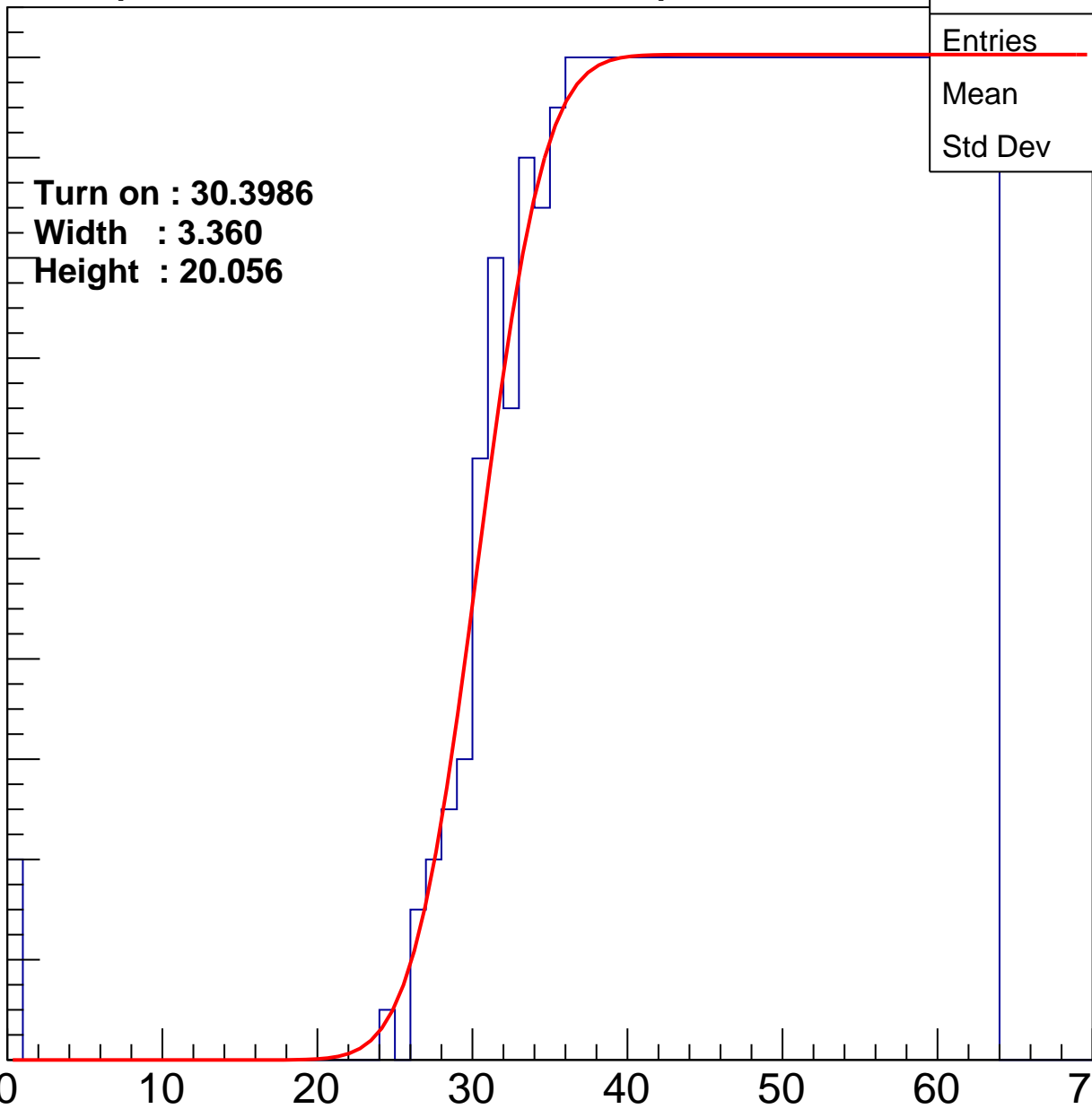
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 30.3986
Width : 3.360
Height : 20.056

Entries	678
Mean	46.24
Std Dev	10.54

ampl



B0L101S, U25-ch40

calib_packv5_042523_0143.root, FC#1, port C1

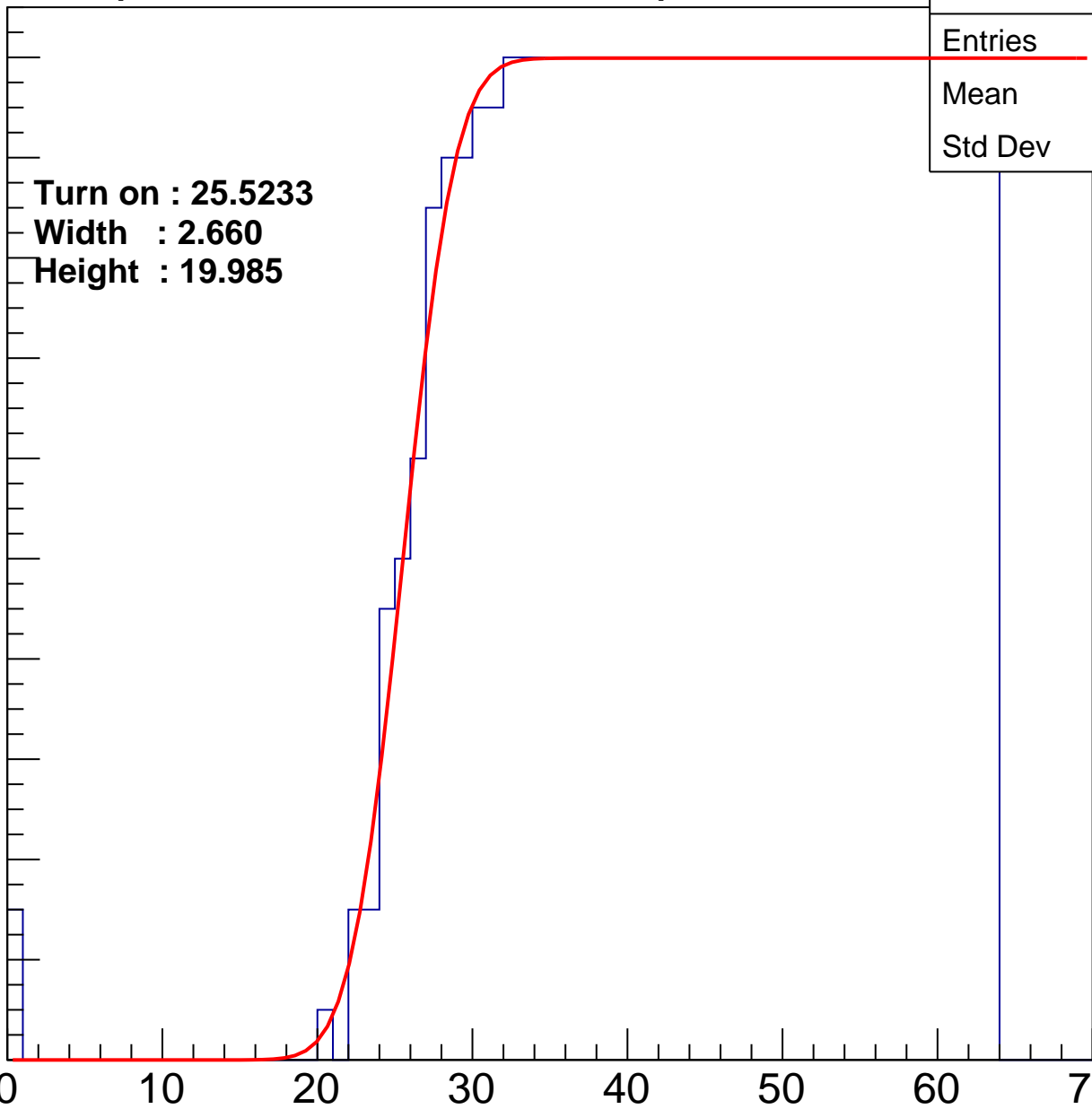
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5233
Width : 2.660
Height : 19.985

Entries	772
Mean	44.01
Std Dev	11.56

ampl



B0L101S, U25-ch41

calib_packv5_042523_0143.root, FC#1, port C1

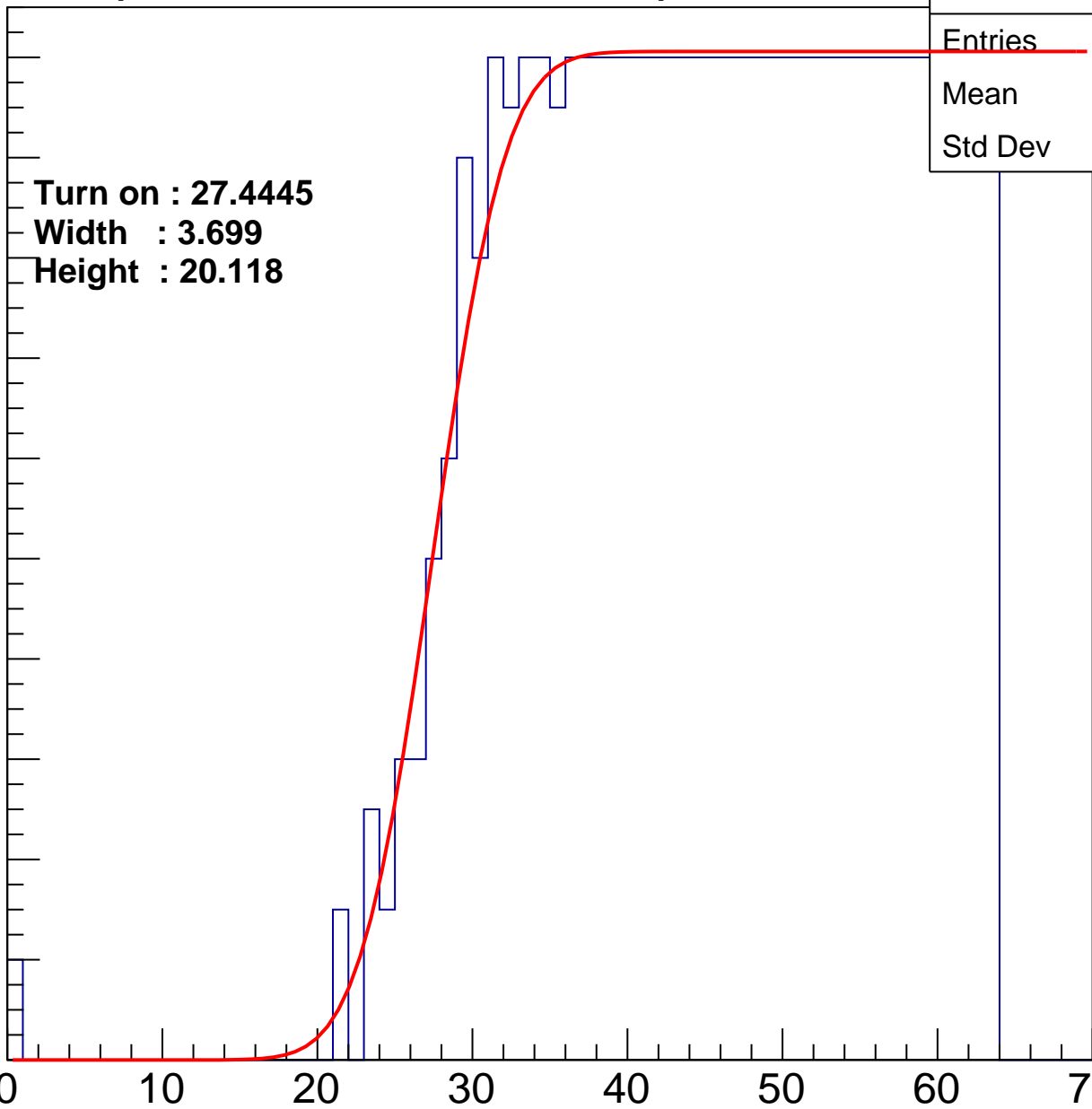
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4445
Width : 3.699
Height : 20.118

Entries	739
Mean	44.81
Std Dev	11.11

ampl



B0L101S, U25-ch42

calib_packv5_042523_0143.root, FC#1, port C1

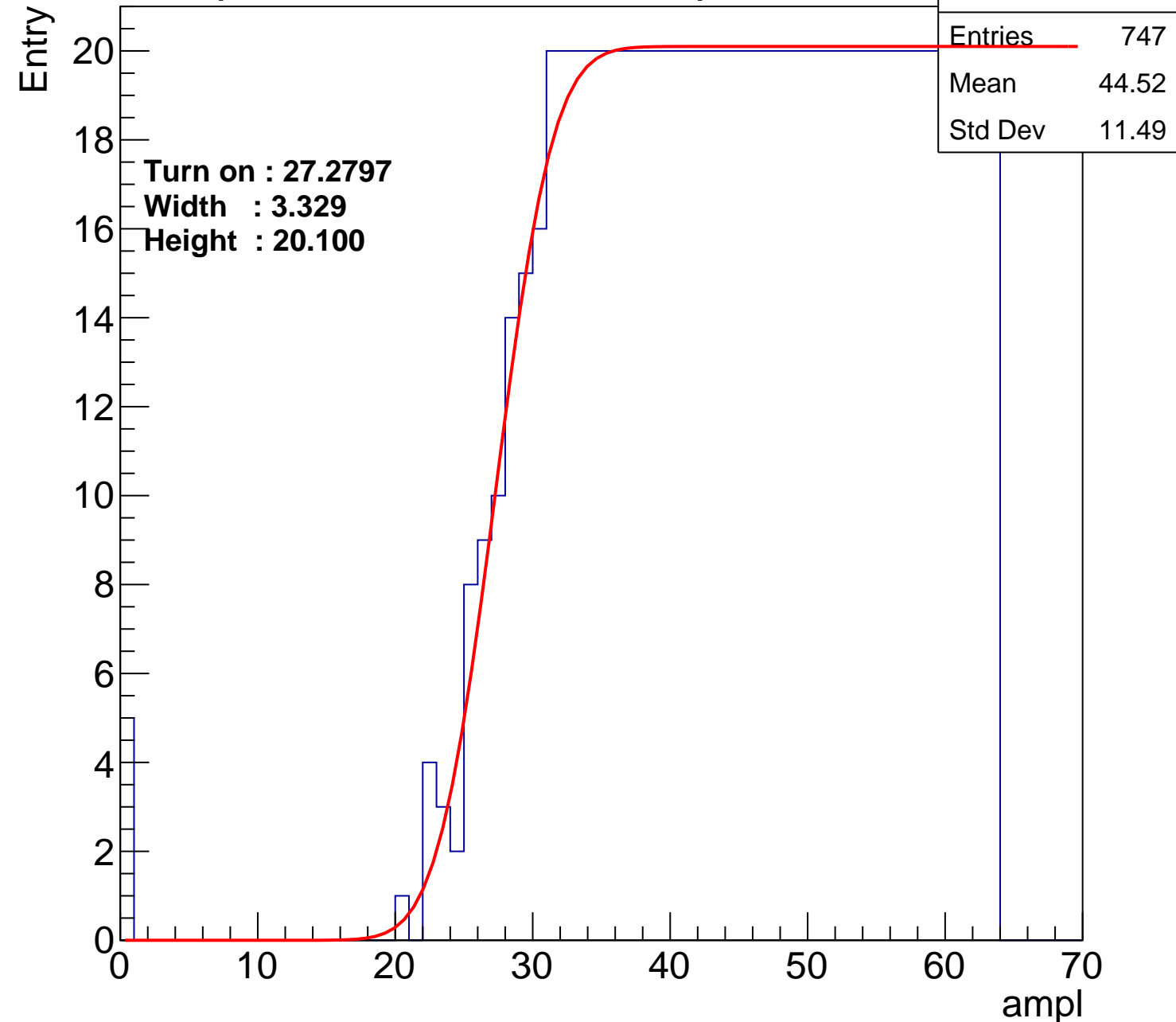
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2797
Width : 3.329
Height : 20.100

Entries	747
Mean	44.52
Std Dev	11.49

ampl



B0L101S, U25-ch43

calib_packv5_042523_0143.root, FC#1, port C1

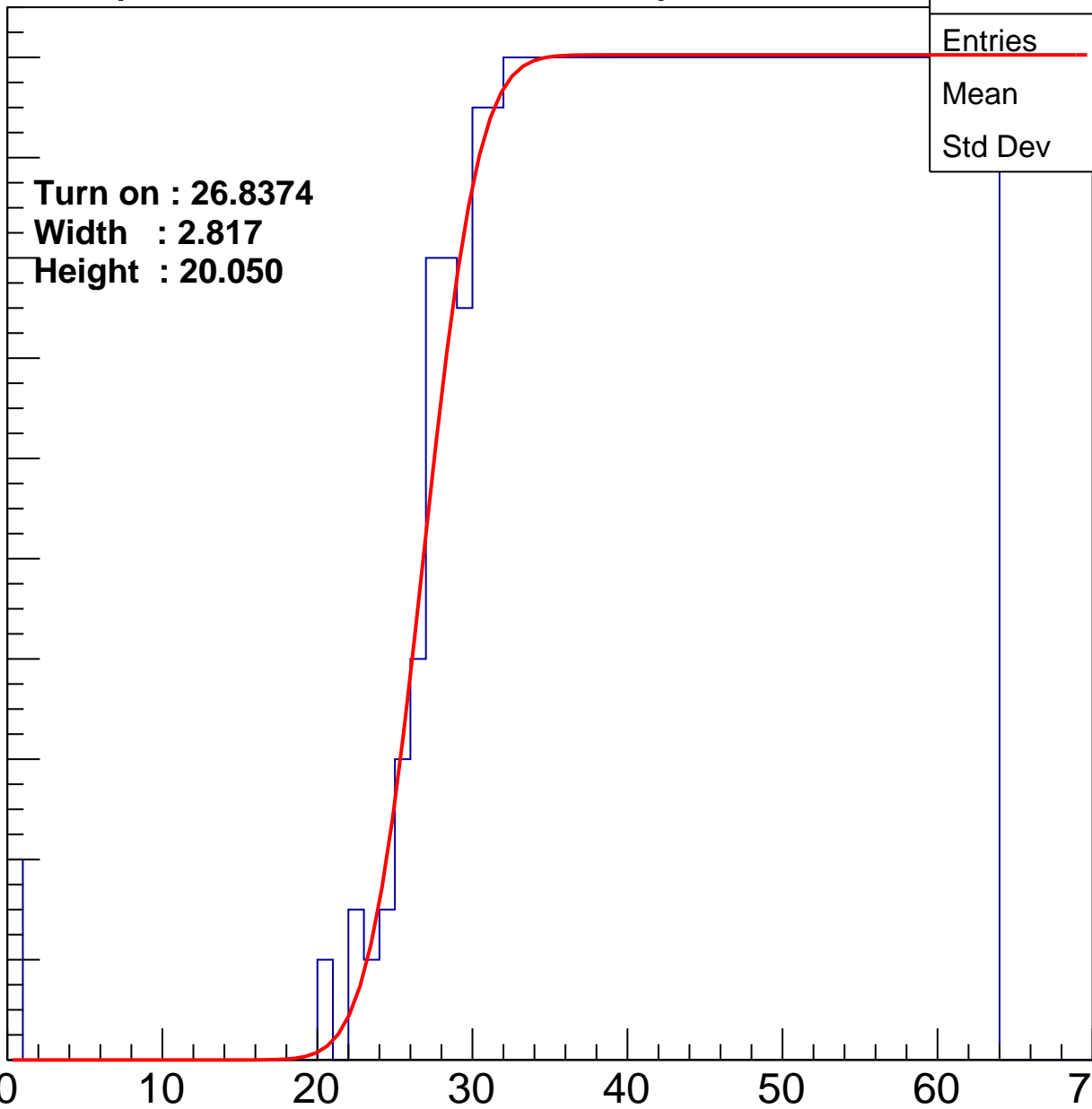
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8374
Width : 2.817
Height : 20.050

Entries	753
Mean	44.43
Std Dev	11.44

ampl



B0L101S, U25-ch44

calib_packv5_042523_0143.root, FC#1, port C1

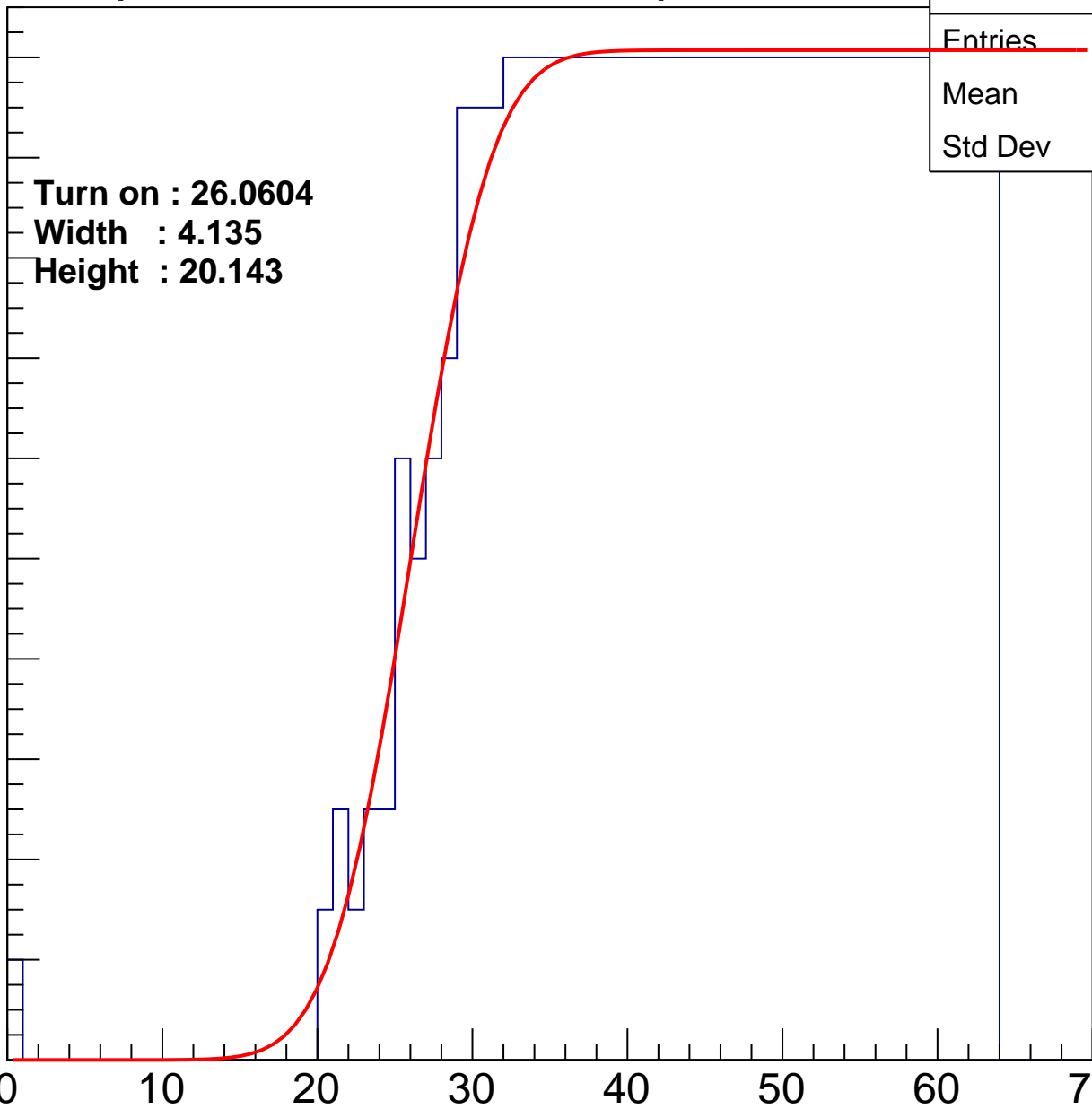
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0604
Width : 4.135
Height : 20.143

Entries	768
Mean	44.08
Std Dev	11.53

ampl



B0L101S, U25-ch45

calib_packv5_042523_0143.root, FC#1, port C1

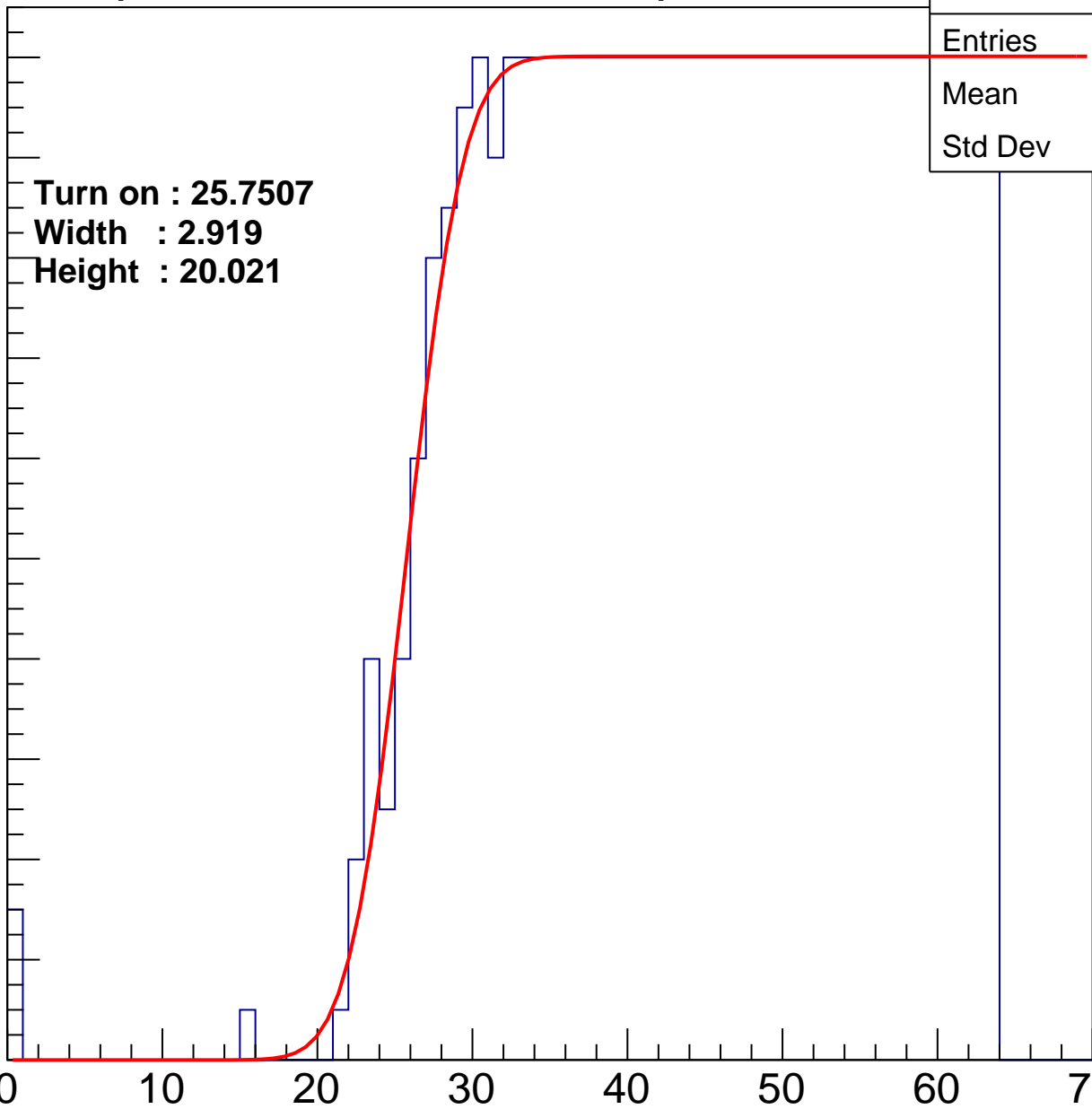
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7507
Width : 2.919
Height : 20.021

Entries	772
Mean	43.99
Std Dev	11.61

ampl



B0L101S, U25-ch46

calib_packv5_042523_0143.root, FC#1, port C1

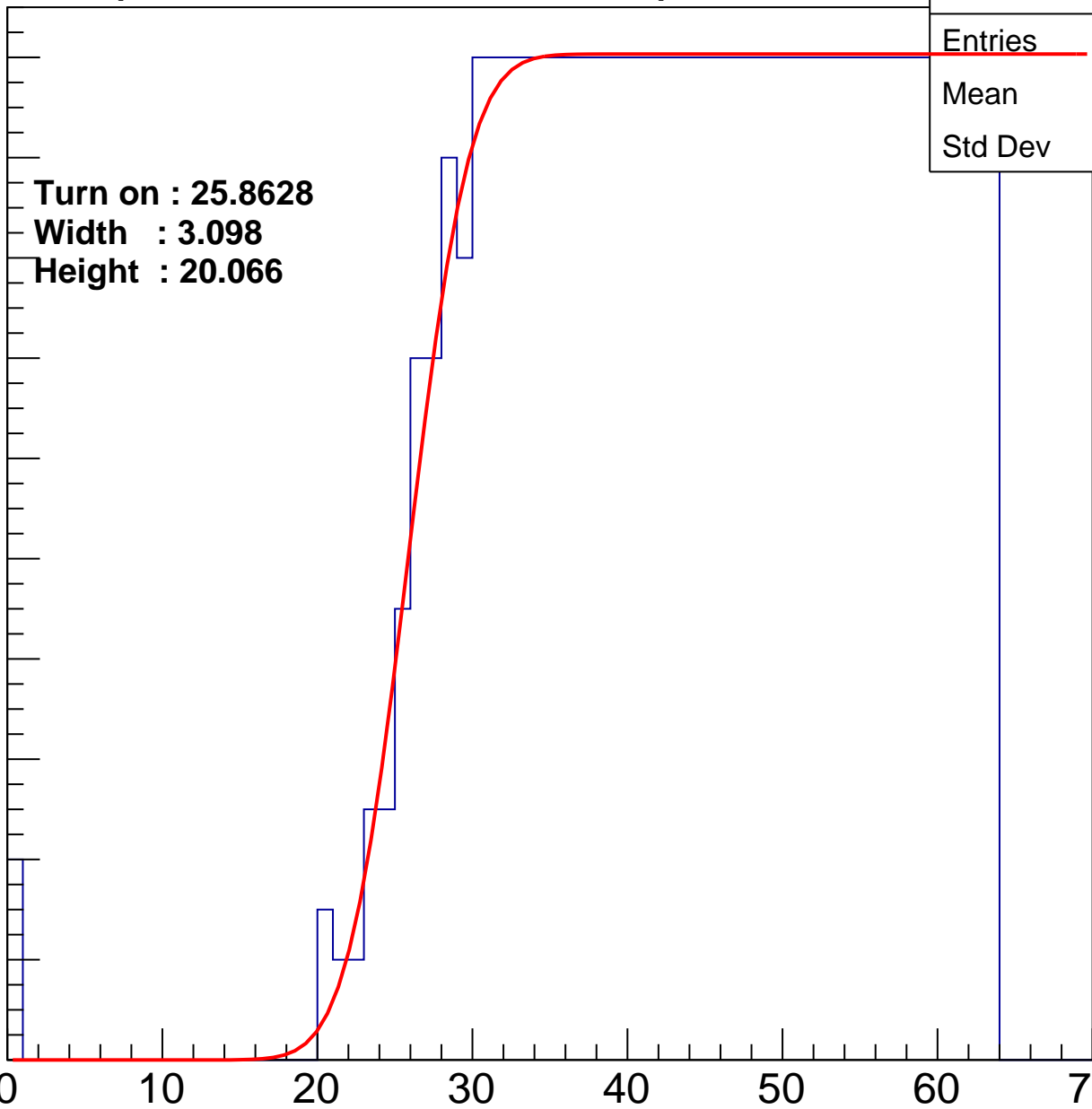
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8628
Width : 3.098
Height : 20.066

Entries	772
Mean	43.96
Std Dev	11.69

ampl



B0L101S, U25-ch47

calib_packv5_042523_0143.root, FC#1, port C1

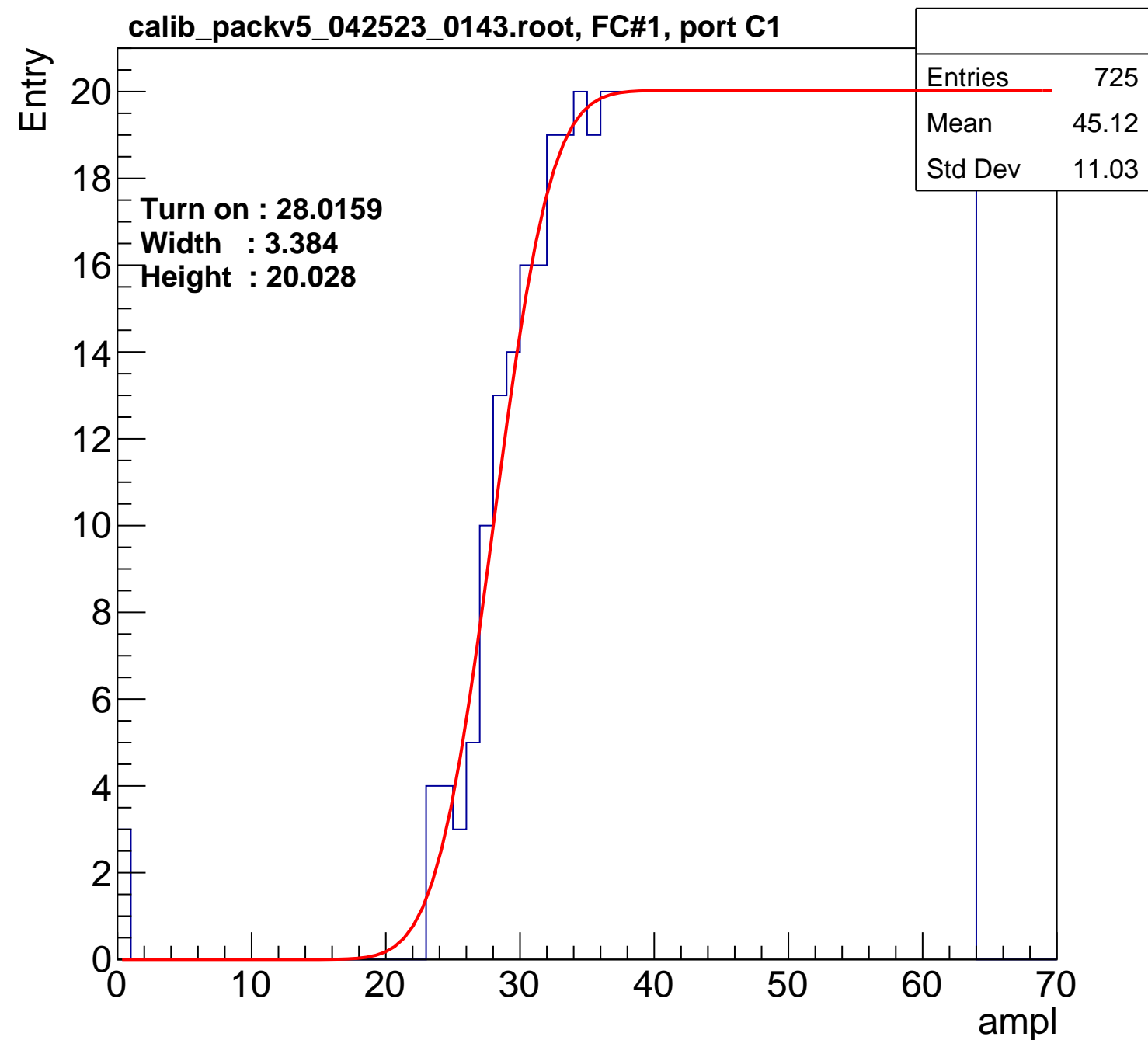
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0159
Width : 3.384
Height : 20.028

Entries	725
Mean	45.12
Std Dev	11.03

ampl



B0L101S, U25-ch48

calib_packv5_042523_0143.root, FC#1, port C1

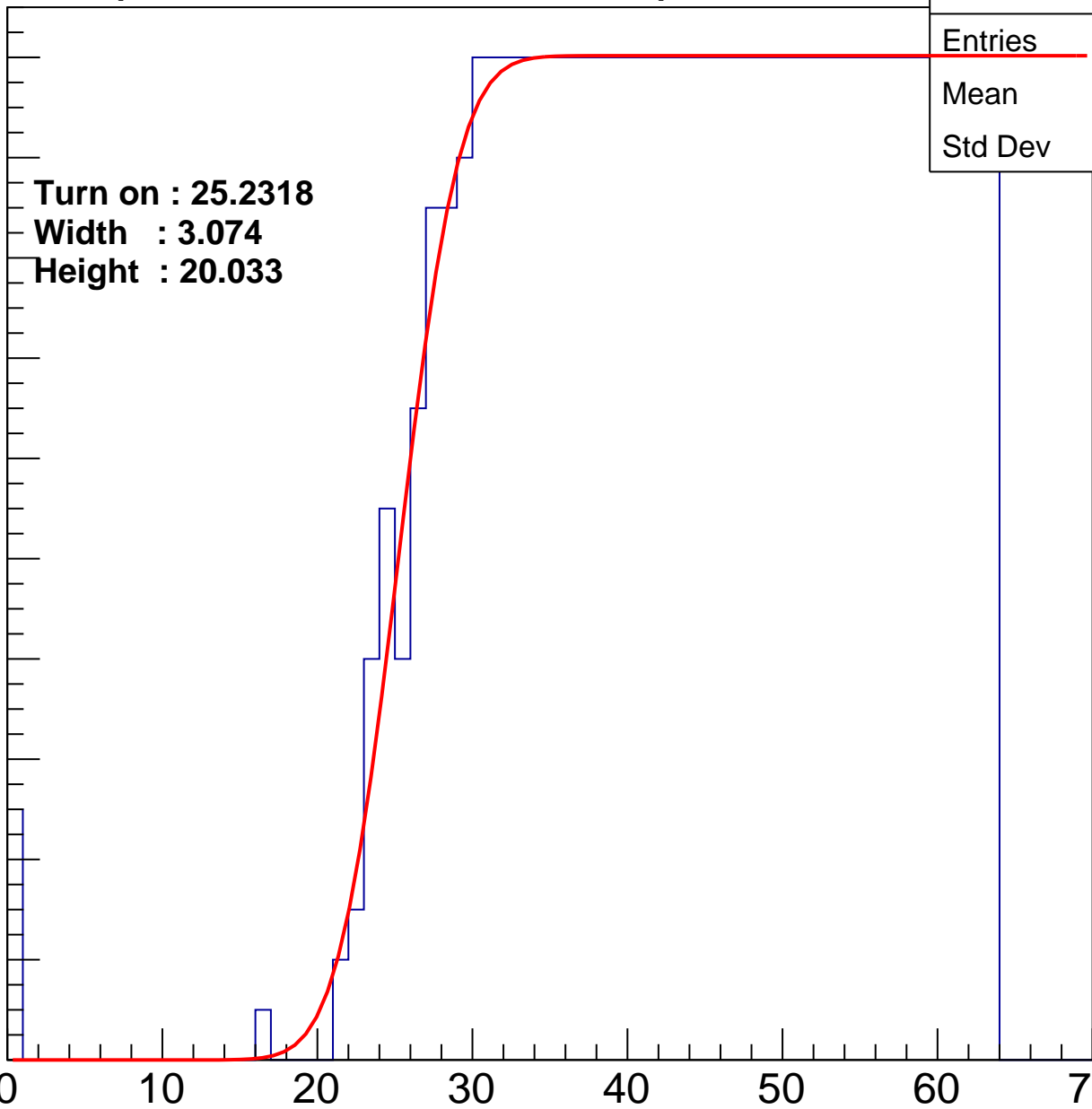
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2318
Width : 3.074
Height : 20.033

Entries	783
Mean	43.66
Std Dev	11.9

ampl



B0L101S, U25-ch49

calib_packv5_042523_0143.root, FC#1, port C1

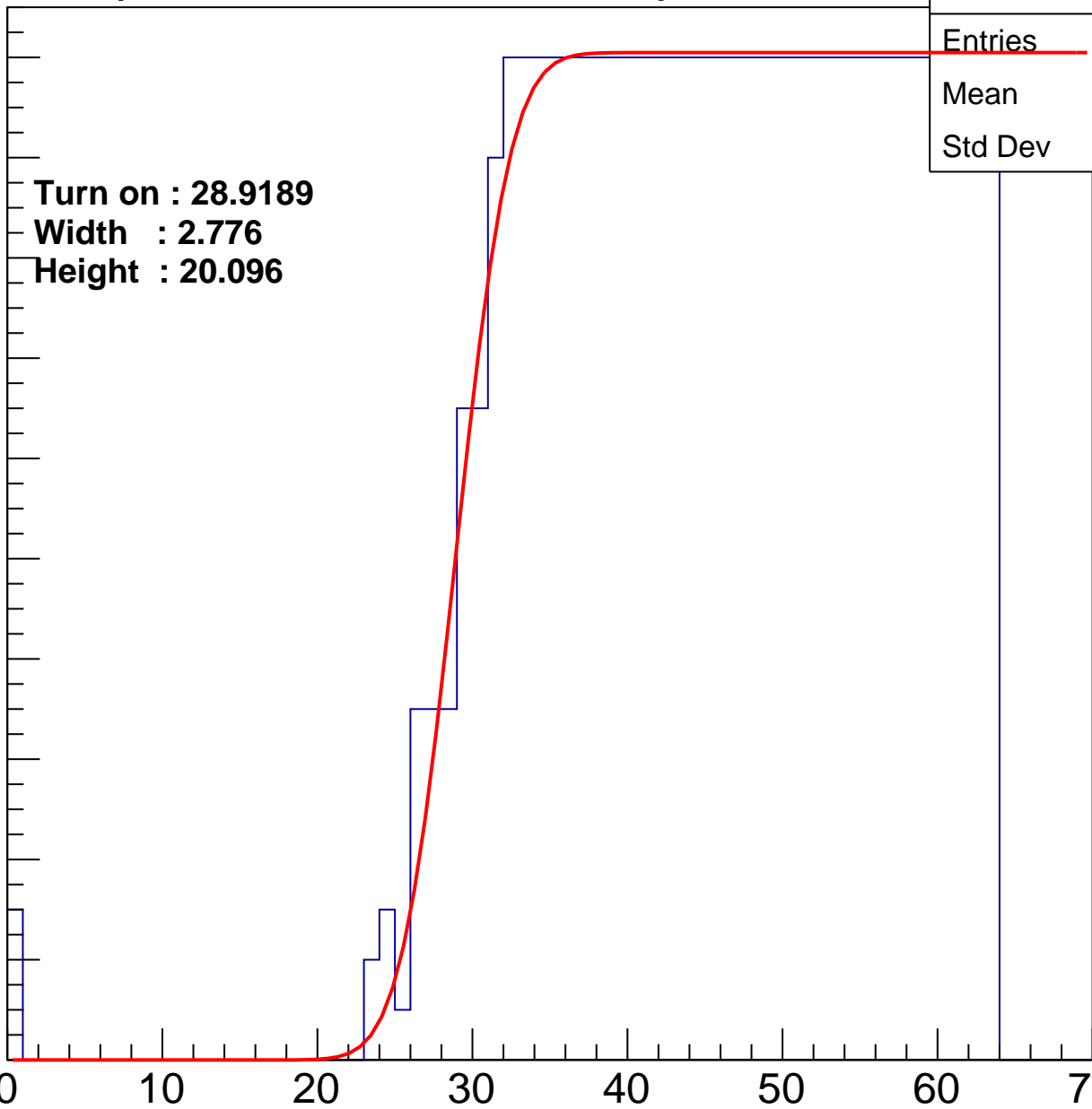
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9189
Width : 2.776
Height : 20.096

Entries	714
Mean	45.43
Std Dev	10.83

ampl



B0L101S, U25-ch50

calib_packv5_042523_0143.root, FC#1, port C1

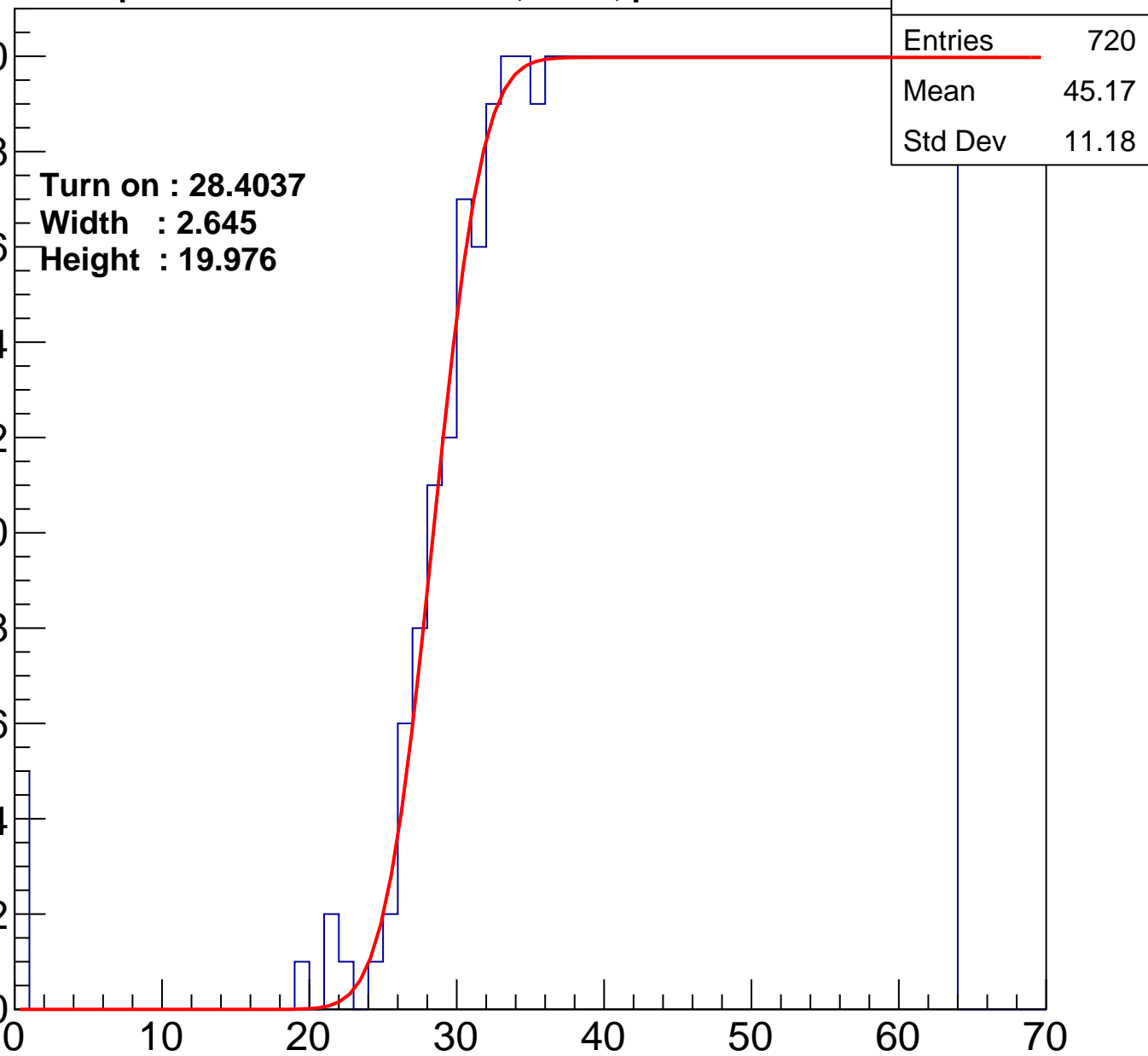
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4037
Width : 2.645
Height : 19.976

Entries	720
Mean	45.17
Std Dev	11.18

ampl



B0L101S, U25-ch51

calib_packv5_042523_0143.root, FC#1, port C1

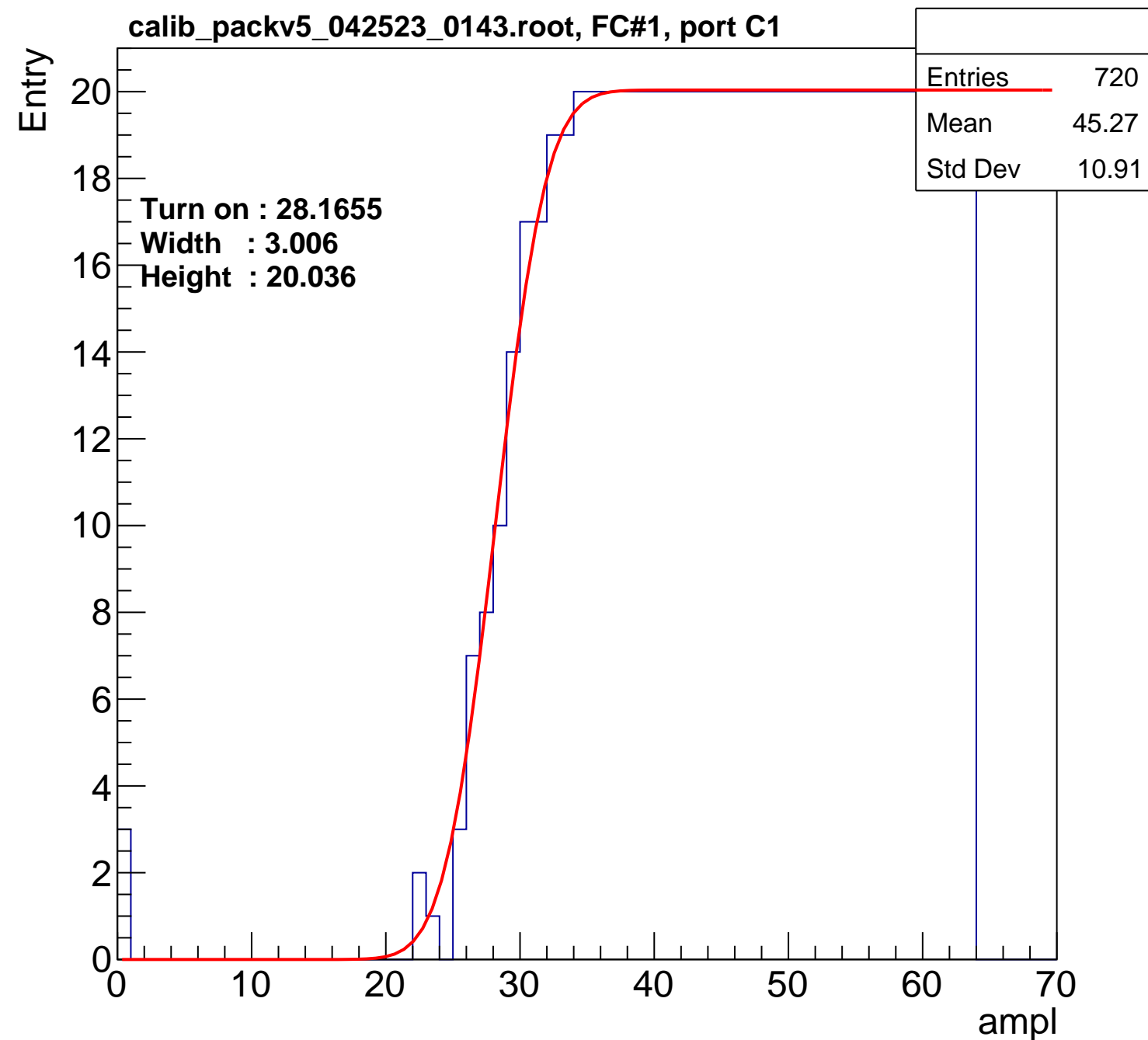
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1655
Width : 3.006
Height : 20.036

Entries	720
Mean	45.27
Std Dev	10.91

ampl



B0L101S, U25-ch52

calib_packv5_042523_0143.root, FC#1, port C1

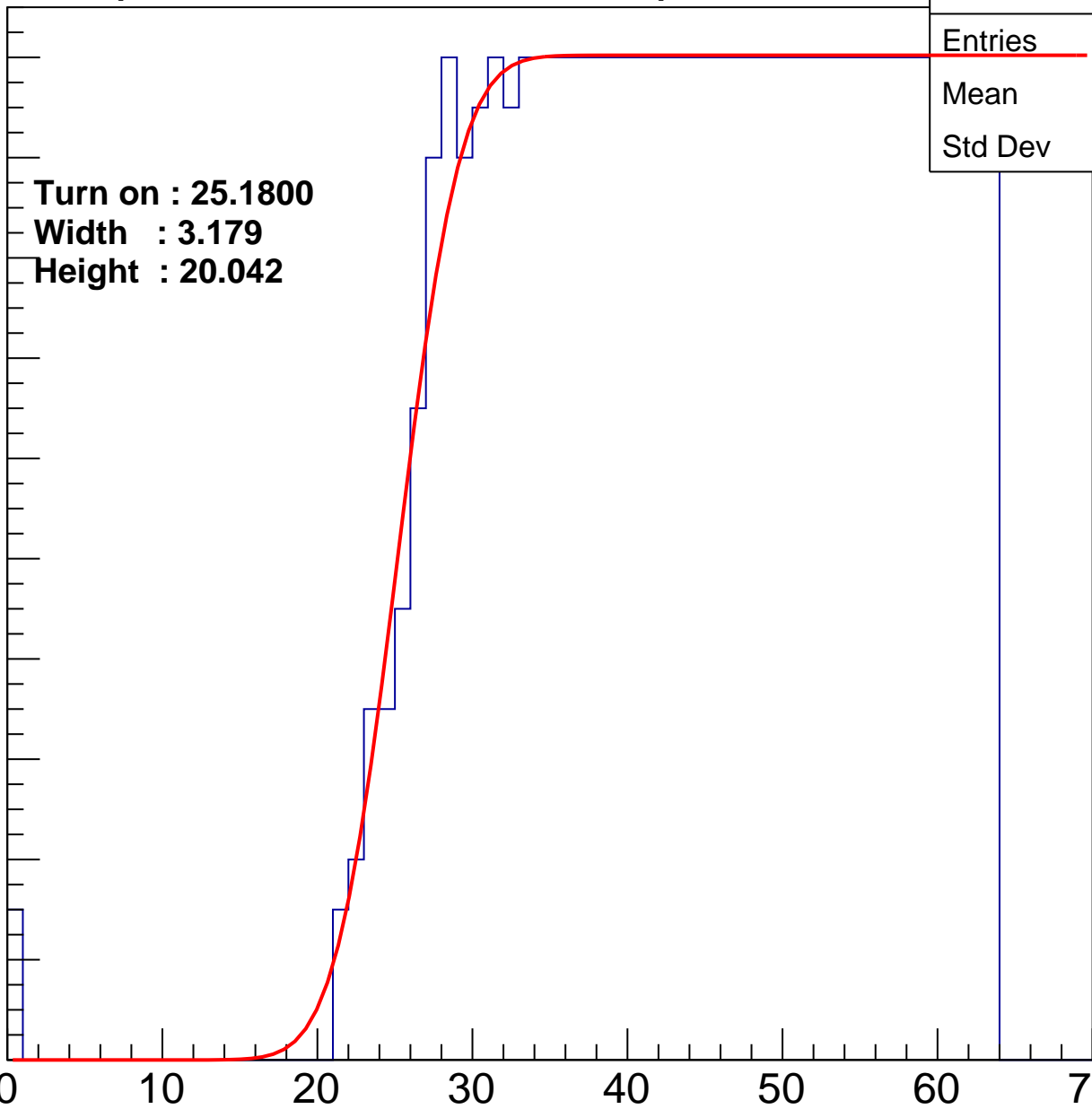
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1800
Width : 3.179
Height : 20.042

Entries	780
Mean	43.81
Std Dev	11.68

ampl



B0L101S, U25-ch53

calib_packv5_042523_0143.root, FC#1, port C1

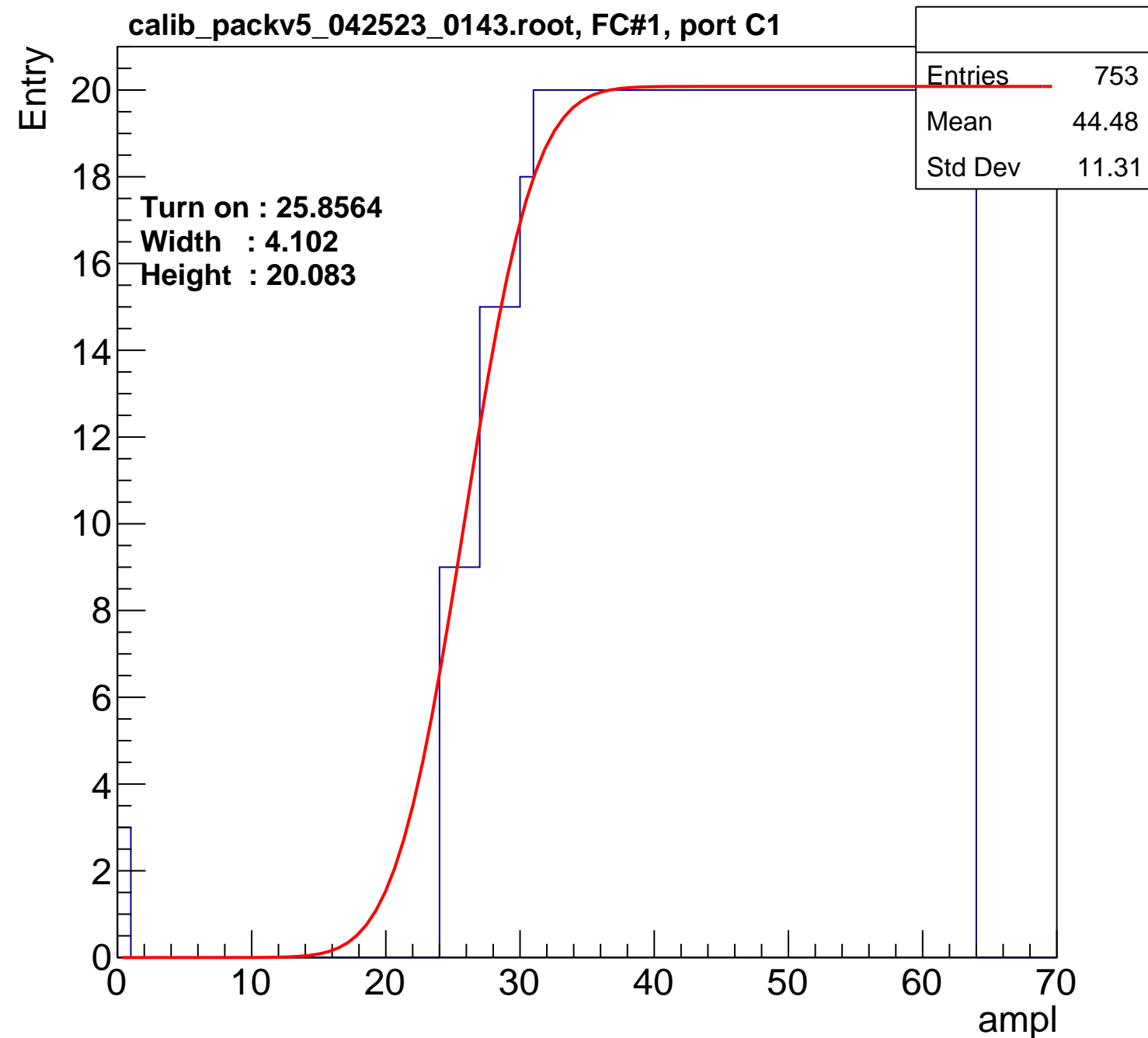
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8564
Width : 4.102
Height : 20.083

Entries	753
Mean	44.48
Std Dev	11.31

ampl



B0L101S, U25-ch54

calib_packv5_042523_0143.root, FC#1, port C1

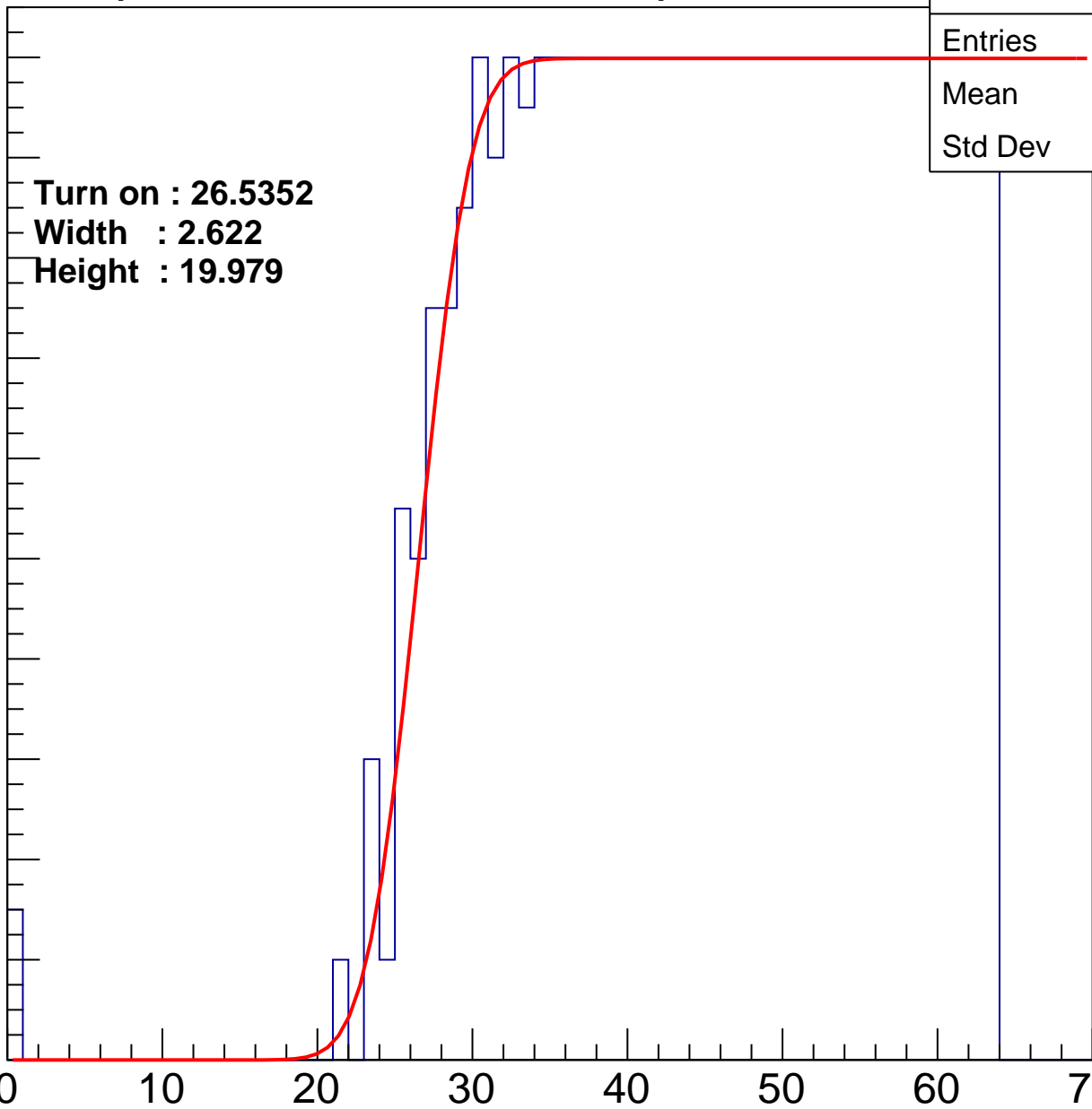
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5352
Width : 2.622
Height : 19.979

Entries	758
Mean	44.34
Std Dev	11.41

ampl



B0L101S, U25-ch55

calib_packv5_042523_0143.root, FC#1, port C1

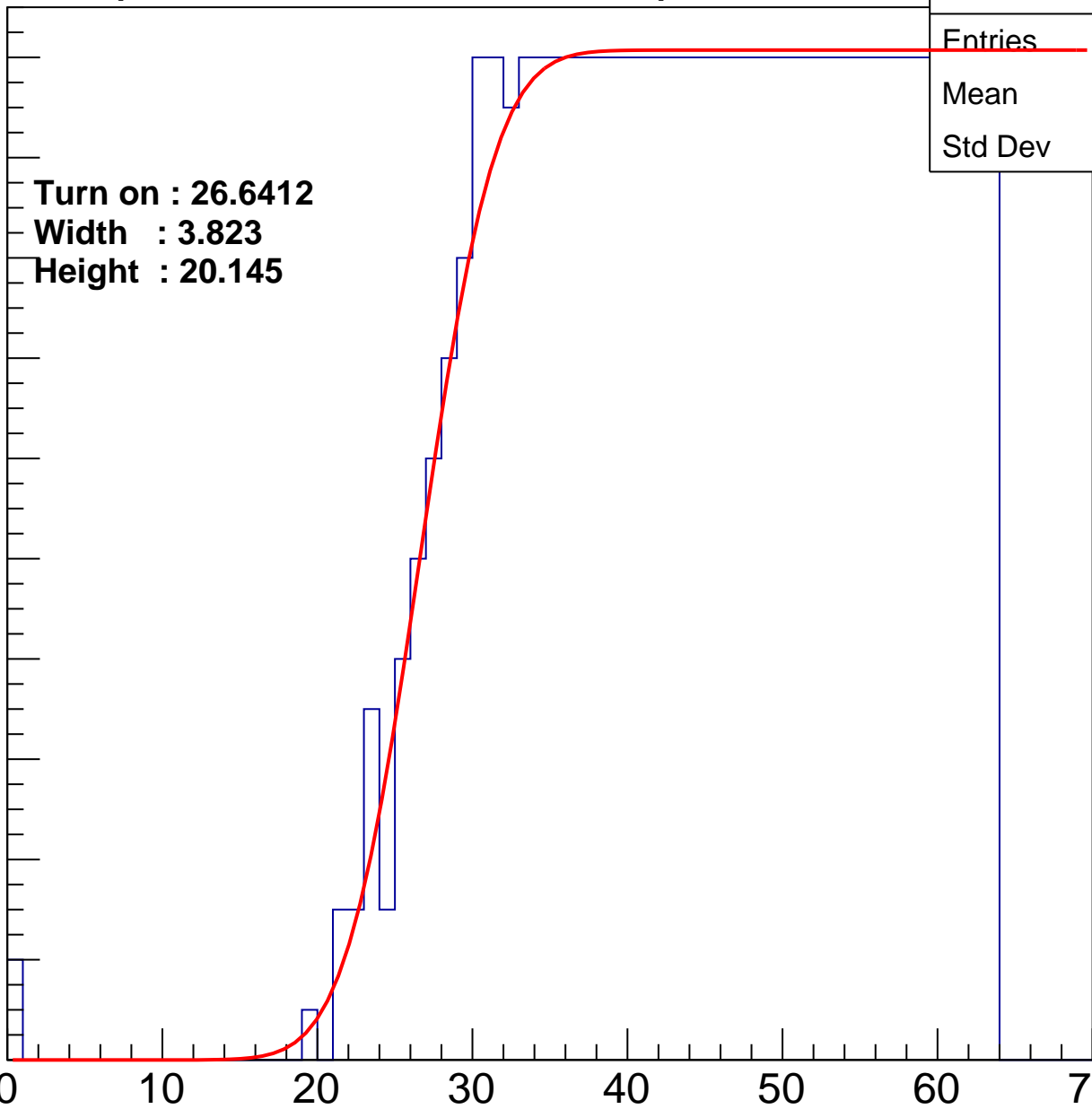
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6412
Width : 3.823
Height : 20.145

Entries	758
Mean	44.34
Std Dev	11.37

ampl



B0L101S, U25-ch56

calib_packv5_042523_0143.root, FC#1, port C1

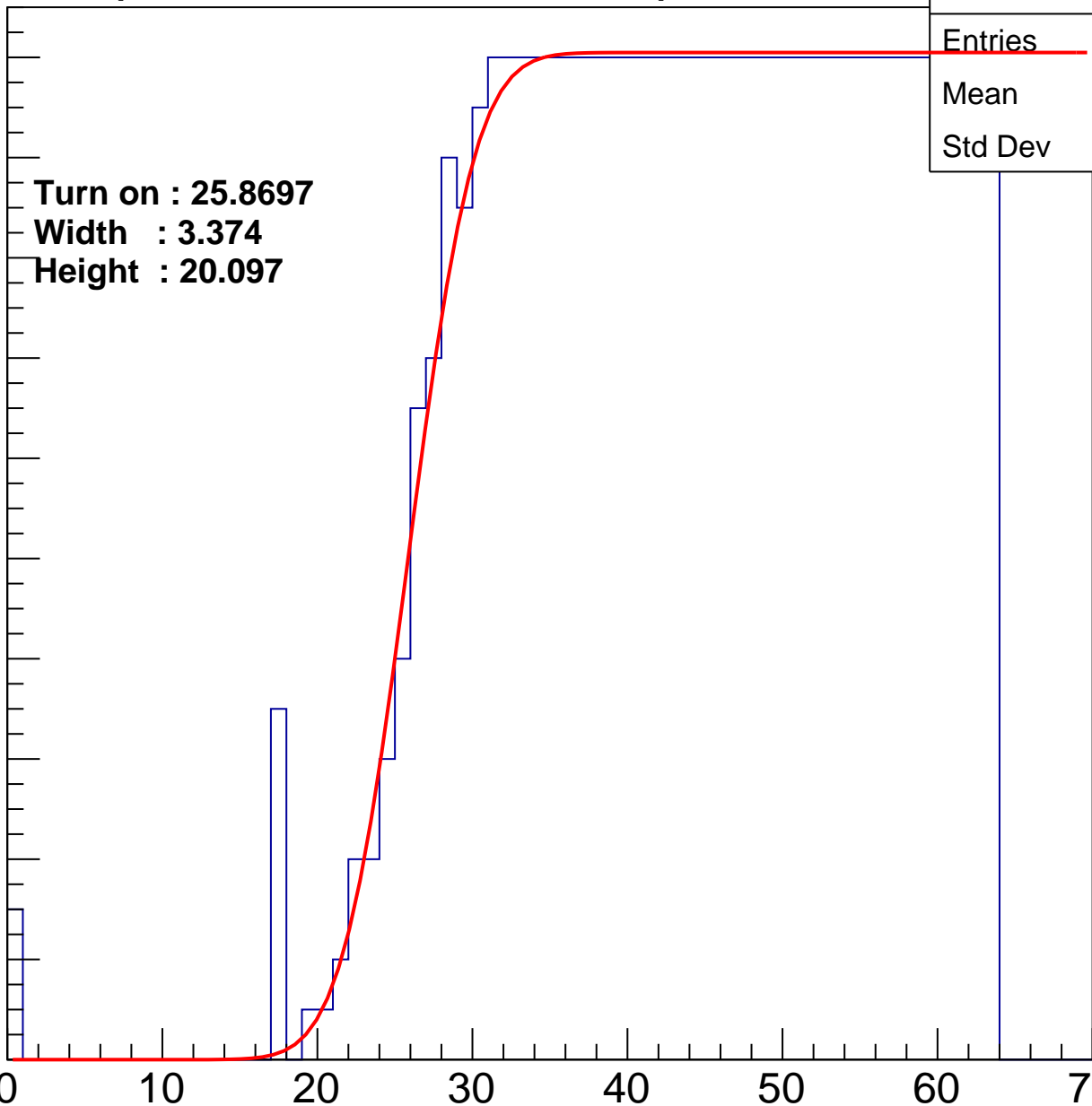
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8697
Width : 3.374
Height : 20.097

Entries	777
Mean	43.79
Std Dev	11.81

ampl



B0L101S, U25-ch57

calib_packv5_042523_0143.root, FC#1, port C1

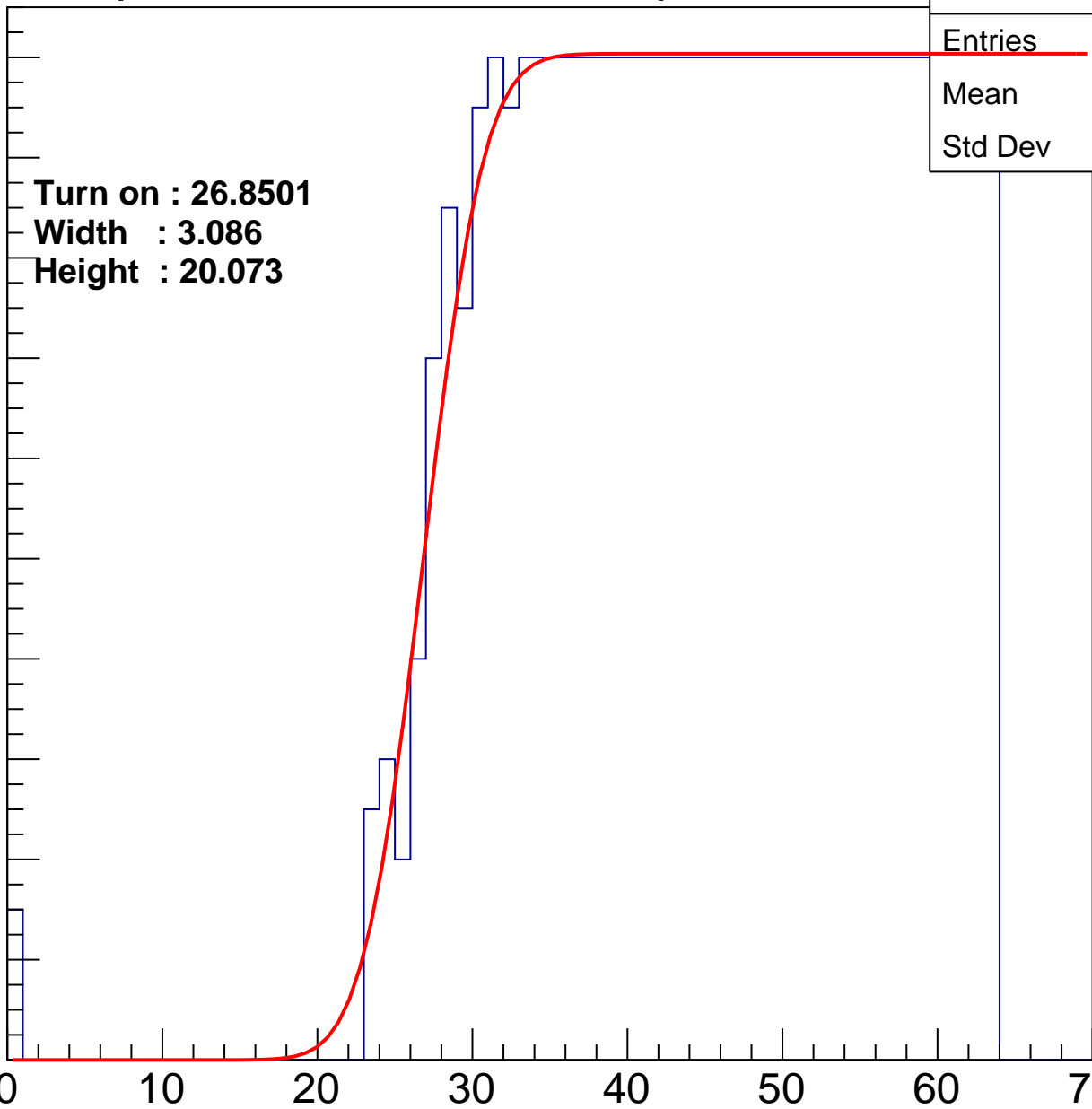
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8501
Width : 3.086
Height : 20.073

Entries	750
Mean	44.55
Std Dev	11.28

ampl



B0L101S, U25-ch58

calib_packv5_042523_0143.root, FC#1, port C1

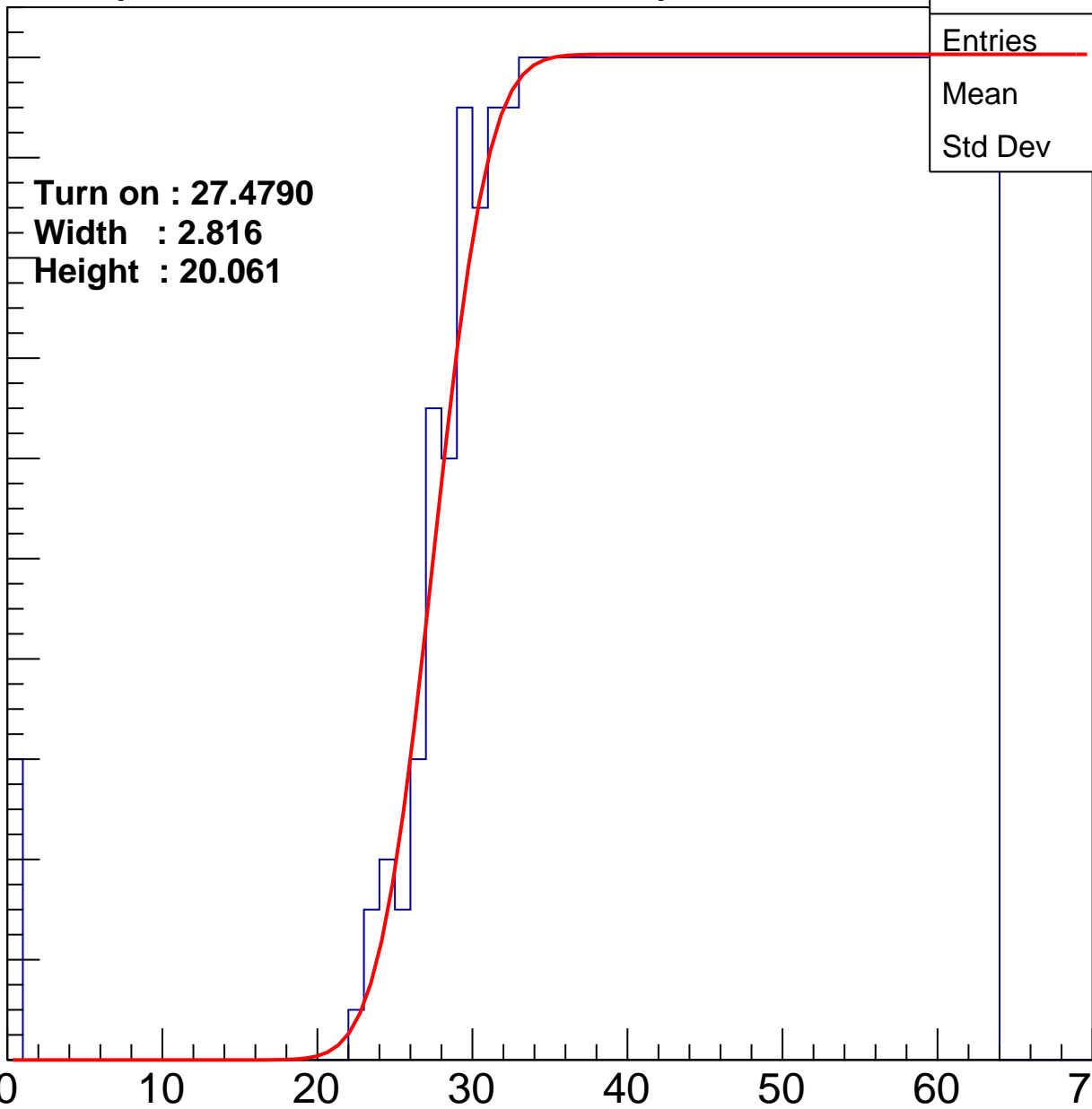
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4790
Width : 2.816
Height : 20.061

Entries	742
Mean	44.64
Std Dev	11.47

ampl



B0L101S, U25-ch59

calib_packv5_042523_0143.root, FC#1, port C1

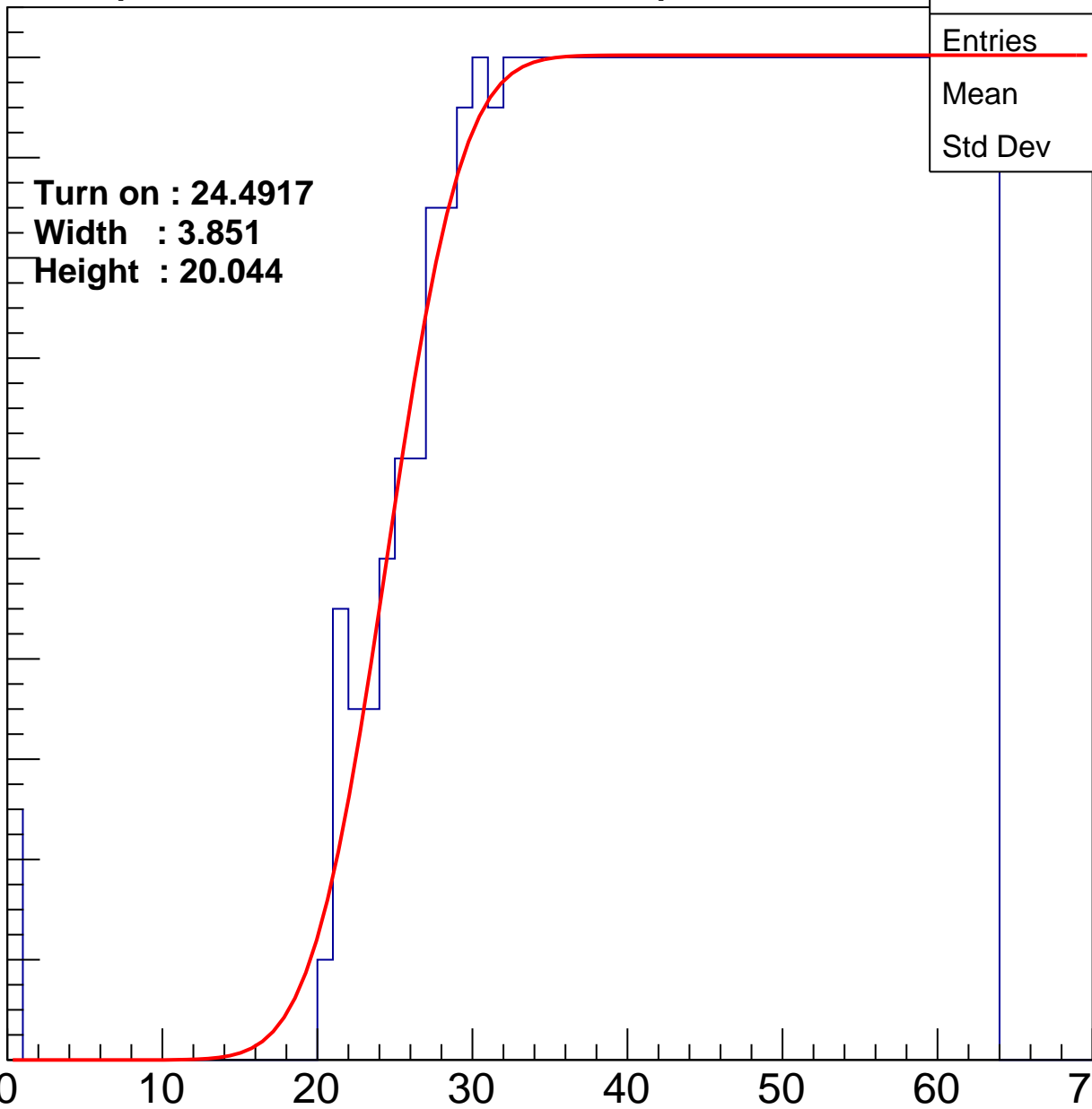
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4917
Width : 3.851
Height : 20.044

Entries	796
Mean	43.31
Std Dev	12.12

ampl



B0L101S, U25-ch60

calib_packv5_042523_0143.root, FC#1, port C1

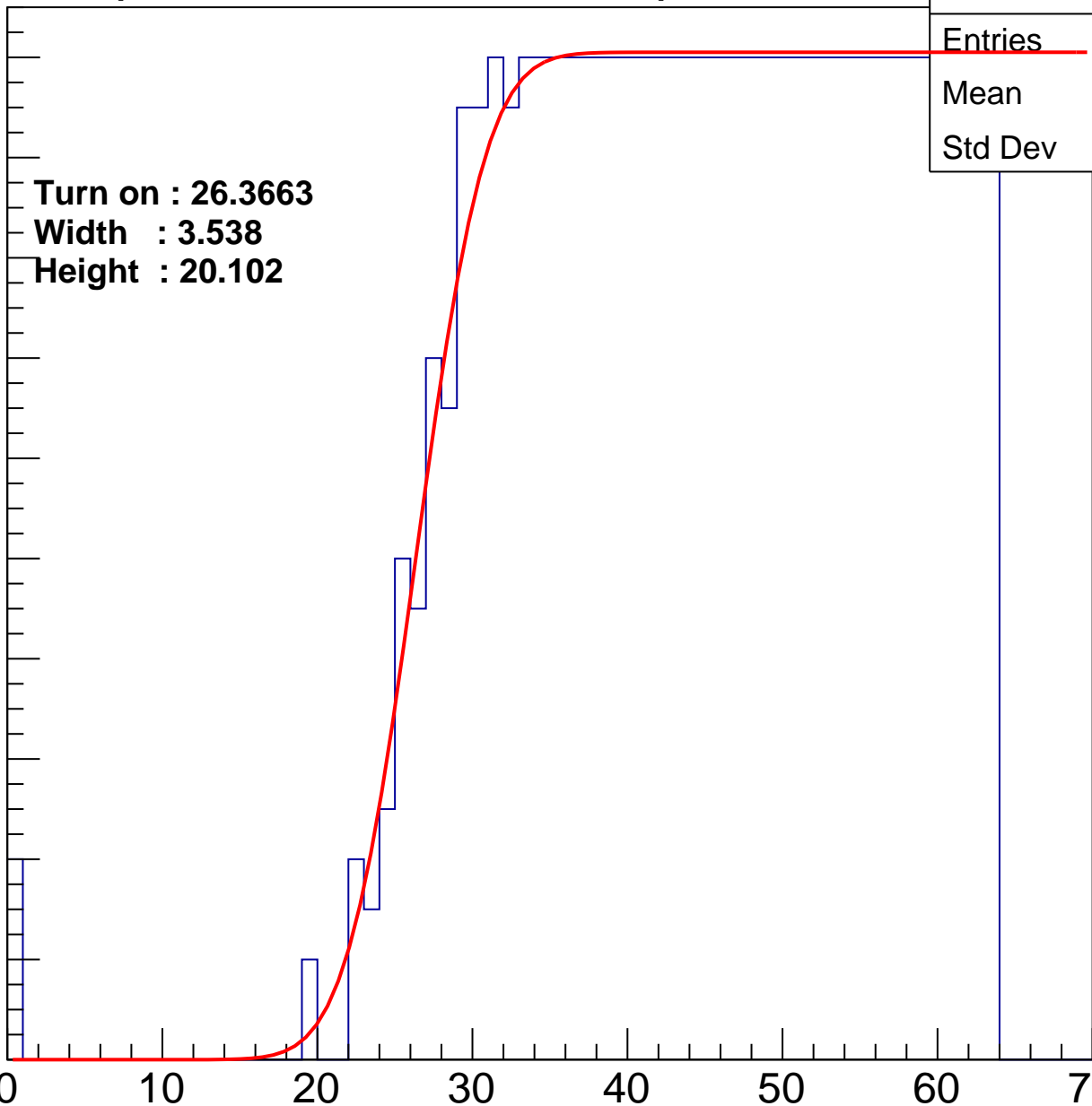
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3663
Width : 3.538
Height : 20.102

Entries	761
Mean	44.22
Std Dev	11.56

ampl



B0L101S, U25-ch61

calib_packv5_042523_0143.root, FC#1, port C1

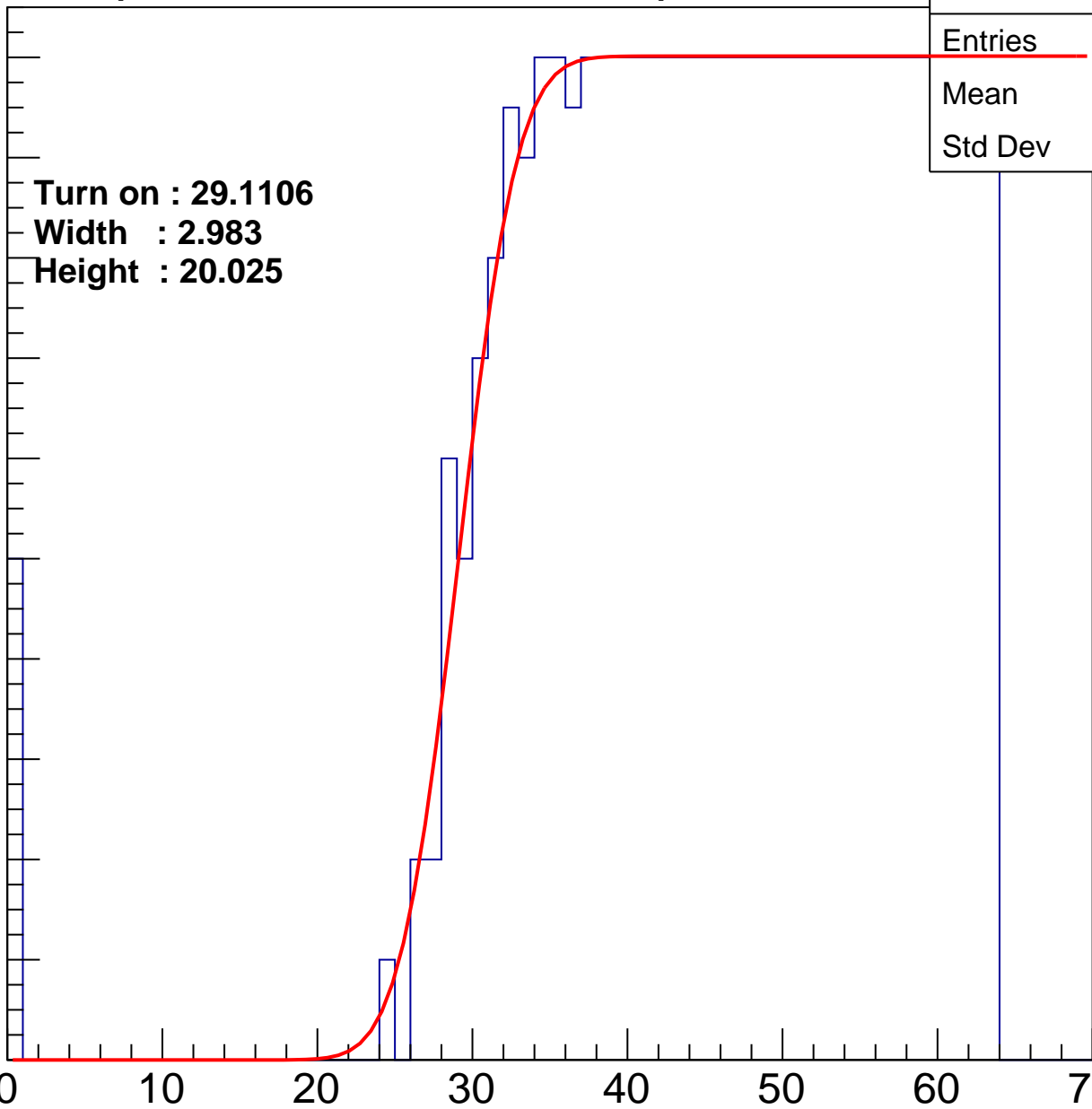
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.1106
Width : 2.983
Height : 20.025

Entries	708
Mean	45.29
Std Dev	11.53

ampl



B0L101S, U25-ch62

calib_packv5_042523_0143.root, FC#1, port C1

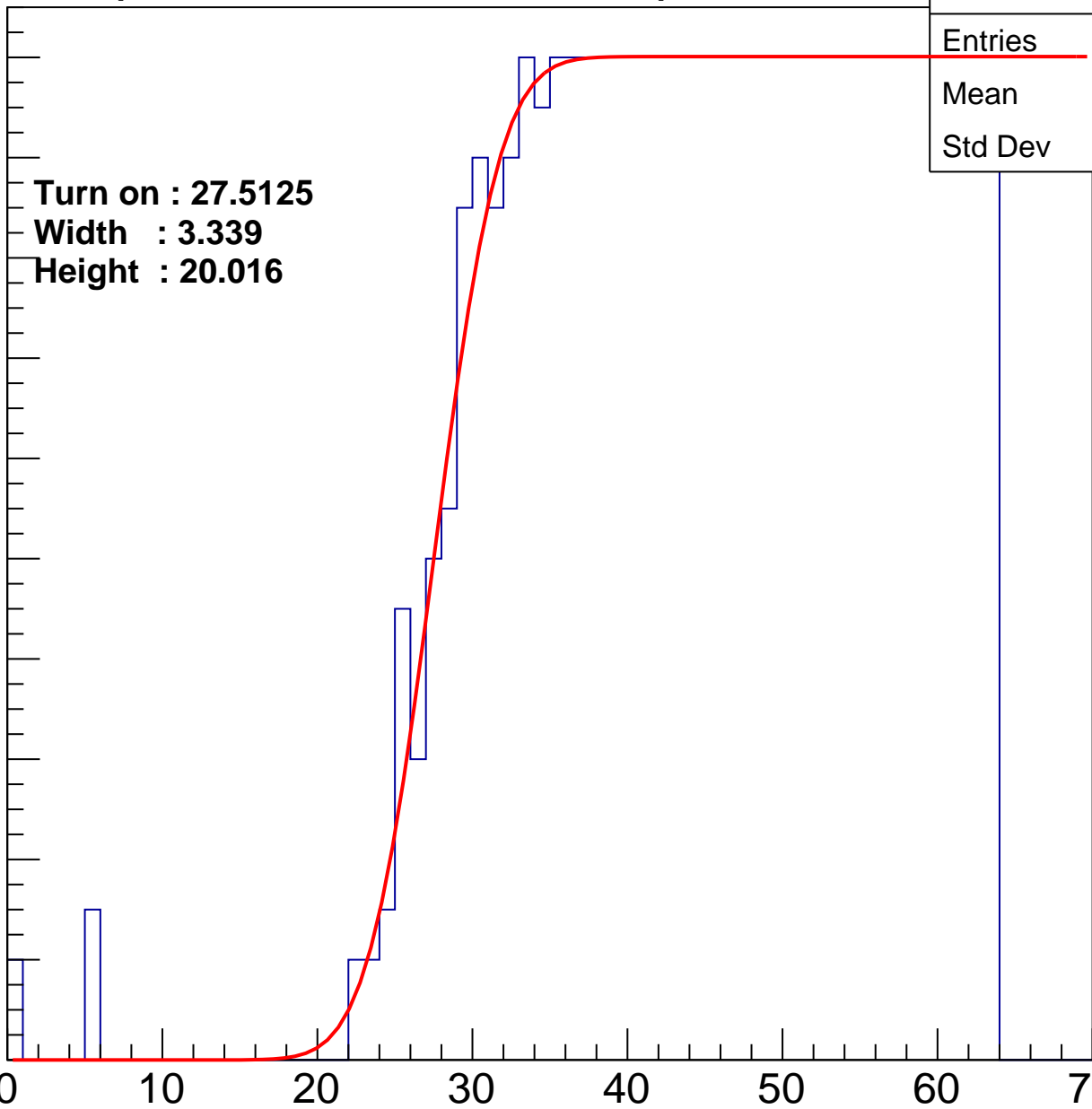
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5125
Width : 3.339
Height : 20.016

Entries	737
Mean	44.77
Std Dev	11.3

ampl



B0L101S, U25-ch63

calib_packv5_042523_0143.root, FC#1, port C1

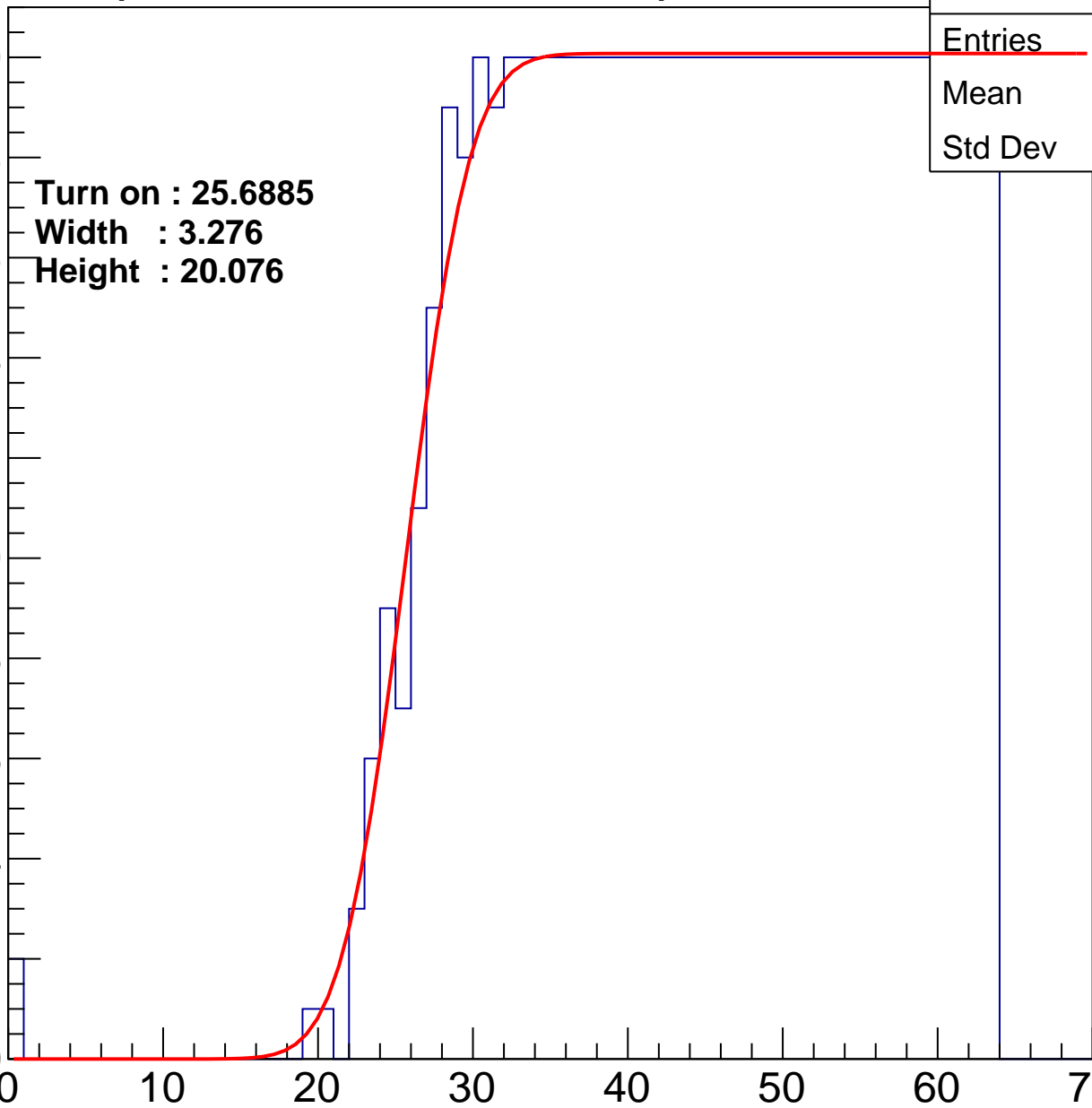
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6885
Width : 3.276
Height : 20.076

Entries	771
Mean	44.06
Std Dev	11.48

ampl



B0L101S, U25-ch64

calib_packv5_042523_0143.root, FC#1, port C1

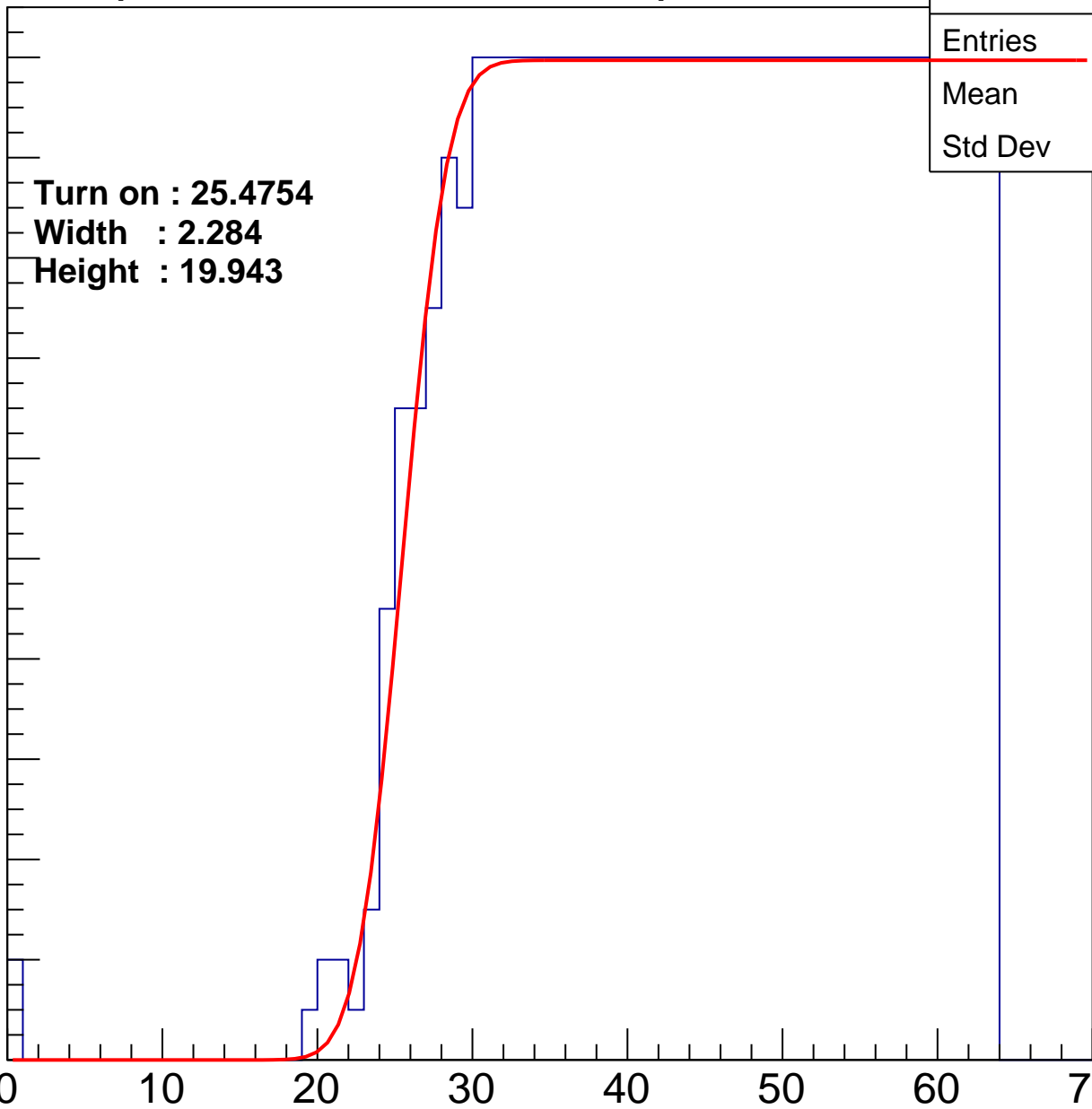
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4754
Width : 2.284
Height : 19.943

Entries	776
Mean	43.93
Std Dev	11.55

ampl



B0L101S, U25-ch65

calib_packv5_042523_0143.root, FC#1, port C1

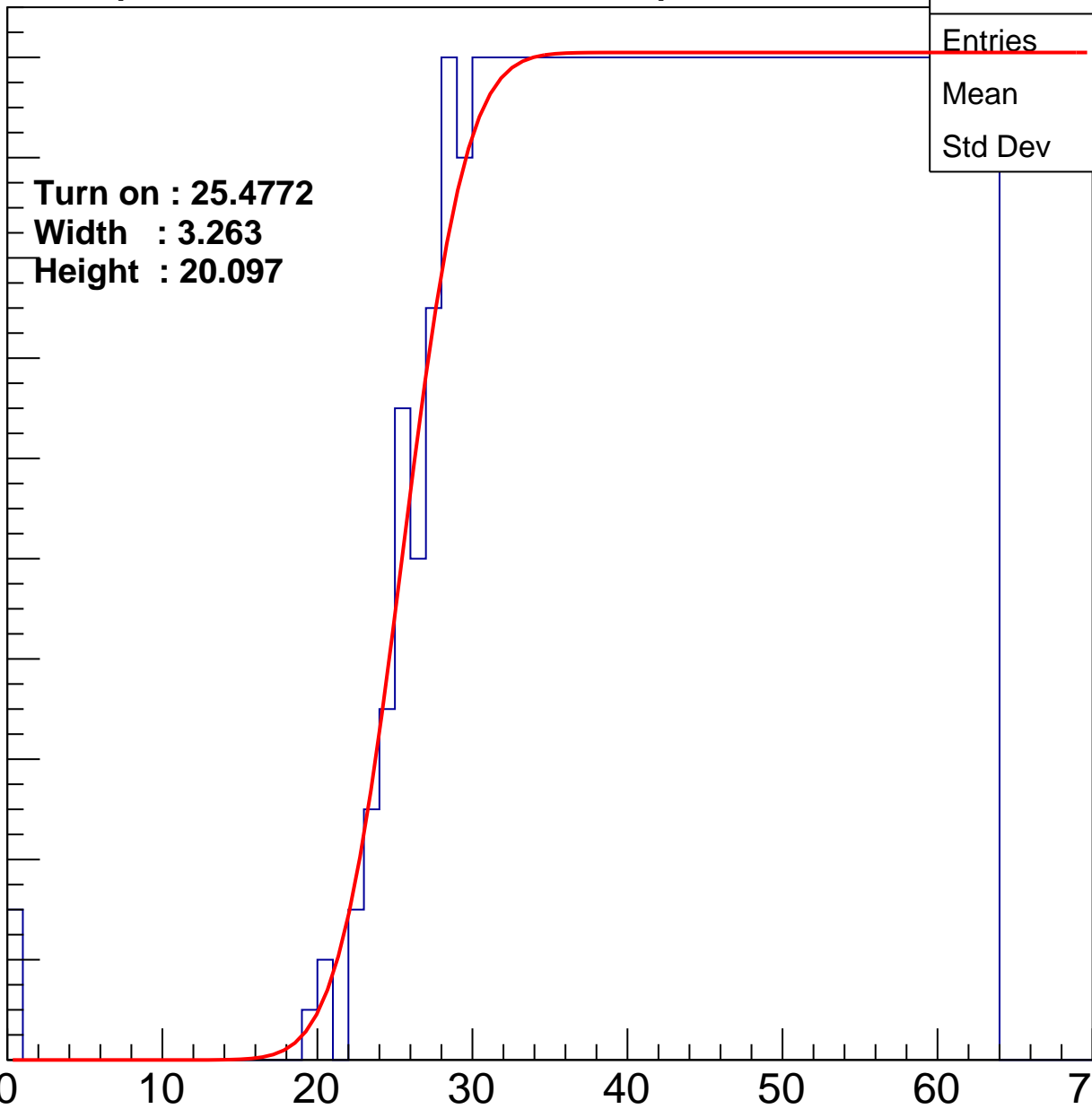
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4772
Width : 3.263
Height : 20.097

Entries	777
Mean	43.89
Std Dev	11.63

ampl



B0L101S, U25-ch66

calib_packv5_042523_0143.root, FC#1, port C1

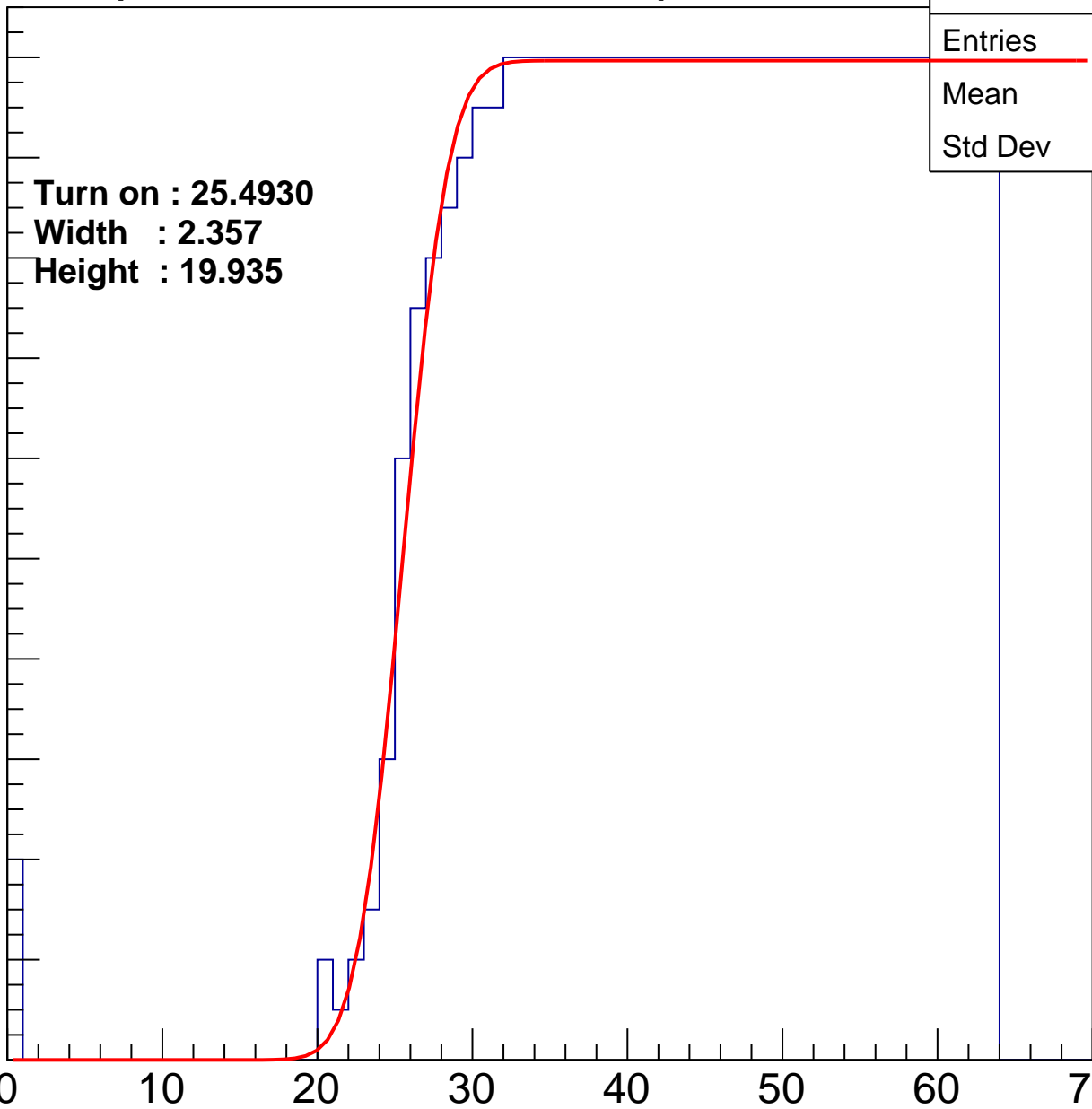
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4930
Width : 2.357
Height : 19.935

Entries	774
Mean	43.92
Std Dev	11.68

ampl



B0L101S, U25-ch67

calib_packv5_042523_0143.root, FC#1, port C1

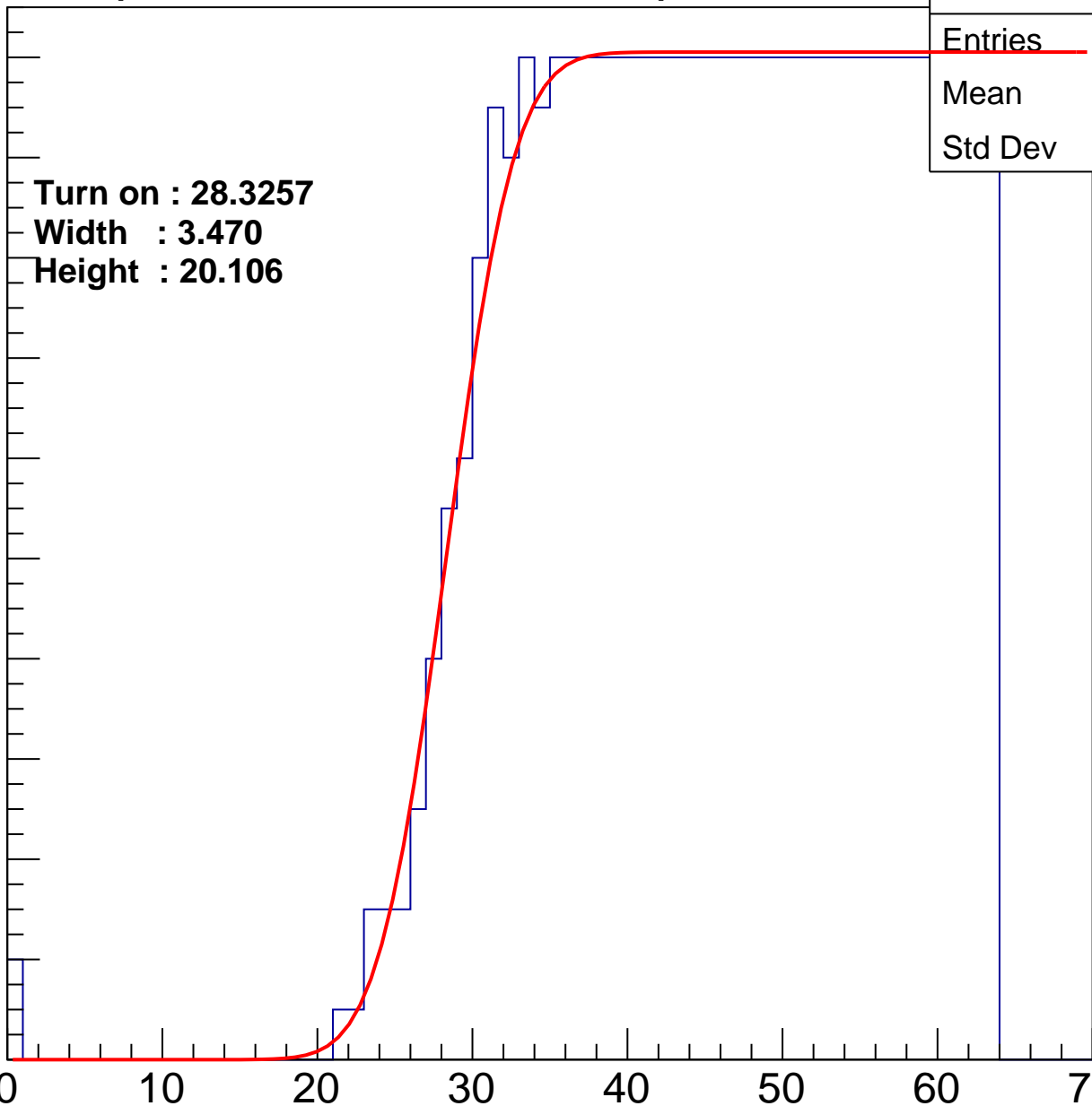
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3257
Width : 3.470
Height : 20.106

Entries	721
Mean	45.26
Std Dev	10.87

ampl



B0L101S, U25-ch68

calib_packv5_042523_0143.root, FC#1, port C1

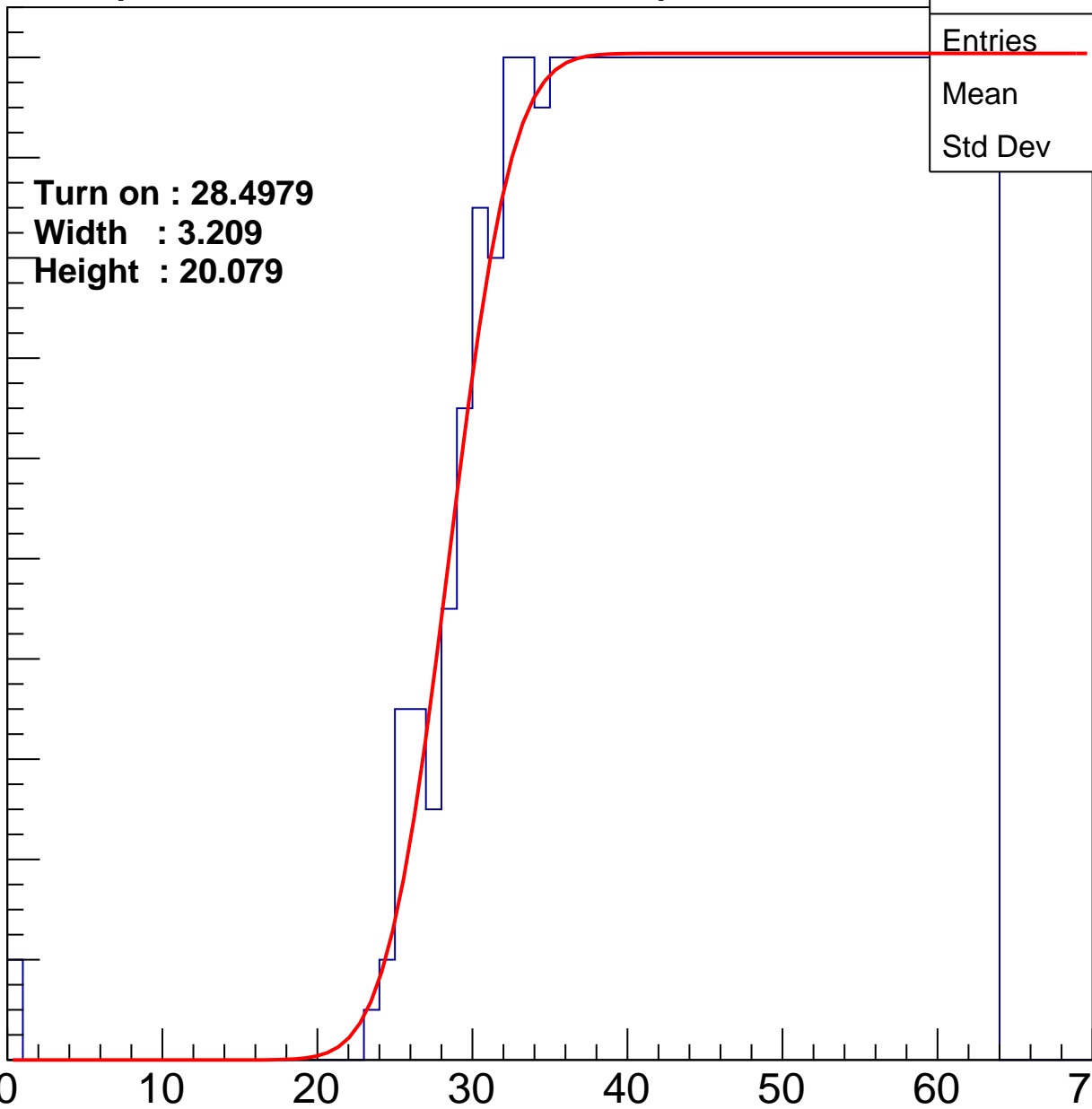
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4979
Width : 3.209
Height : 20.079

Entries	718
Mean	45.35
Std Dev	10.79

ampl



B0L101S, U25-ch69

calib_packv5_042523_0143.root, FC#1, port C1

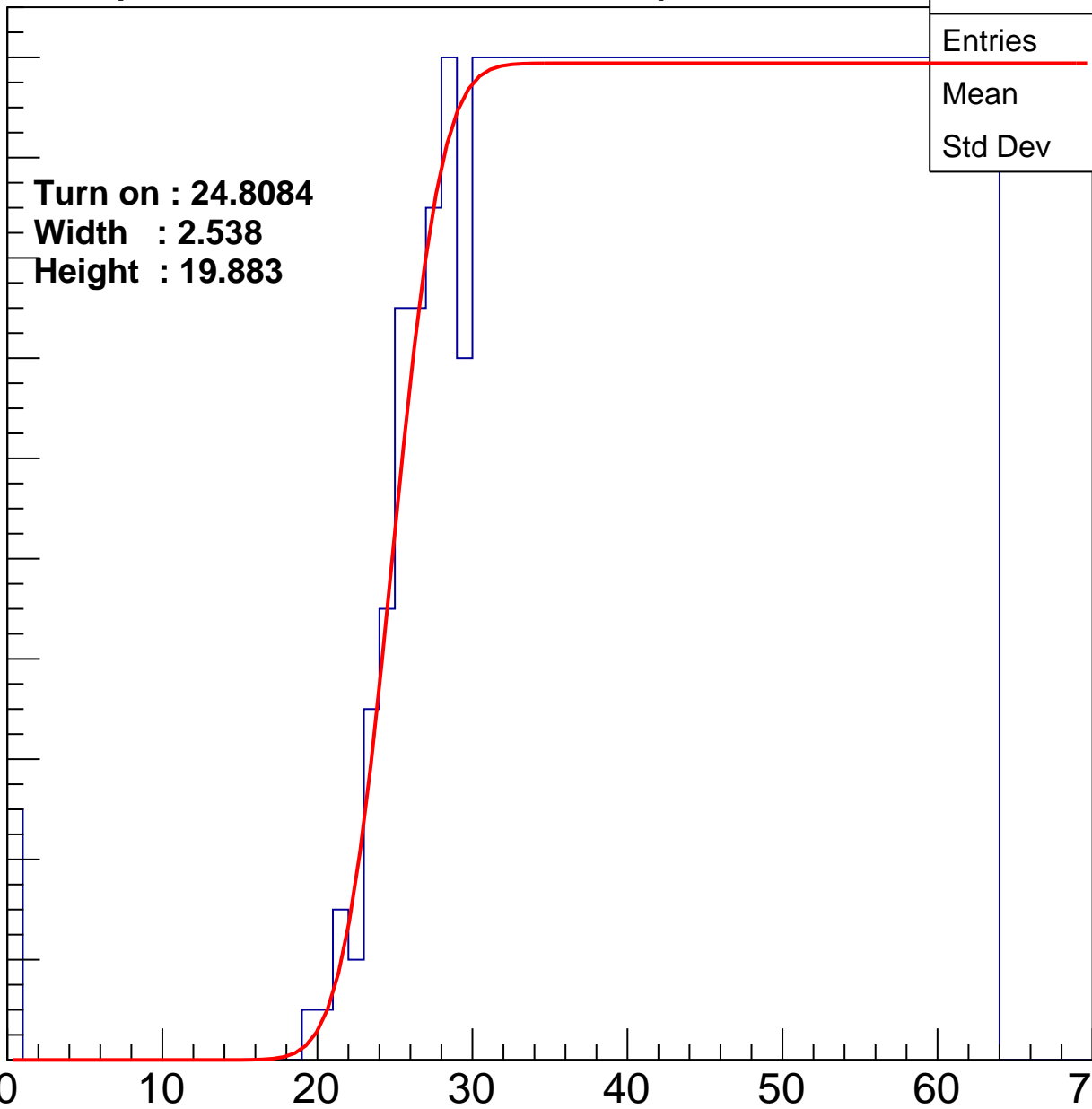
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8084
Width : 2.538
Height : 19.883

Entries	789
Mean	43.51
Std Dev	11.97

ampl



B0L101S, U25-ch70

calib_packv5_042523_0143.root, FC#1, port C1

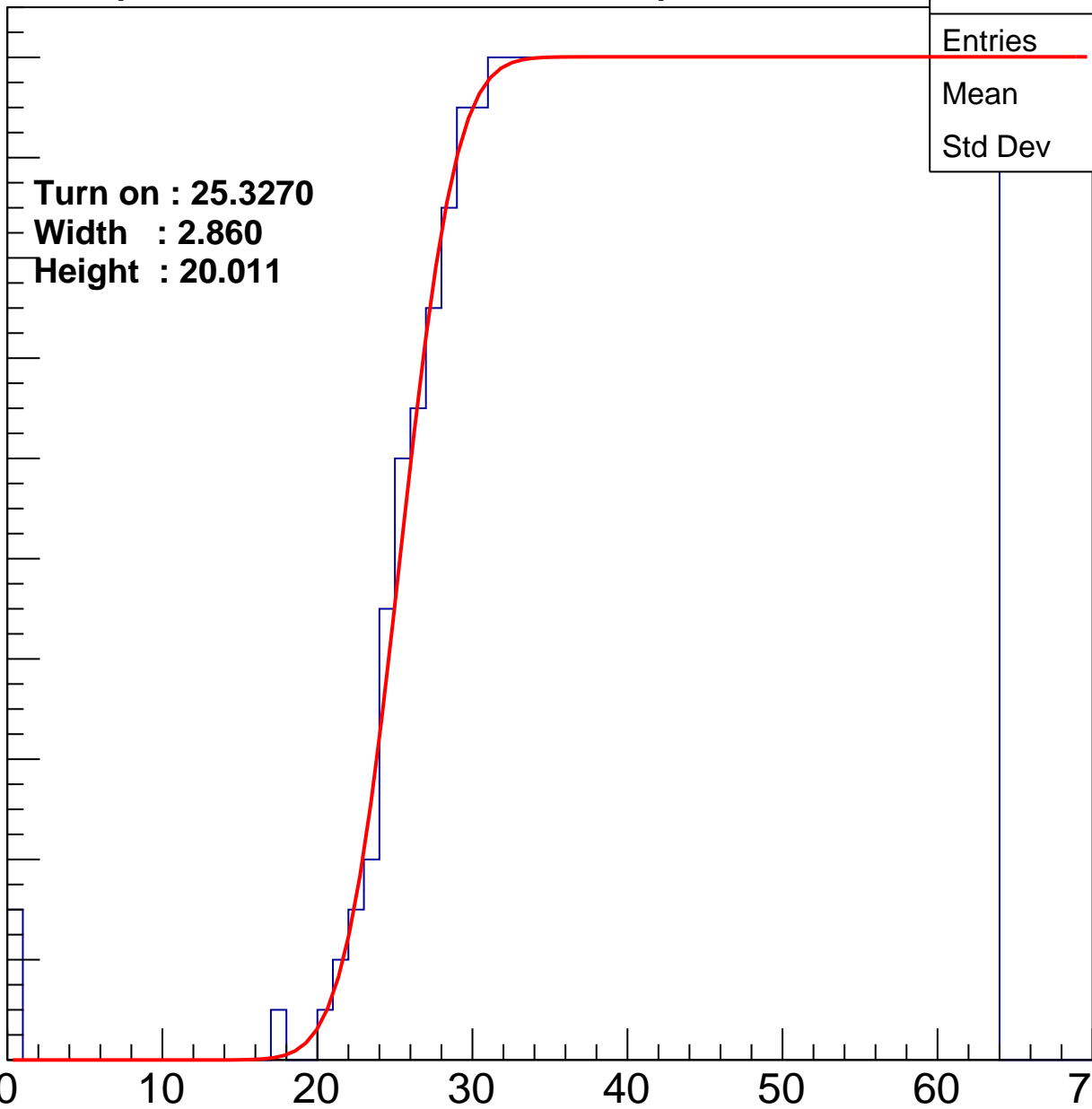
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3270
Width : 2.860
Height : 20.011

Entries	778
Mean	43.85
Std Dev	11.67

ampl



B0L101S, U25-ch71

calib_packv5_042523_0143.root, FC#1, port C1

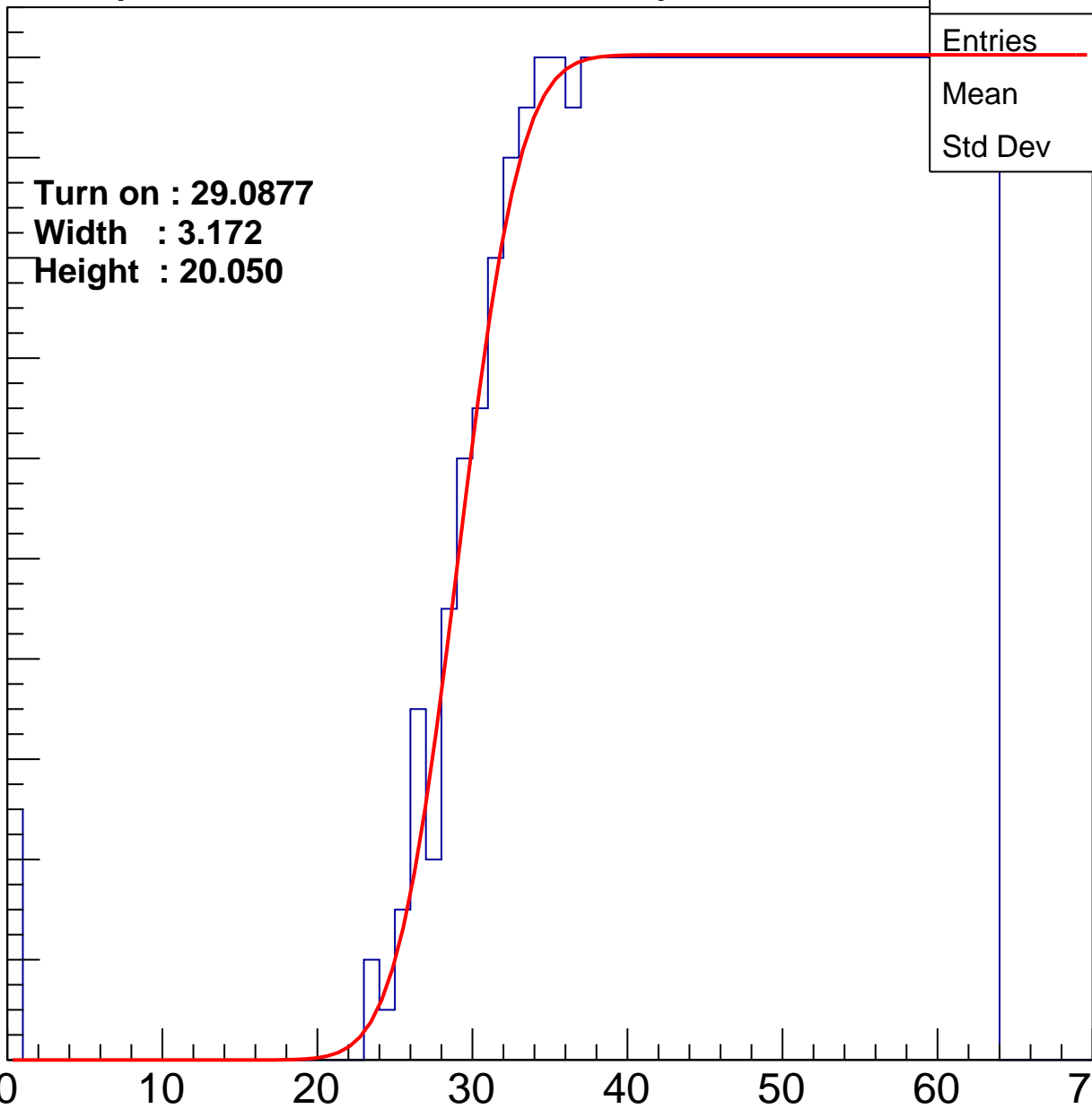
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.0877
Width : 3.172
Height : 20.050

Entries	708
Mean	45.46
Std Dev	11.03

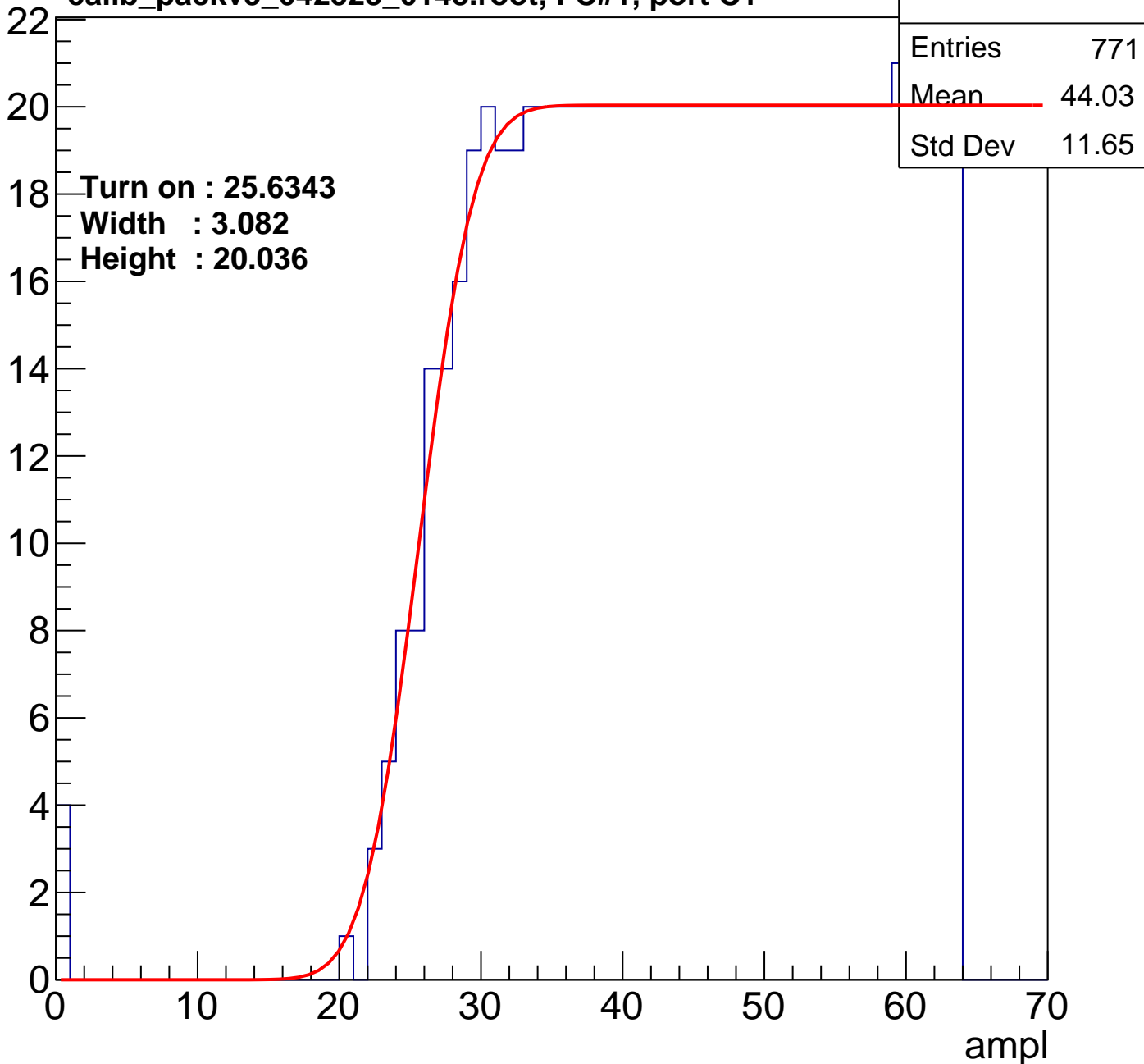
ampl



B0L101S, U25-ch72

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch73

calib_packv5_042523_0143.root, FC#1, port C1

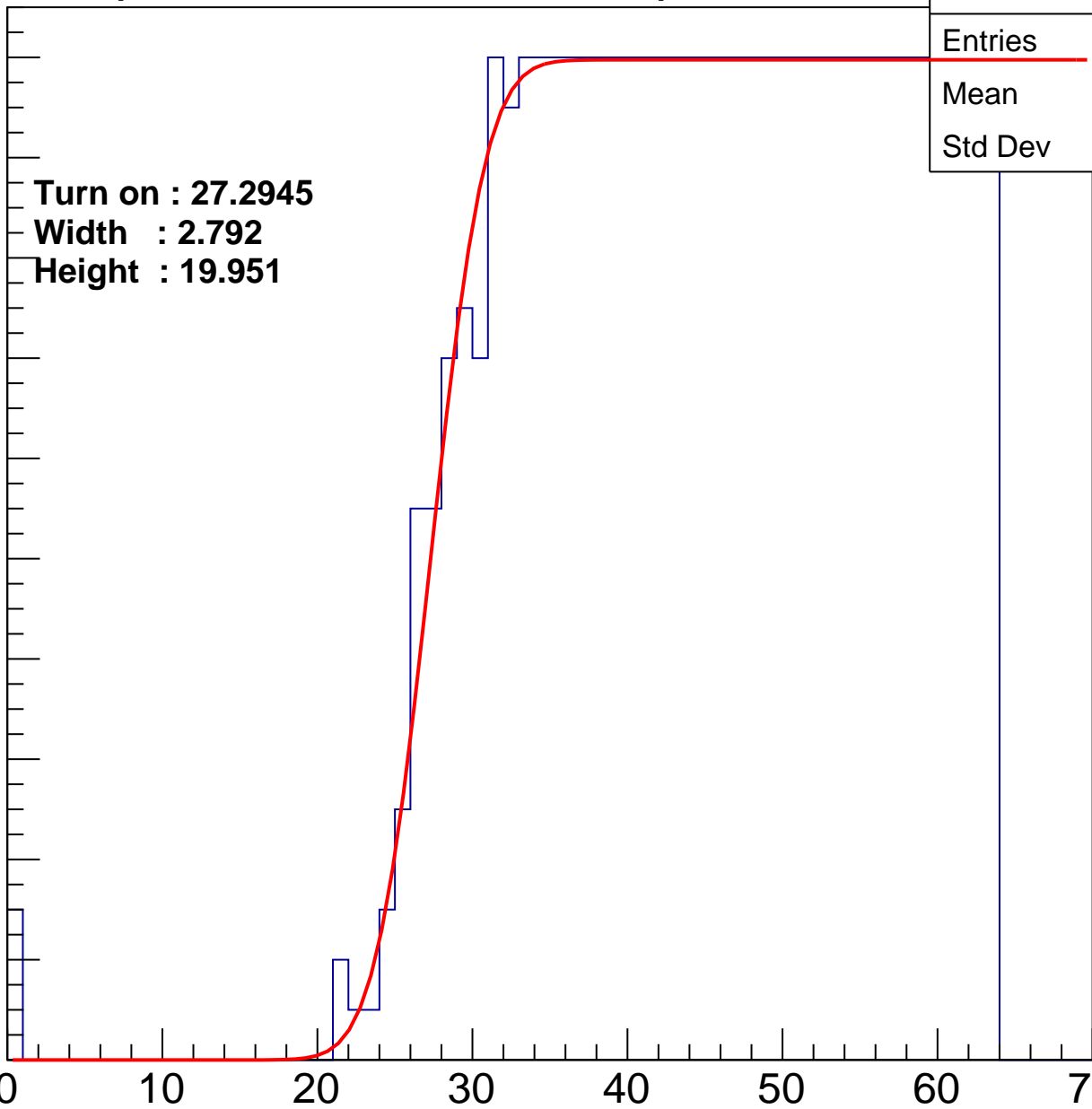
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2945
Width : 2.792
Height : 19.951

Entries	739
Mean	44.79
Std Dev	11.18

ampl



B0L101S, U25-ch74

calib_packv5_042523_0143.root, FC#1, port C1

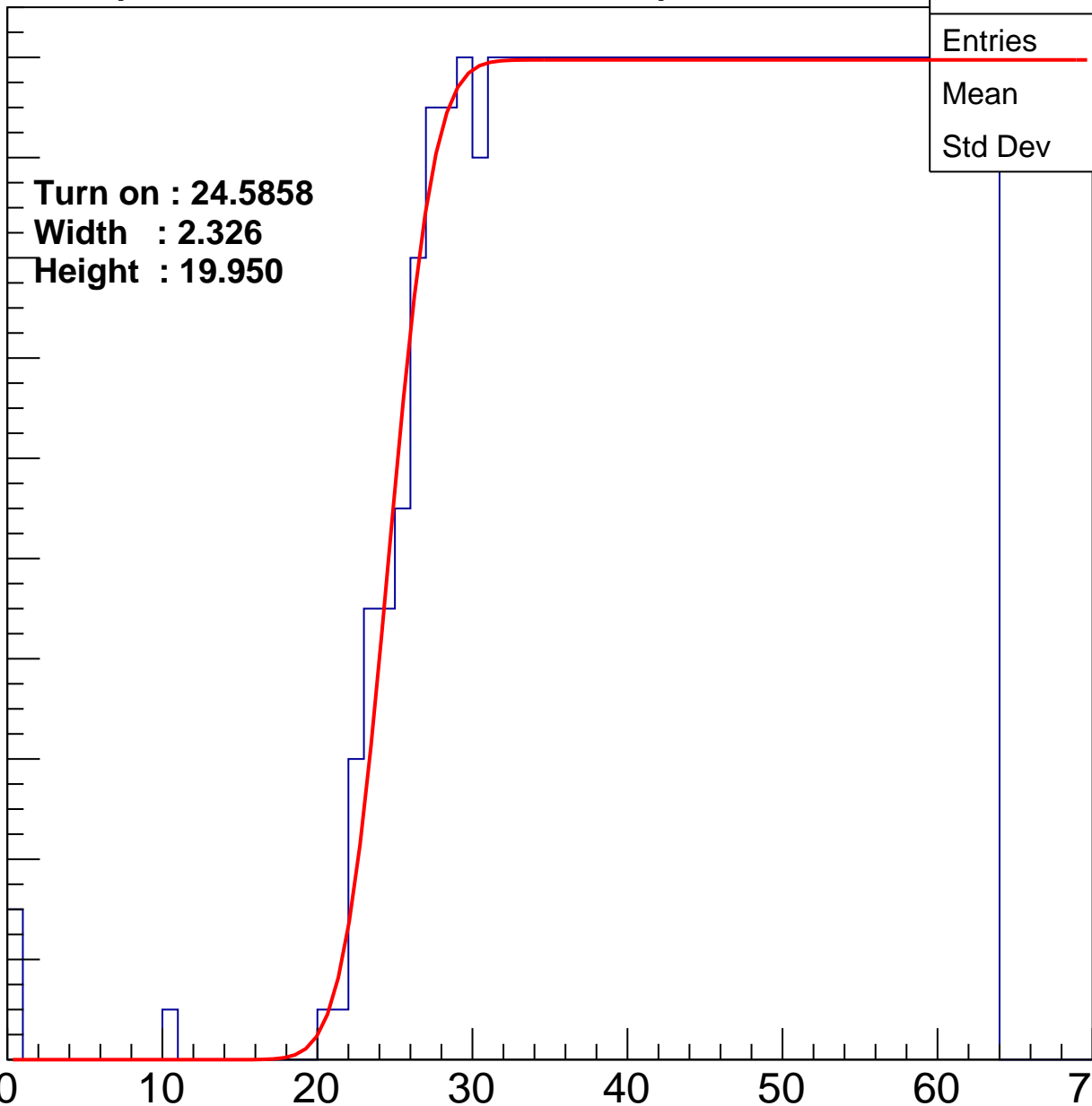
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.5858
Width : 2.326
Height : 19.950

Entries	793
Mean	43.48
Std Dev	11.87

ampl



B0L101S, U25-ch75

calib_packv5_042523_0143.root, FC#1, port C1

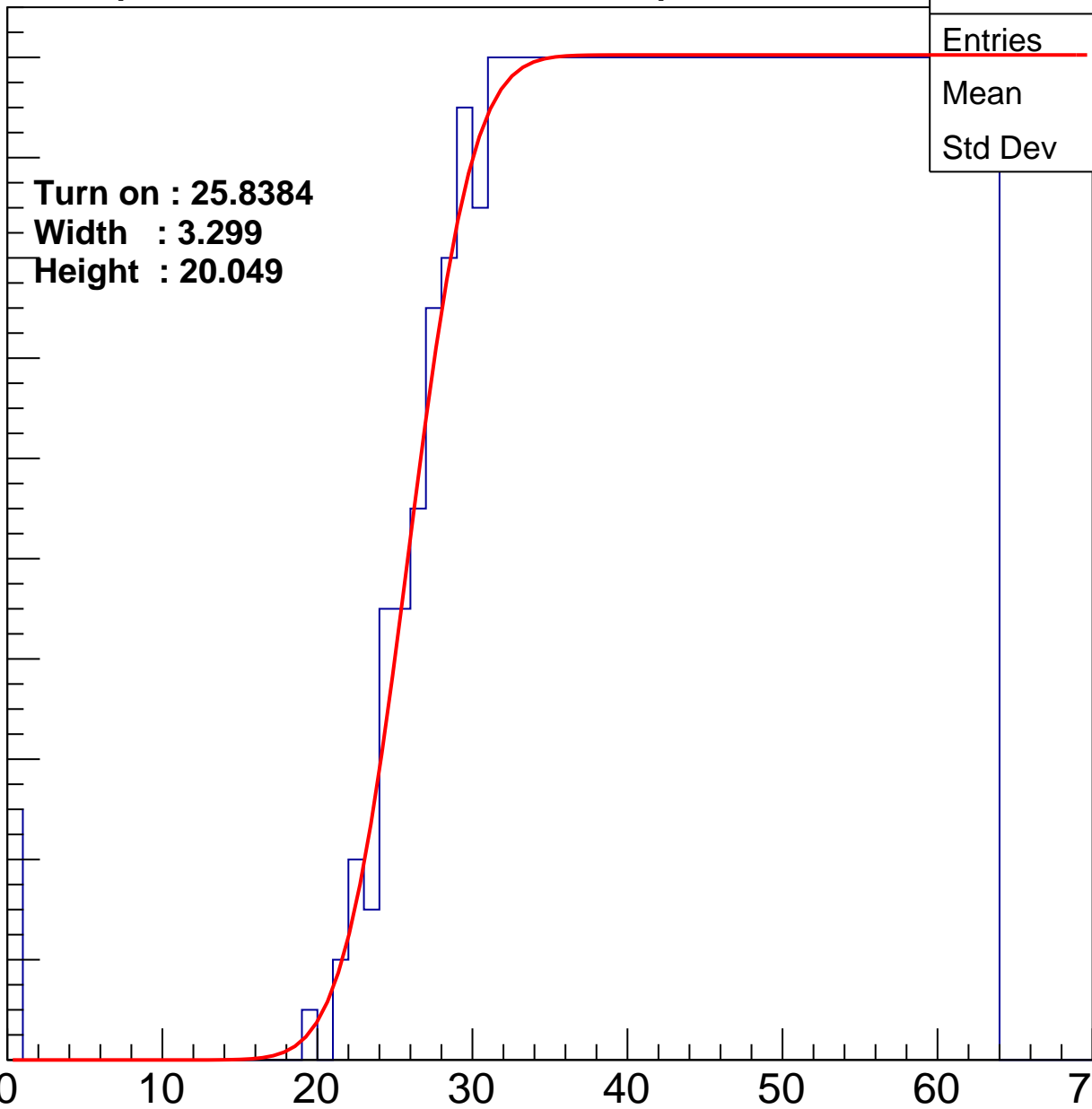
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8384
Width : 3.299
Height : 20.049

Entries	771
Mean	43.94
Std Dev	11.77

ampl



B0L101S, U25-ch76

calib_packv5_042523_0143.root, FC#1, port C1

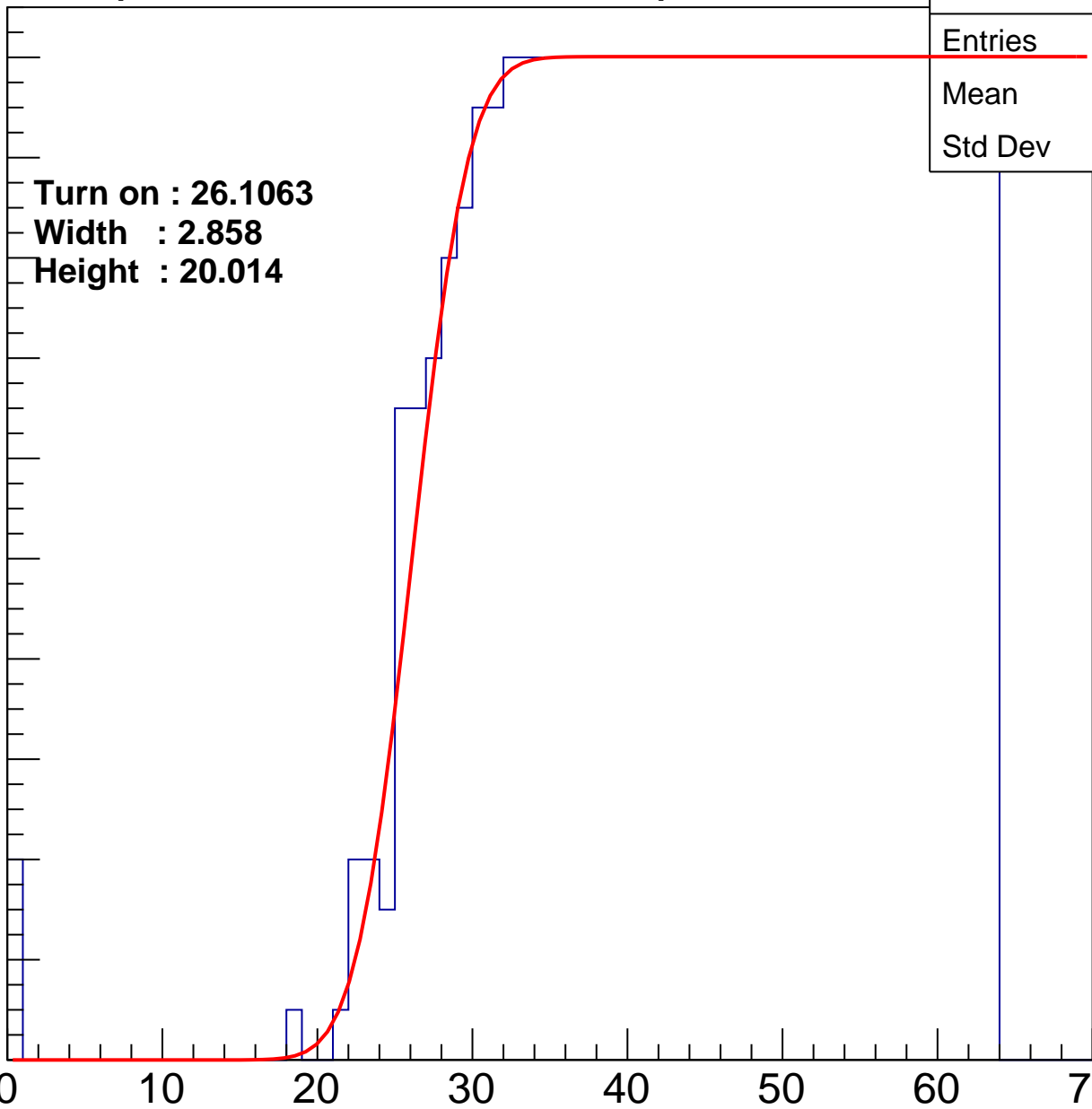
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1063
Width : 2.858
Height : 20.014

Entries	768
Mean	44.05
Std Dev	11.64

ampl



B0L101S, U25-ch77

calib_packv5_042523_0143.root, FC#1, port C1

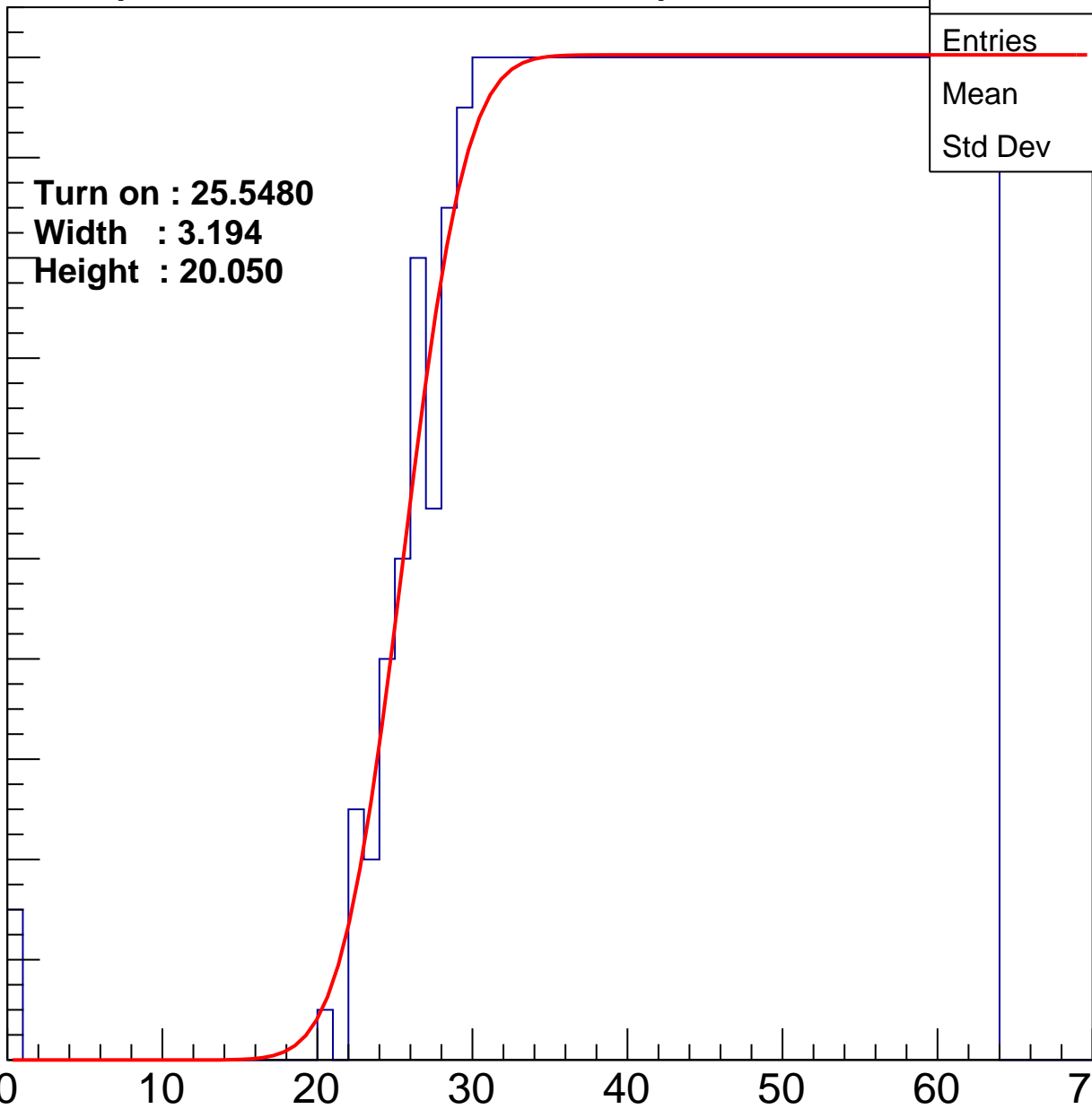
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5480
Width : 3.194
Height : 20.050

Entries	774
Mean	43.96
Std Dev	11.6

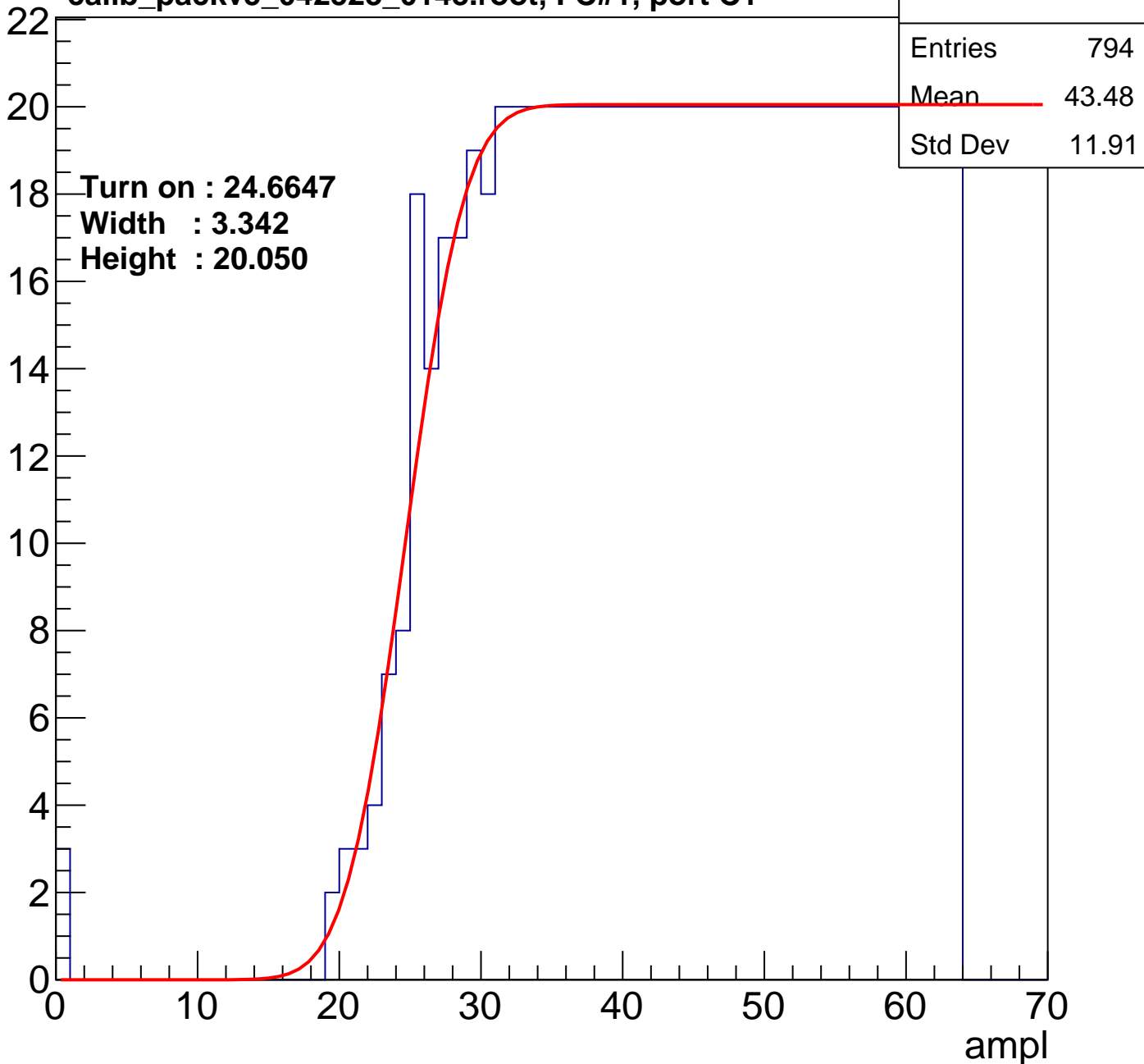
ampl



B0L101S, U25-ch78

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch79

calib_packv5_042523_0143.root, FC#1, port C1

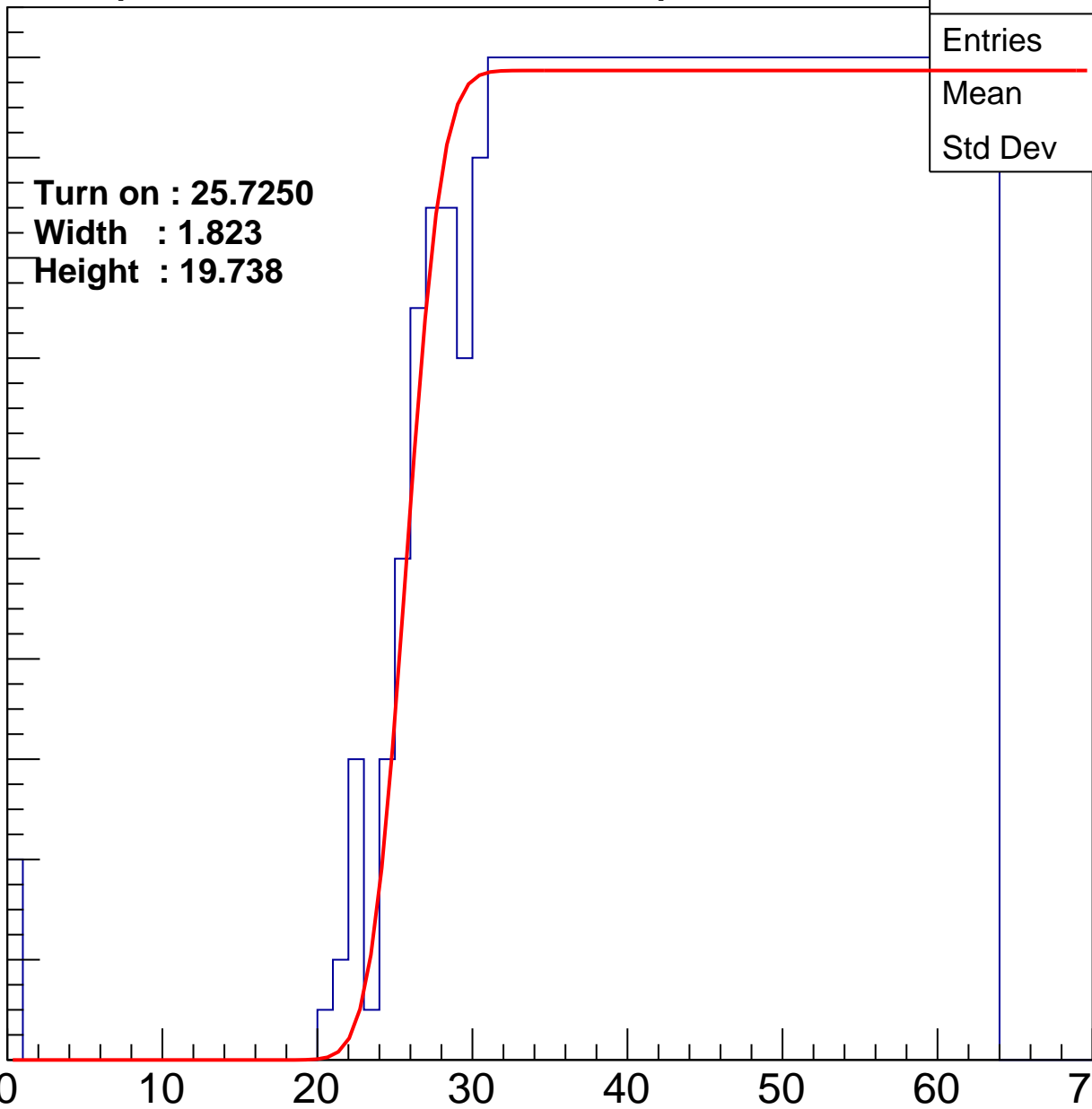
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7250
Width : 1.823
Height : 19.738

Entries	771
Mean	43.97
Std Dev	11.69

ampl



B0L101S, U25-ch80

calib_packv5_042523_0143.root, FC#1, port C1

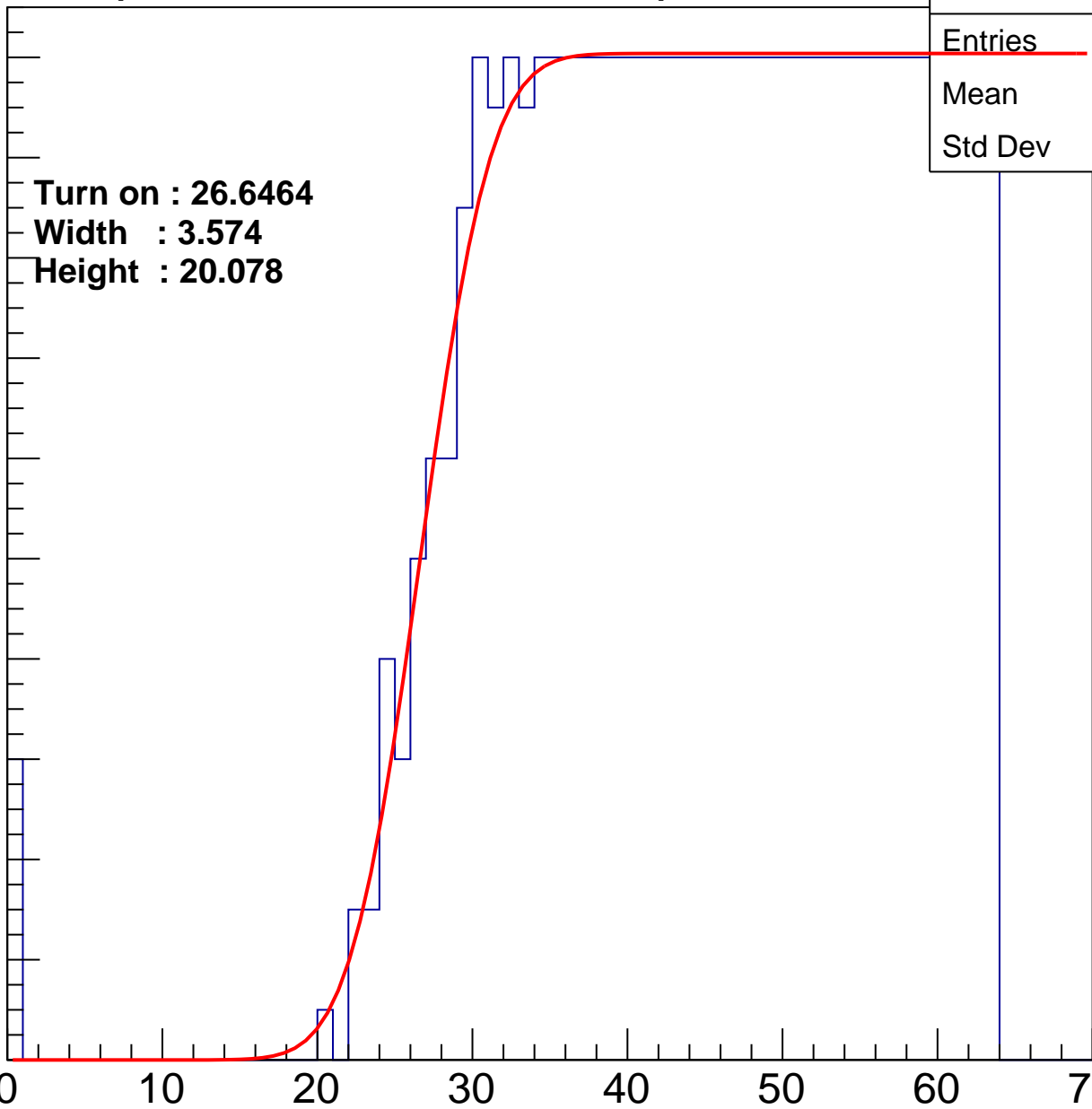
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6464
Width : 3.574
Height : 20.078

Entries	756
Mean	44.27
Std Dev	11.69

ampl



B0L101S, U25-ch81

calib_packv5_042523_0143.root, FC#1, port C1

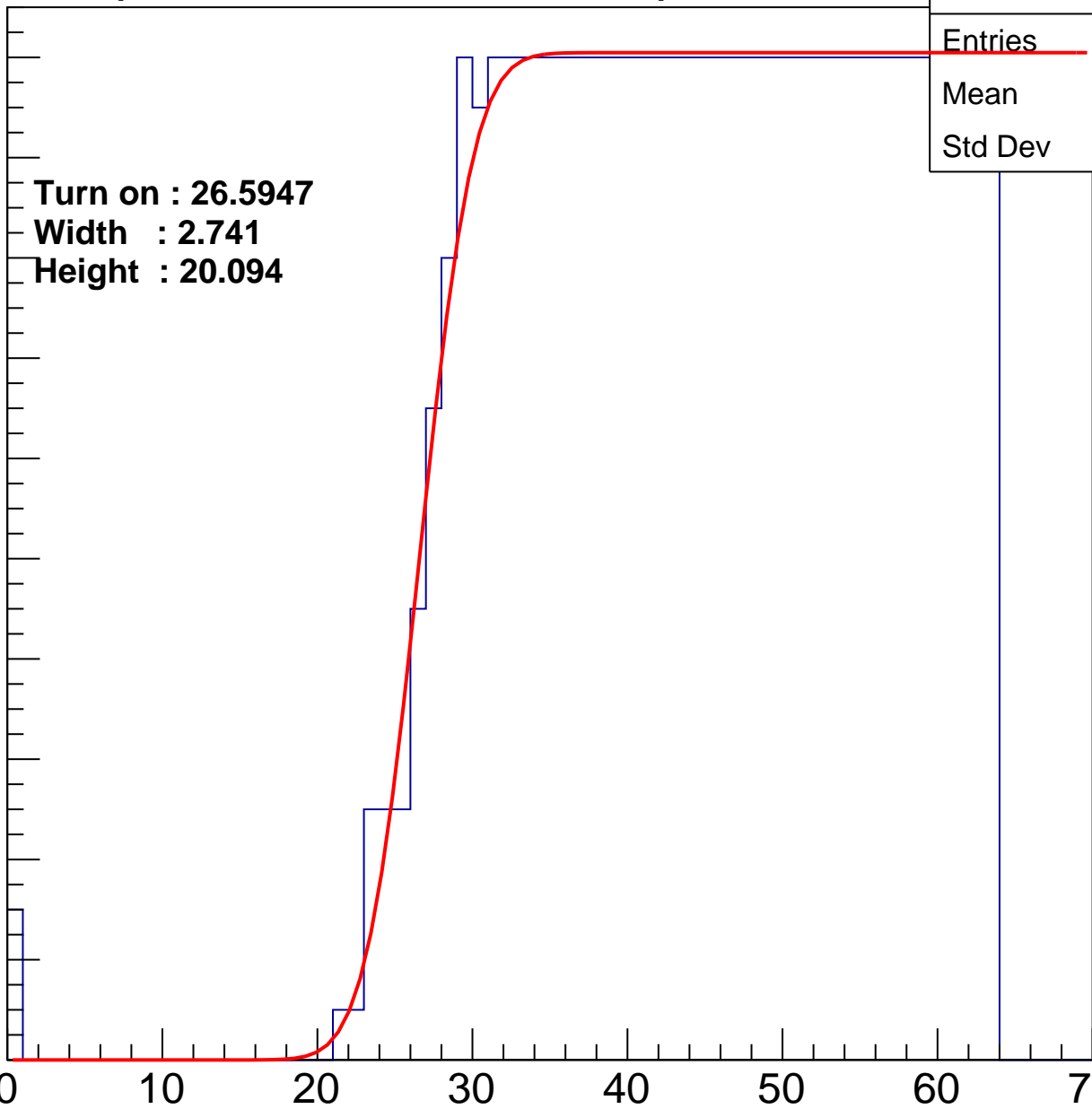
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5947
Width : 2.741
Height : 20.094

Entries	757
Mean	44.39
Std Dev	11.35

ampl



B0L101S, U25-ch82

calib_packv5_042523_0143.root, FC#1, port C1

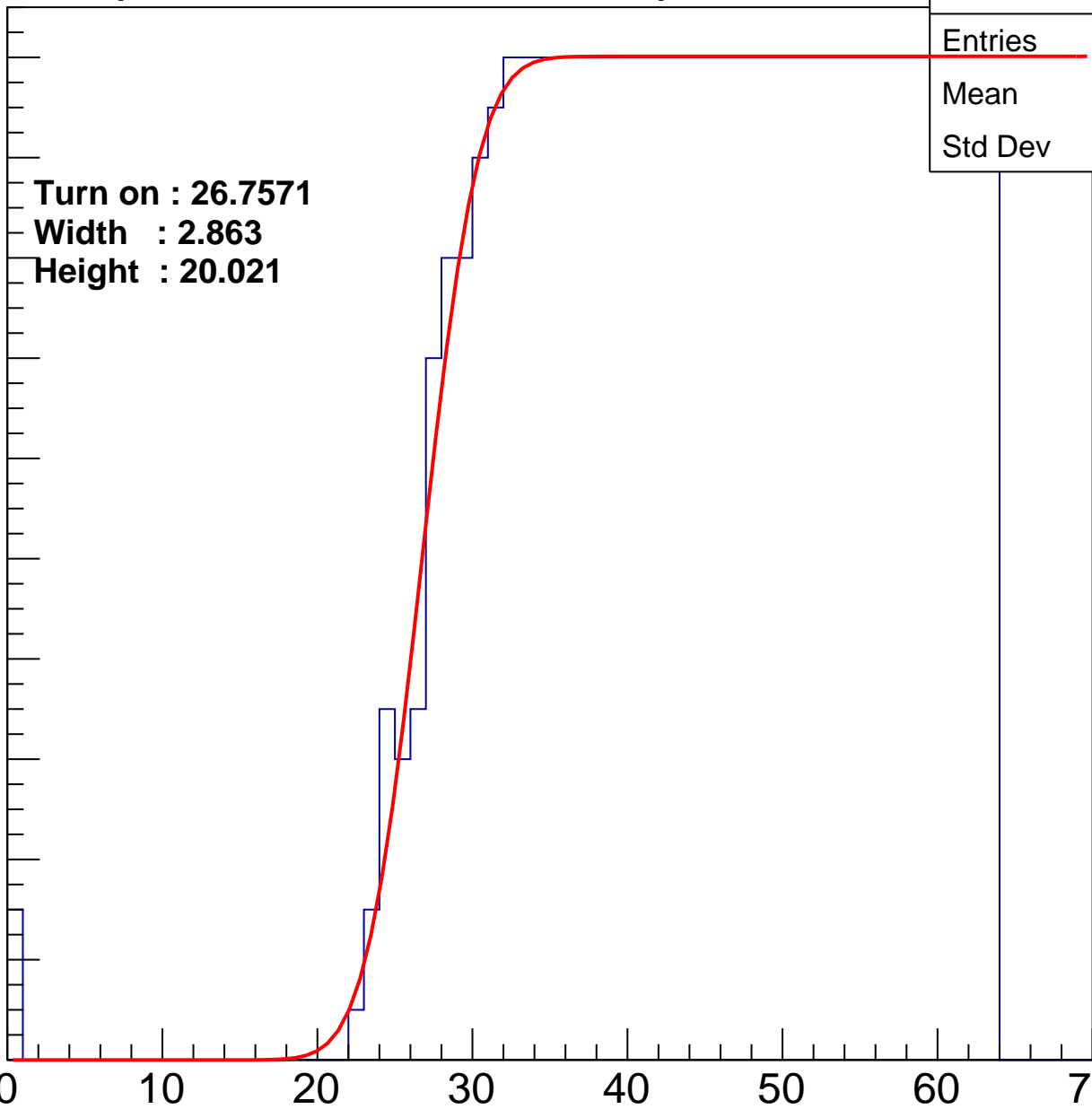
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7571
Width : 2.863
Height : 20.021

Entries	750
Mean	44.55
Std Dev	11.28

ampl



B0L101S, U25-ch83

calib_packv5_042523_0143.root, FC#1, port C1

Entry

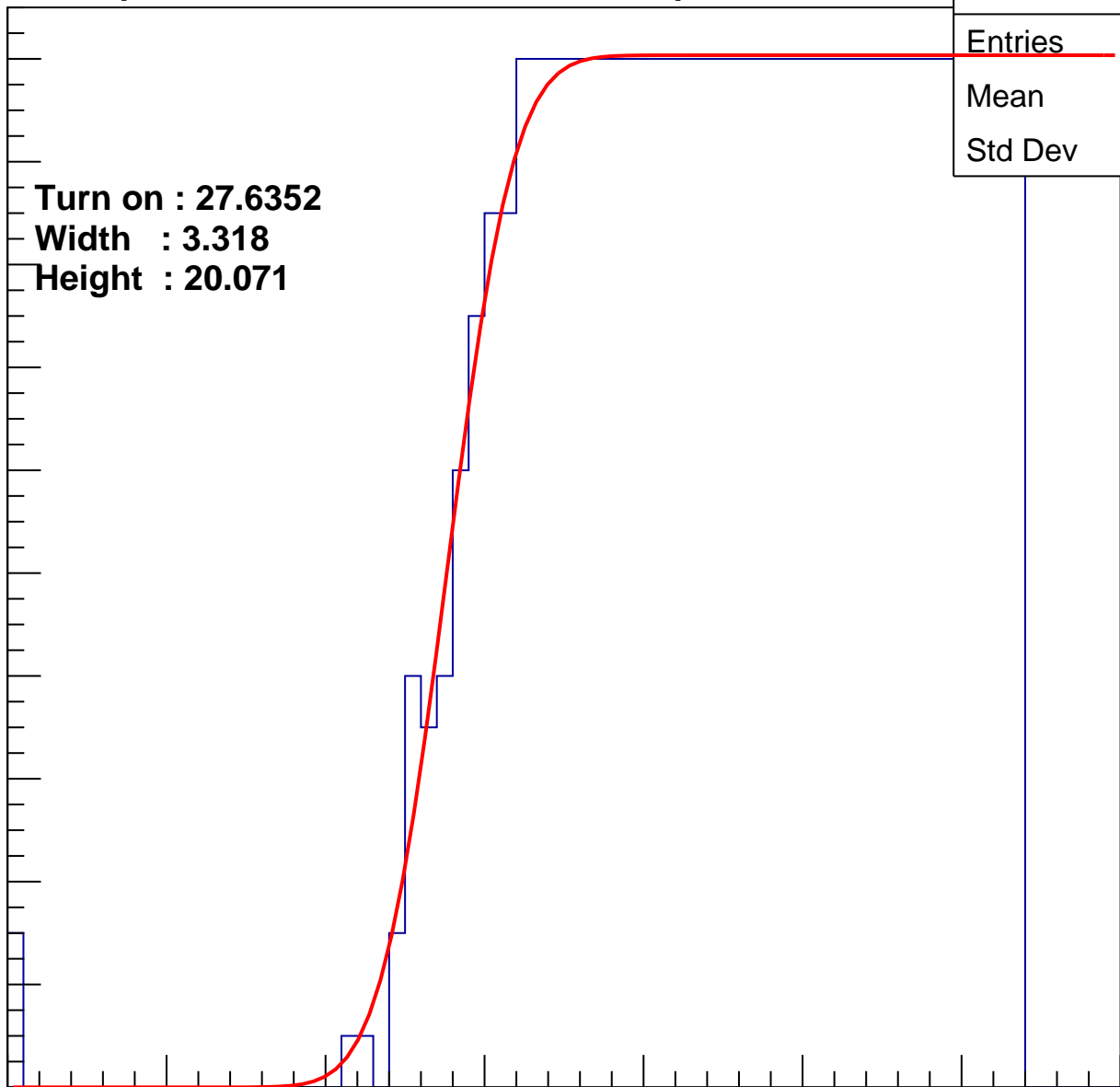
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6352
Width : 3.318
Height : 20.071

Entries	732
Mean	44.97
Std Dev	11.08

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch84

calib_packv5_042523_0143.root, FC#1, port C1

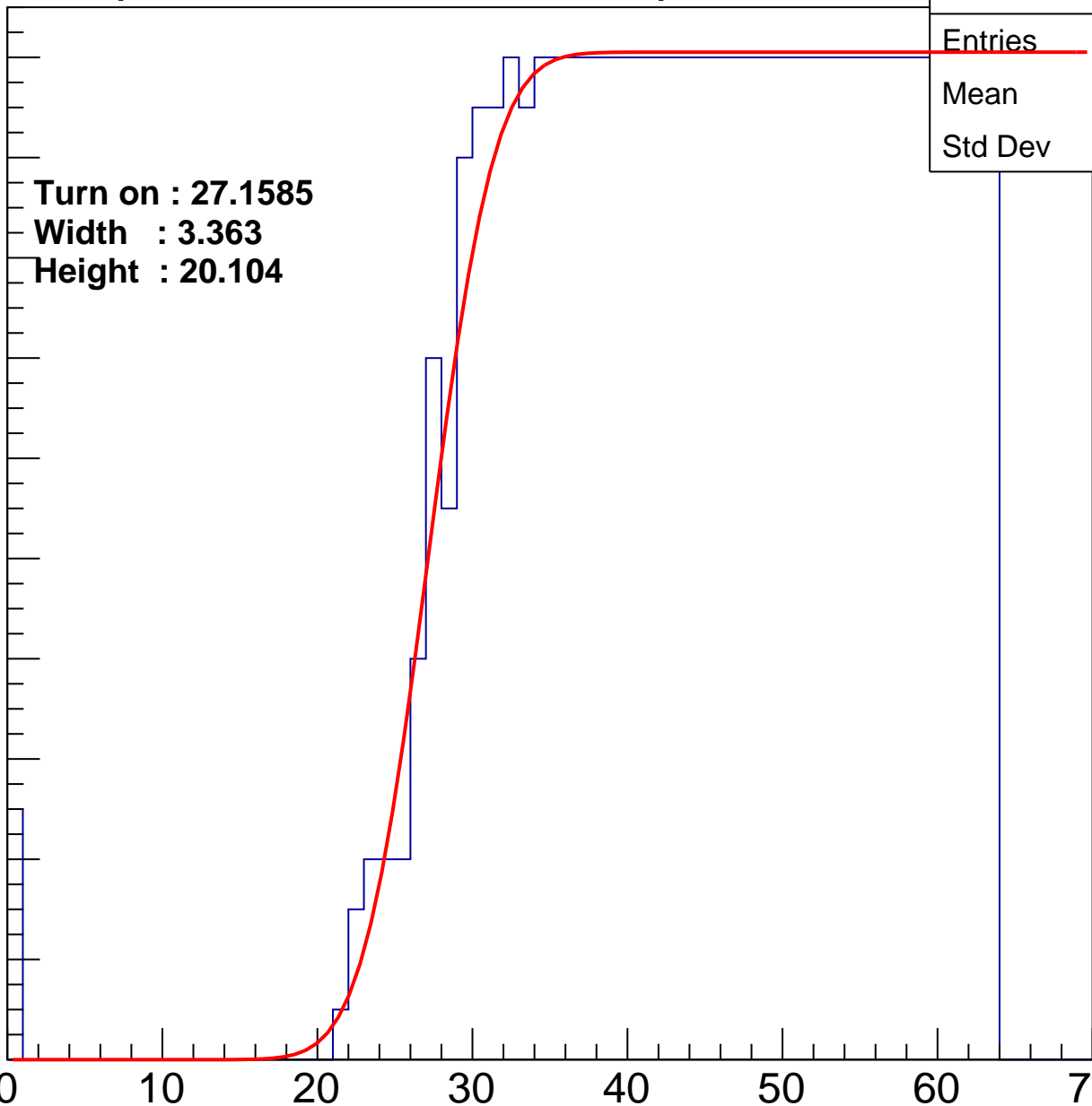
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1585
Width : 3.363
Height : 20.104

Entries	749
Mean	44.48
Std Dev	11.5

ampl



B0L101S, U25-ch85

calib_packv5_042523_0143.root, FC#1, port C1

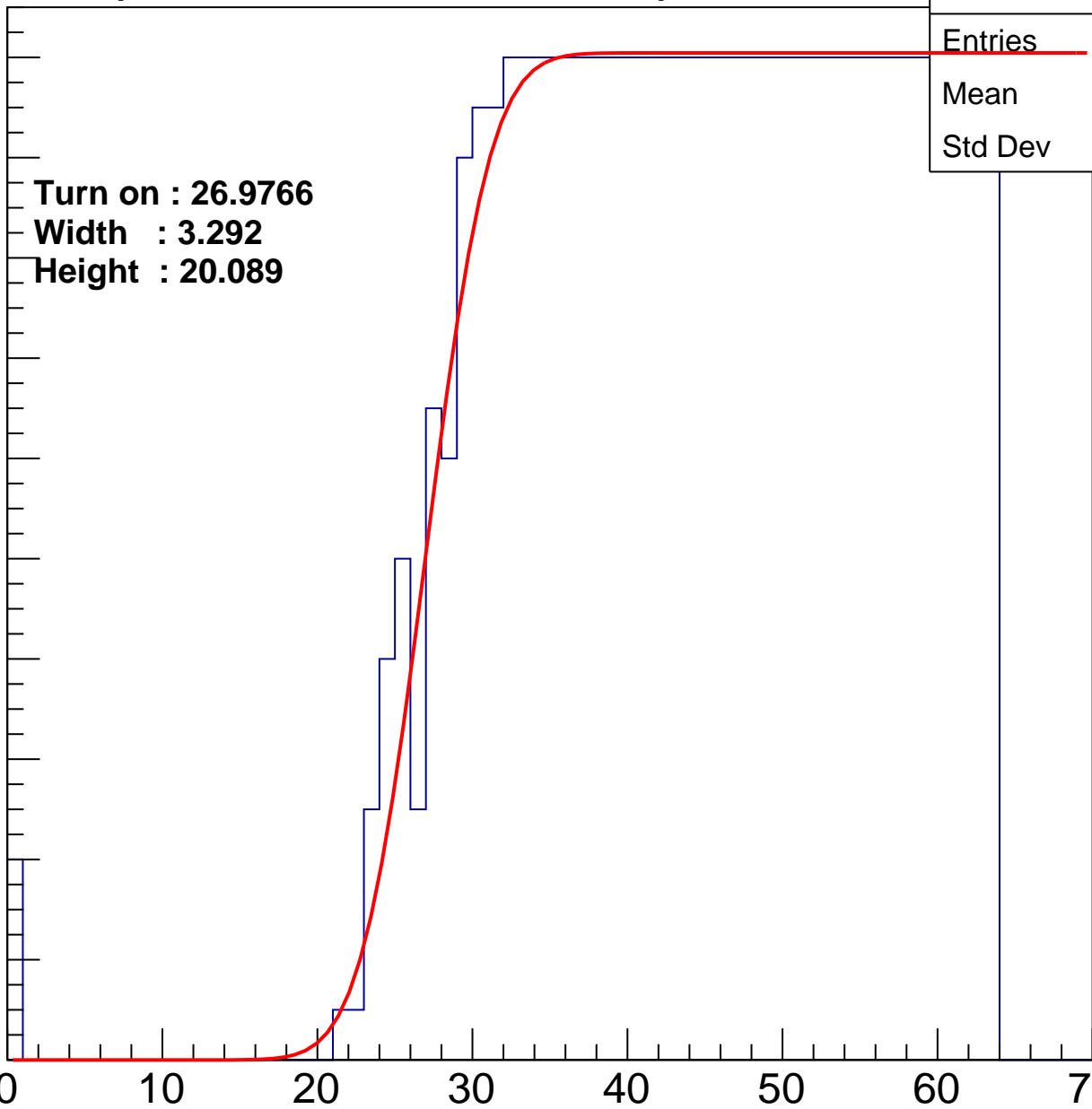
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9766
Width : 3.292
Height : 20.089

Entries	755
Mean	44.37
Std Dev	11.48

ampl



B0L101S, U25-ch86

calib_packv5_042523_0143.root, FC#1, port C1

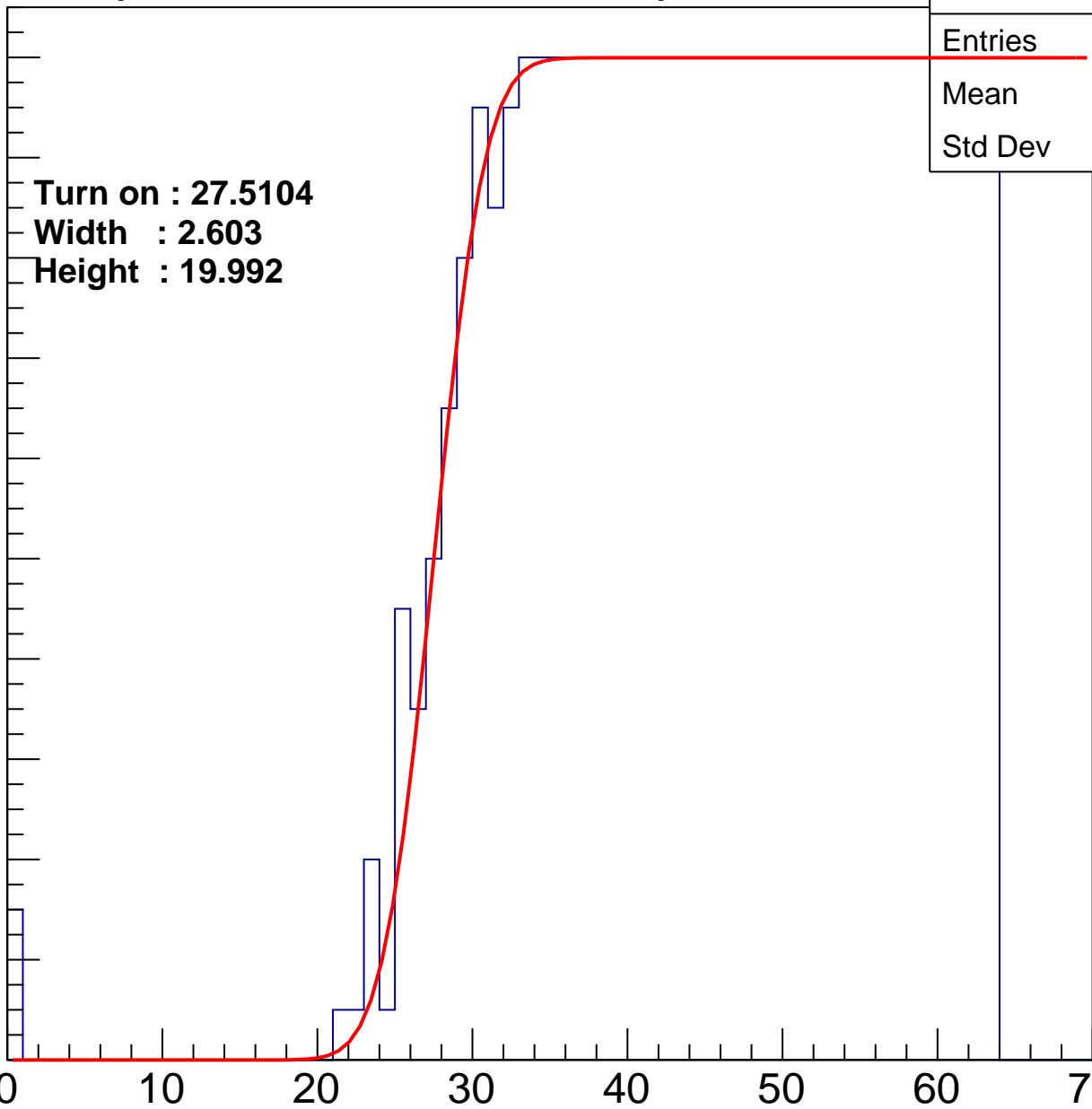
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5104
Width : 2.603
Height : 19.992

Entries	740
Mean	44.77
Std Dev	11.19

ampl



B0L101S, U25-ch87

calib_packv5_042523_0143.root, FC#1, port C1

Entry

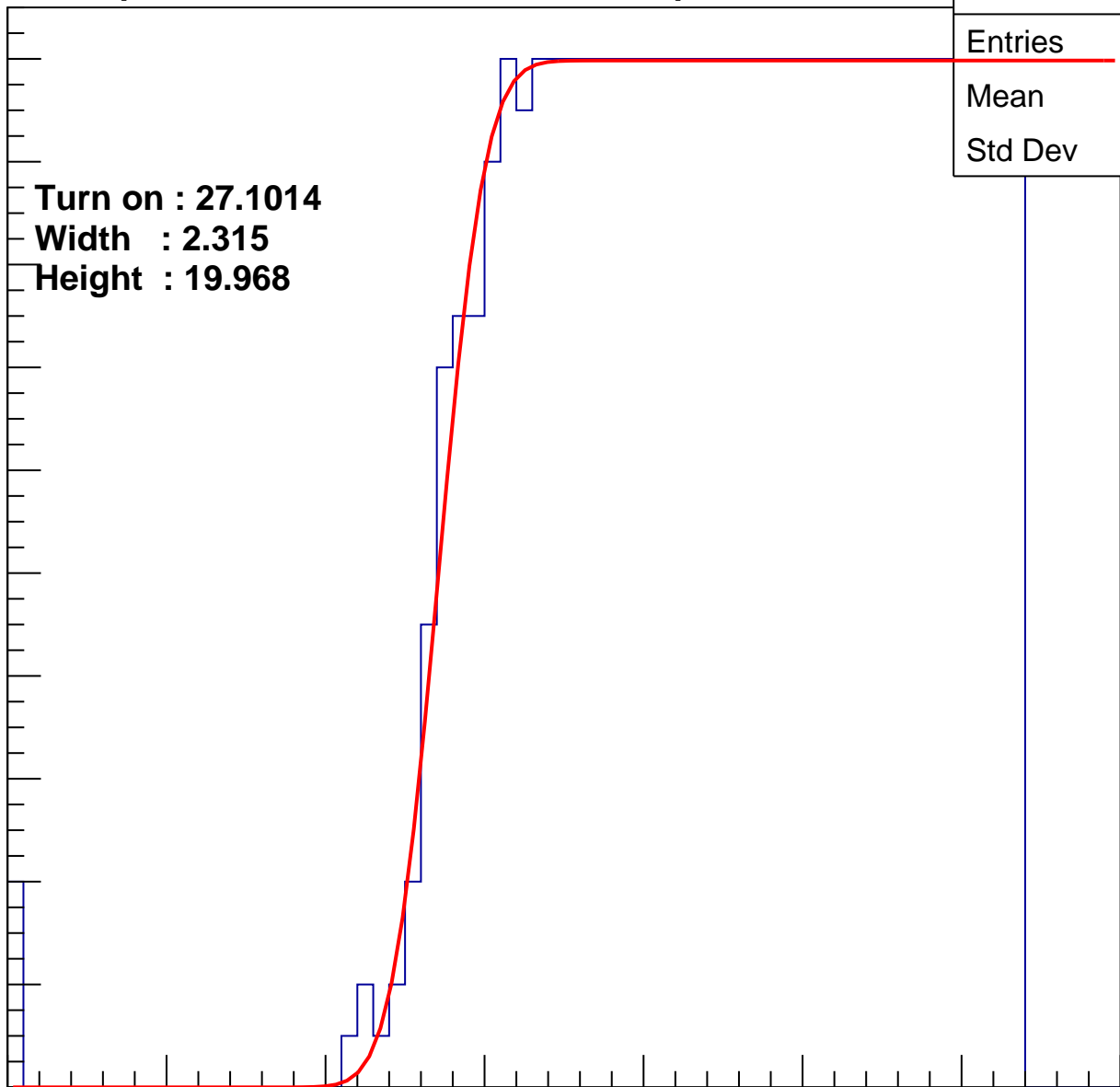
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1014
Width : 2.315
Height : 19.968

Entries	744
Mean	44.67
Std Dev	11.3

ampl

0 10 20 30 40 50 60 70



B0L101S, U25-ch88

calib_packv5_042523_0143.root, FC#1, port C1

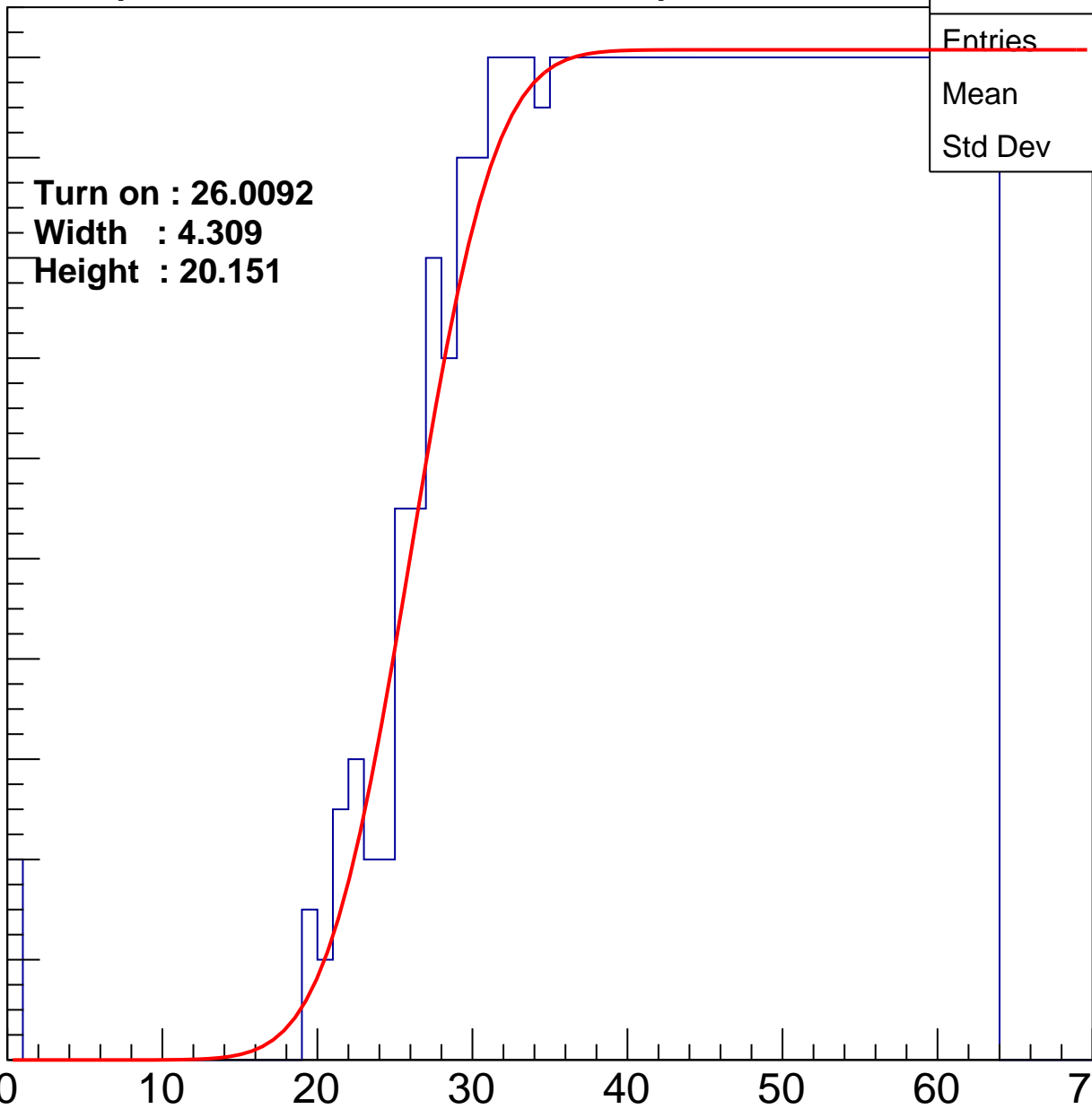
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0092
Width : 4.309
Height : 20.151

Entries	775
Mean	43.81
Std Dev	11.84

ampl



B0L101S, U25-ch89

calib_packv5_042523_0143.root, FC#1, port C1

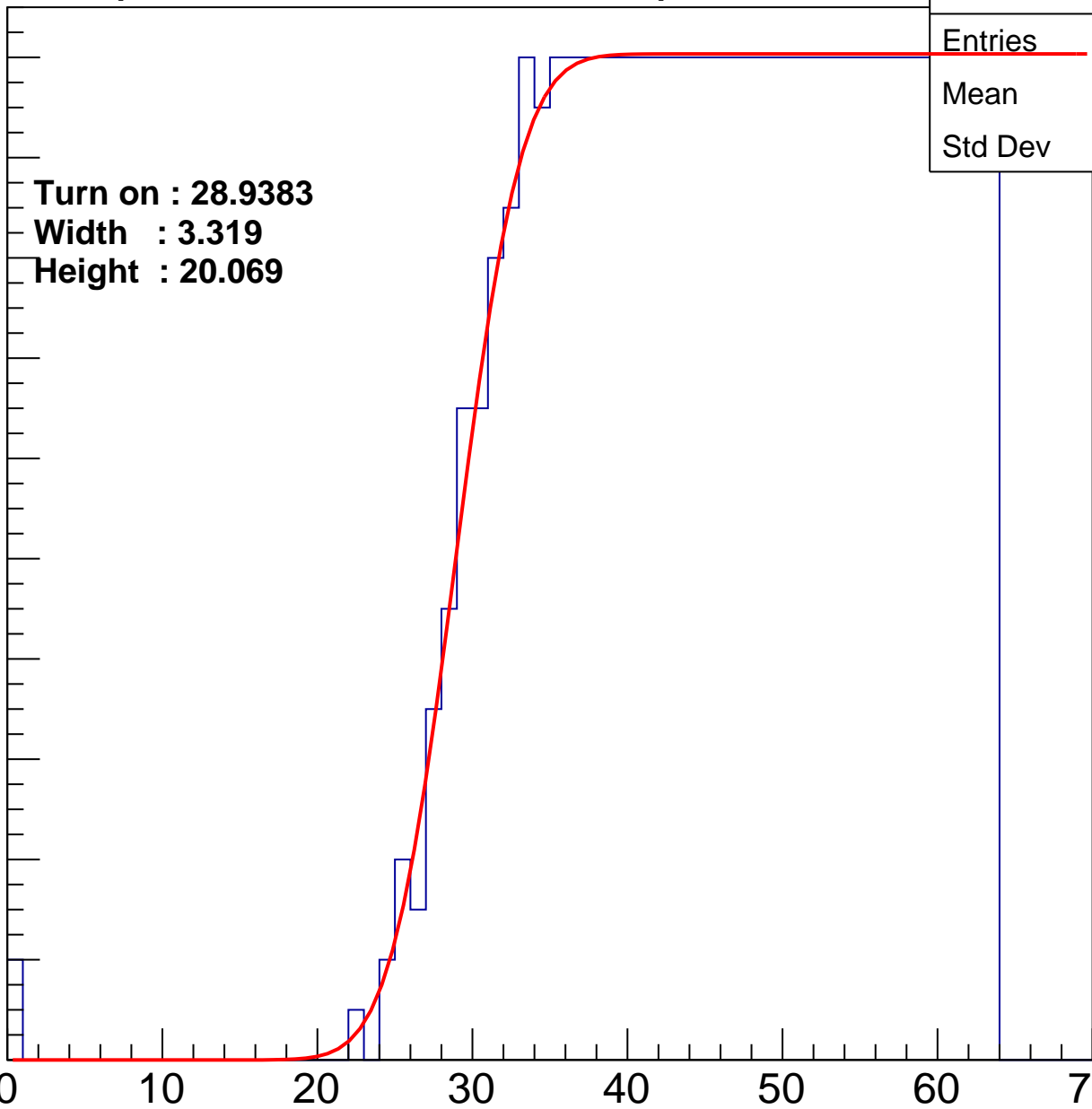
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9383
Width : 3.319
Height : 20.069

Entries	706
Mean	45.64
Std Dev	10.65

ampl



B0L101S, U25-ch90

calib_packv5_042523_0143.root, FC#1, port C1

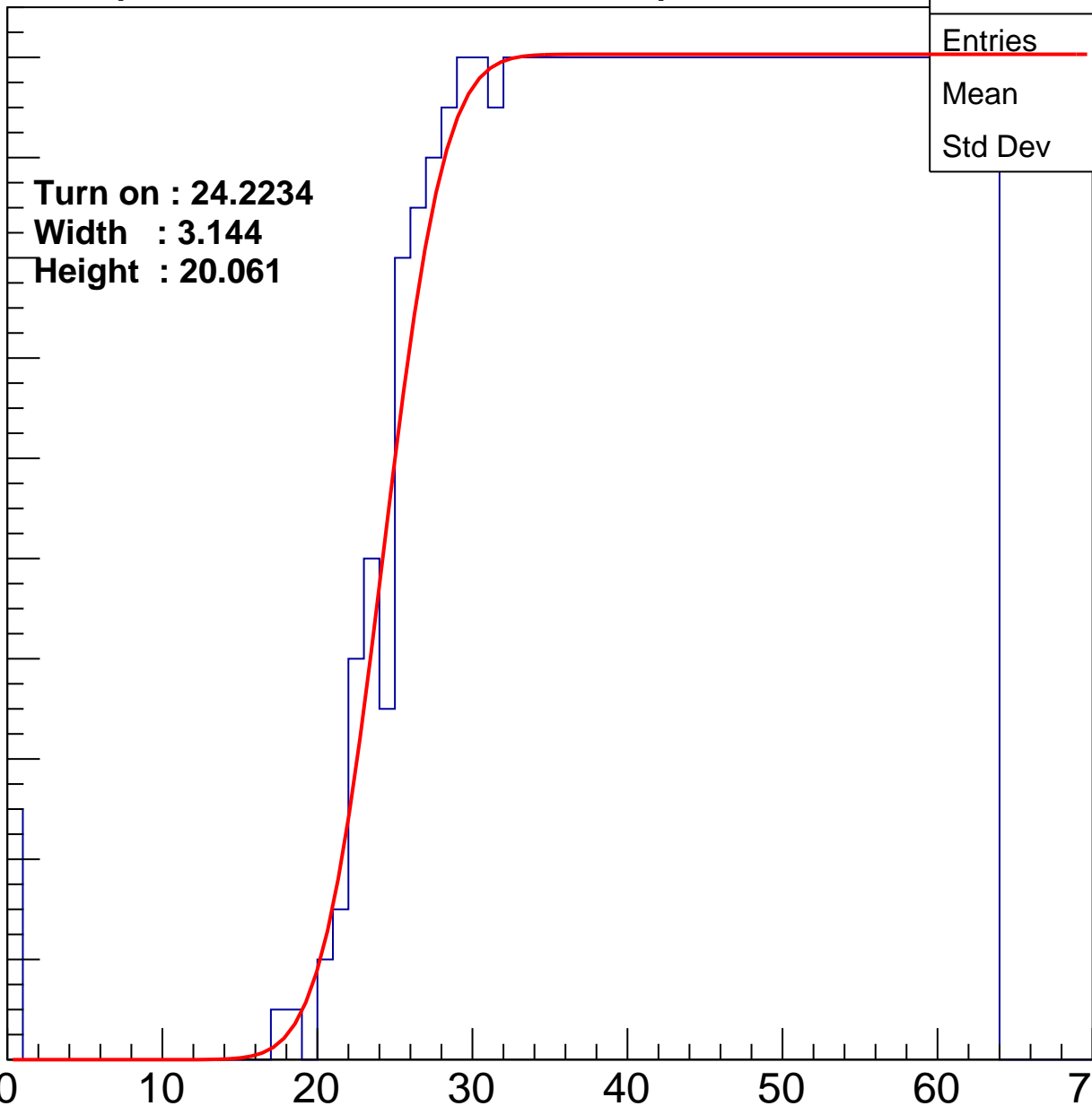
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.2234
Width : 3.144
Height : 20.061

Entries	806
Mean	43.1
Std Dev	12.18

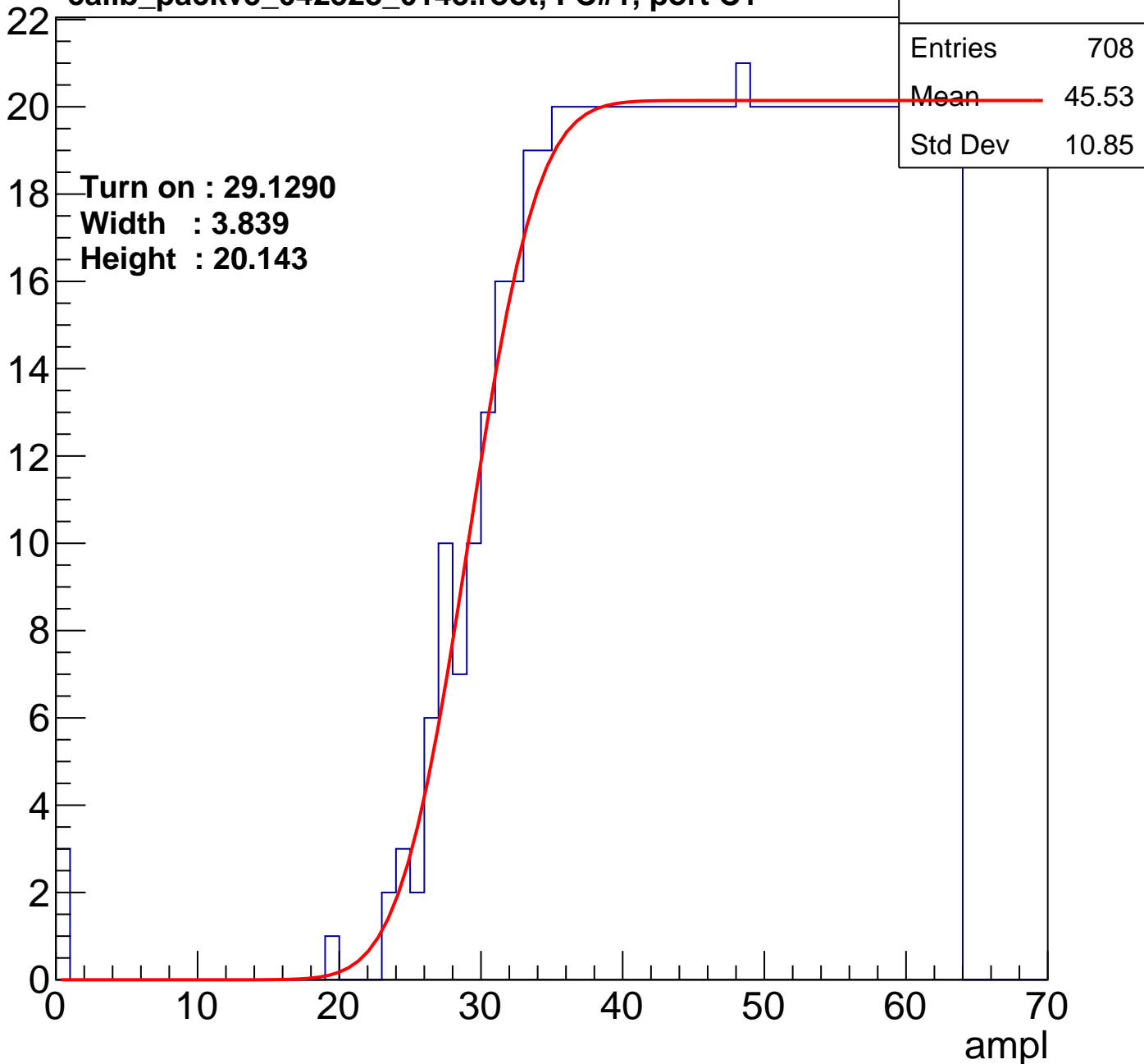
ampl



B0L101S, U25-ch91

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch92

calib_packv5_042523_0143.root, FC#1, port C1

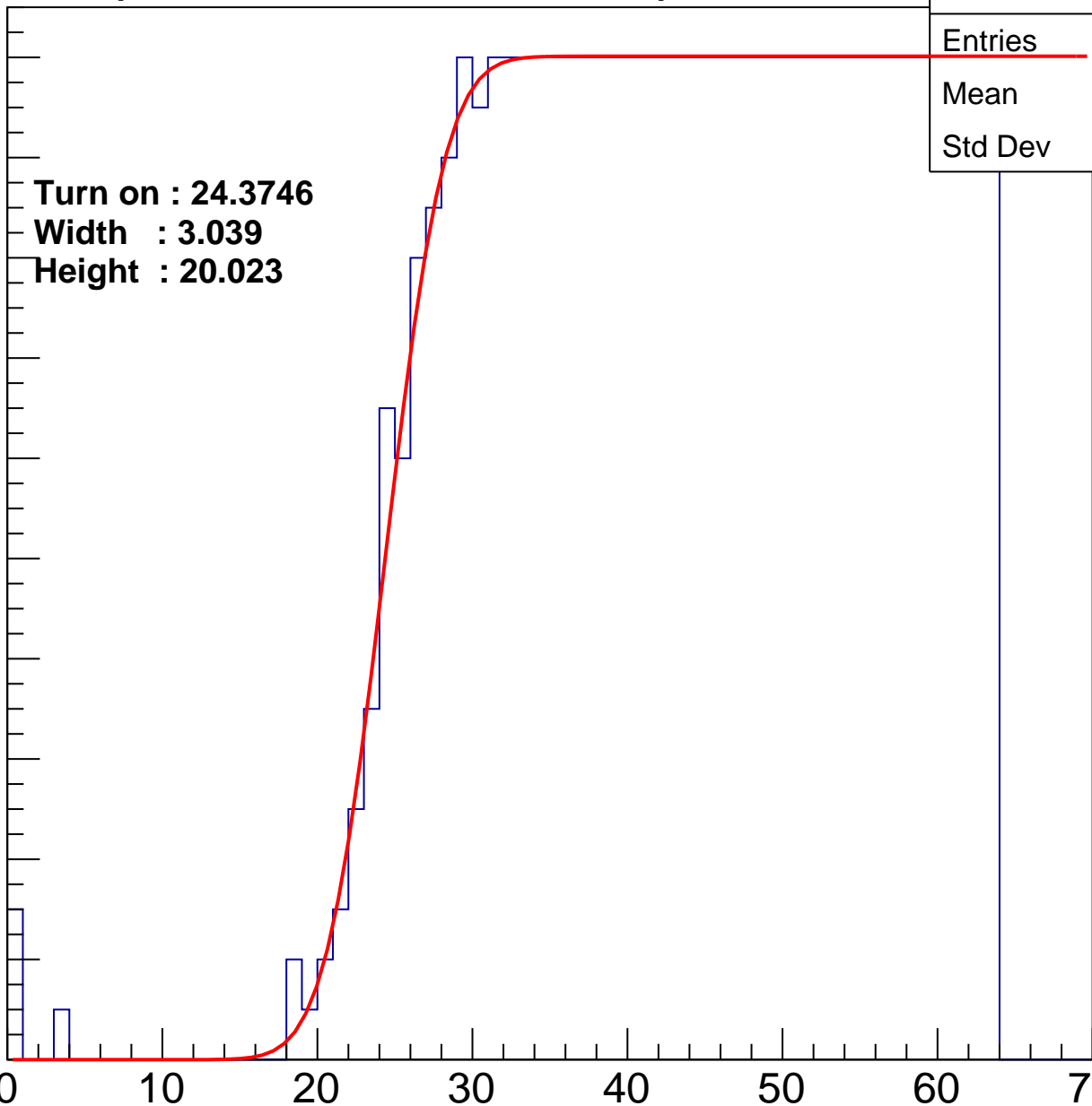
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.3746
Width : 3.039
Height : 20.023

Entries	799
Mean	43.3
Std Dev	12.03

ampl



B0L101S, U25-ch93

calib_packv5_042523_0143.root, FC#1, port C1

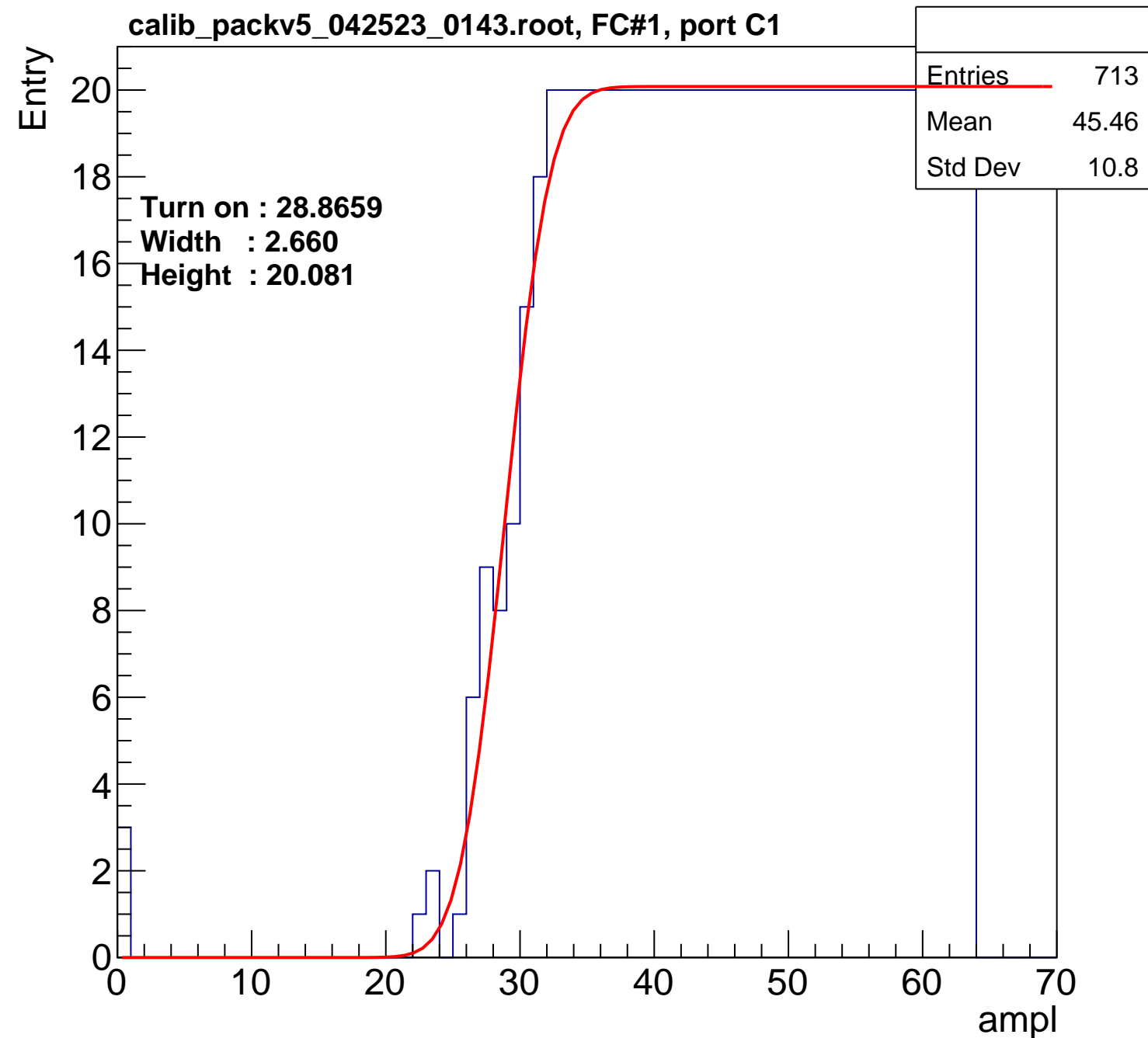
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.8659
Width : 2.660
Height : 20.081

Entries	713
Mean	45.46
Std Dev	10.8

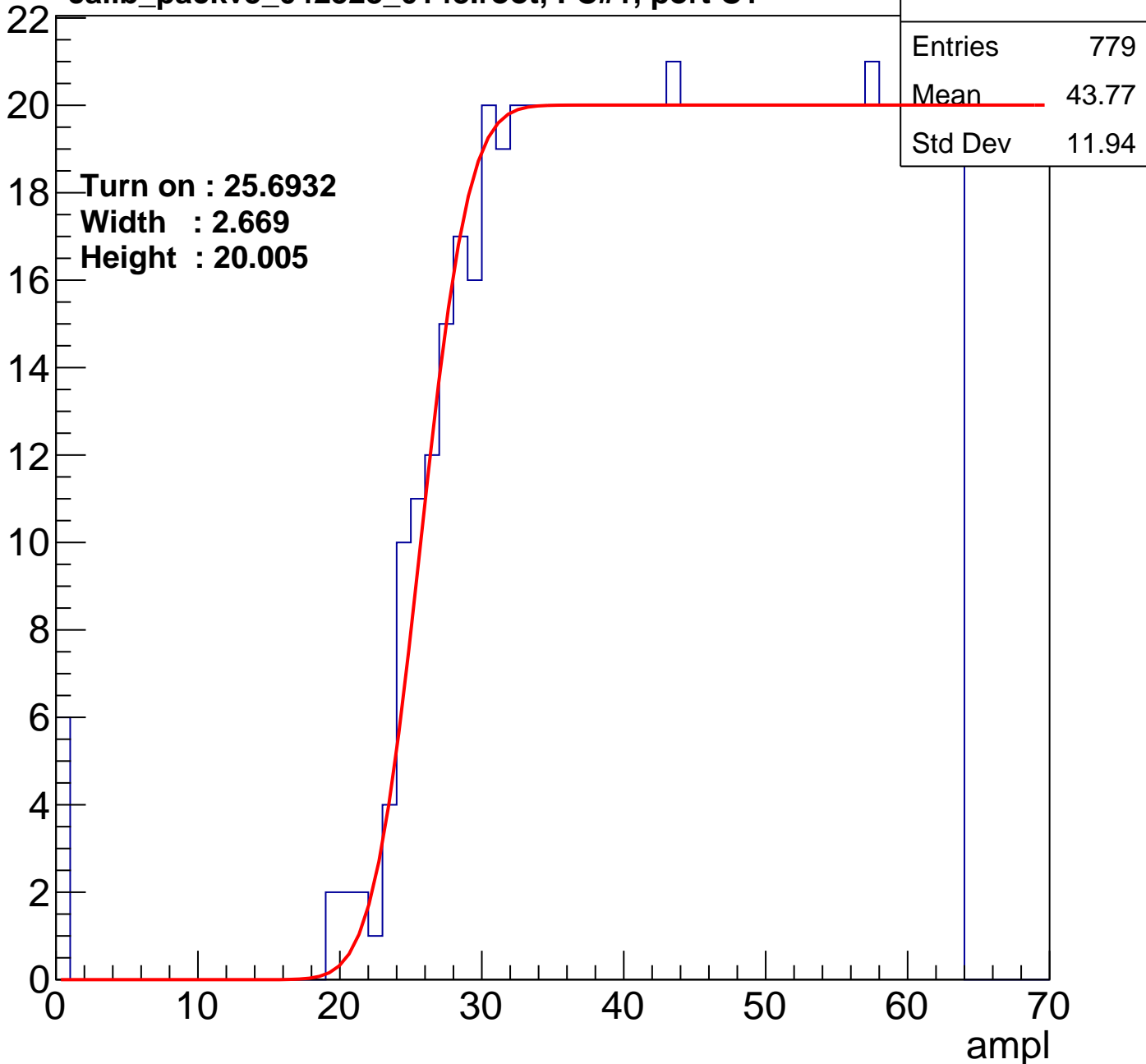
ampl



B0L101S, U25-ch94

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch95

calib_packv5_042523_0143.root, FC#1, port C1

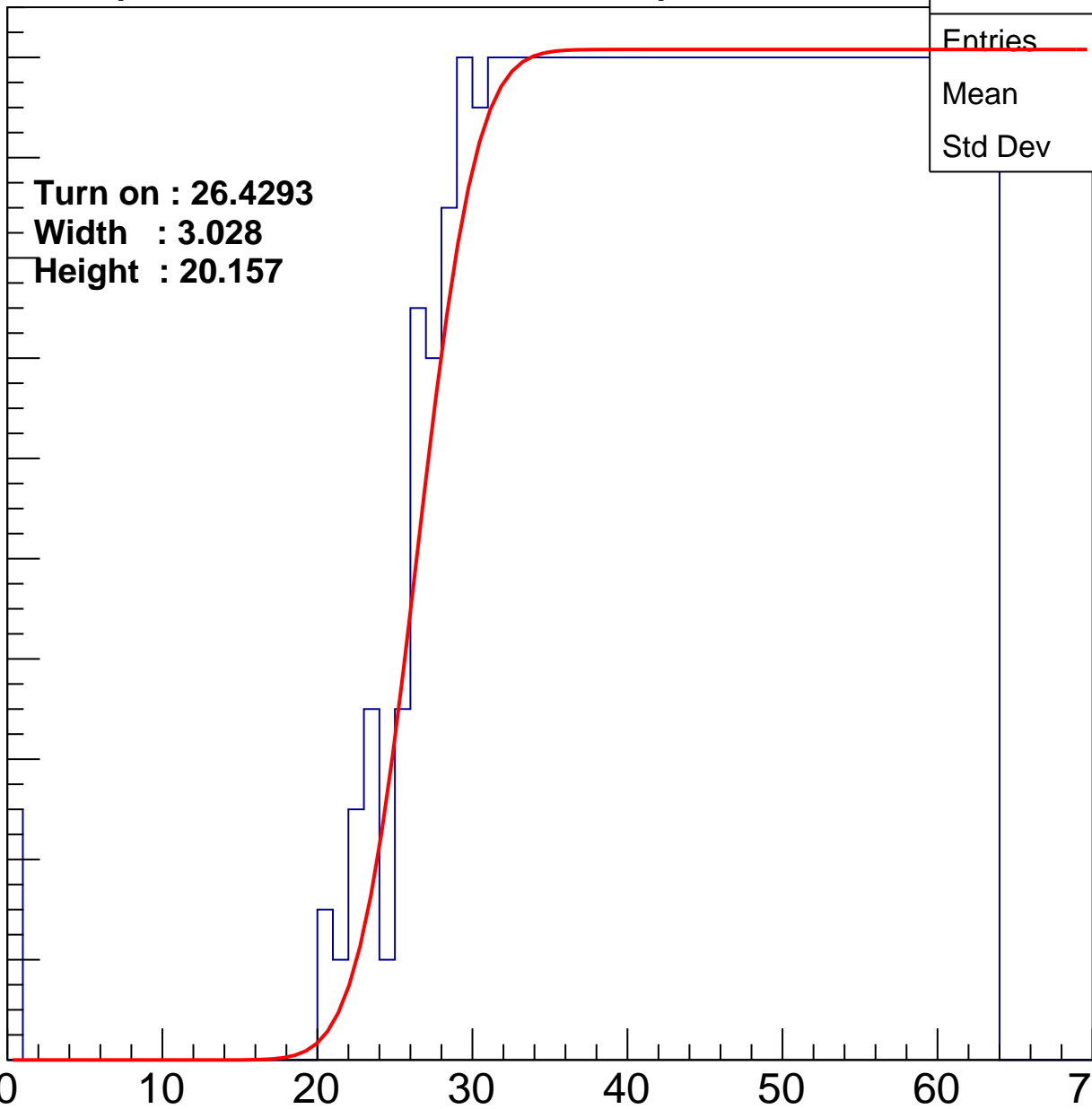
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4293
Width : 3.028
Height : 20.157

Entries	776
Mean	43.83
Std Dev	11.83

ampl



B0L101S, U25-ch96

calib_packv5_042523_0143.root, FC#1, port C1

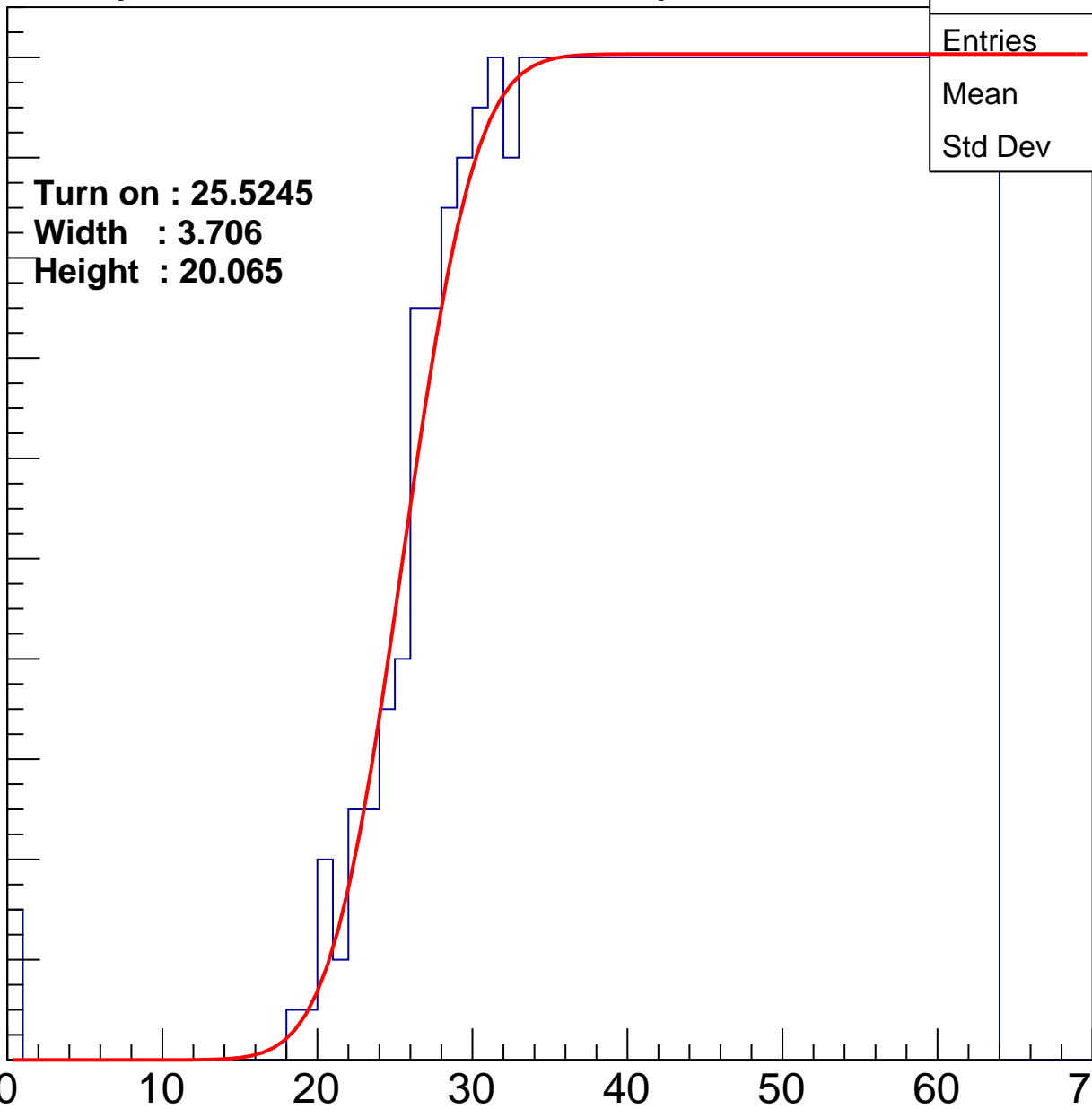
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5245
Width : 3.706
Height : 20.065

Entries	778
Mean	43.79
Std Dev	11.76

ampl



B0L101S, U25-ch97

calib_packv5_042523_0143.root, FC#1, port C1

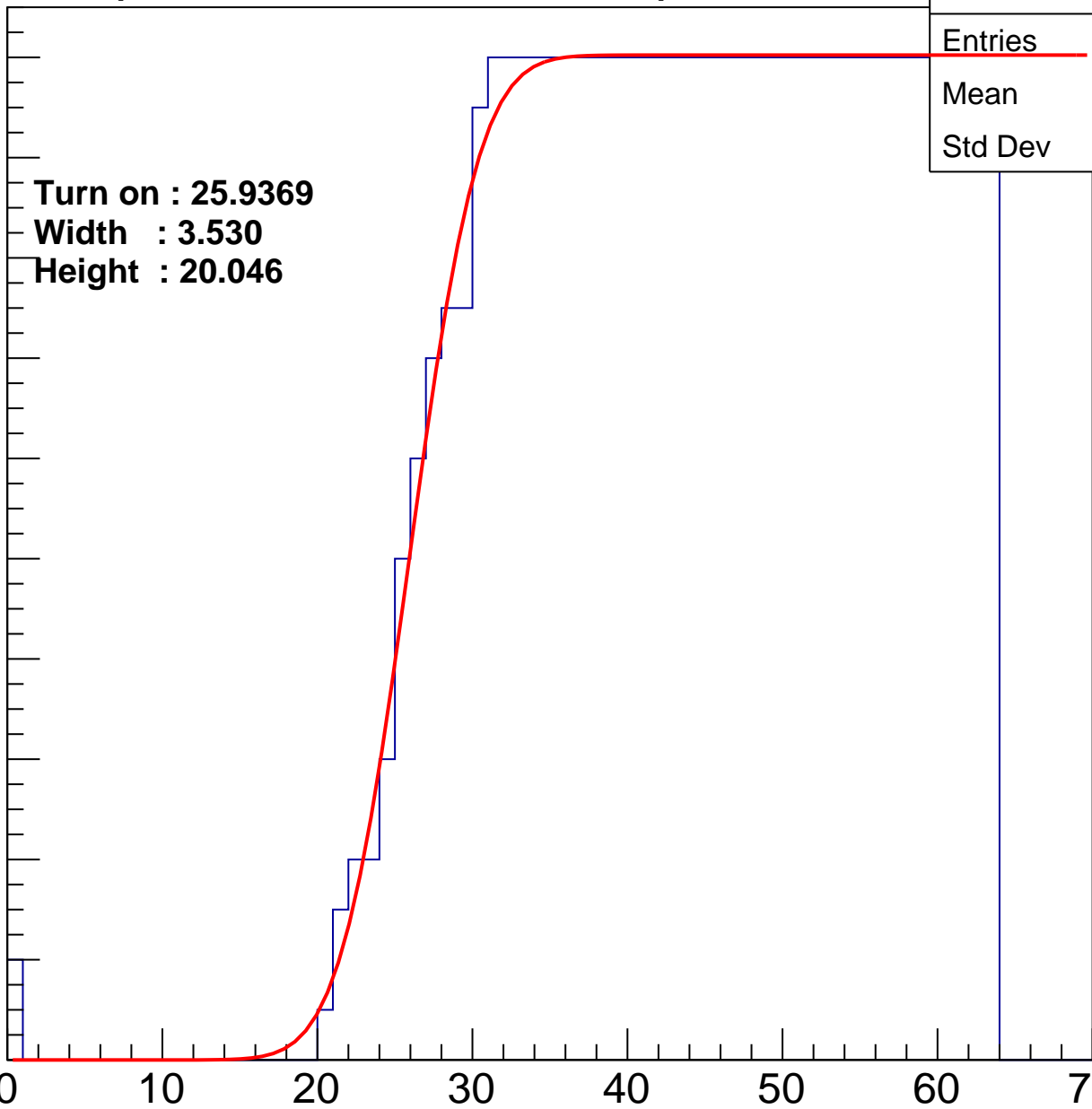
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9369
Width : 3.530
Height : 20.046

Entries	765
Mean	44.17
Std Dev	11.45

ampl



B0L101S, U25-ch98

calib_packv5_042523_0143.root, FC#1, port C1

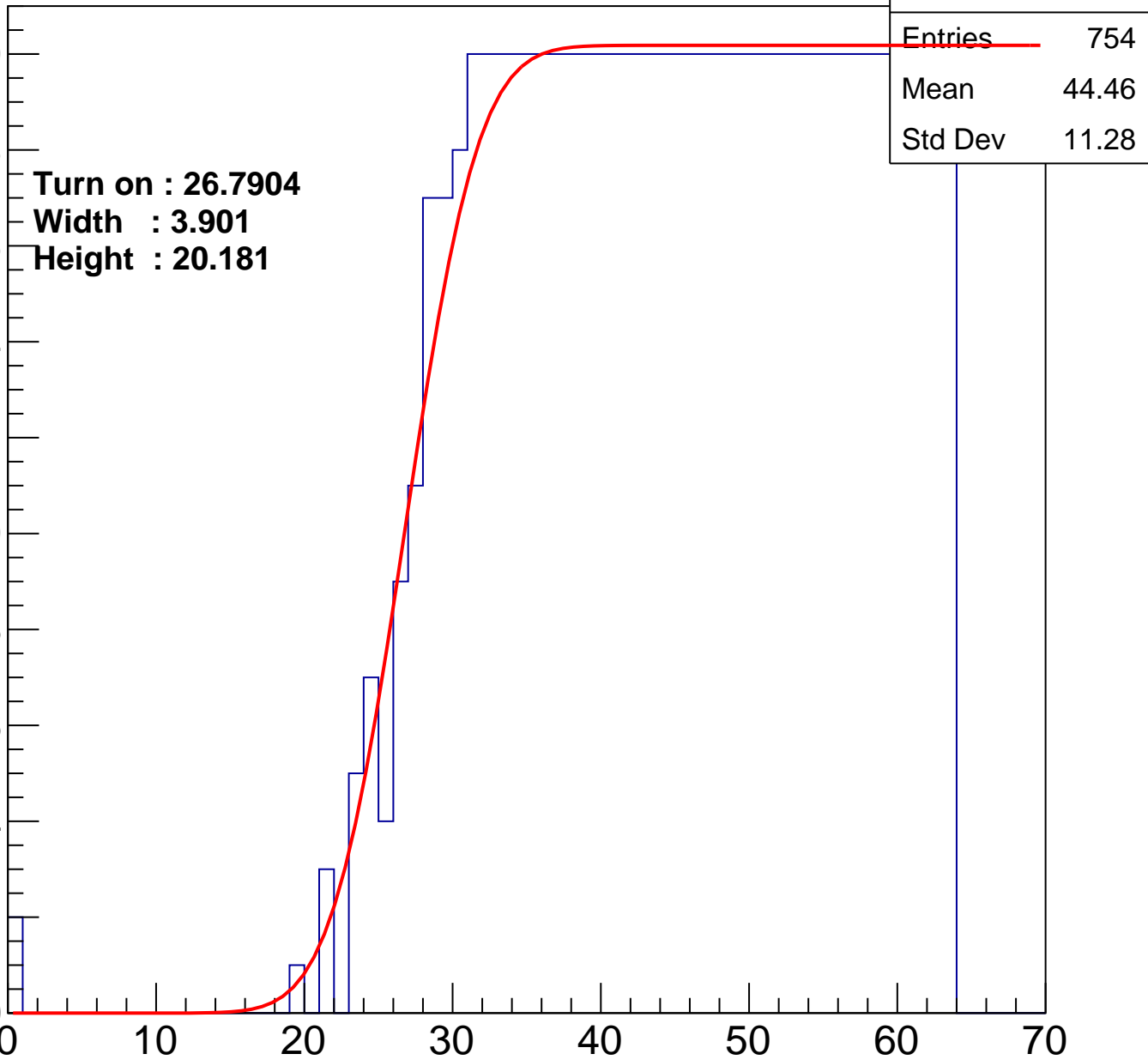
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7904
Width : 3.901
Height : 20.181

Entries	754
Mean	44.46
Std Dev	11.28

ampl



B0L101S, U25-ch99

calib_packv5_042523_0143.root, FC#1, port C1

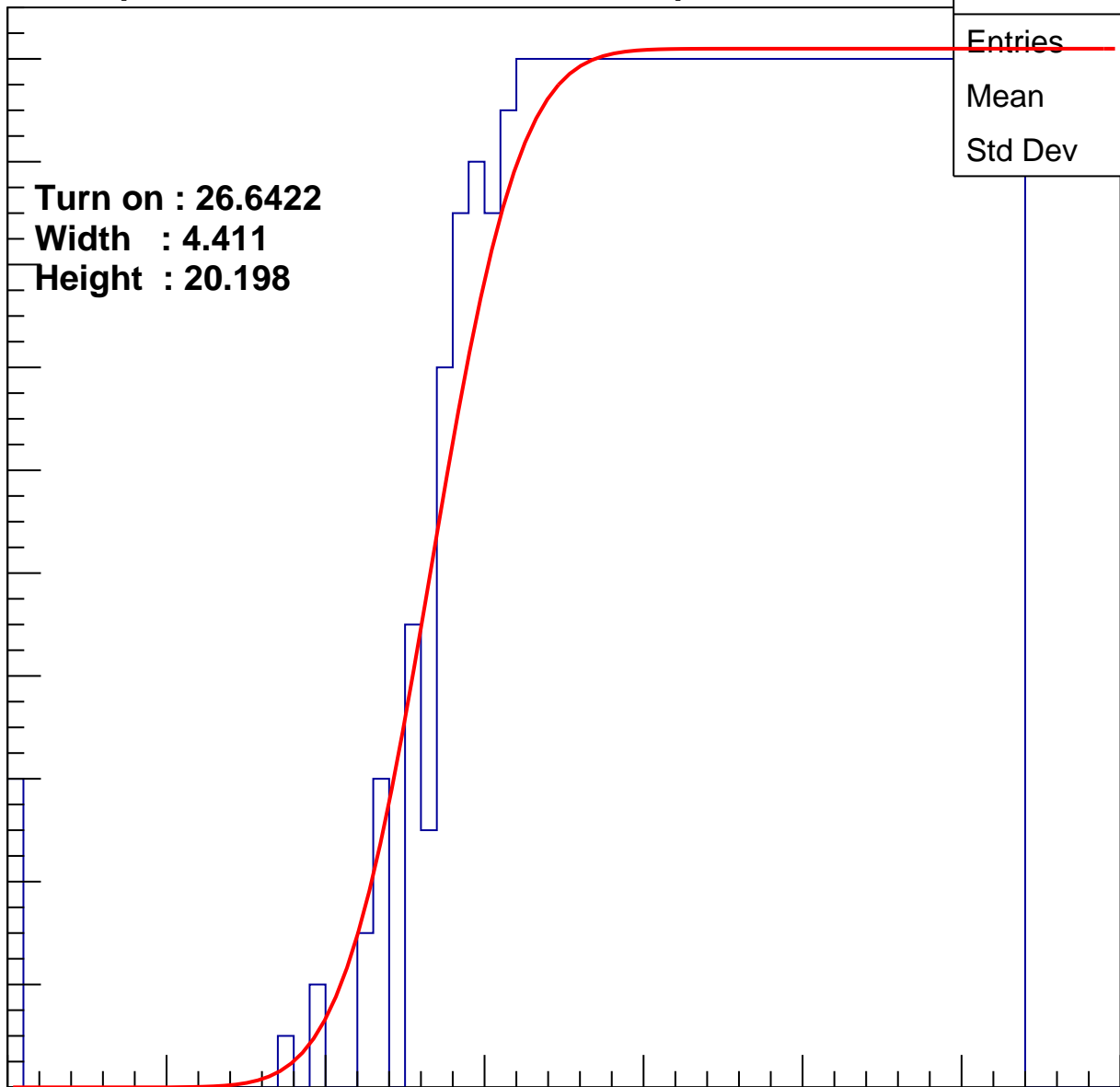
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6422
Width : 4.411
Height : 20.198

Entries	757
Mean	44.24
Std Dev	11.72

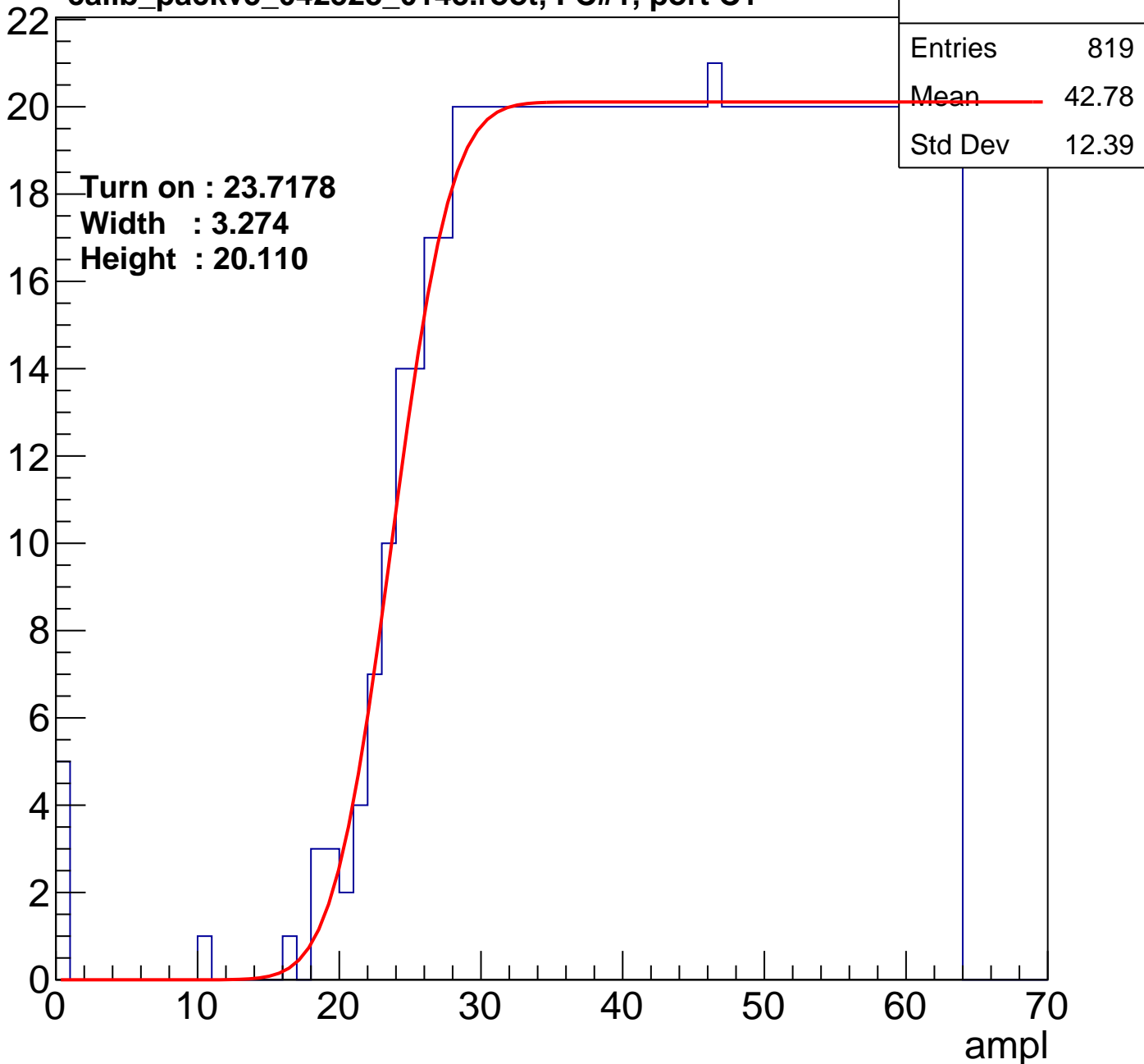
ampl



B0L101S, U25-ch100

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch101

calib_packv5_042523_0143.root, FC#1, port C1

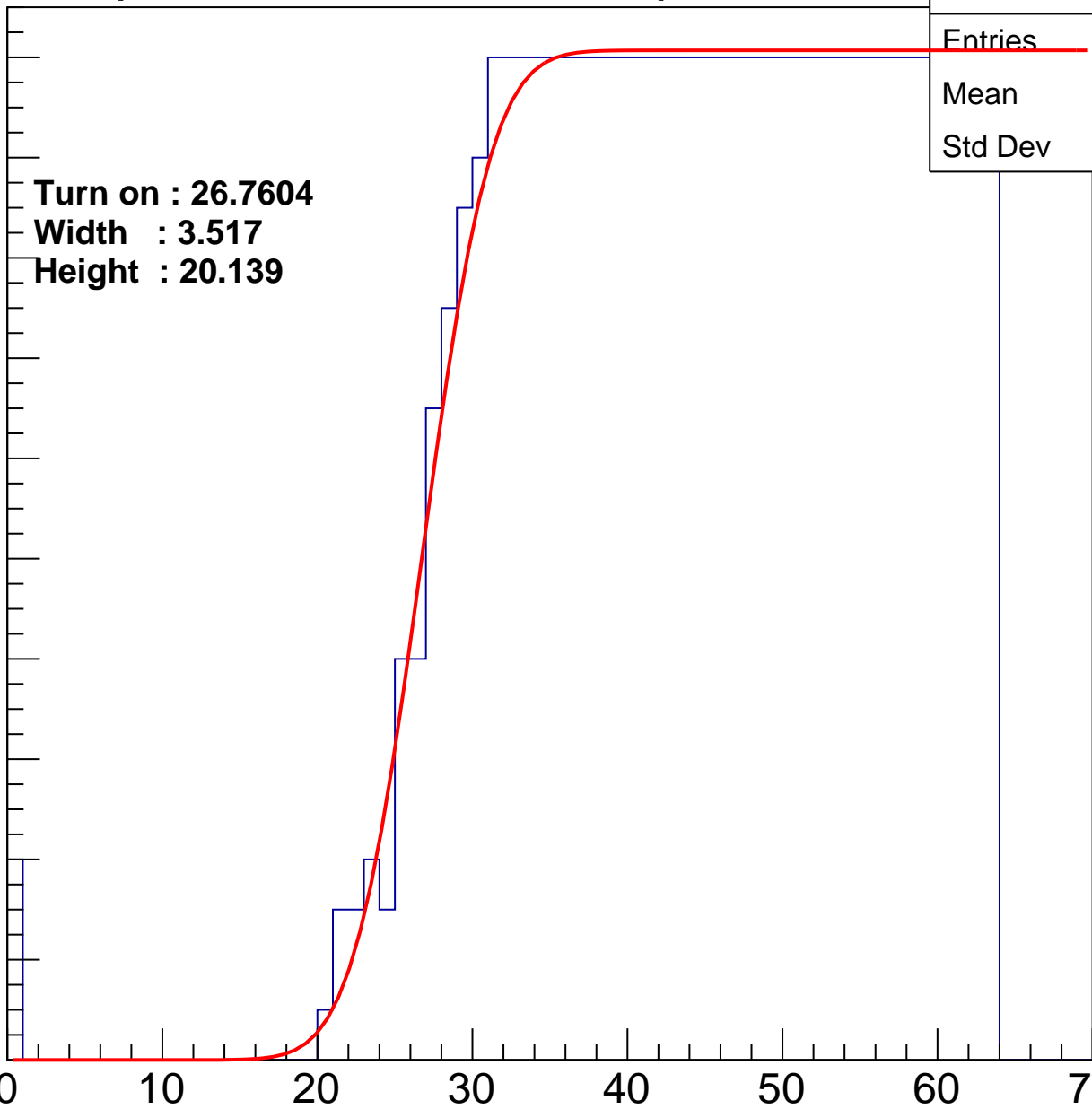
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7604
Width : 3.517
Height : 20.139

Entries	757
Mean	44.31
Std Dev	11.52

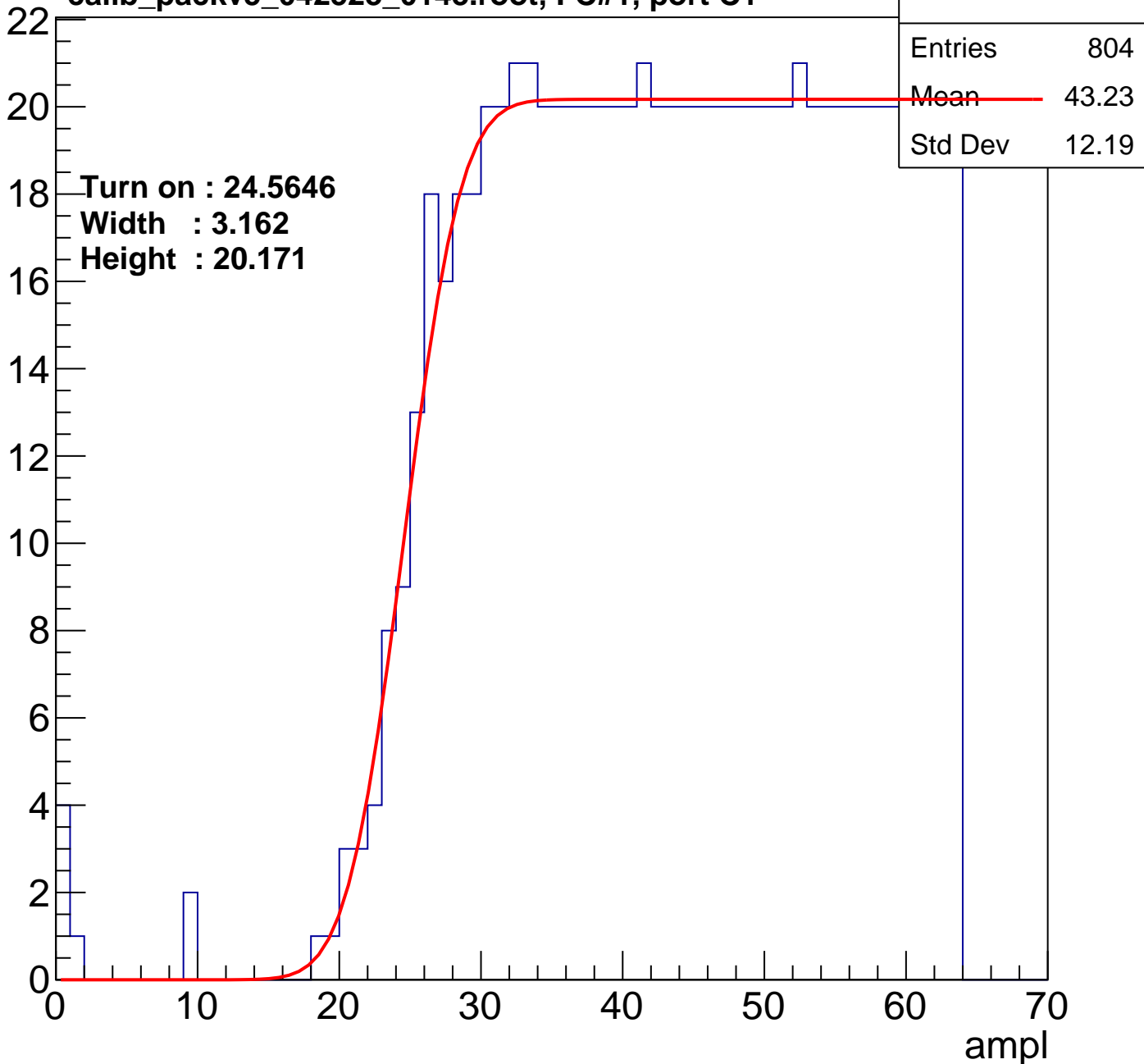
ampl



B0L101S, U25-ch102

calib_packv5_042523_0143.root, FC#1, port C1

Entry



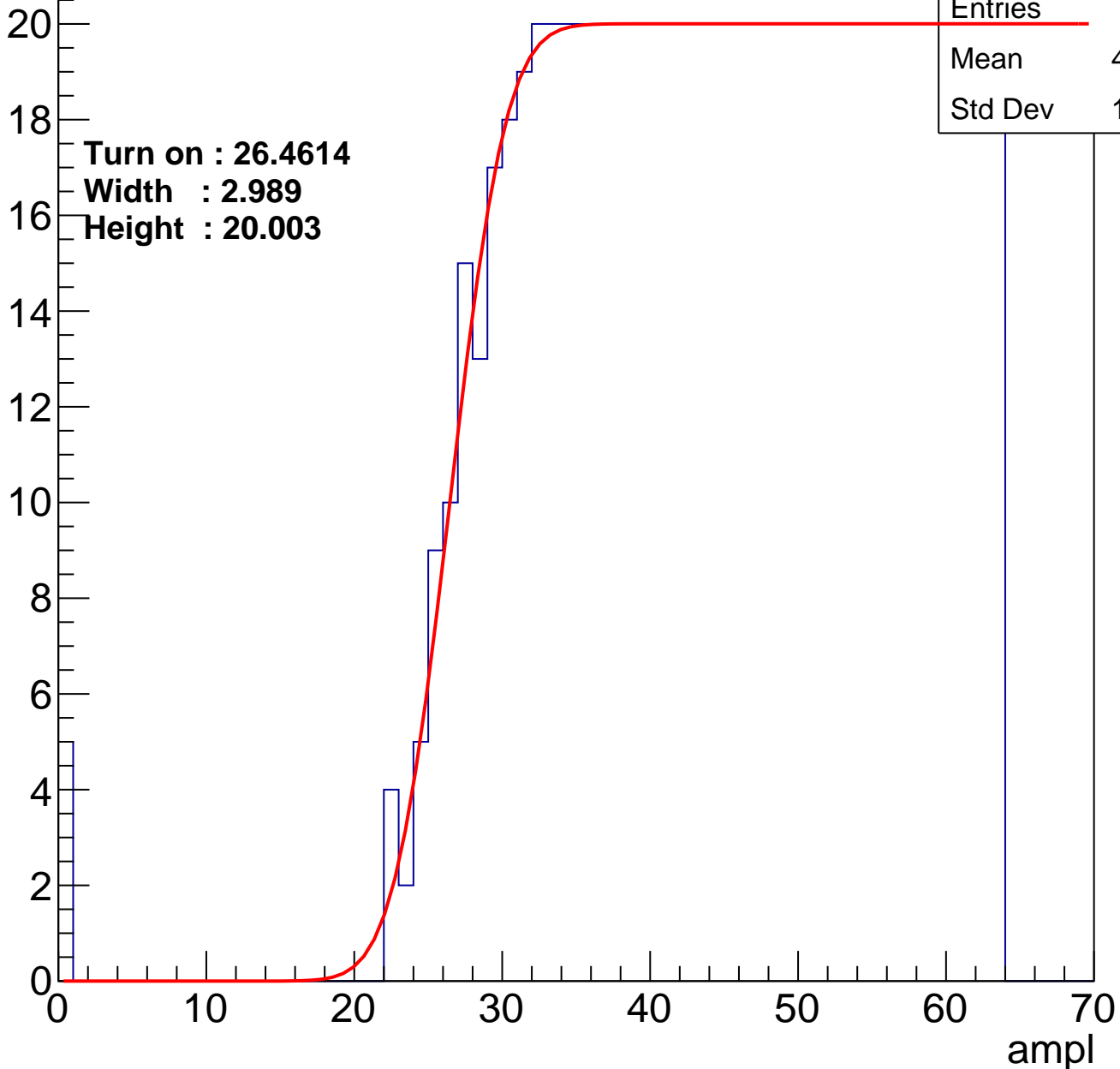
B0L101S, U25-ch103

calib_packv5_042523_0143.root, FC#1, port C1

Entries	757
Mean	44.29
Std Dev	11.58

Turn on : 26.4614
Width : 2.989
Height : 20.003

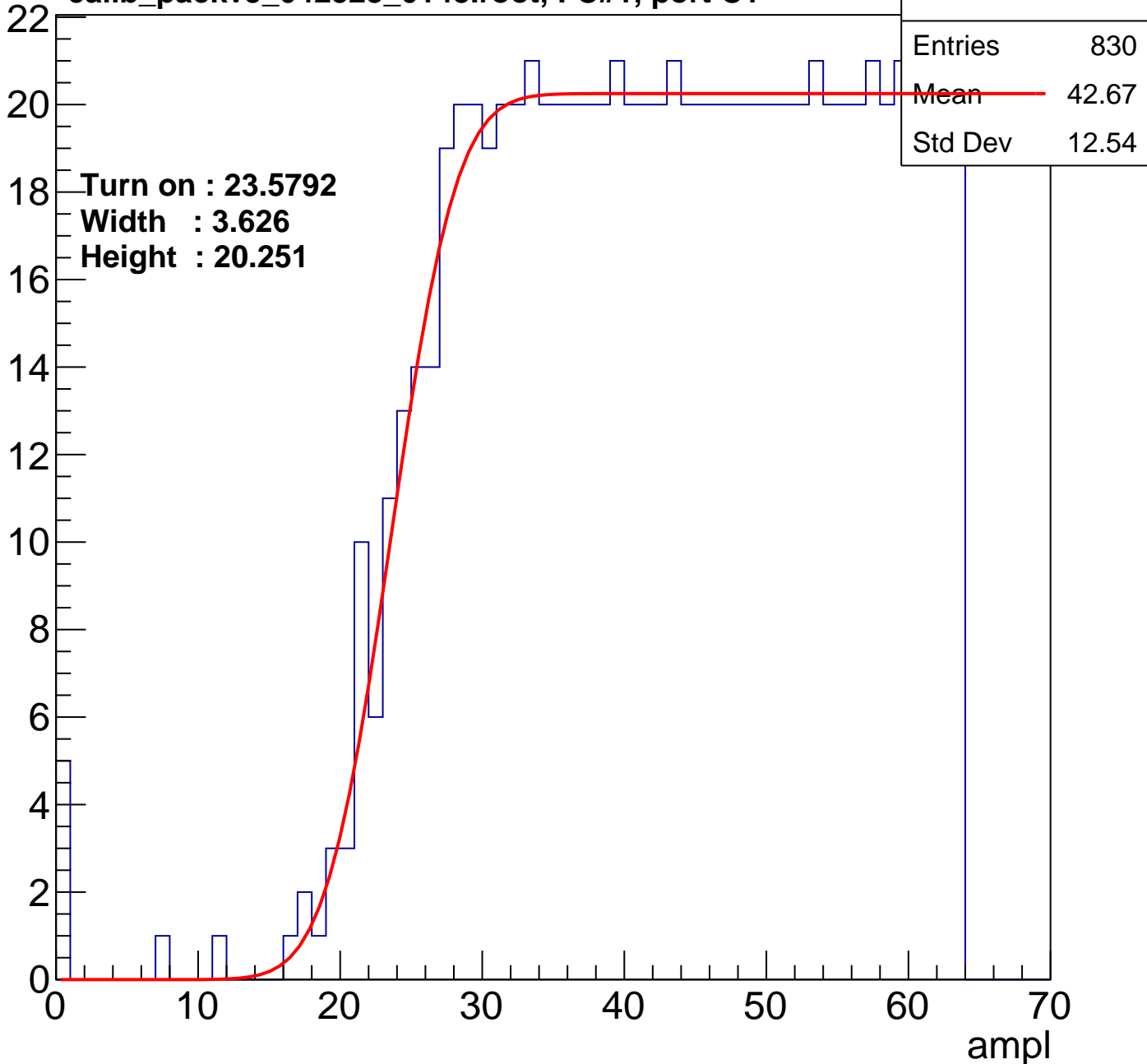
Entry



B0L101S, U25-ch104

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch105

calib_packv5_042523_0143.root, FC#1, port C1

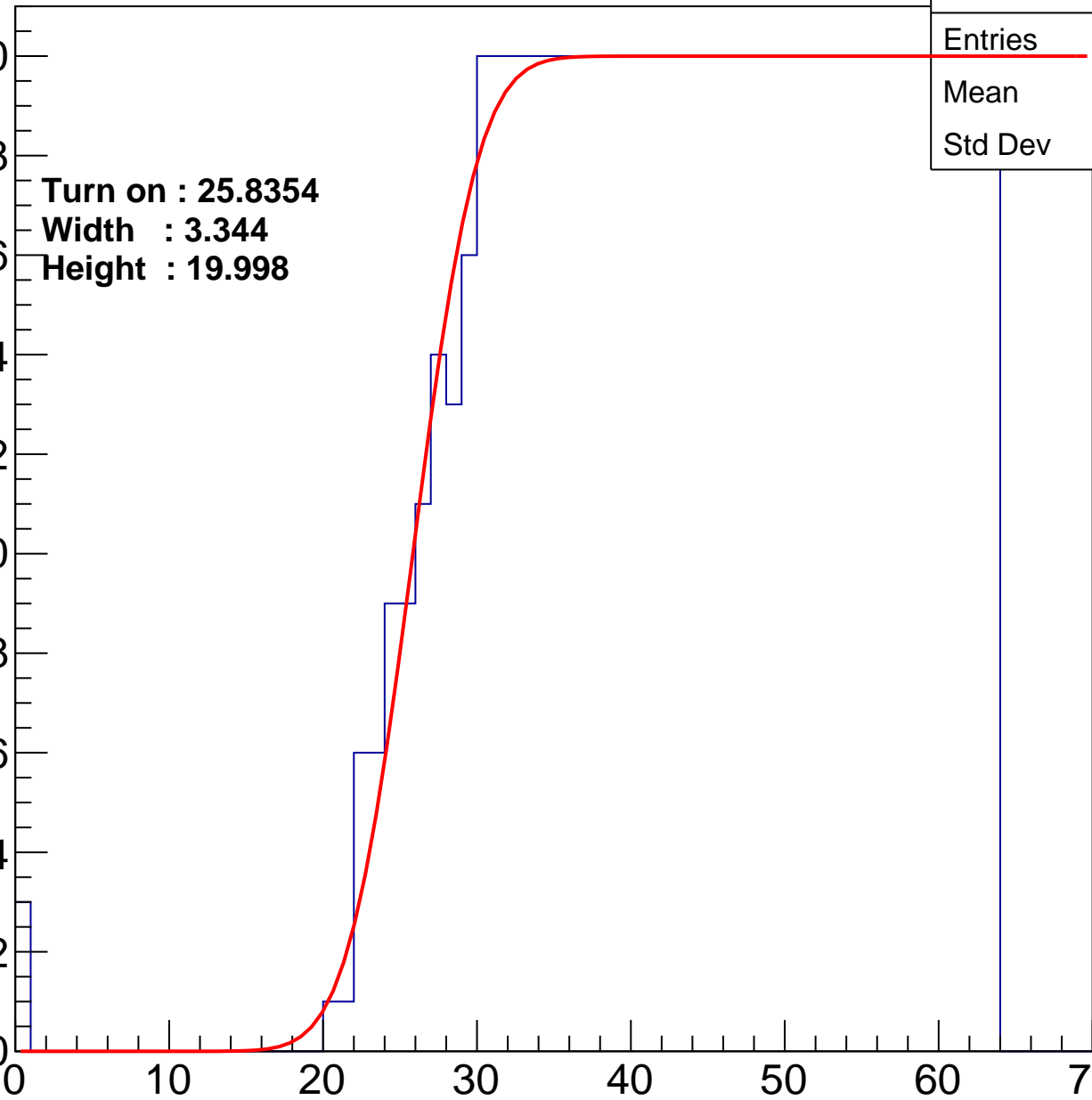
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8354
Width : 3.344
Height : 19.998

Entries	769
Mean	44.04
Std Dev	11.6

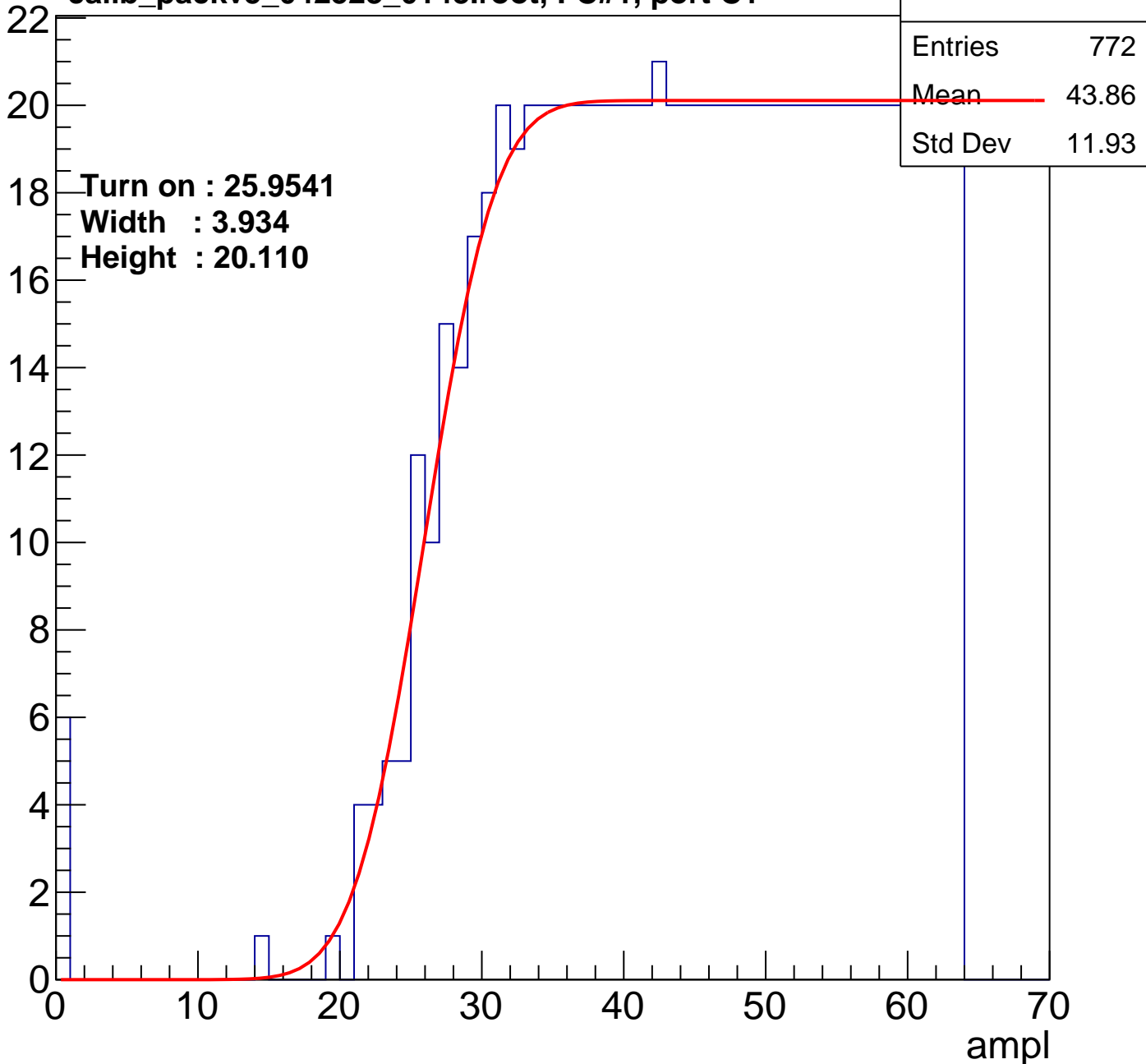
ampl



B0L101S, U25-ch106

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch107

calib_packv5_042523_0143.root, FC#1, port C1

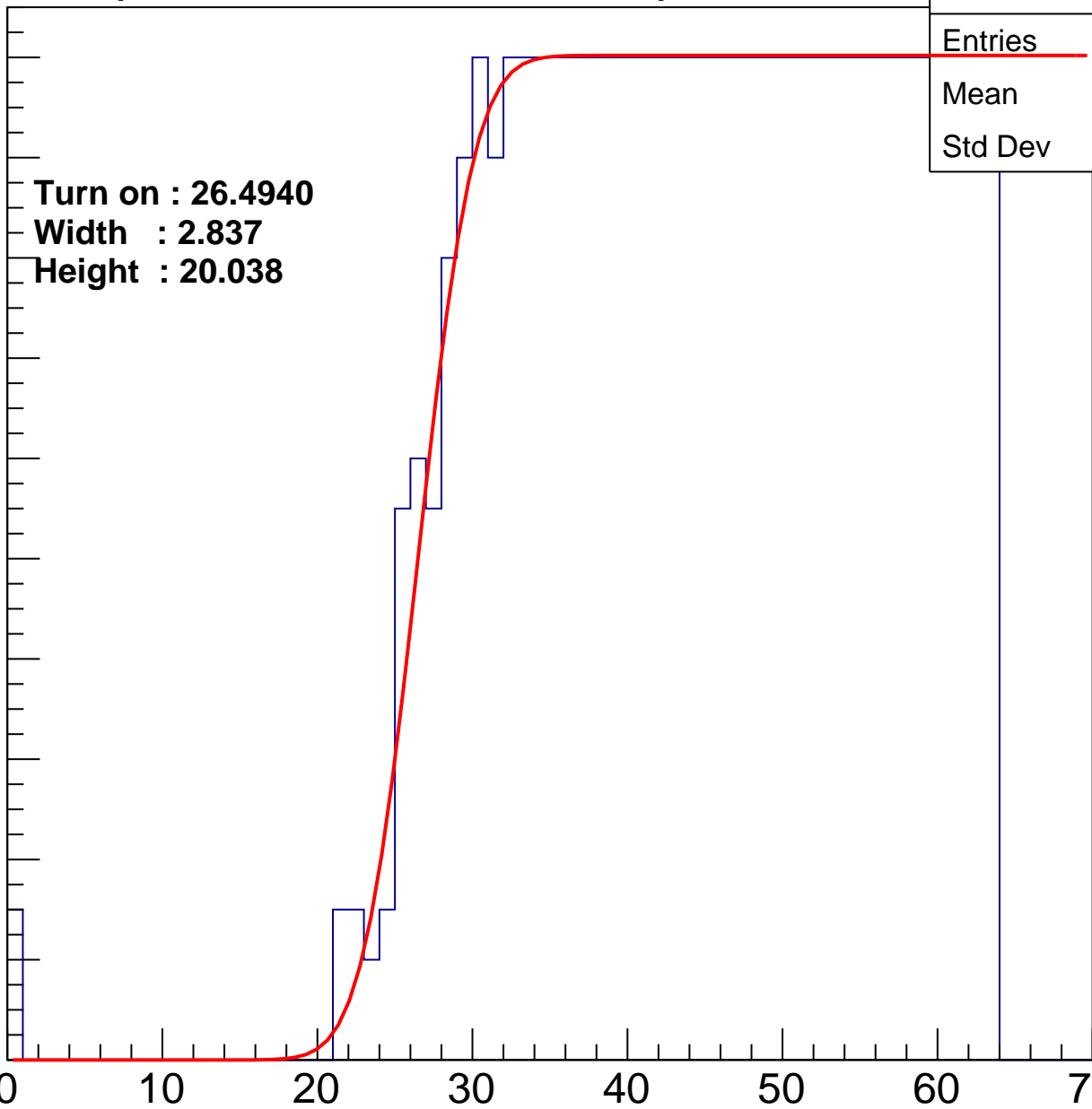
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4940
Width : 2.837
Height : 20.038

Entries	760
Mean	44.29
Std Dev	11.44

ampl



B0L101S, U25-ch108

calib_packv5_042523_0143.root, FC#1, port C1

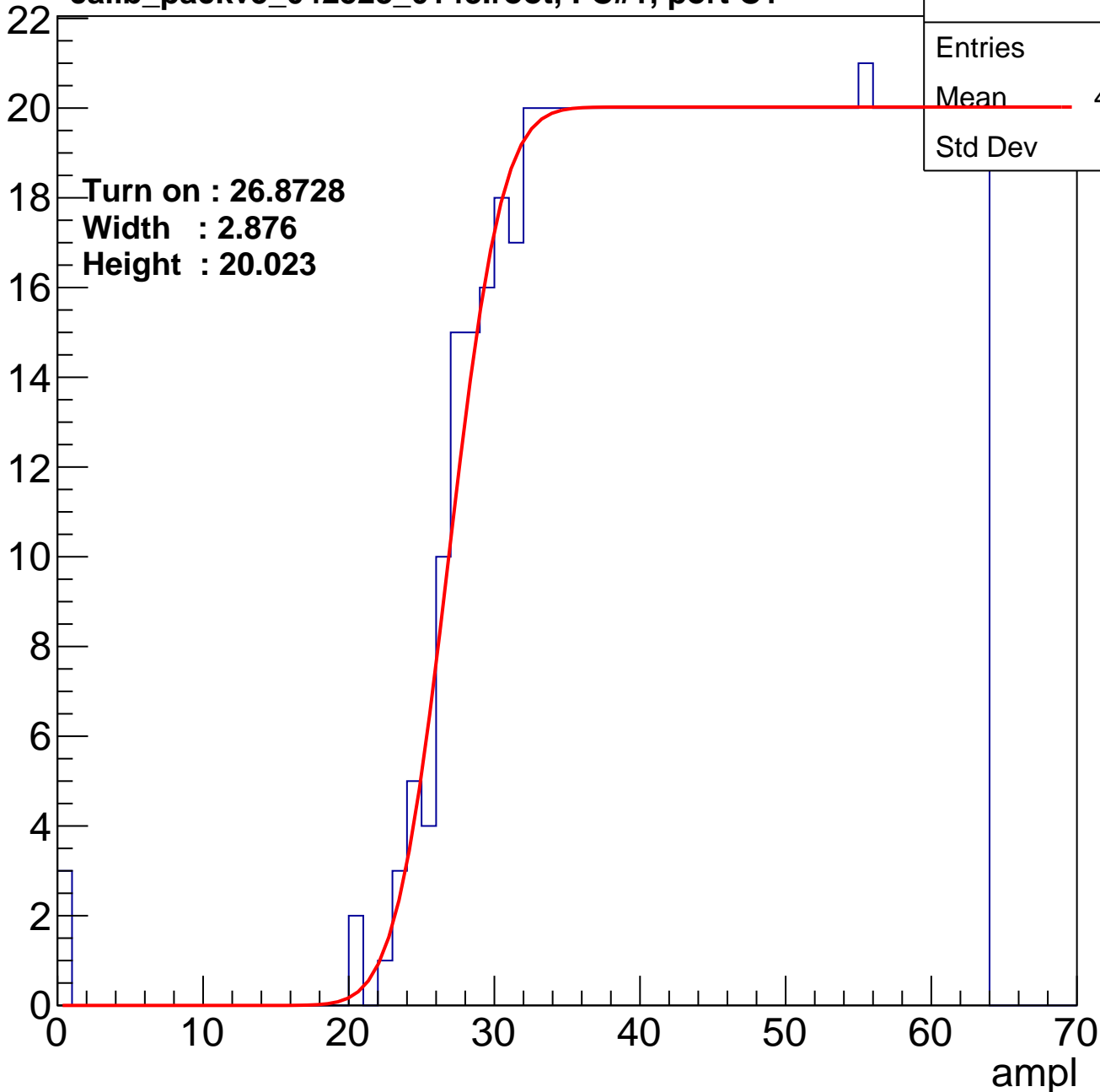
Entries	750
Mean	44.56
Std Dev	11.31

Turn on : 26.8728

Width : 2.876

Height : 20.023

Entry



B0L101S, U25-ch109

calib_packv5_042523_0143.root, FC#1, port C1

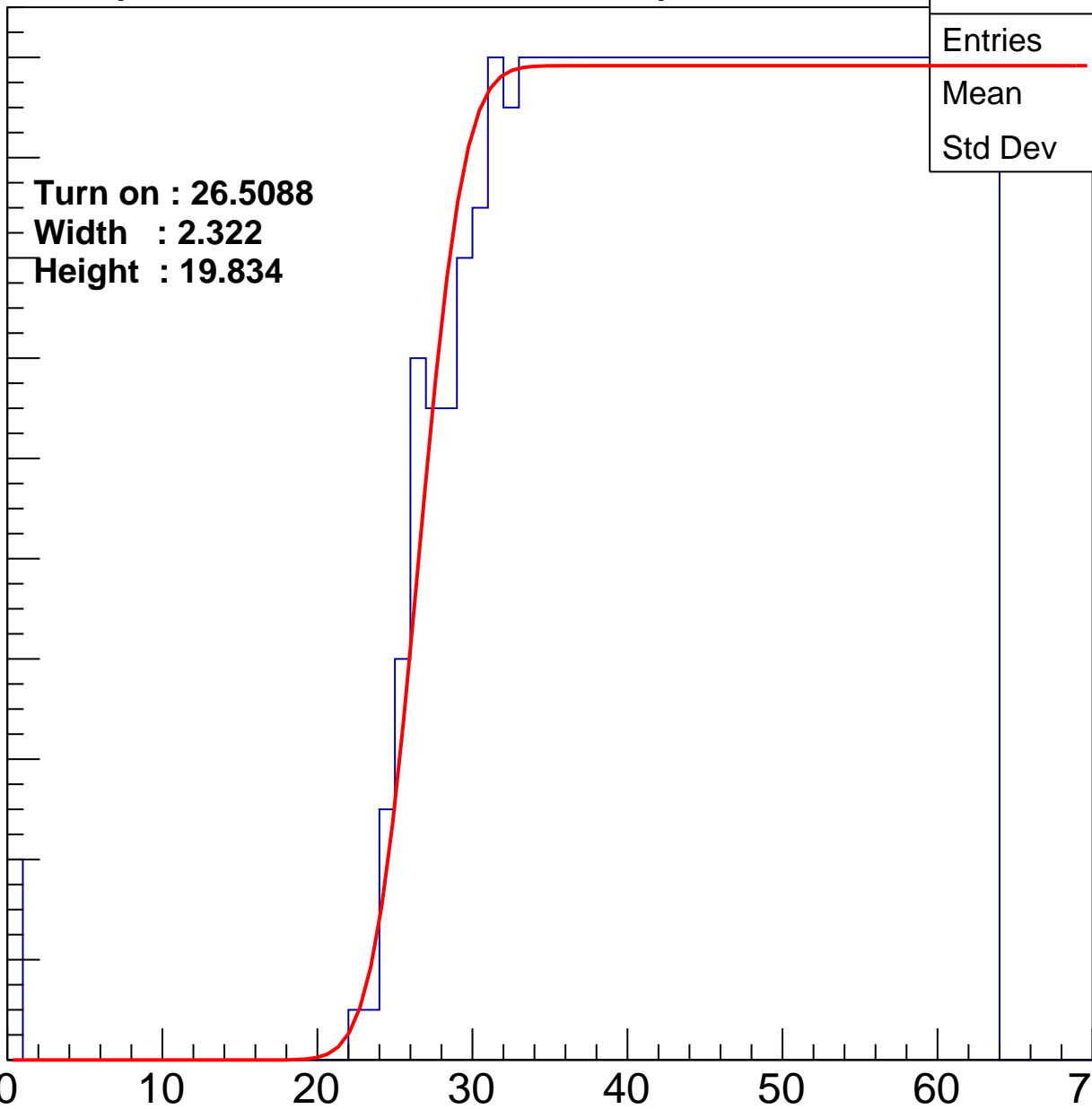
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5088
Width : 2.322
Height : 19.834

Entries	751
Mean	44.48
Std Dev	11.4

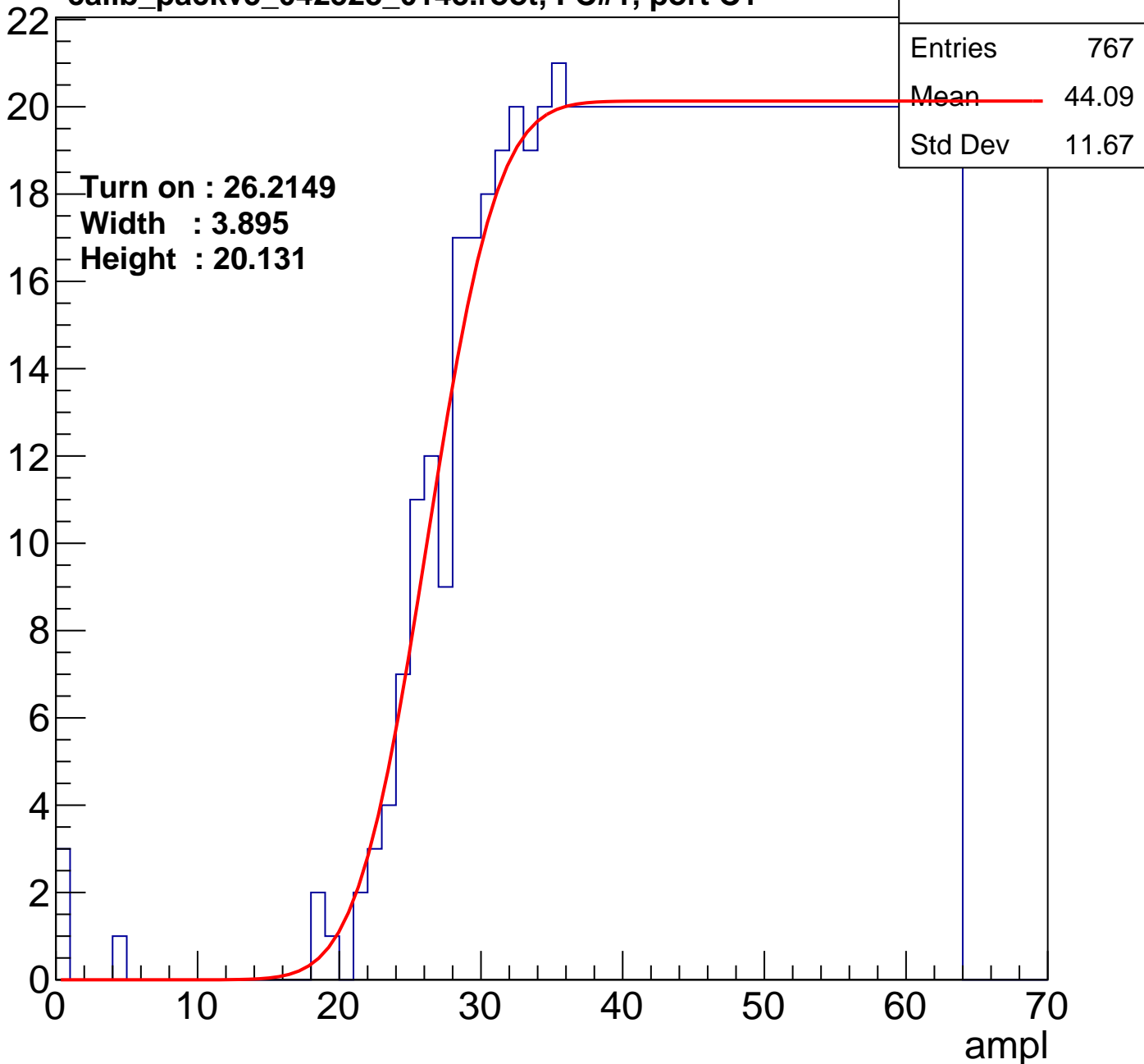
ampl



B0L101S, U25-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch111

calib_packv5_042523_0143.root, FC#1, port C1

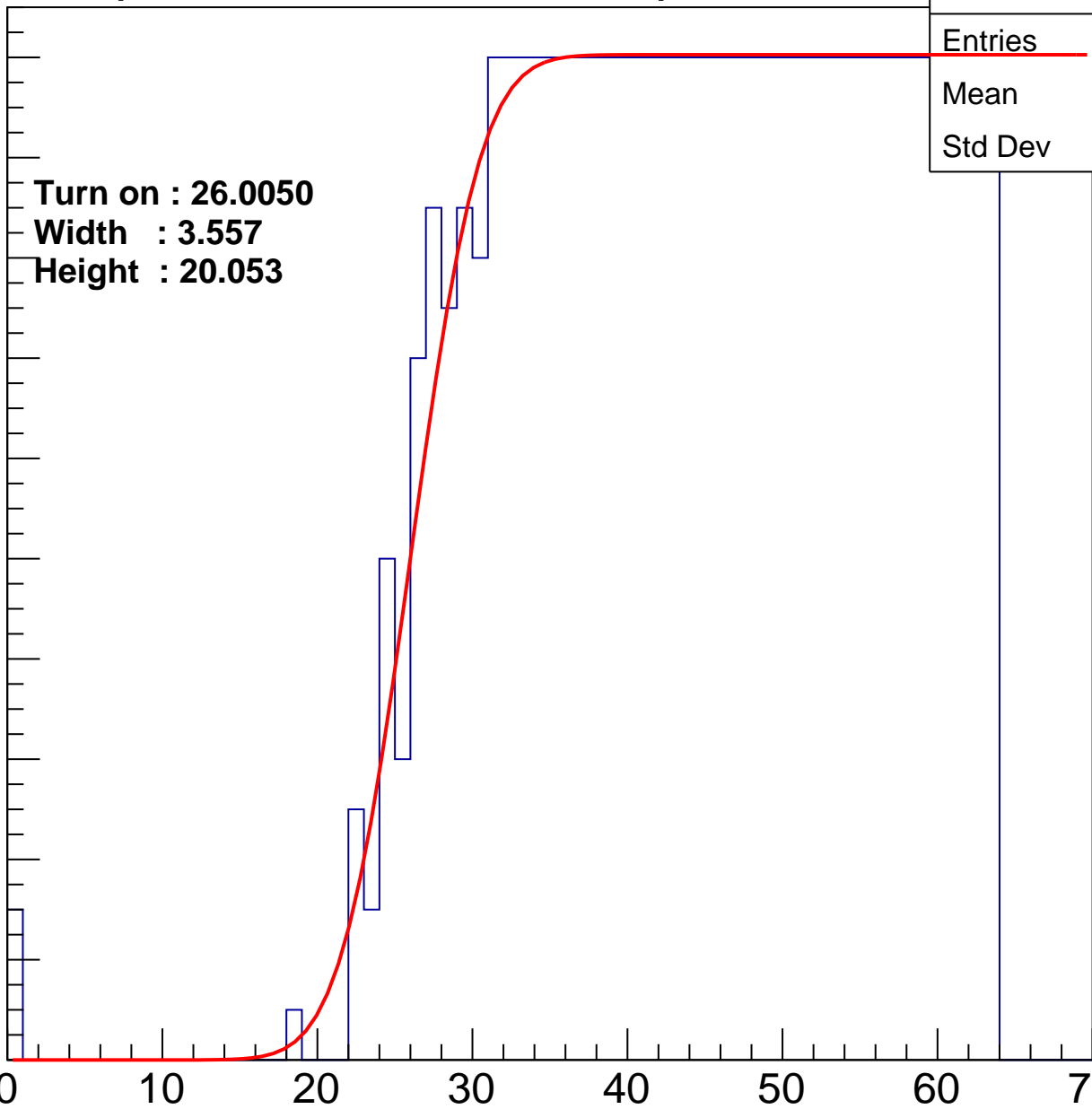
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0050
Width : 3.557
Height : 20.053

Entries	767
Mean	44.1
Std Dev	11.56

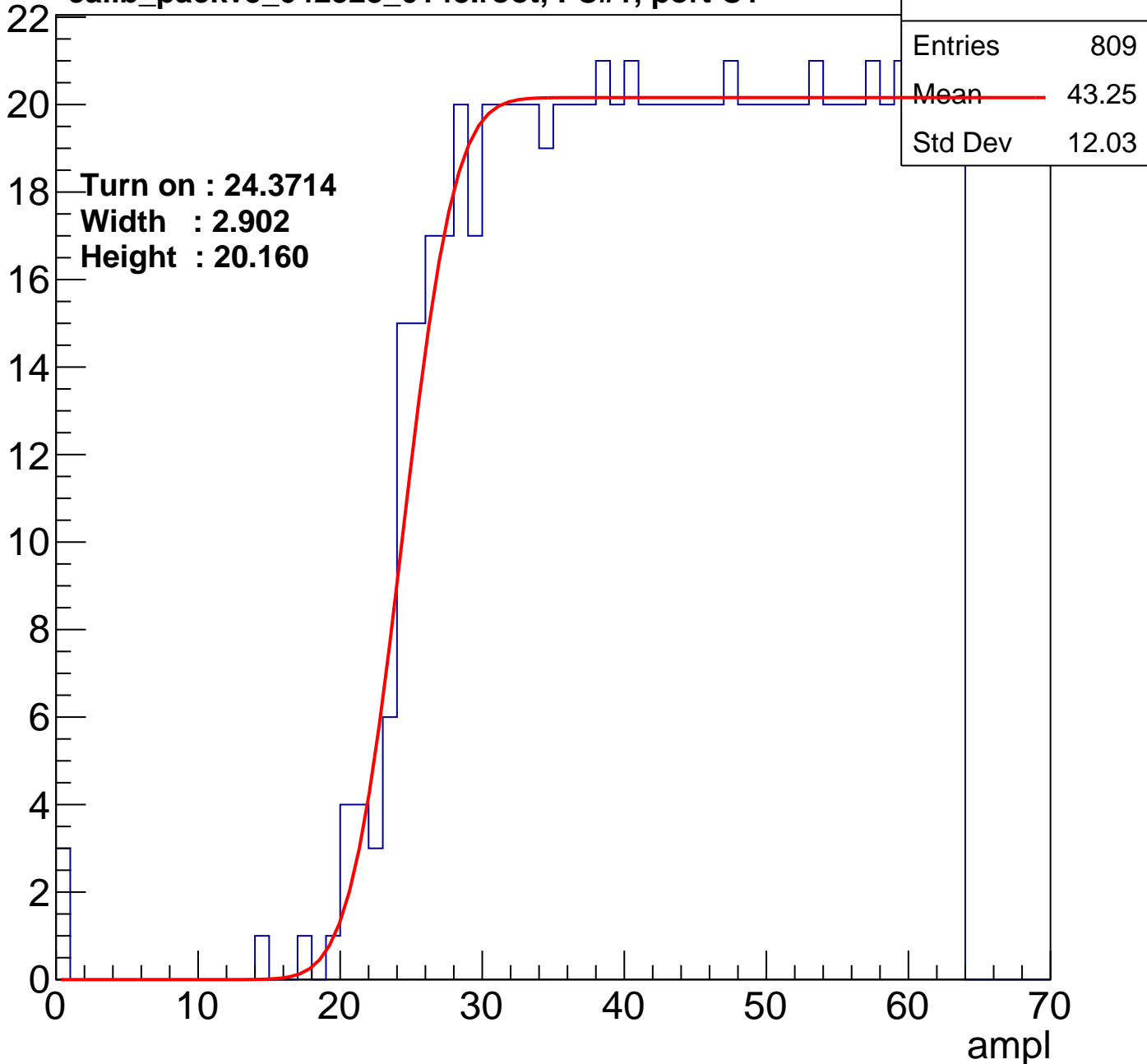
ampl



B0L101S, U25-ch112

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch113

calib_packv5_042523_0143.root, FC#1, port C1

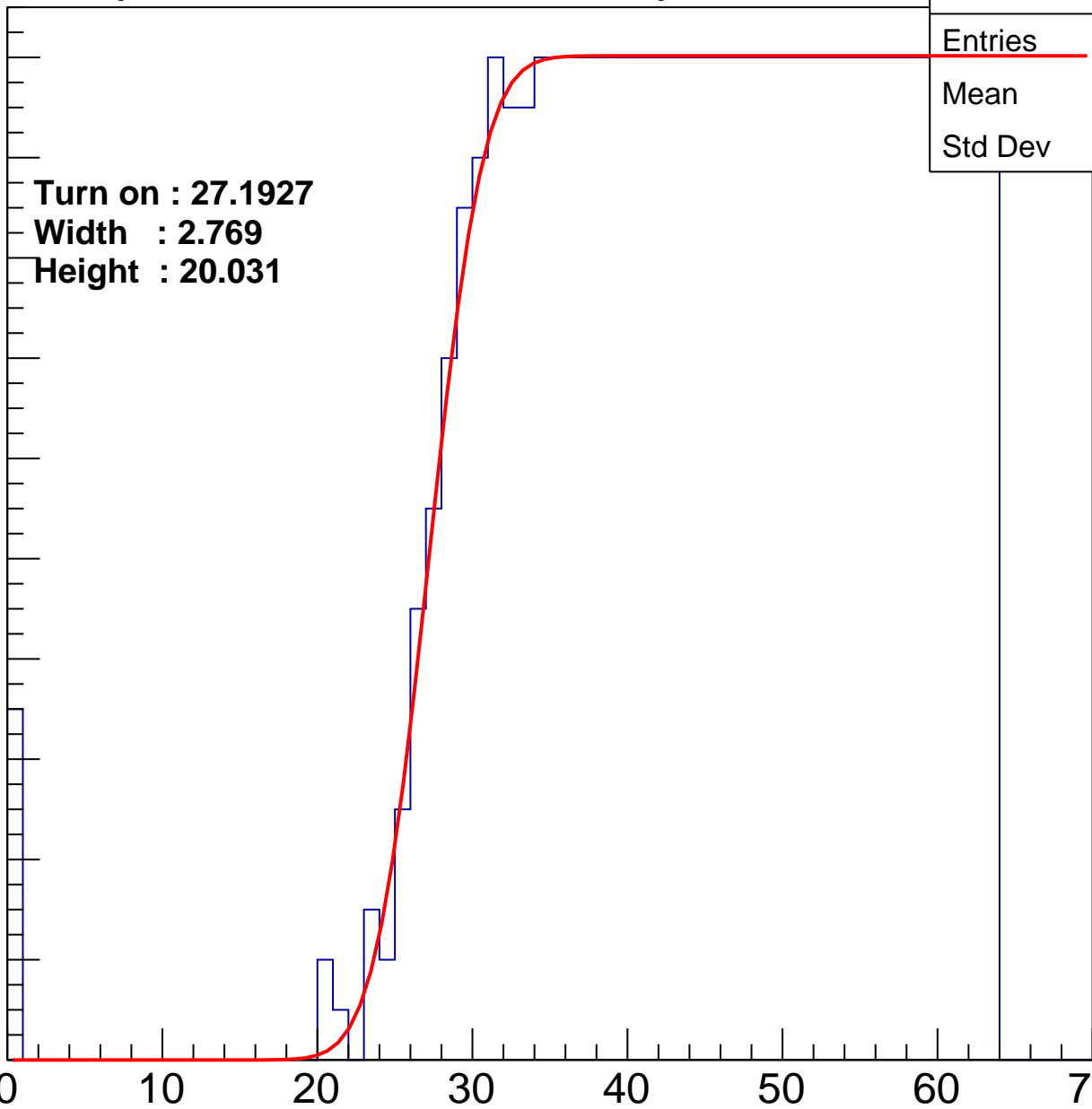
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1927
Width : 2.769
Height : 20.031

Entries	747
Mean	44.46
Std Dev	11.66

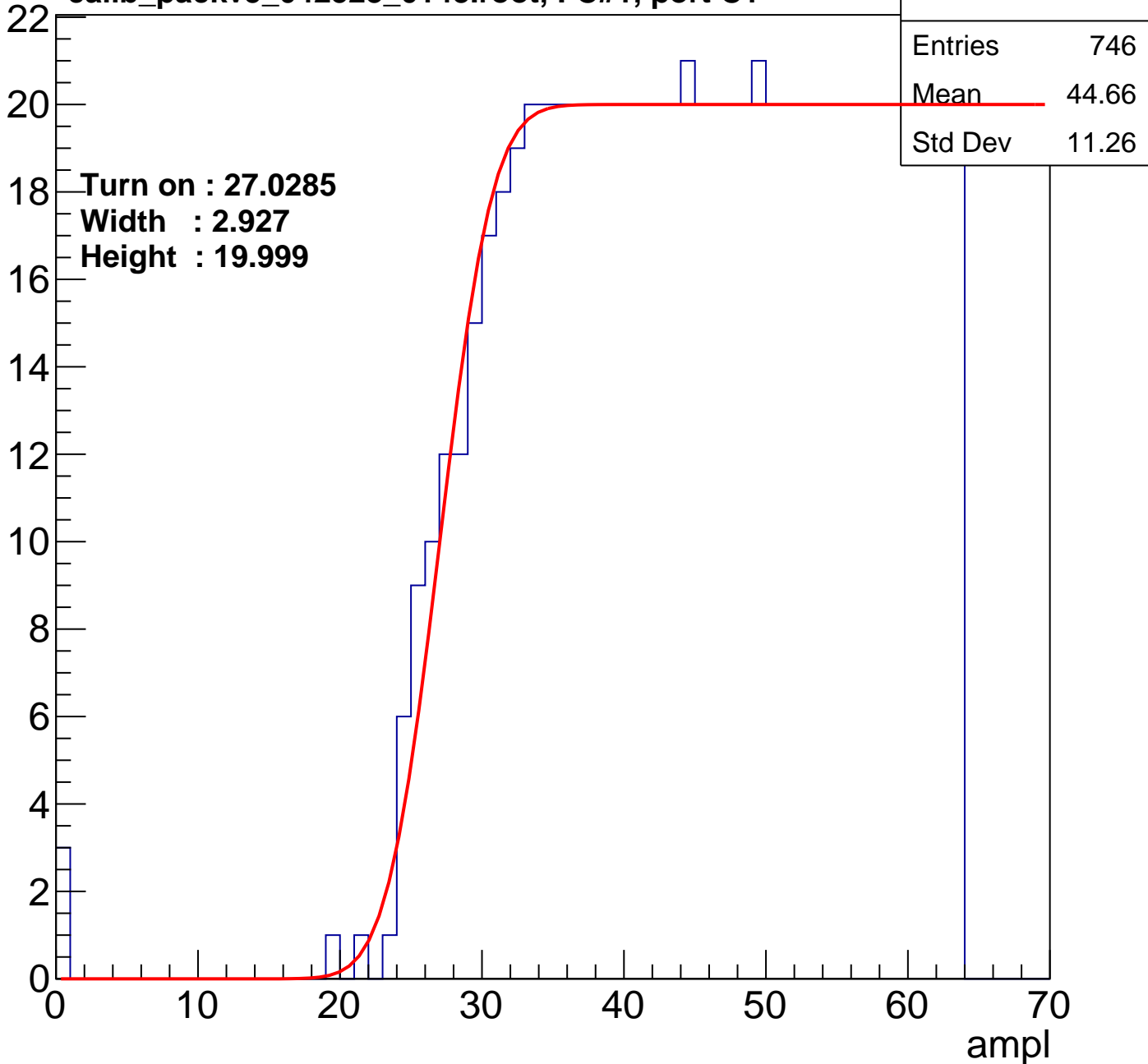
ampl



B0L101S, U25-ch114

calib_packv5_042523_0143.root, FC#1, port C1

Entry

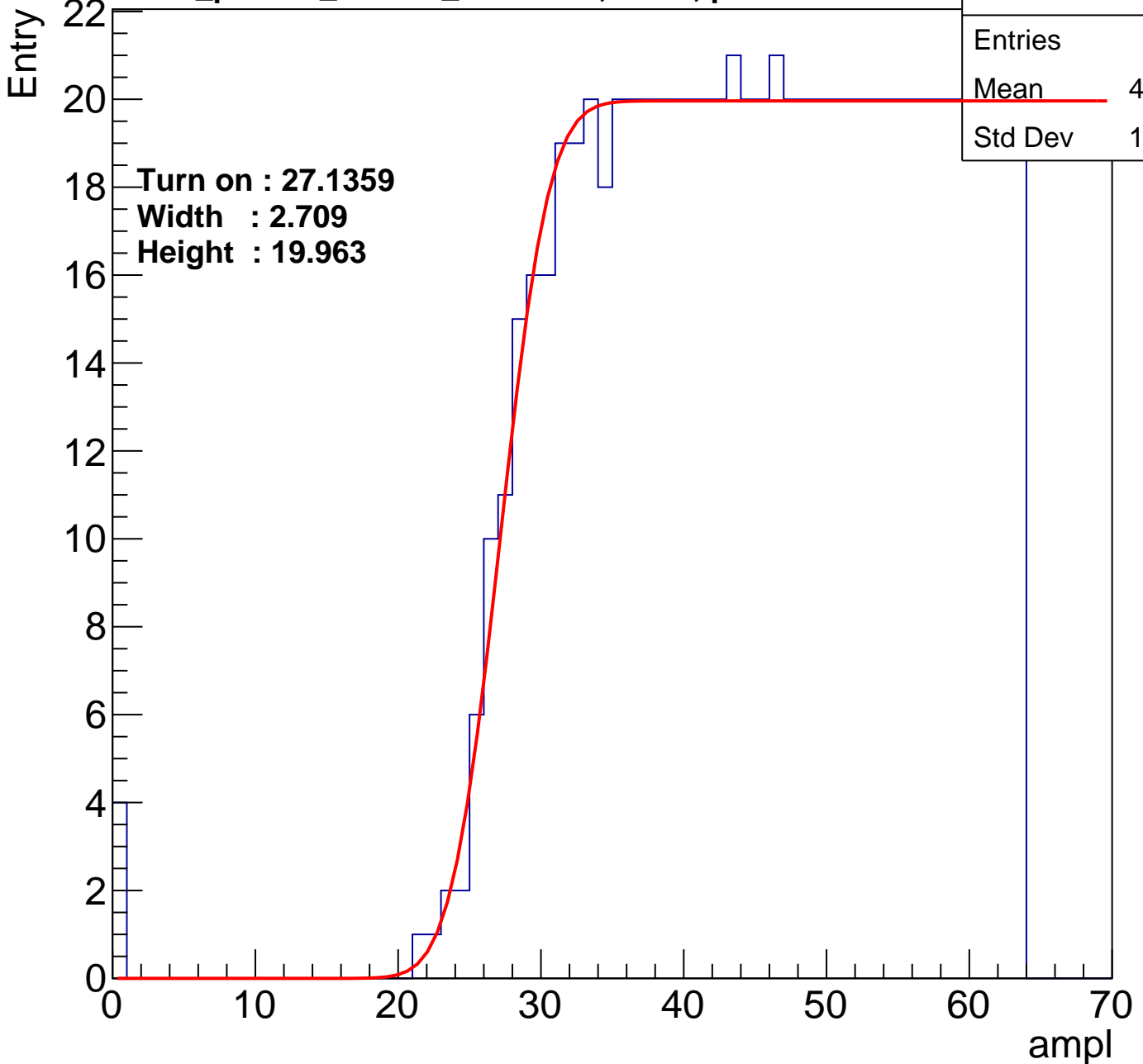


B0L101S, U25-ch115

calib_packv5_042523_0143.root, FC#1, port C1

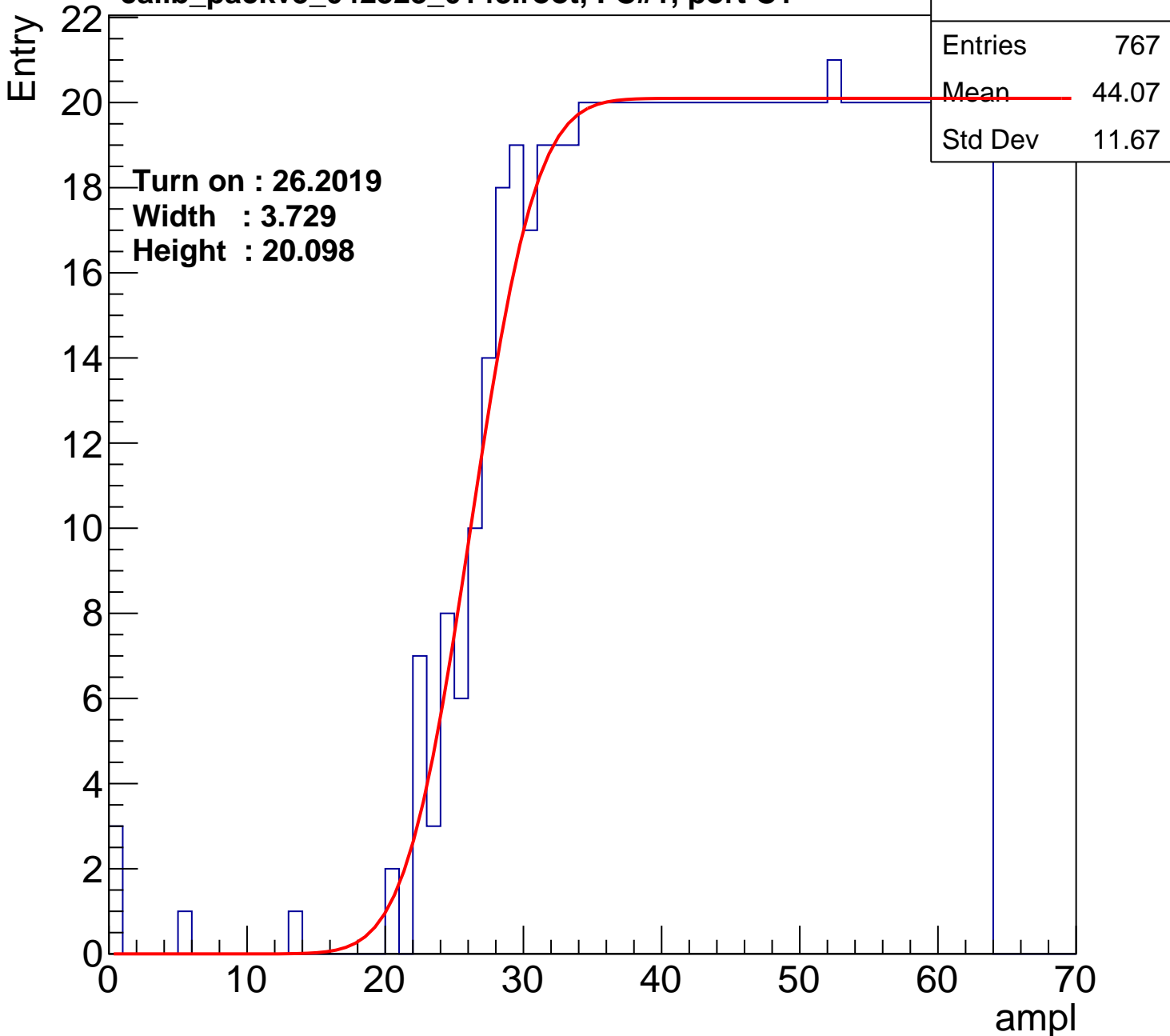
Entries	742
Mean	44.73
Std Dev	11.28

Turn on : 27.1359
Width : 2.709
Height : 19.963



B0L101S, U25-ch116

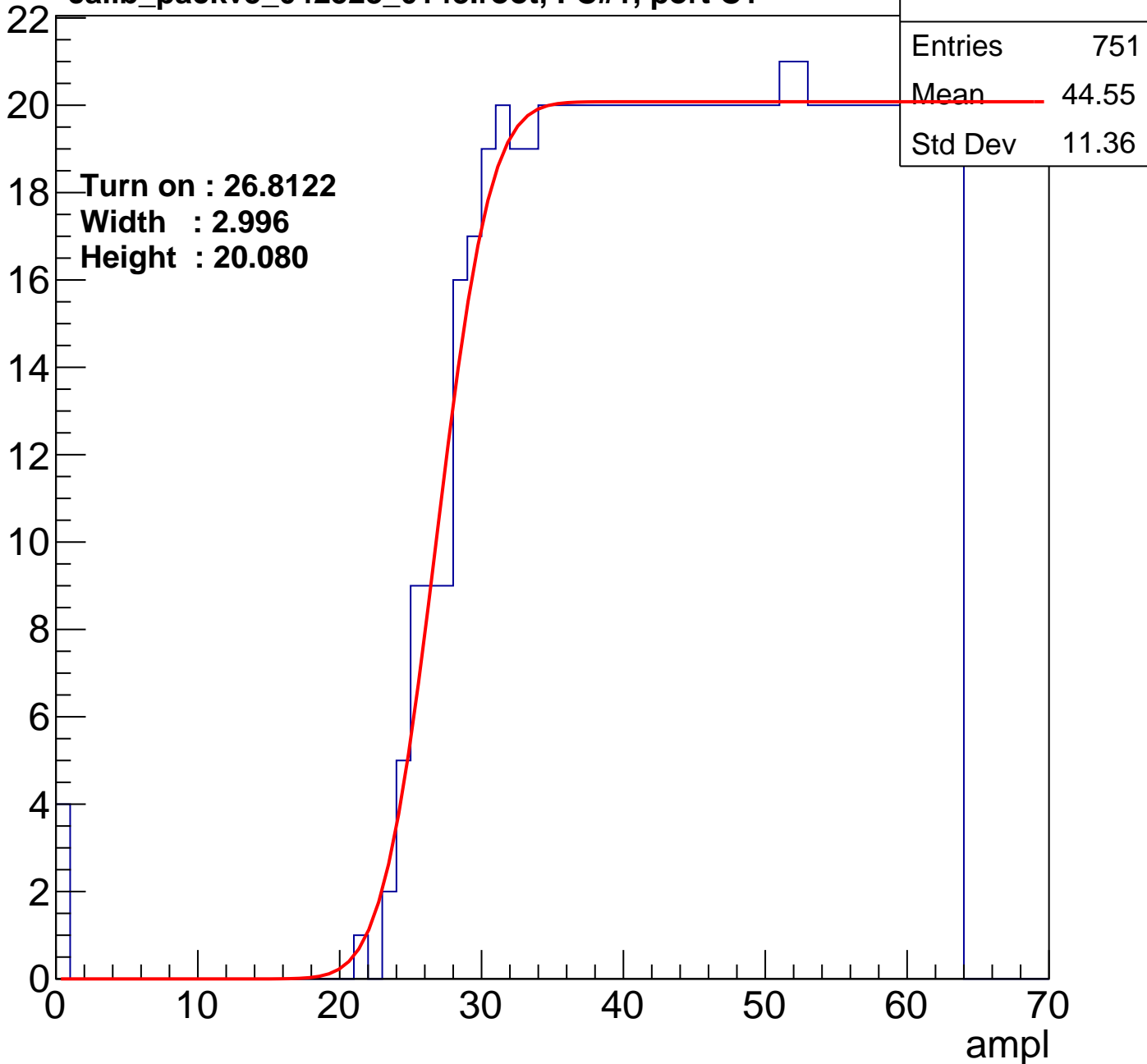
calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U25-ch117

calib_packv5_042523_0143.root, FC#1, port C1

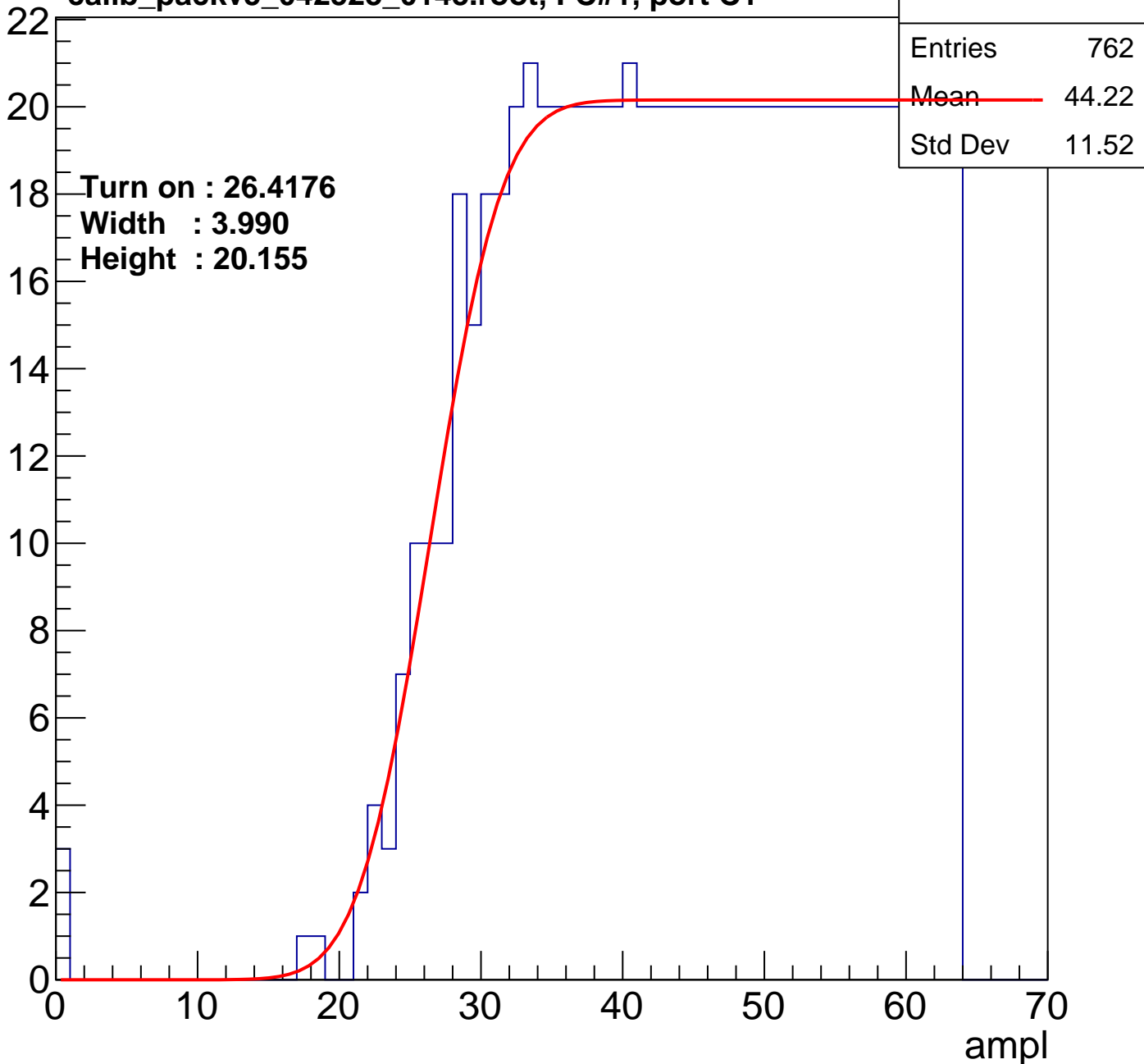
Entry



B0L101S, U25-ch118

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch119

calib_packv5_042523_0143.root, FC#1, port C1

Entries	715
Mean	45.19
Std Dev	11.37

Turn on : 28.8149

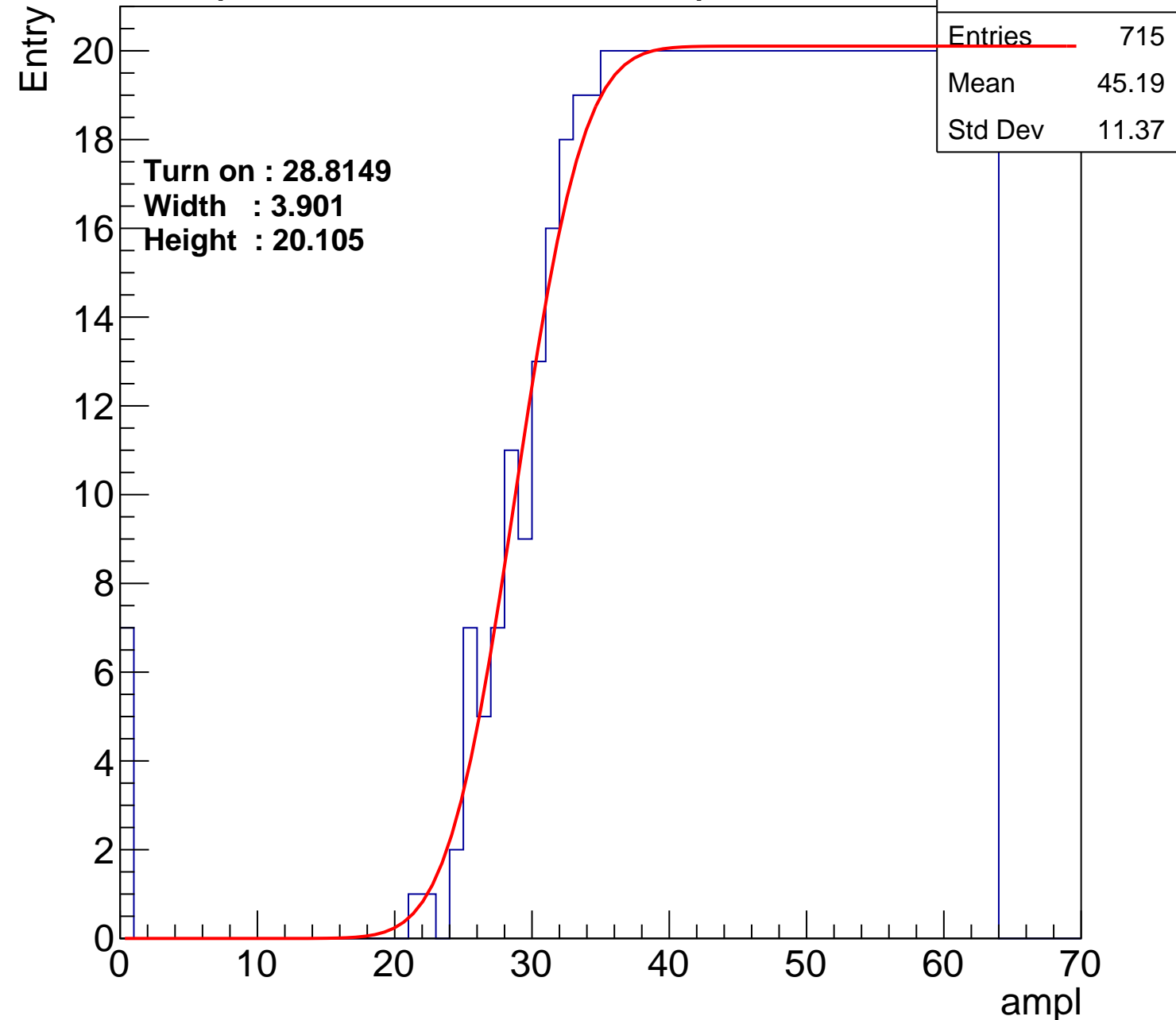
Width : 3.901

Height : 20.105

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl



B0L101S, U25-ch120

calib_packv5_042523_0143.root, FC#1, port C1

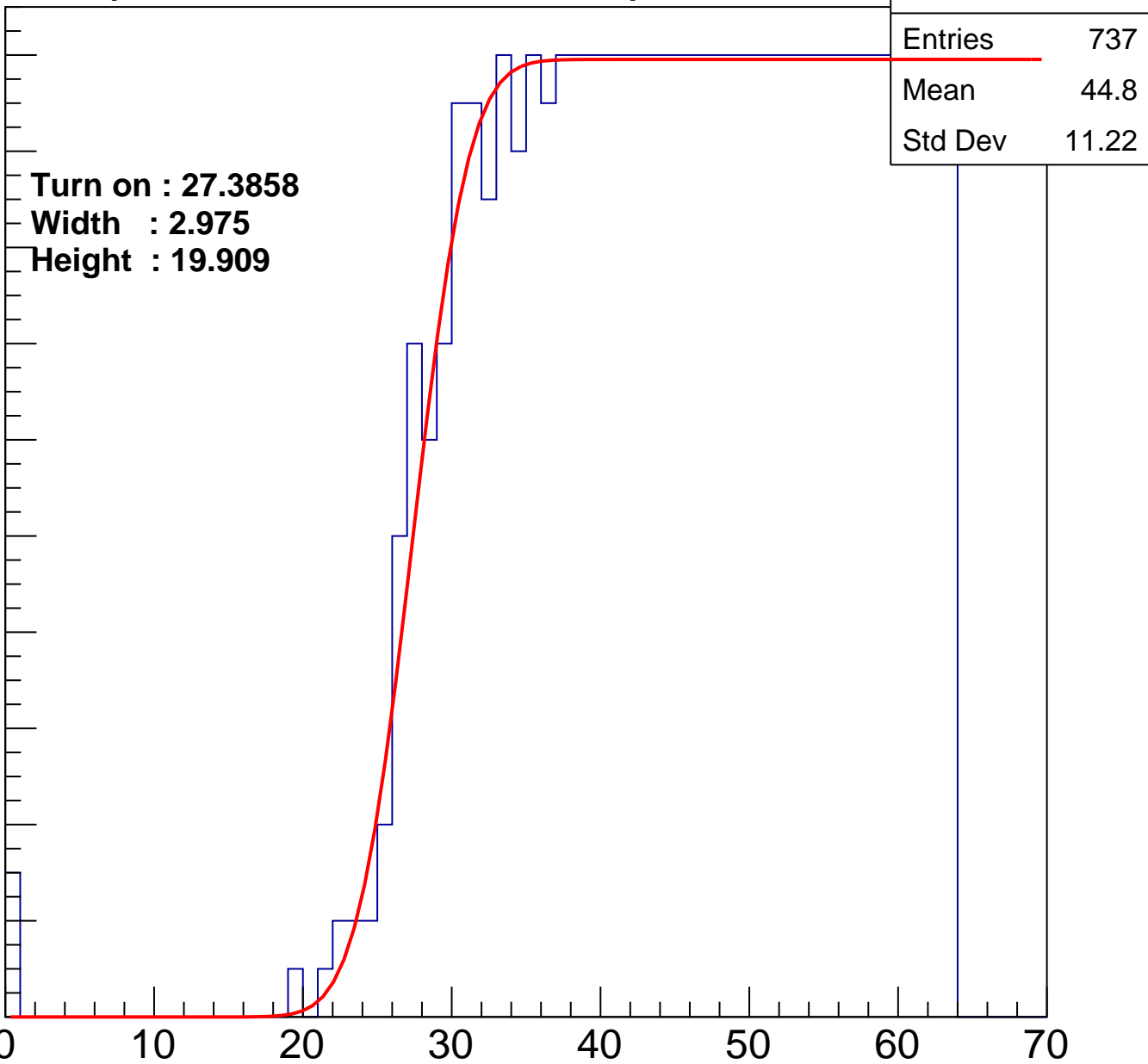
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3858
Width : 2.975
Height : 19.909

Entries	737
Mean	44.8
Std Dev	11.22

ampl



B0L101S, U25-ch121

calib_packv5_042523_0143.root, FC#1, port C1

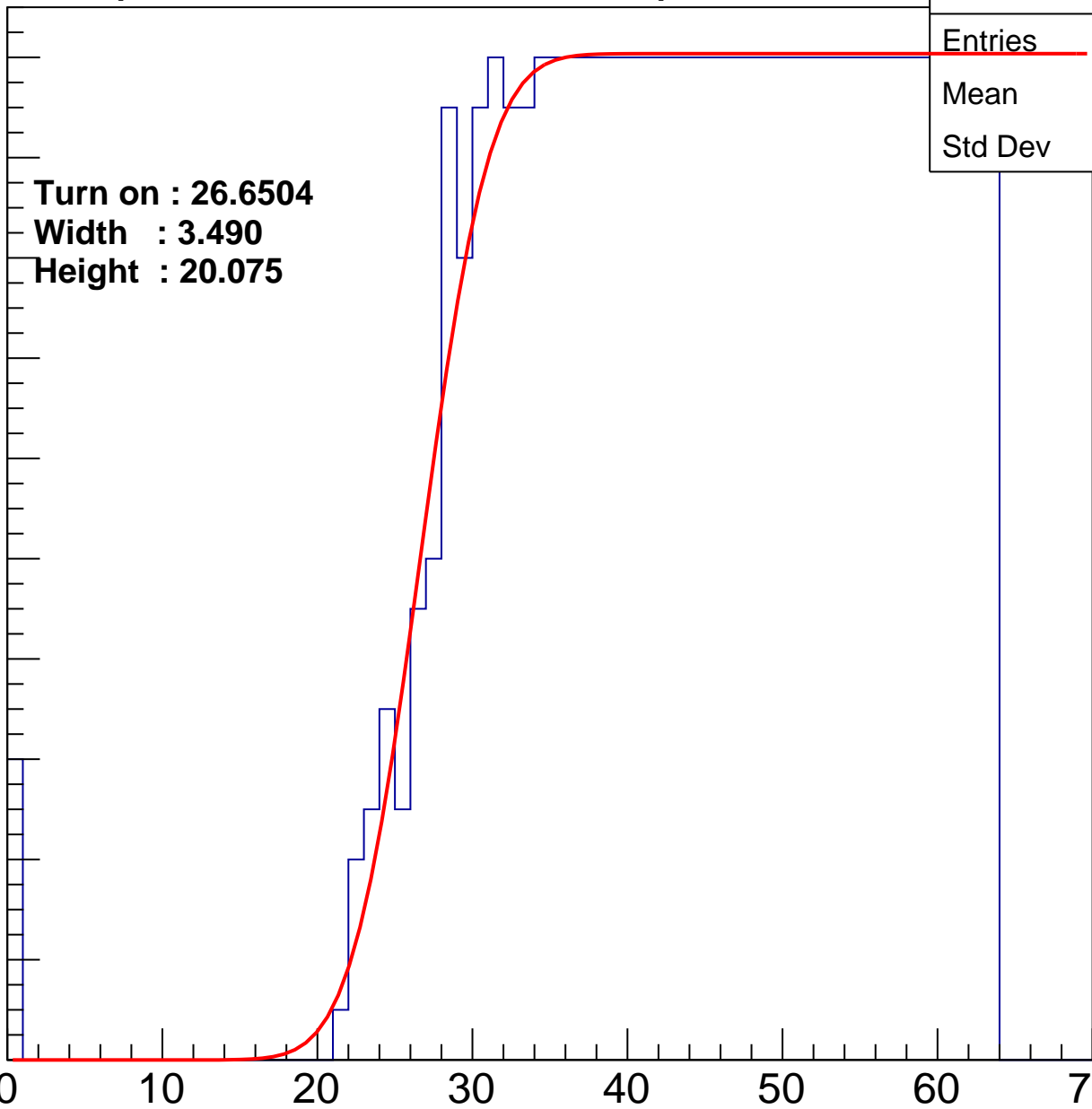
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6504
Width : 3.490
Height : 20.075

Entries	759
Mean	44.19
Std Dev	11.73

ampl



B0L101S, U25-ch122

calib_packv5_042523_0143.root, FC#1, port C1

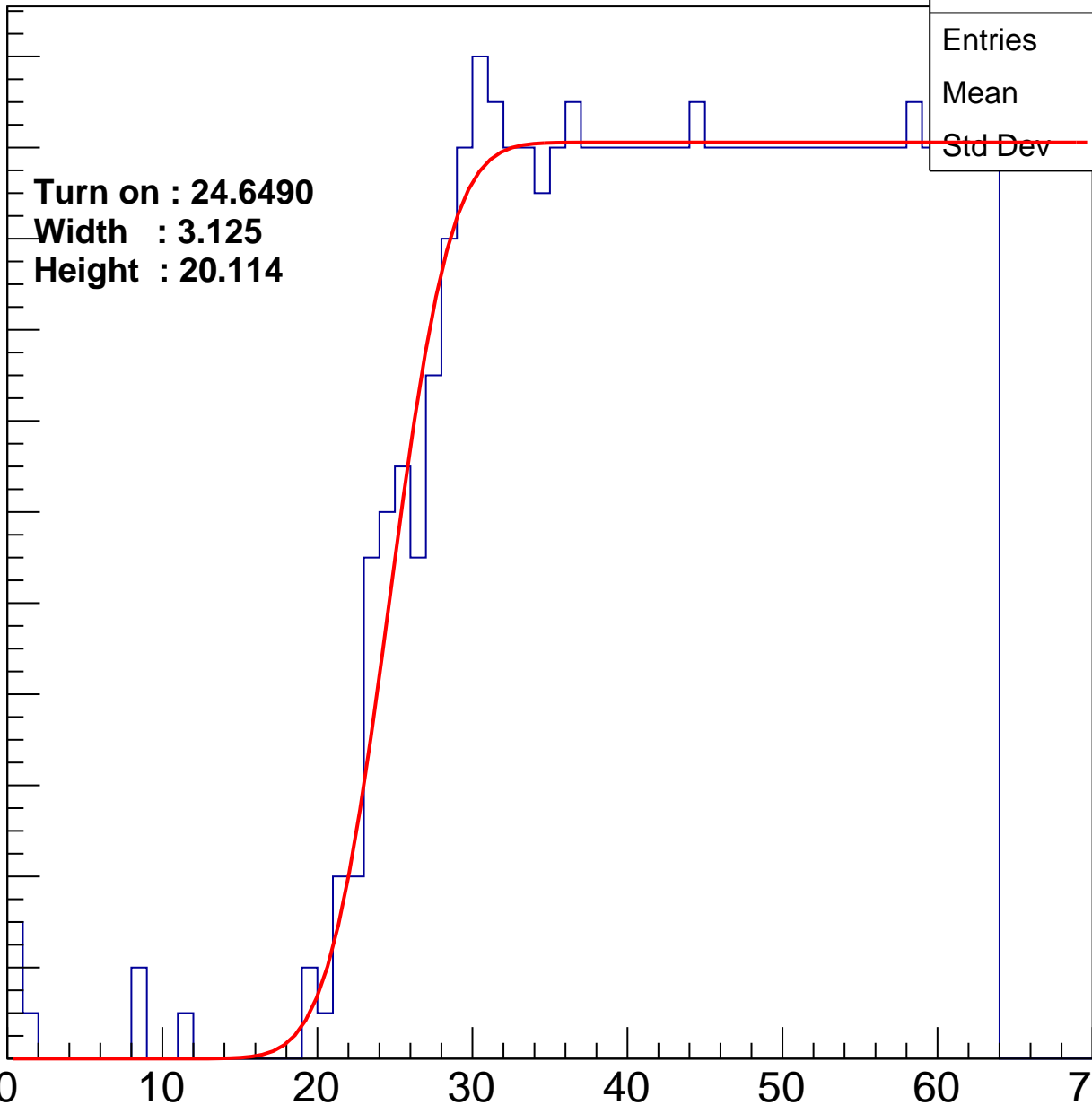
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6490
Width : 3.125
Height : 20.114

Entries	803
Mean	43.23
Std Dev	12.15

ampl



B0L101S, U25-ch123

calib_packv5_042523_0143.root, FC#1, port C1

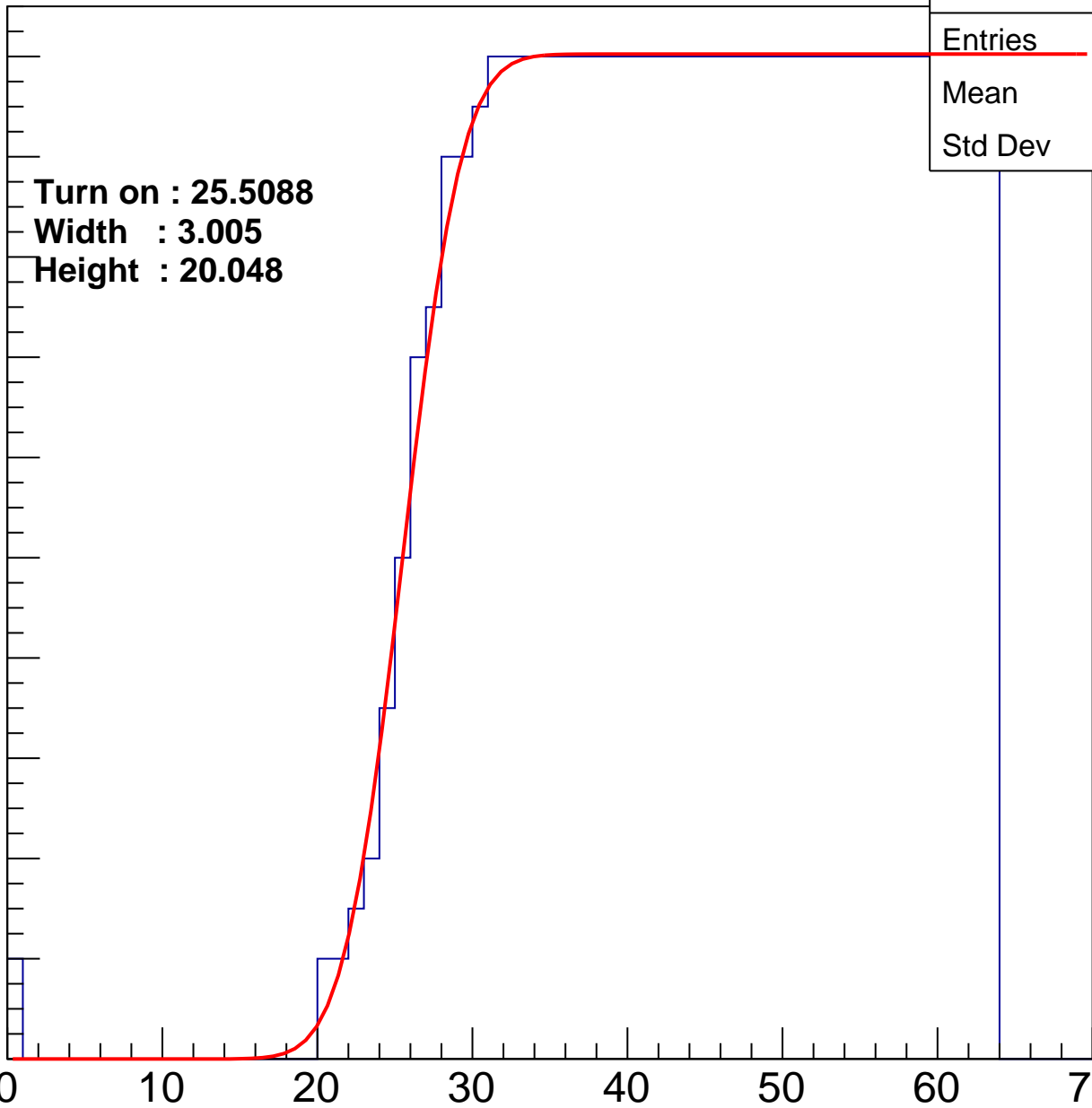
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5088
Width : 3.005
Height : 20.048

Entries	774
Mean	43.98
Std Dev	11.52

ampl



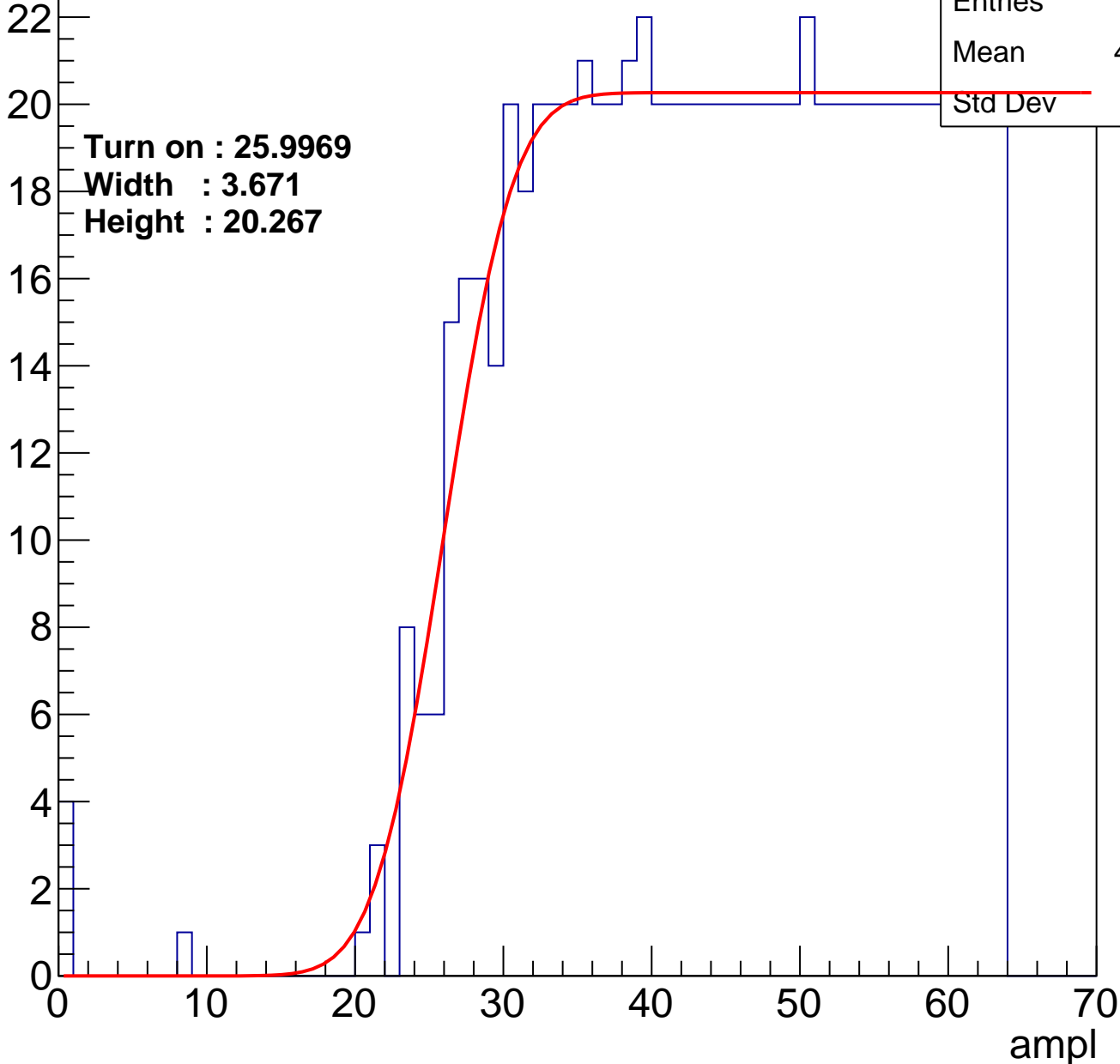
B0L101S, U25-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entries	776
Mean	44.04
Std Dev	11.7

Turn on : 25.9969
Width : 3.671
Height : 20.267

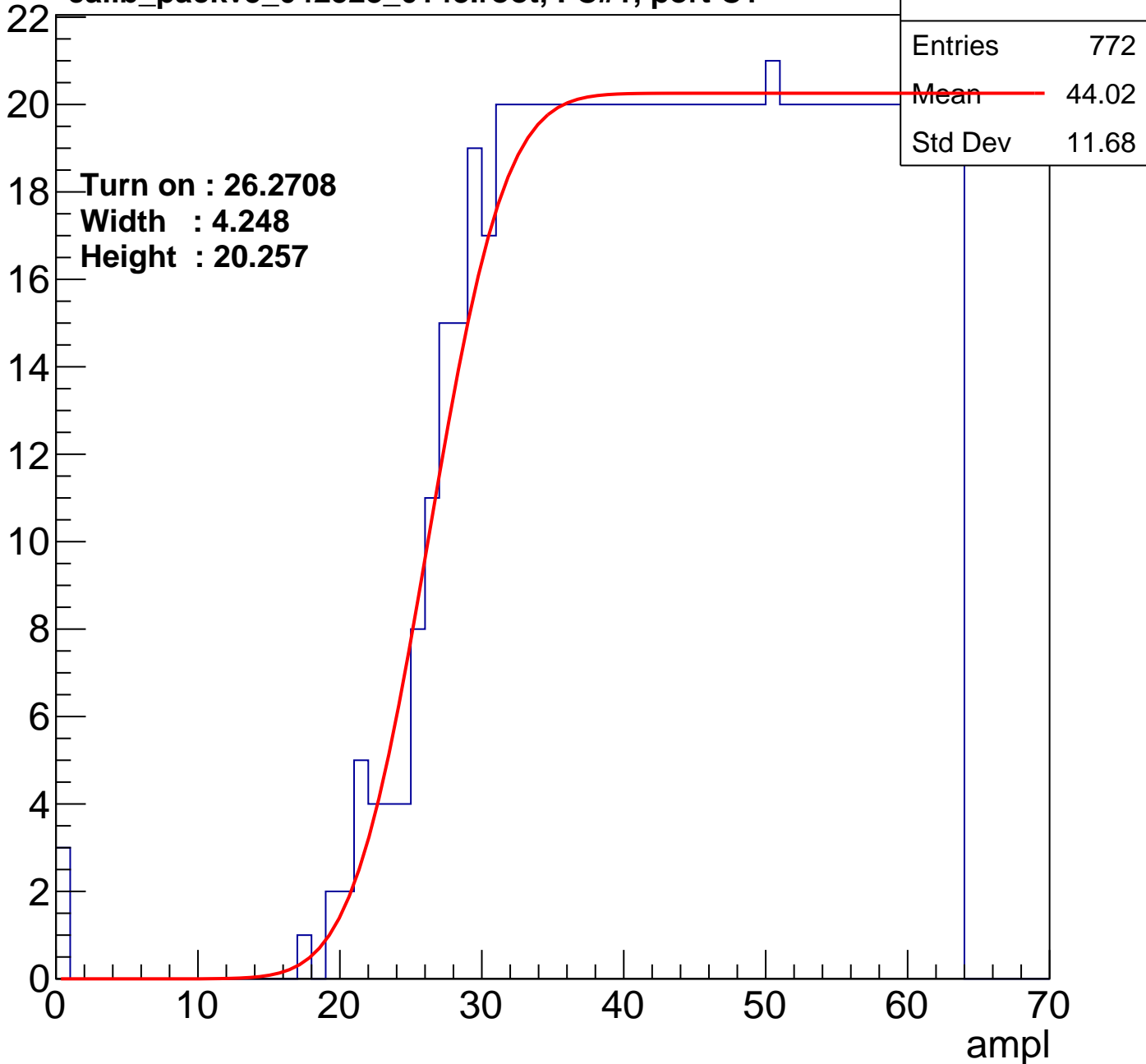
Entry



B0L101S, U25-ch125

calib_packv5_042523_0143.root, FC#1, port C1

Entry

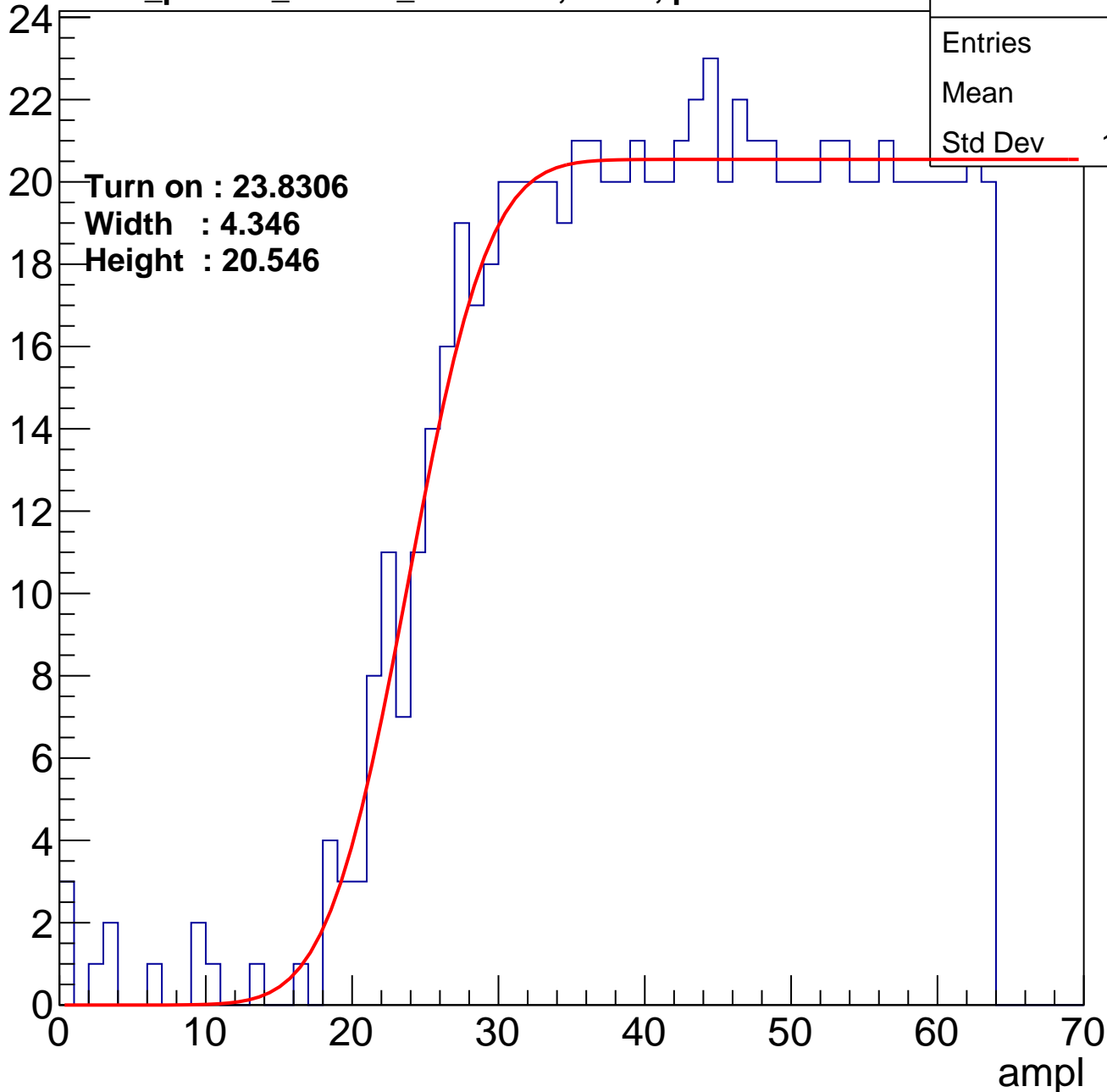


B0L101S, U25-ch126

calib_packv5_042523_0143.root, FC#1, port C1

Entries	839
Mean	42.6
Std Dev	12.64

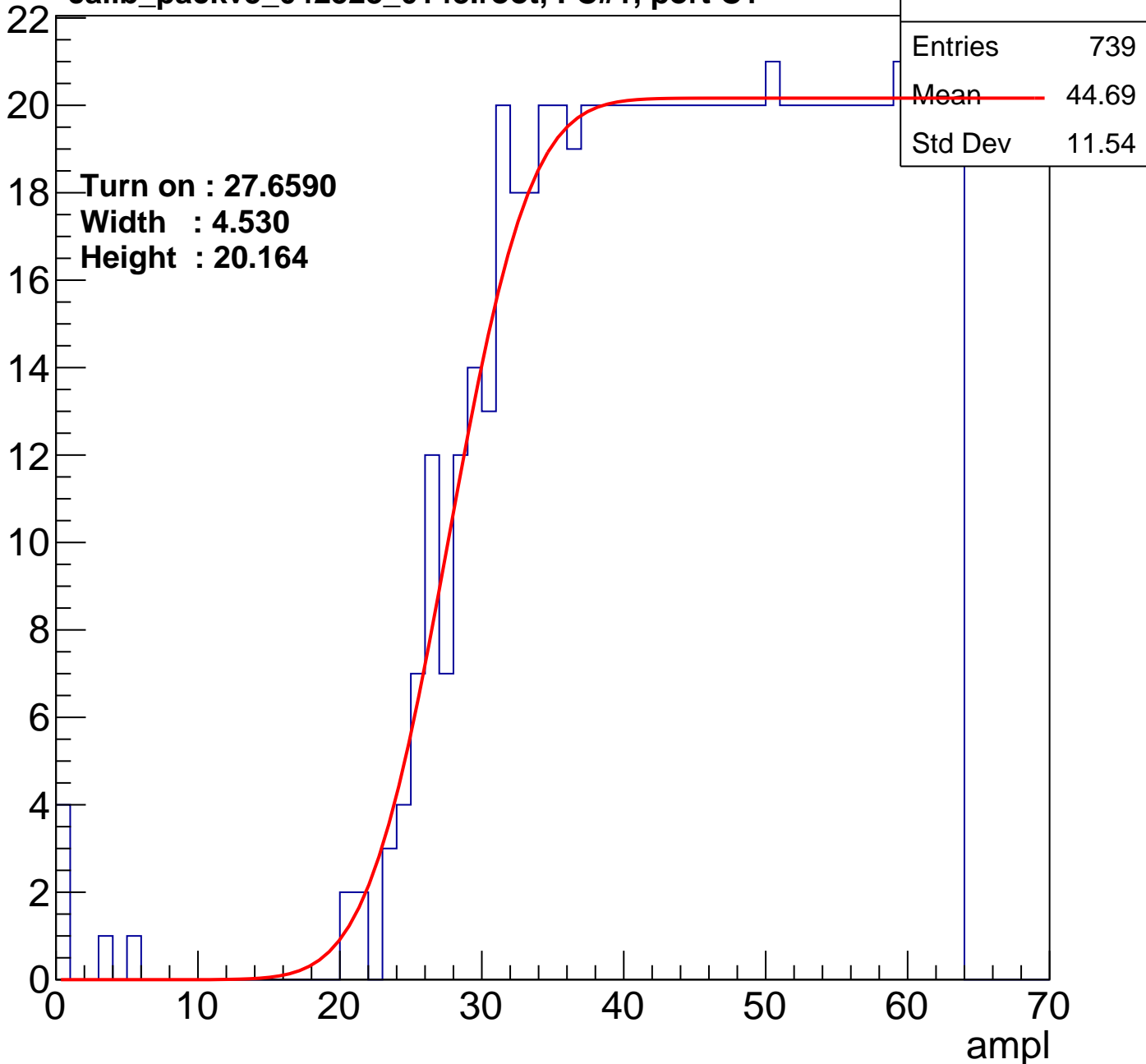
Entry



B0L101S, U25-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U25-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry

