

B0L102S, U12-ch0, adc0

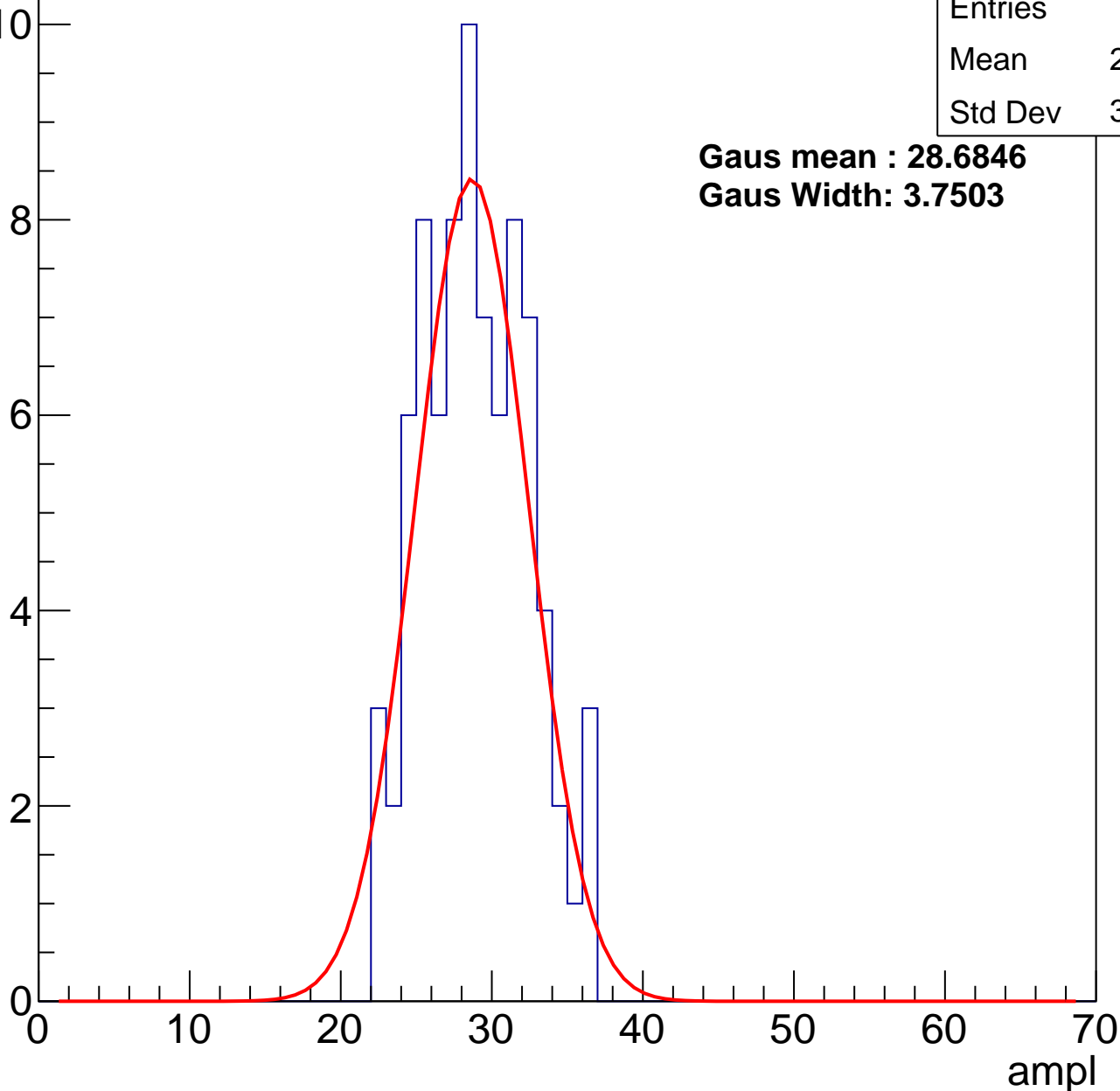
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	28.47
Std Dev	3.457

Gaus mean : 28.6846

Gaus Width: 3.7503



B0L102S, U12-ch0, adc1

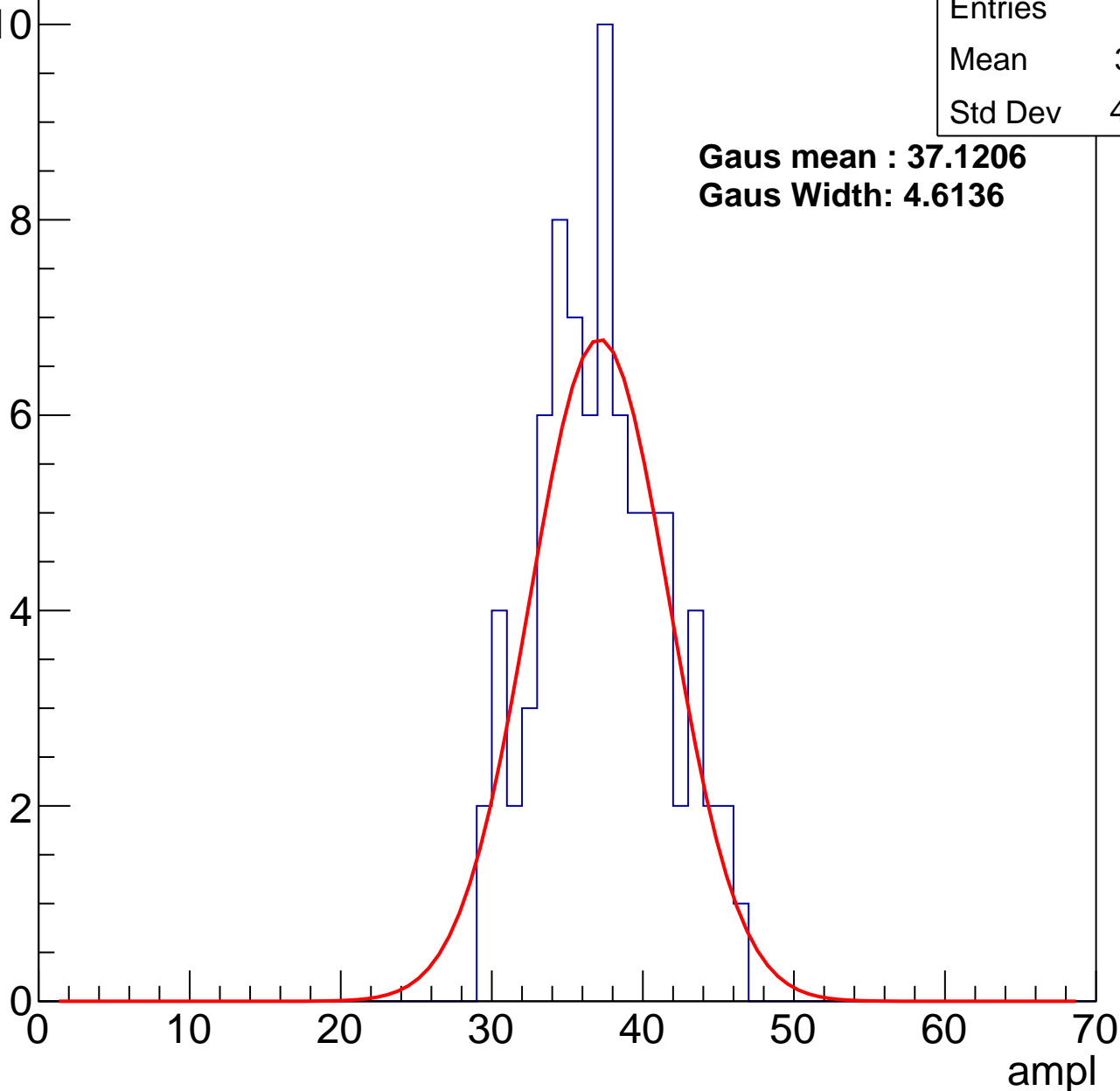
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	36.81
Std Dev	4.065

Gaus mean : 37.1206

Gaus Width: 4.6136



B0L102S, U12-ch0, adc2

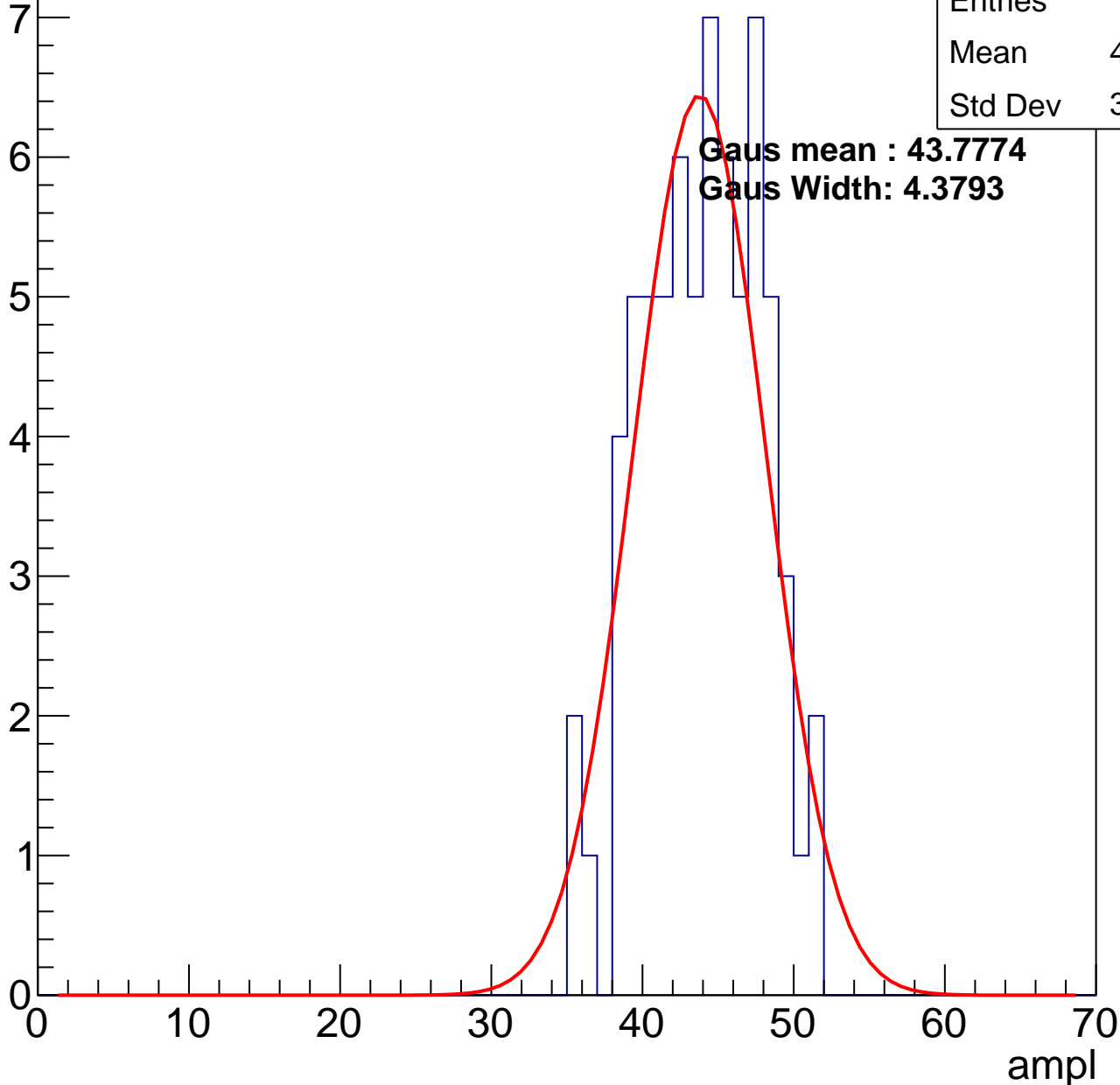
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	43.49
Std Dev	3.824

Gaus mean : 43.7774

Gaus Width: 4.3793

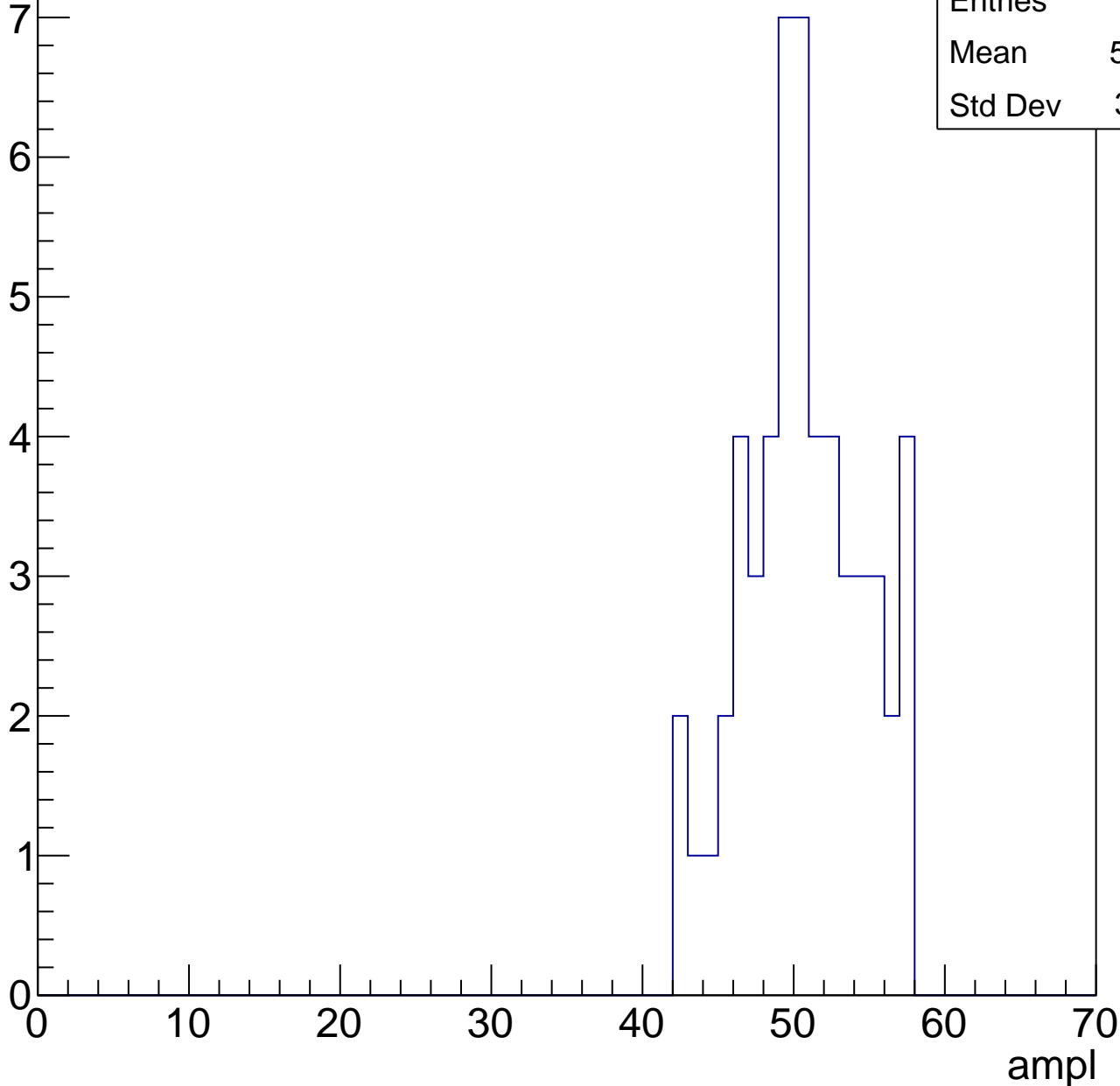


B0L102S, U12-ch0, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

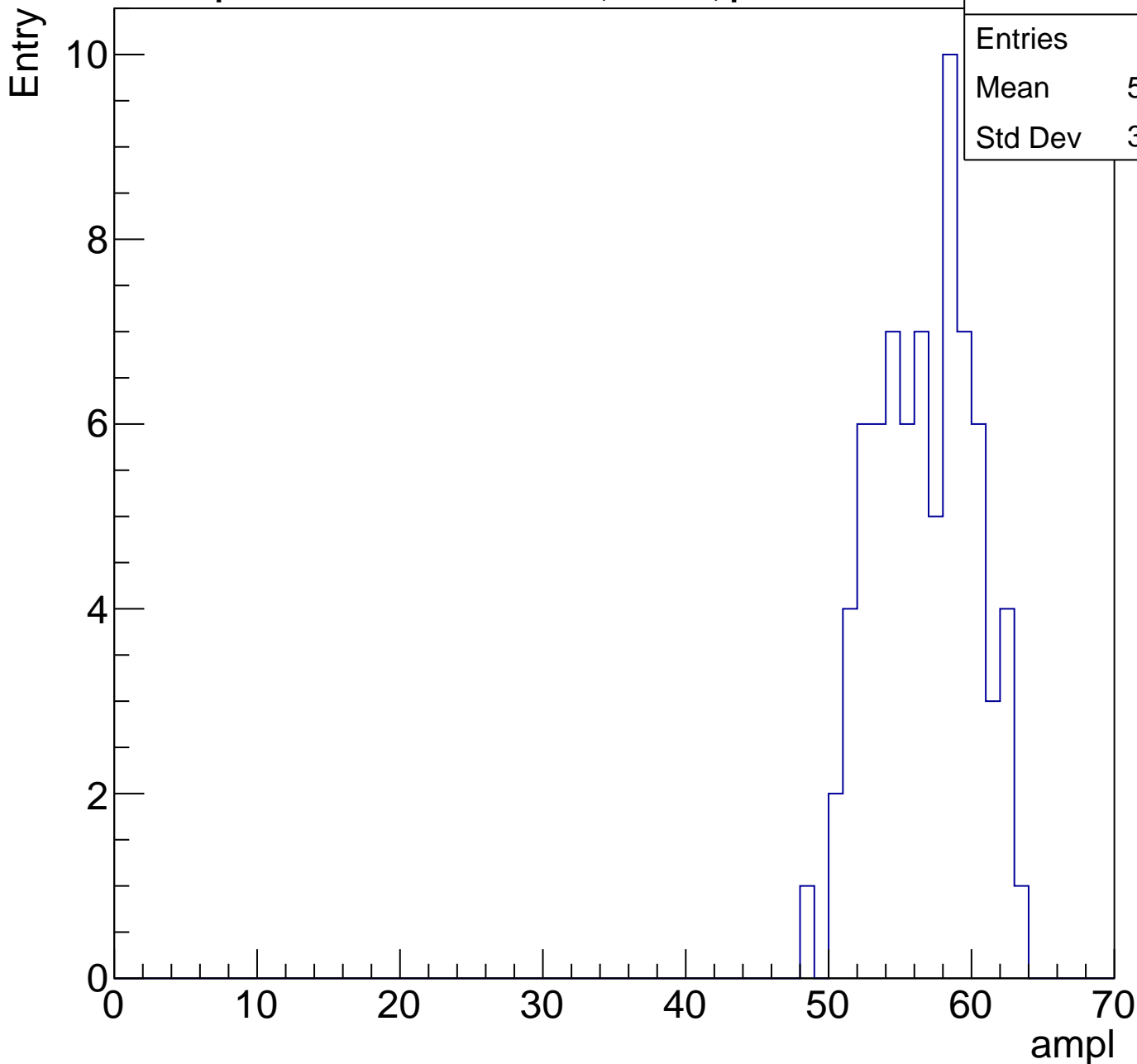
Entries	54
Mean	50.17
Std Dev	3.881



B0L102S, U12-ch0, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	56.19
Std Dev	3.444

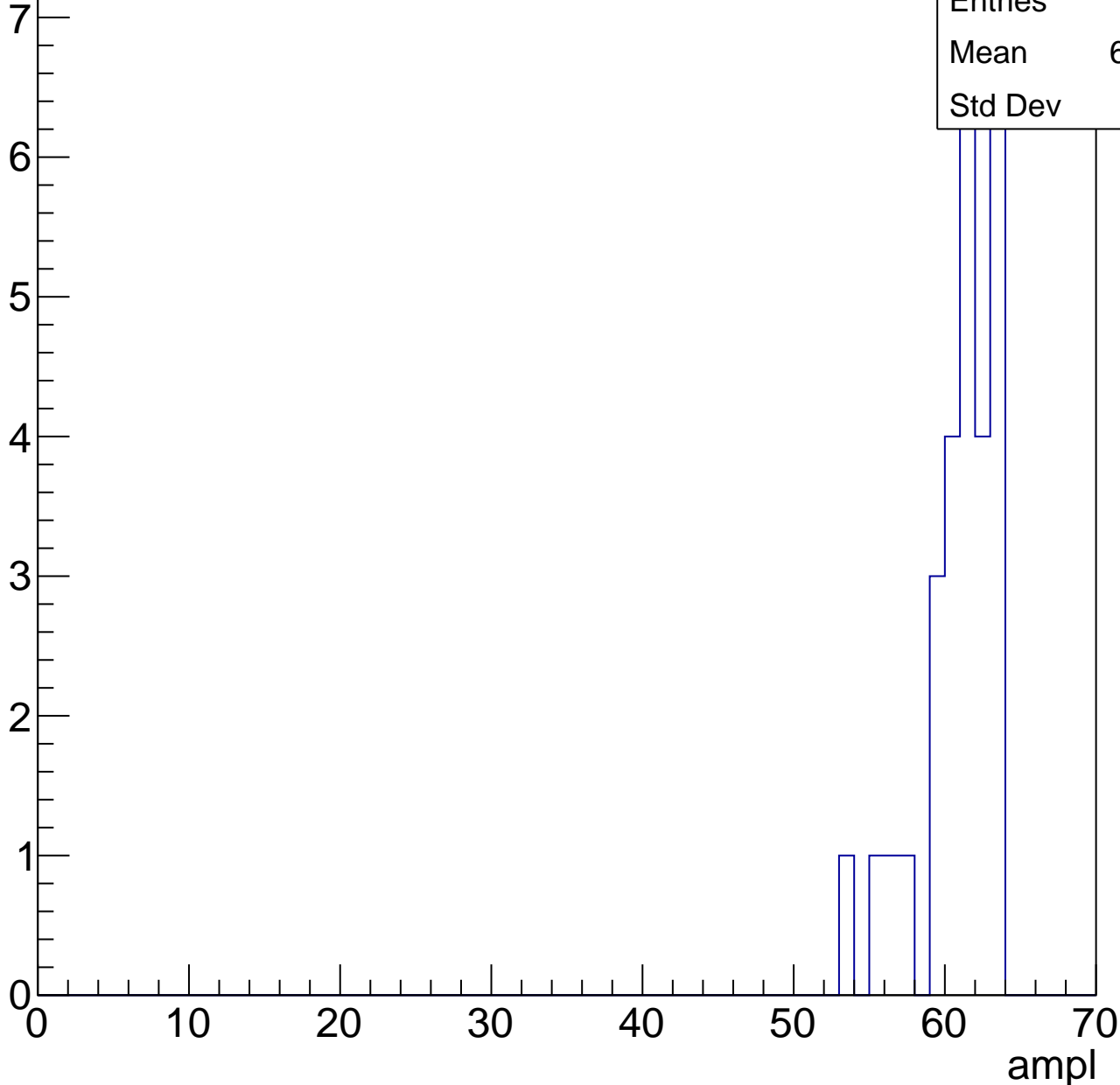


B0L102S, U12-ch0, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

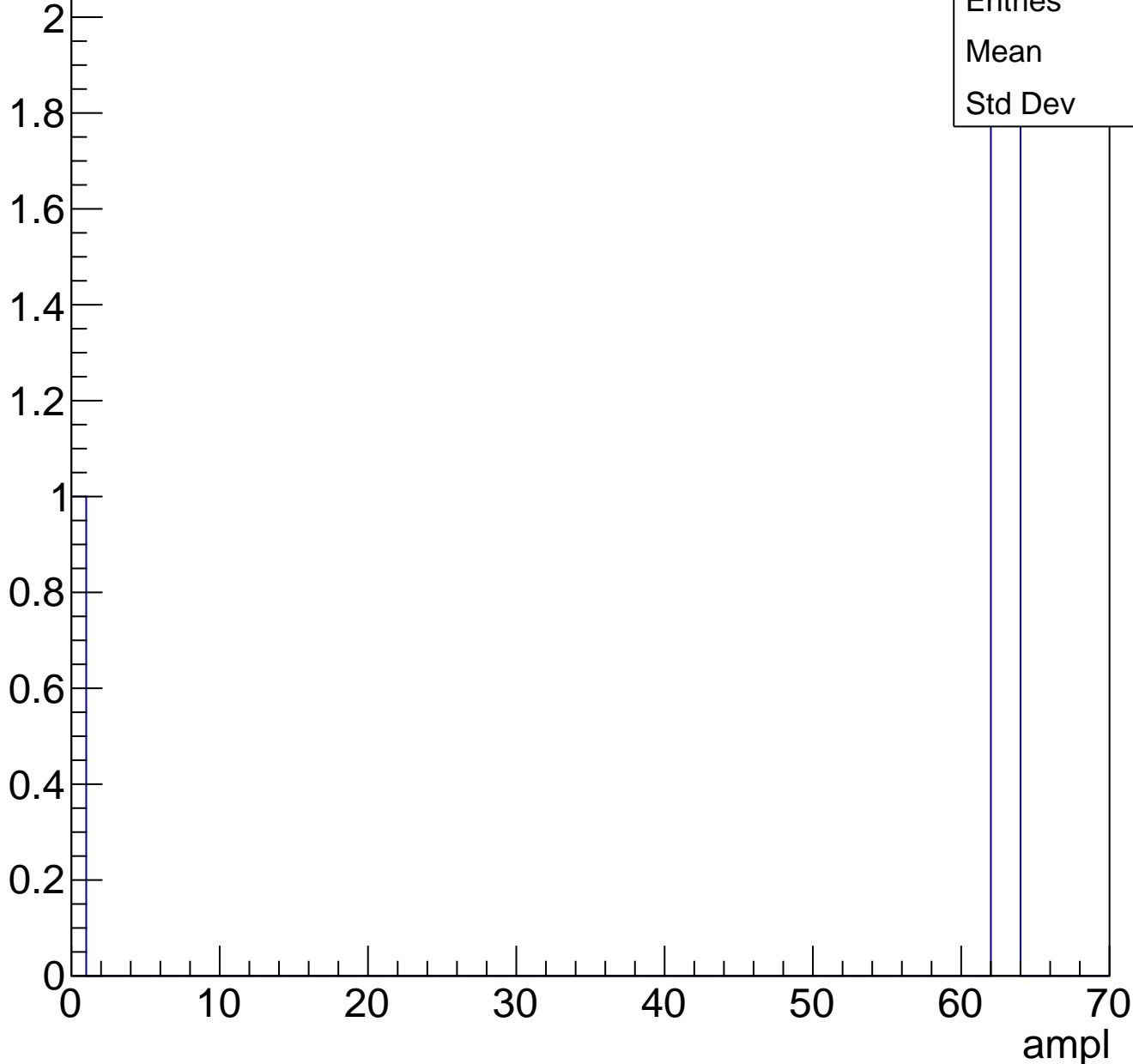
Entries	29
Mean	60.48
Std Dev	2.5



B0L102S, U12-ch0, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch0, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch1, adc0

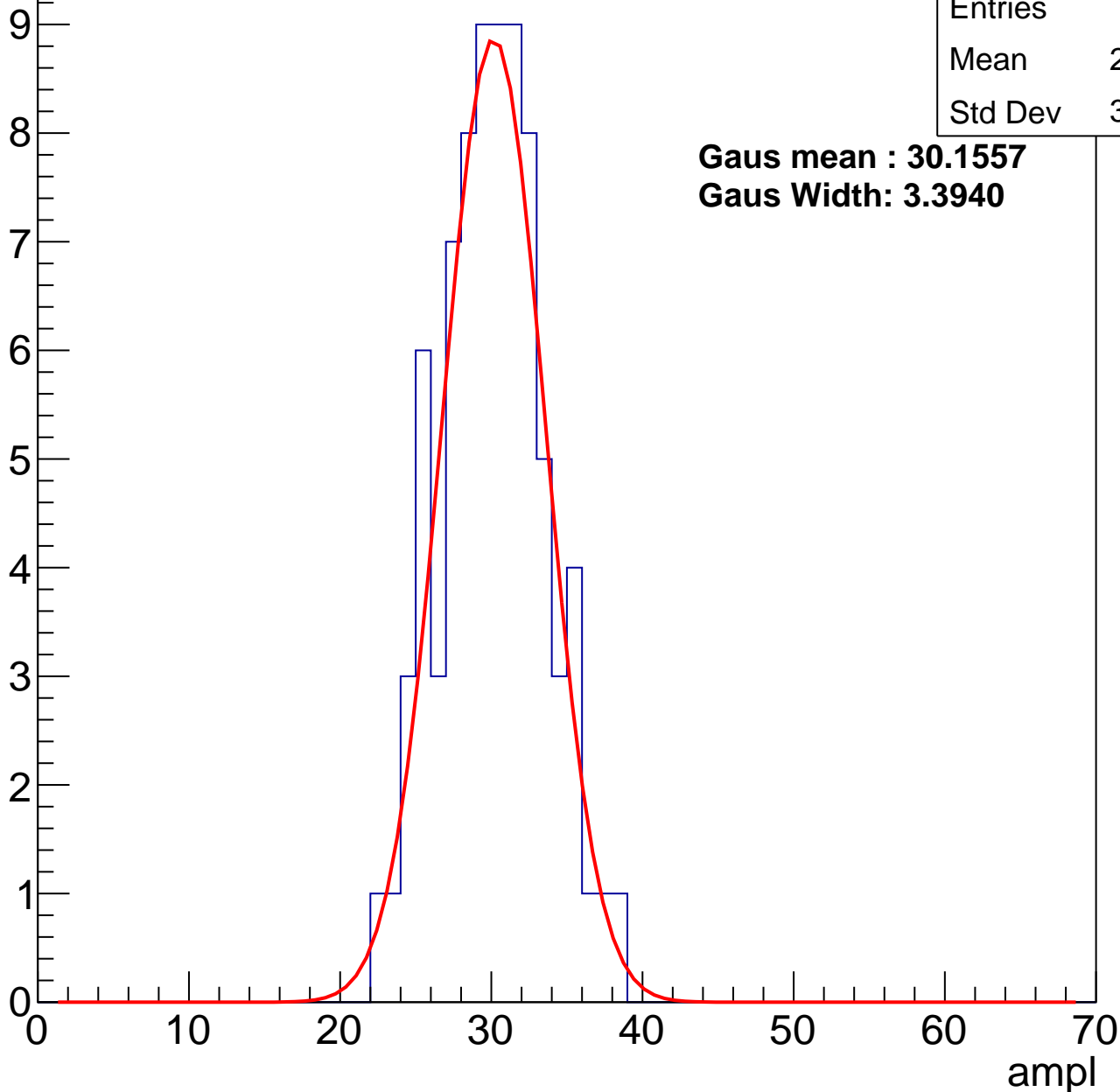
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	29.65
Std Dev	3.368

Gaus mean : 30.1557

Gaus Width: 3.3940



B0L102S, U12-ch1, adc1

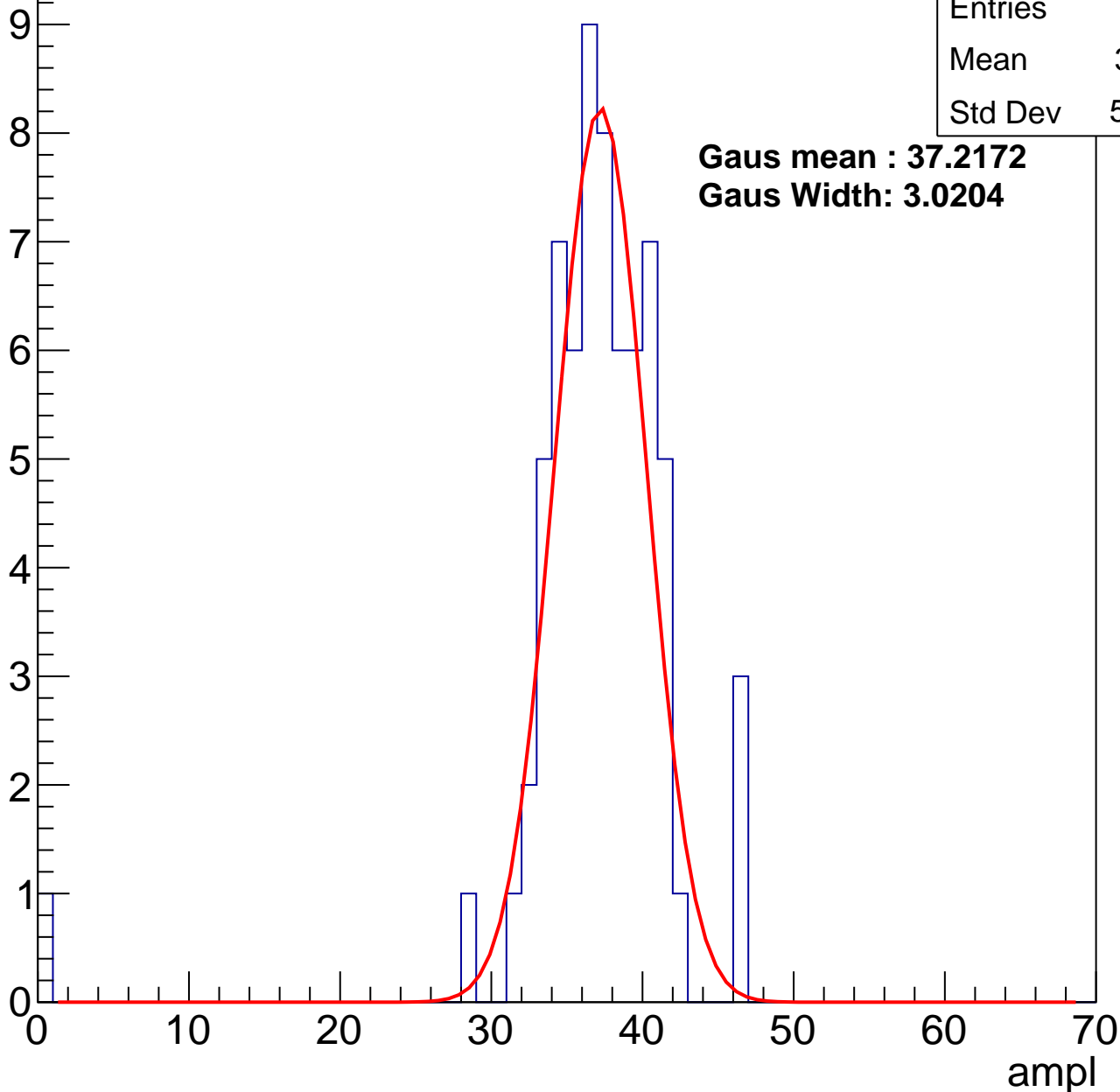
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.51
Std Dev	5.606

Gaus mean : 37.2172

Gaus Width: 3.0204



B0L102S, U12-ch1, adc2

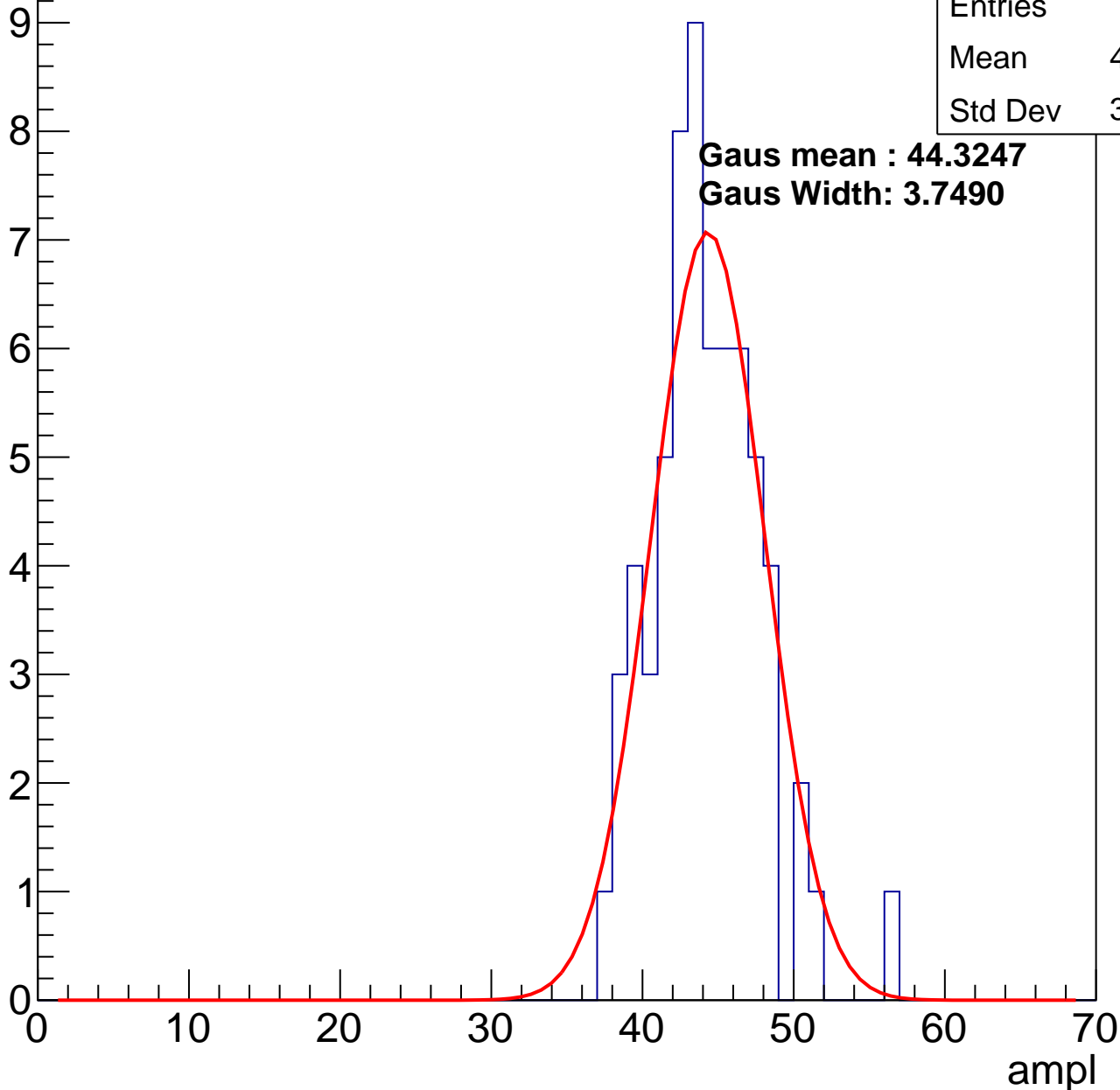
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	43.73
Std Dev	3.514

Gaus mean : 44.3247

Gaus Width: 3.7490



B0L102S, U12-ch1, adc3

calib_packv5_042523_0143.root, FC#12, port B1

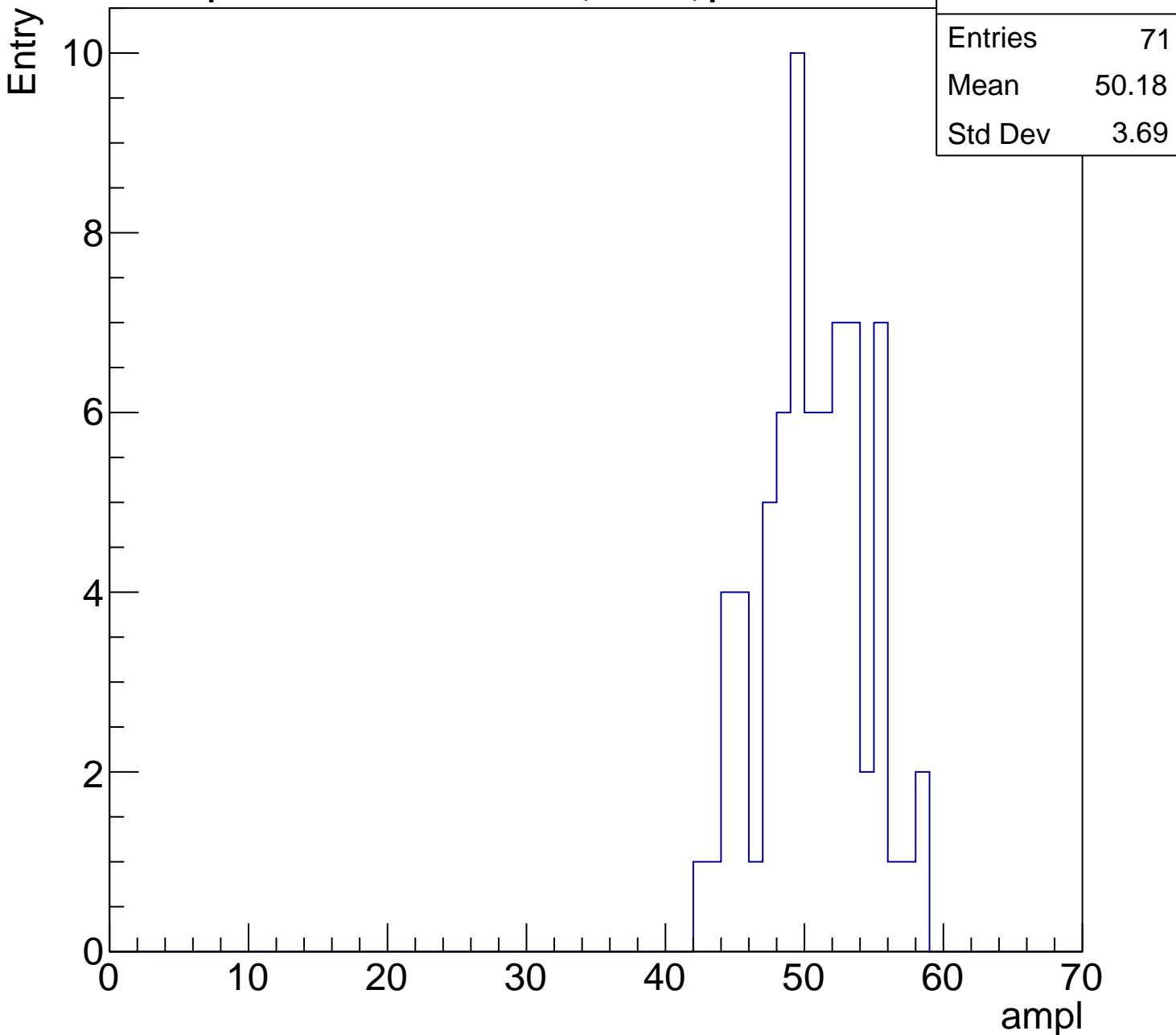
Entries	71
Mean	50.18
Std Dev	3.69

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

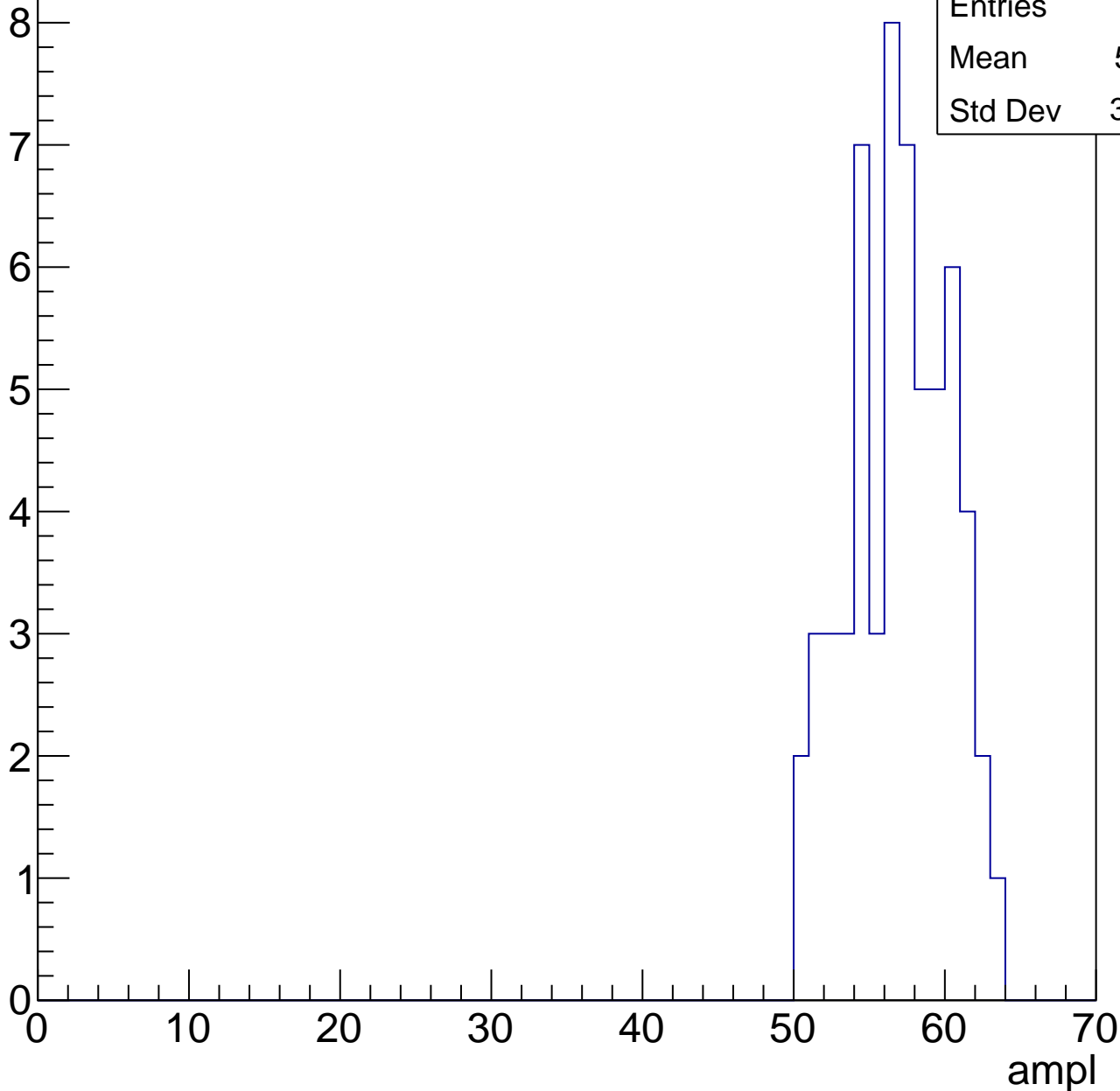


B0L102S, U12-ch1, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	56.51
Std Dev	3.259

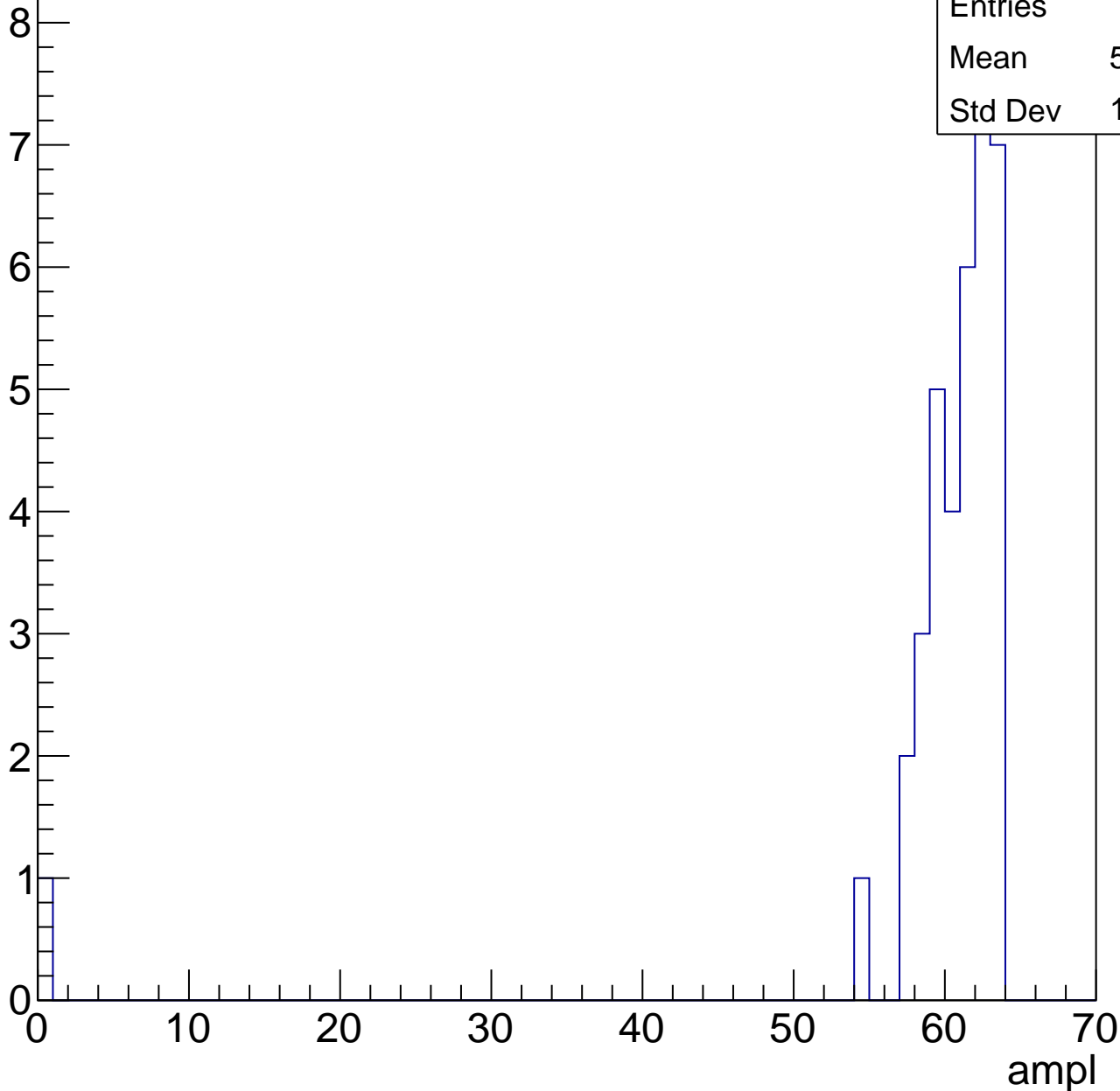


B0L102S, U12-ch1, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	37
Mean	58.92
Std Dev	10.04



B0L102S, U12-ch1, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch1, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch2, adc0

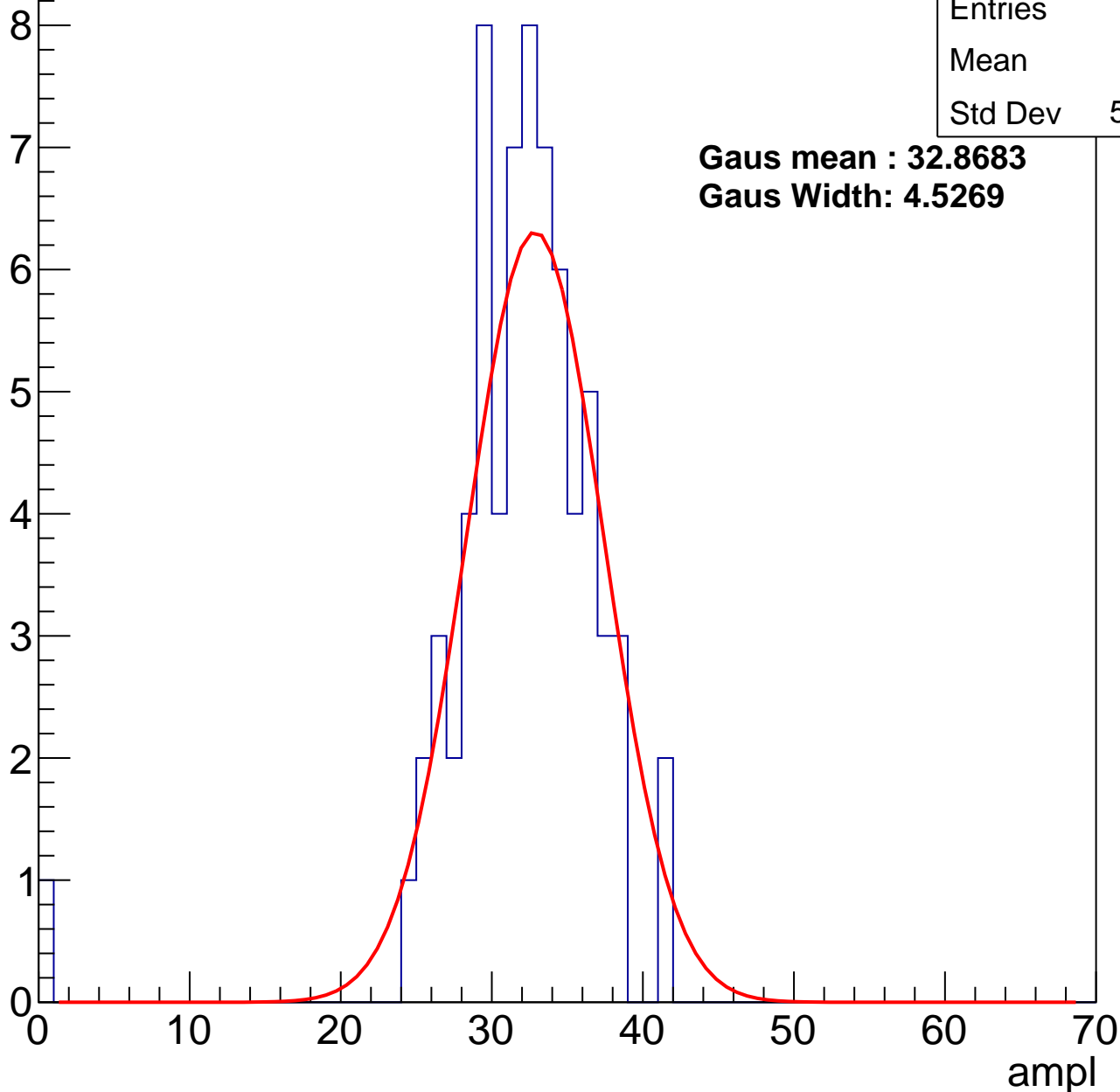
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	31.5
Std Dev	5.315

Gaus mean : 32.8683

Gaus Width: 4.5269



B0L102S, U12-ch2, adc1

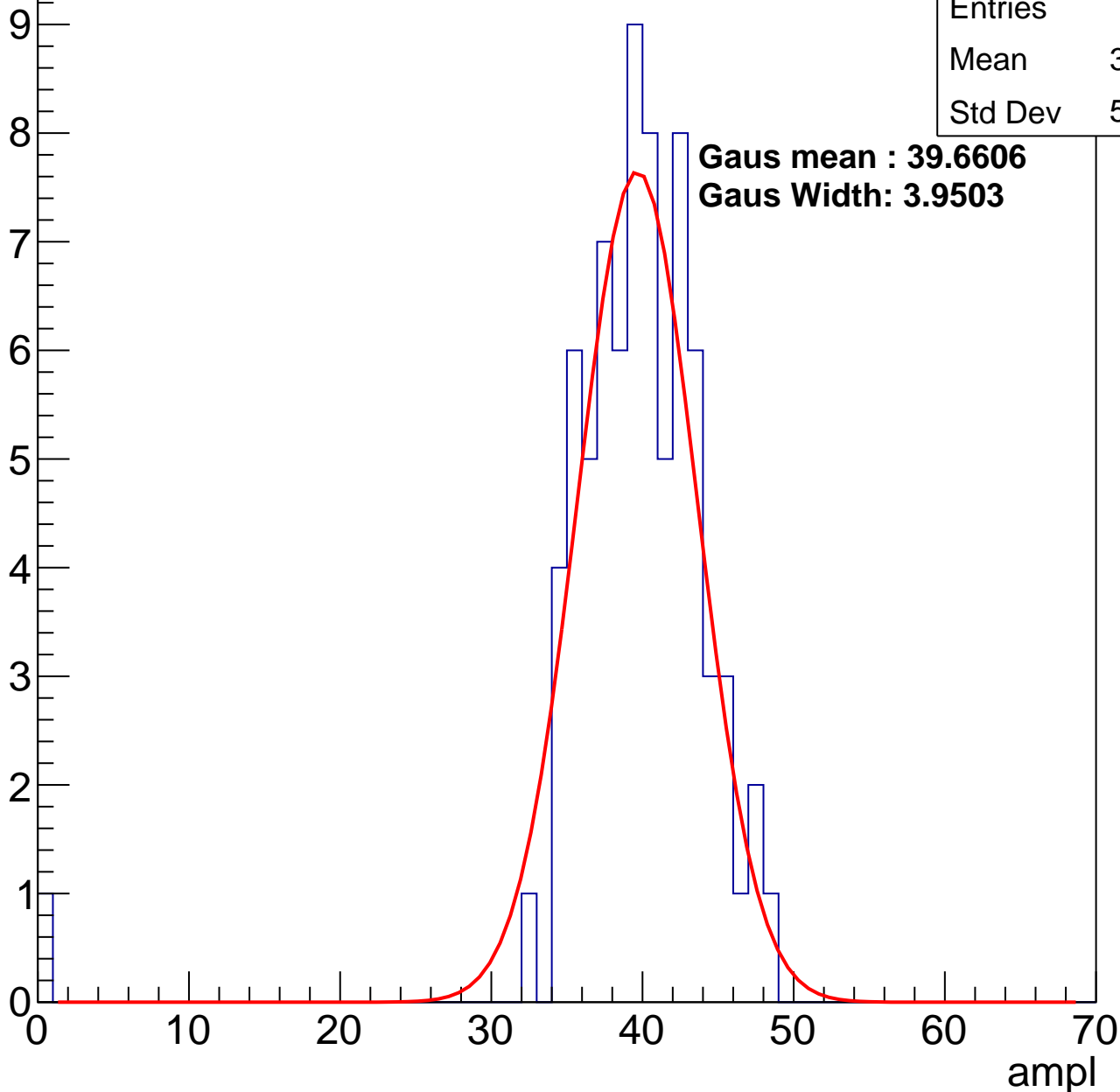
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	39.08
Std Dev	5.714

Gaus mean : 39.6606

Gaus Width: 3.9503



B0L102S, U12-ch2, adc2

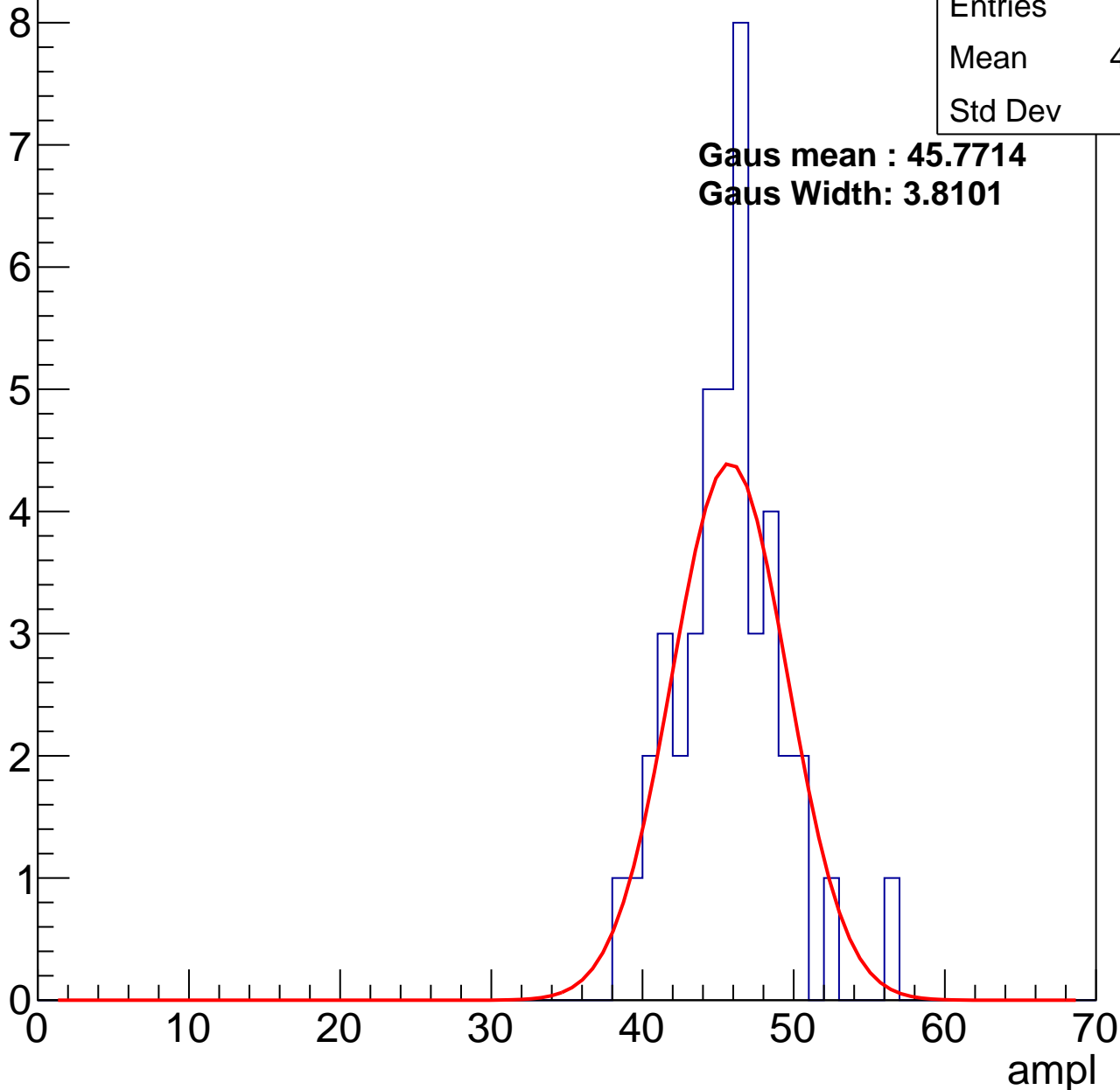
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	45.23
Std Dev	3.49

Gaus mean : 45.7714

Gaus Width: 3.8101

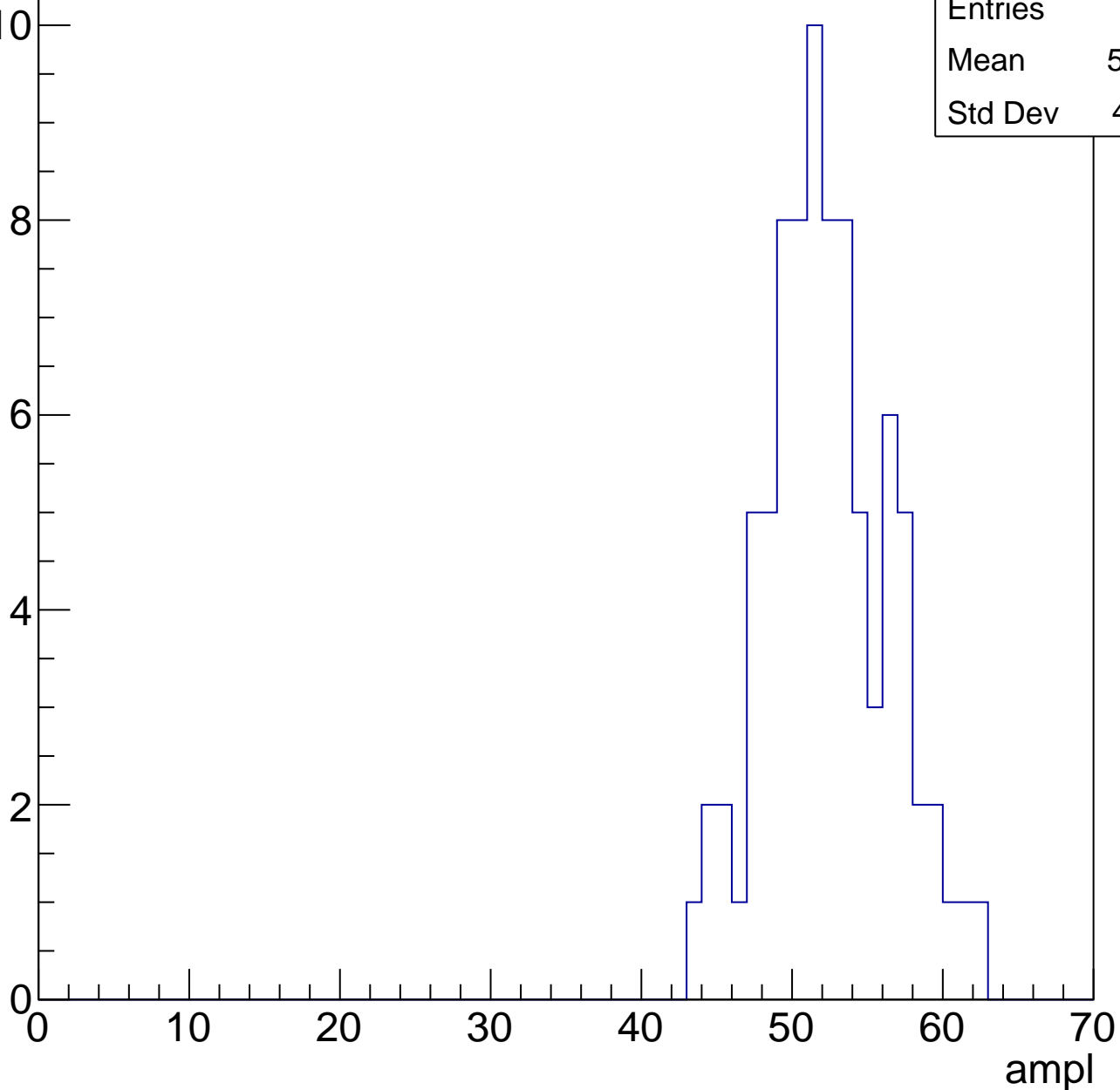


B0L102S, U12-ch2, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	51.87
Std Dev	4.011



B0L102S, U12-ch2, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 47

Mean 57.7

Std Dev 3.003

ampl

0

10

20

30

40

50

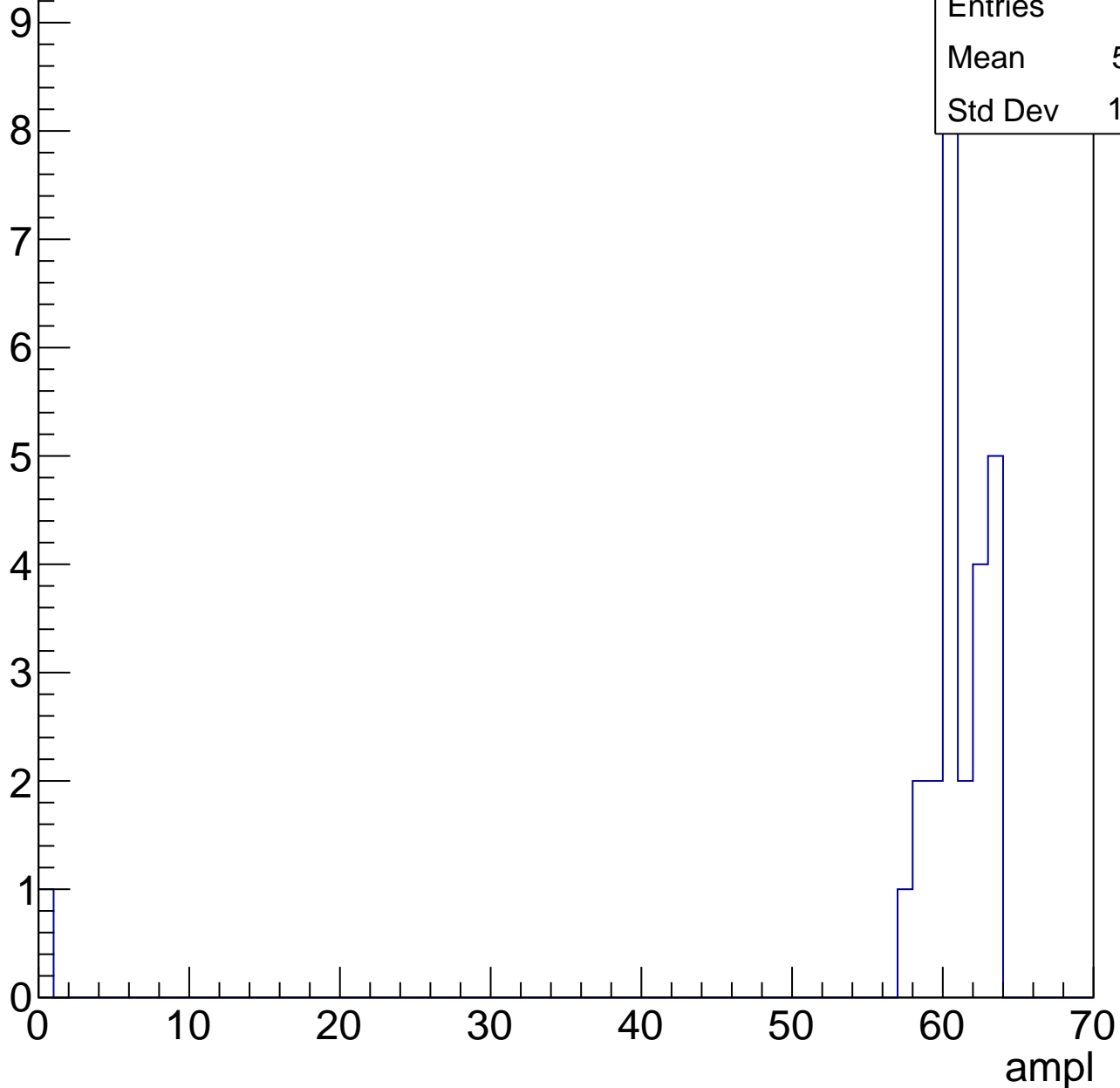
60

70

B0L102S, U12-ch2, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch2, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B0L102S, U12-ch2, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch3, adc0

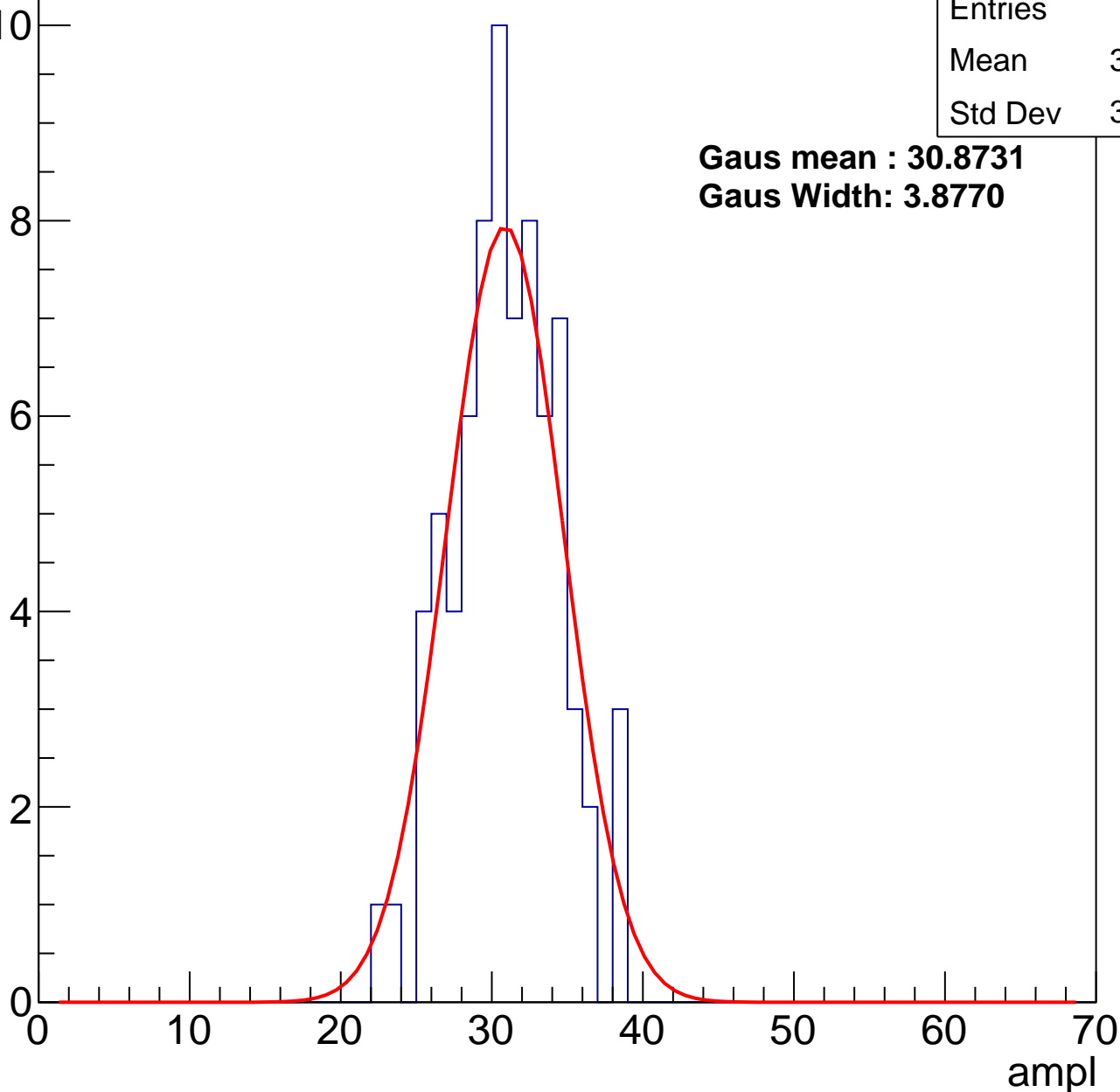
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	30.44
Std Dev	3.446

Gaus mean : 30.8731

Gaus Width: 3.8770



B0L102S, U12-ch3, adc1

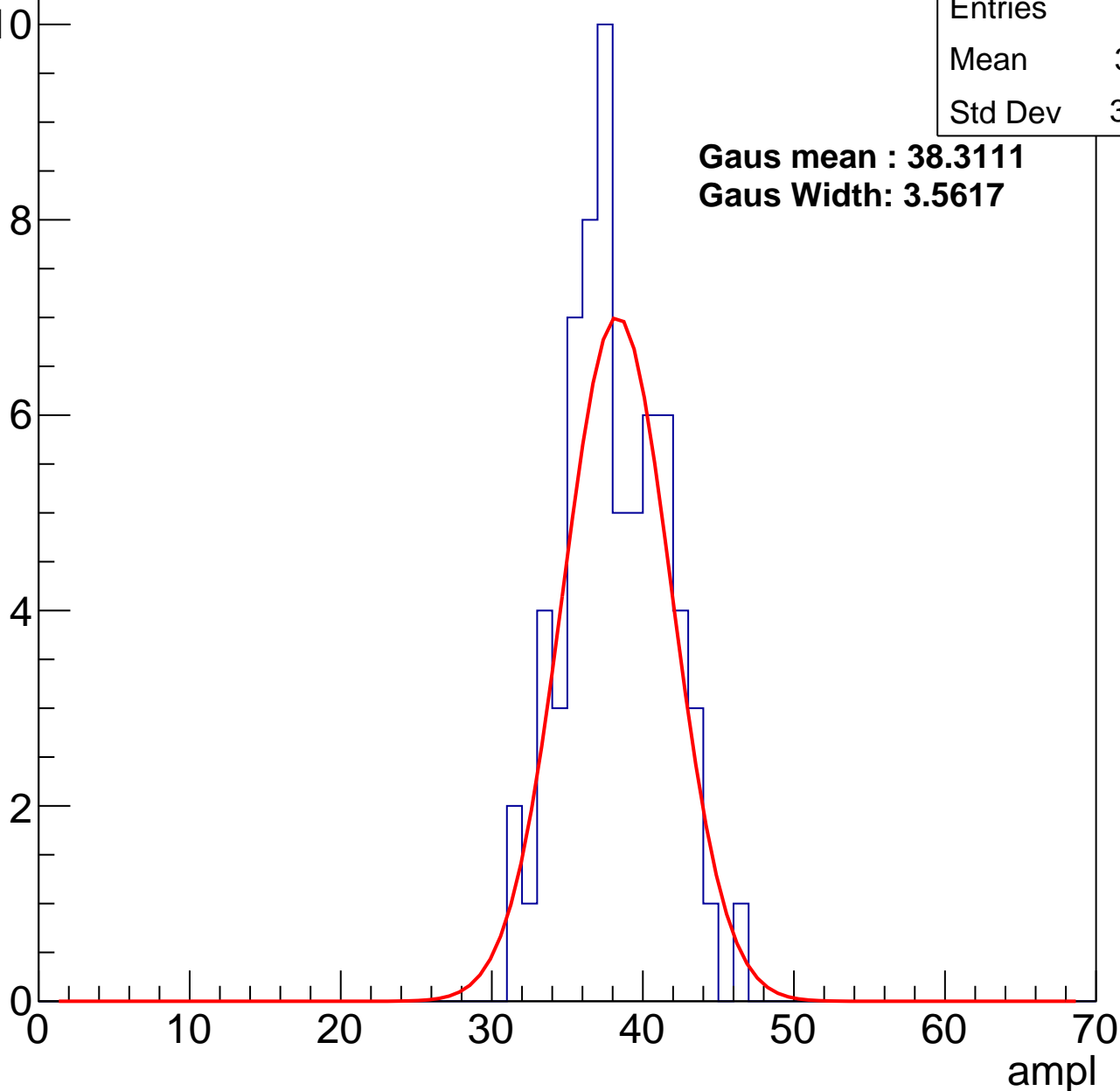
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	37.71
Std Dev	3.265

Gaus mean : 38.3111

Gaus Width: 3.5617



B0L102S, U12-ch3, adc2

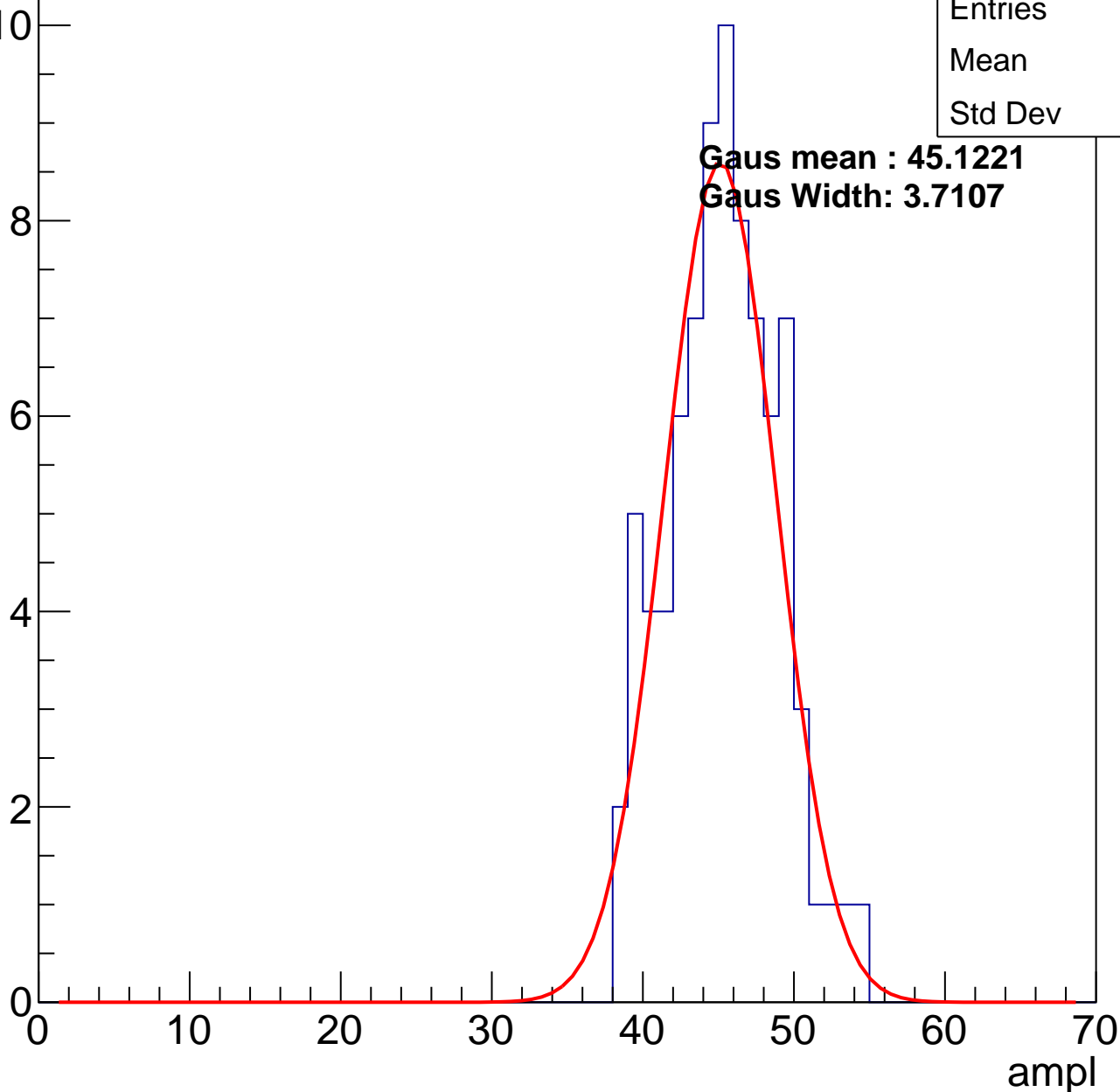
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	44.9
Std Dev	3.56

Gaus mean : 45.1221

Gaus Width: 3.7107

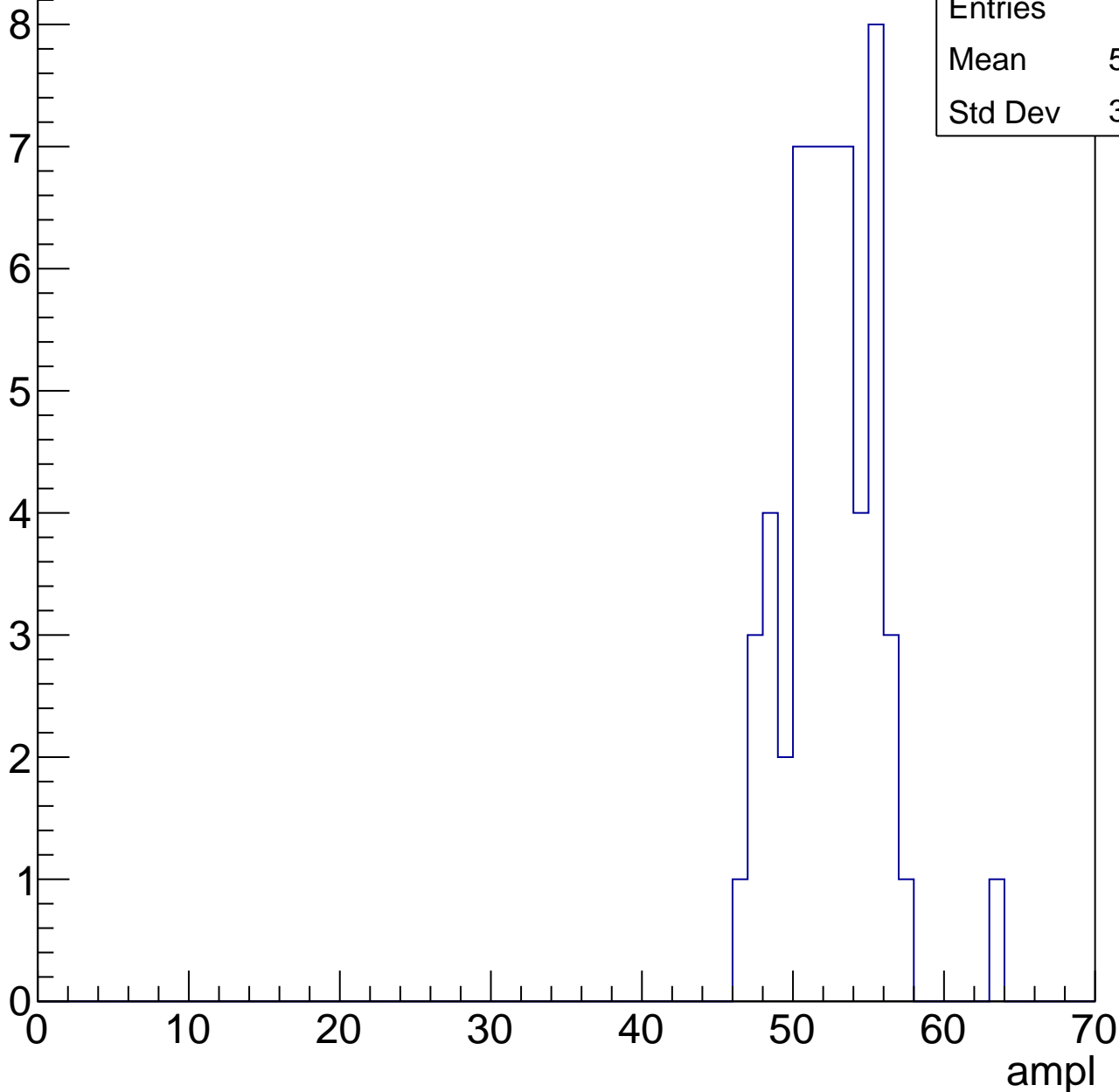


B0L102S, U12-ch3, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

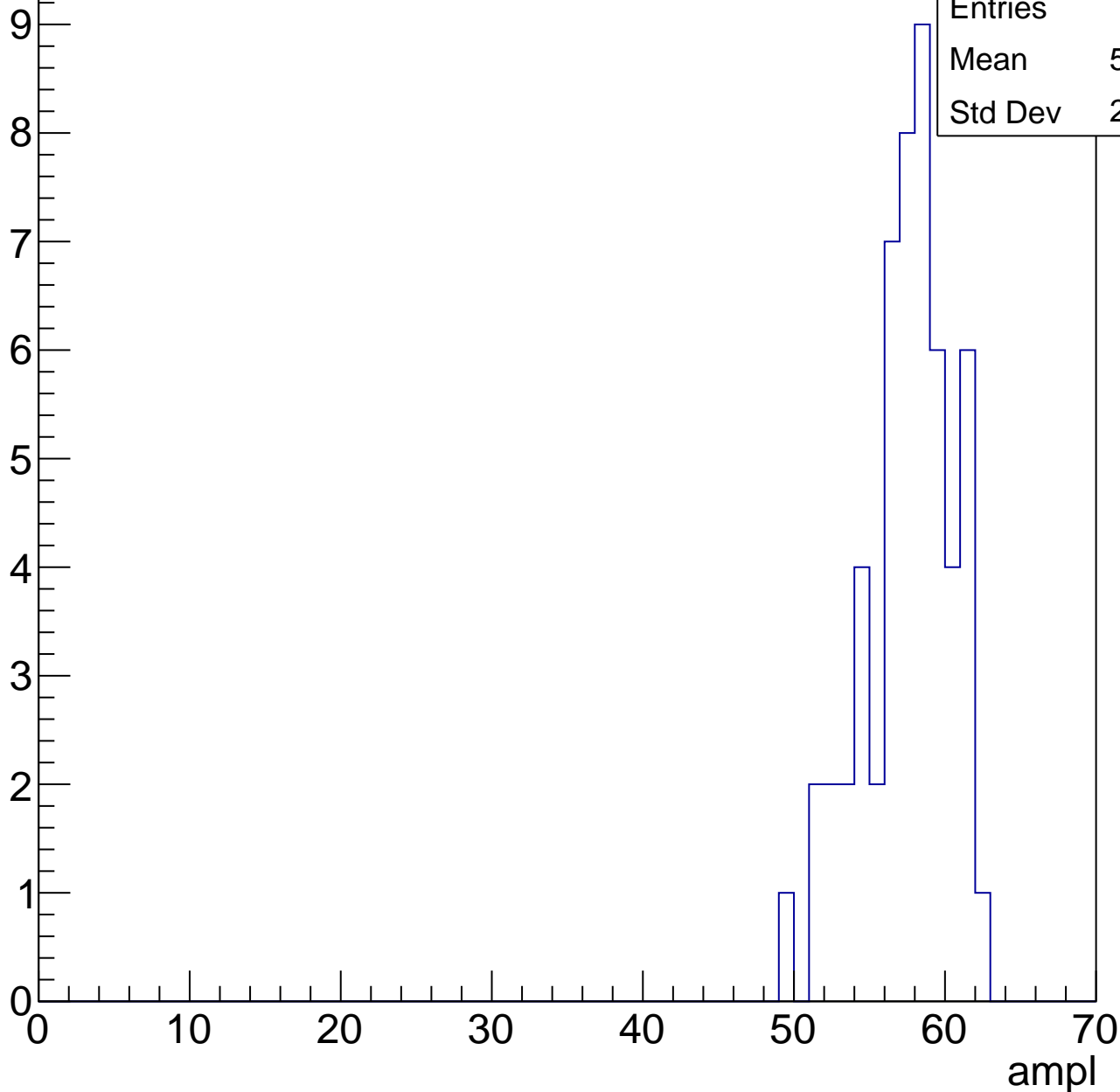
Entries	55
Mean	52.05
Std Dev	3.065



B0L102S, U12-ch3, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

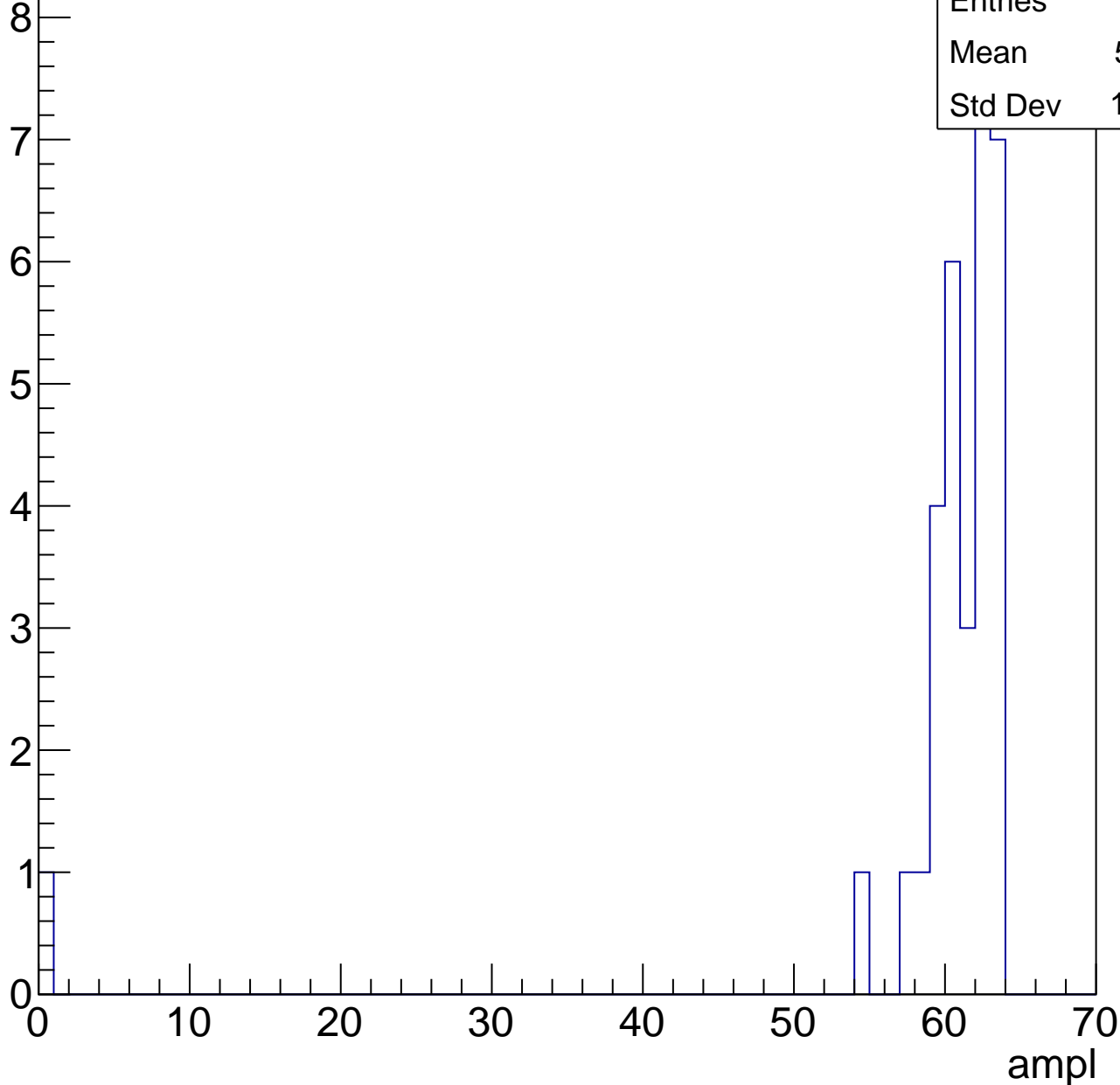


B0L102S, U12-ch3, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	32
Mean	58.91
Std Dev	10.77



B0L102S, U12-ch3, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch3, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch4, adc0

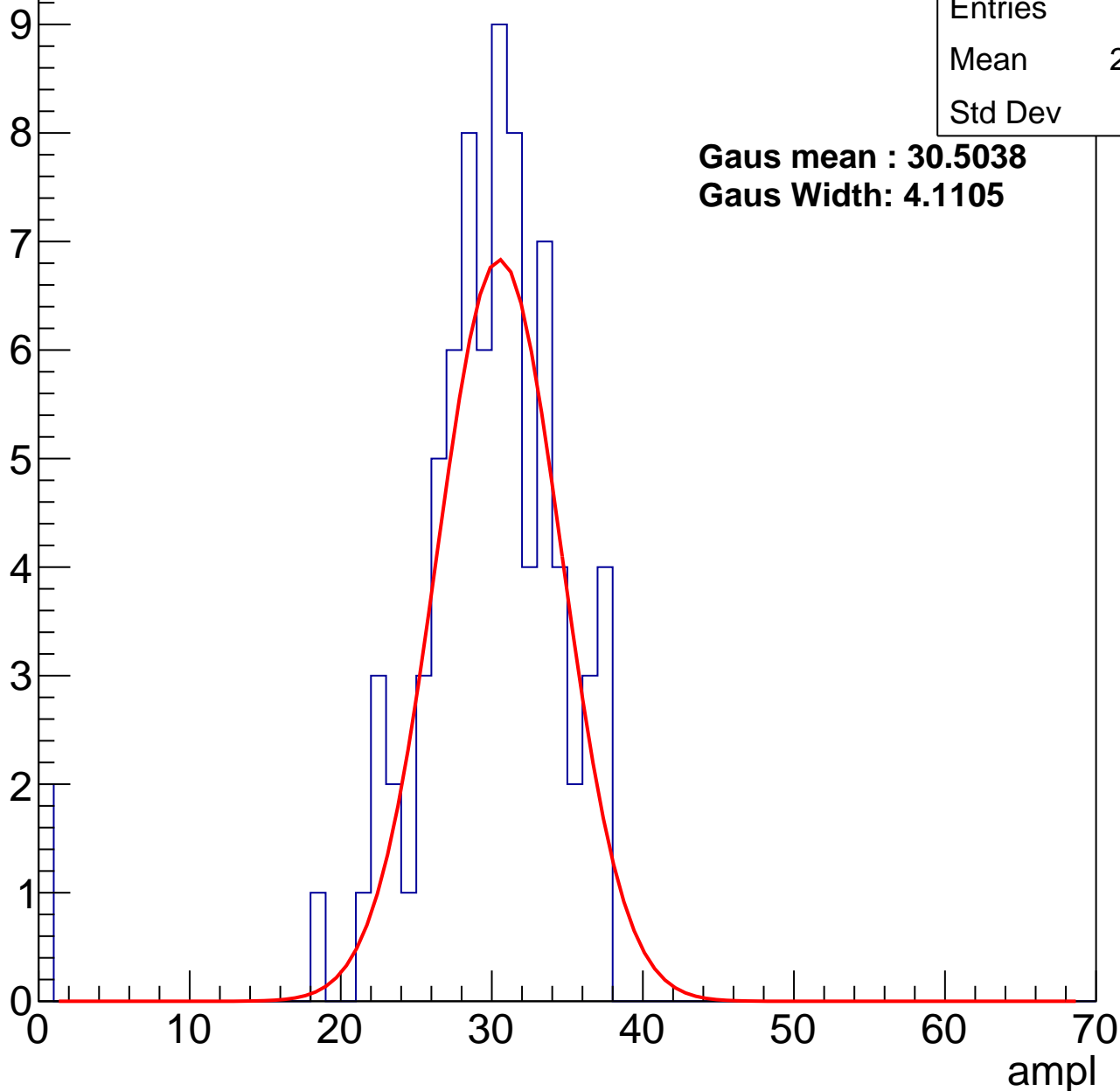
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	28.85
Std Dev	6.16

Gaus mean : 30.5038

Gaus Width: 4.1105



B0L102S, U12-ch4, adc1

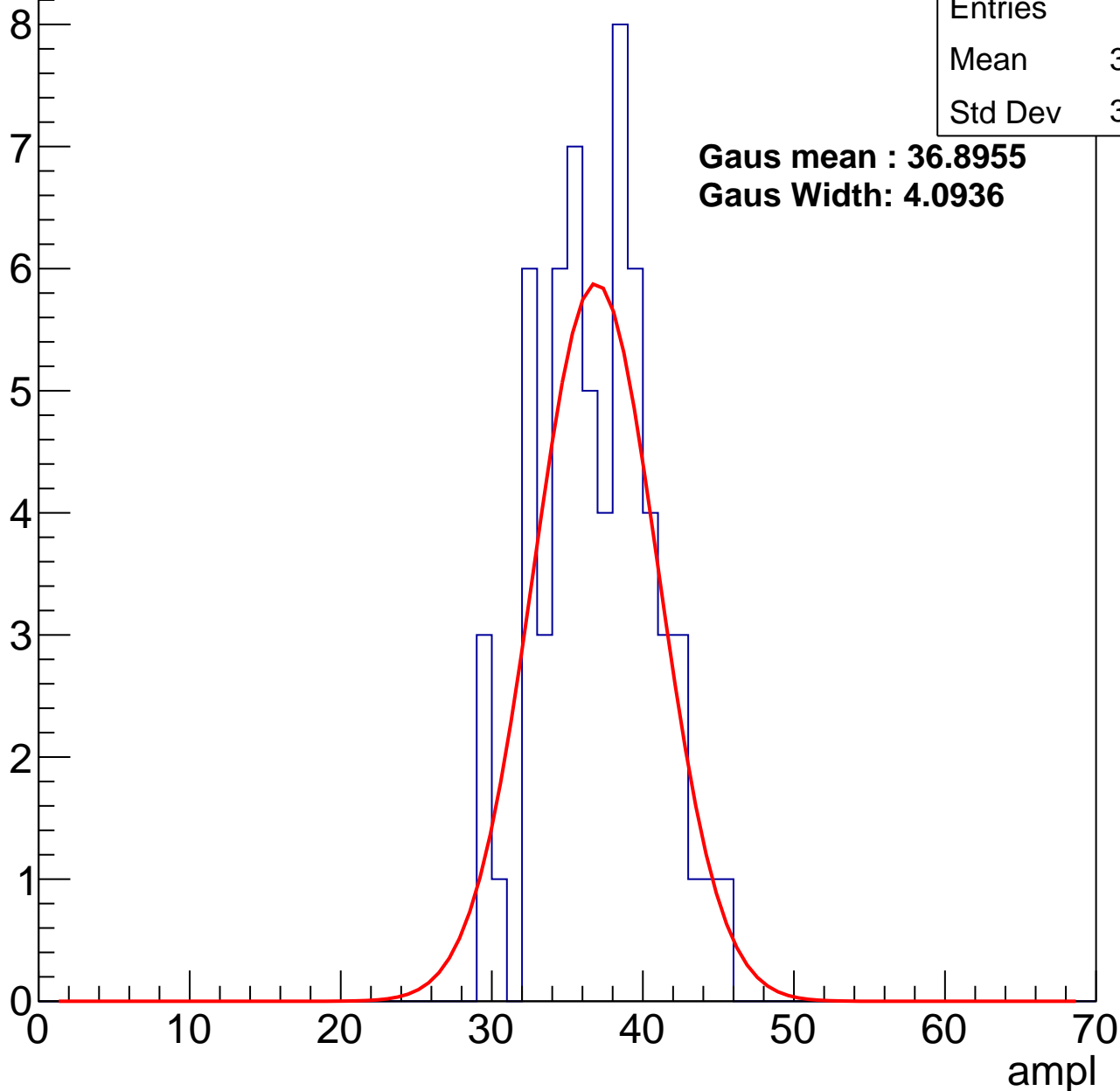
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	36.52
Std Dev	3.706

Gaus mean : 36.8955

Gaus Width: 4.0936



B0L102S, U12-ch4, adc2

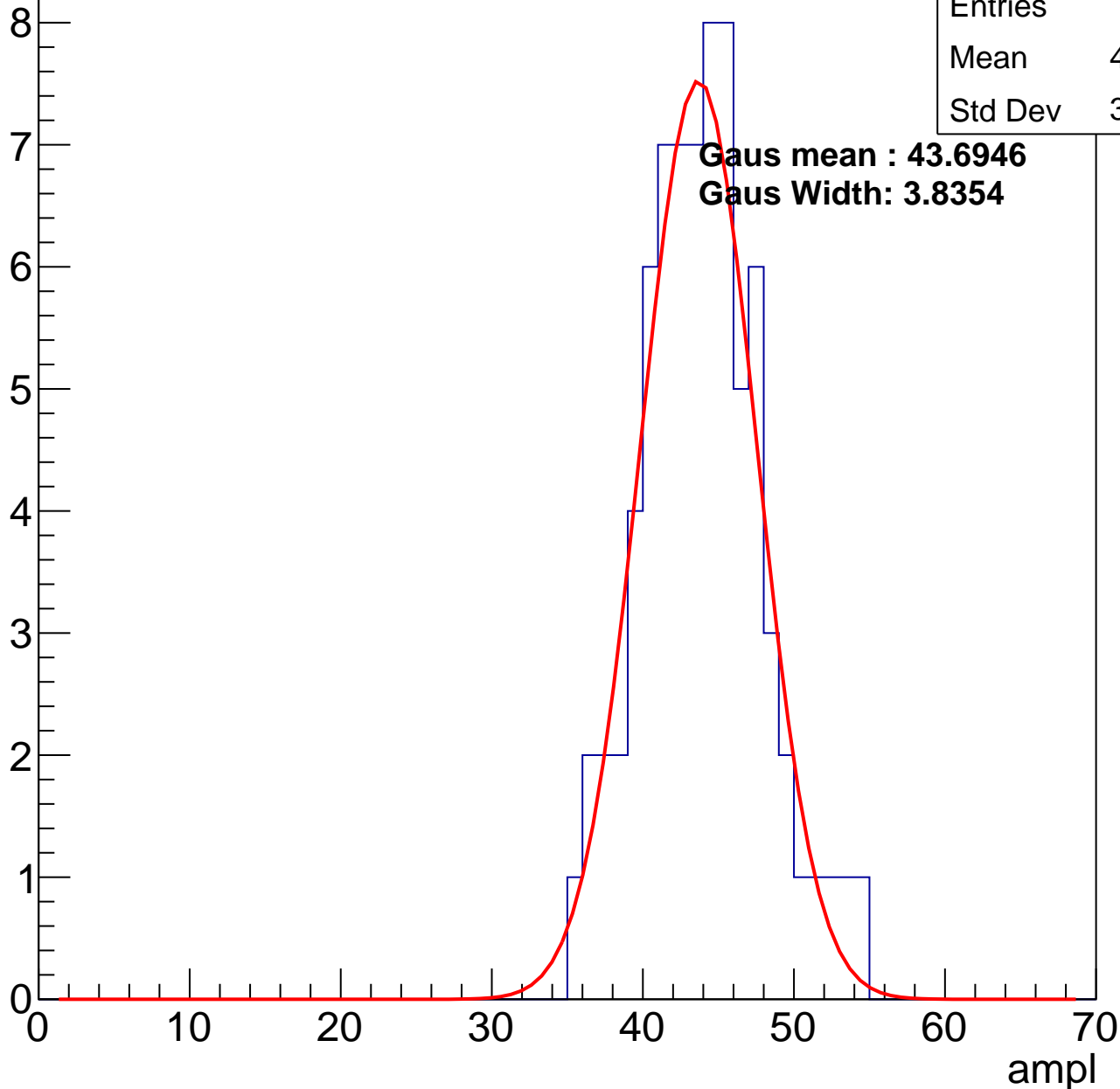
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	43.48
Std Dev	3.937

Gaus mean : 43.6946

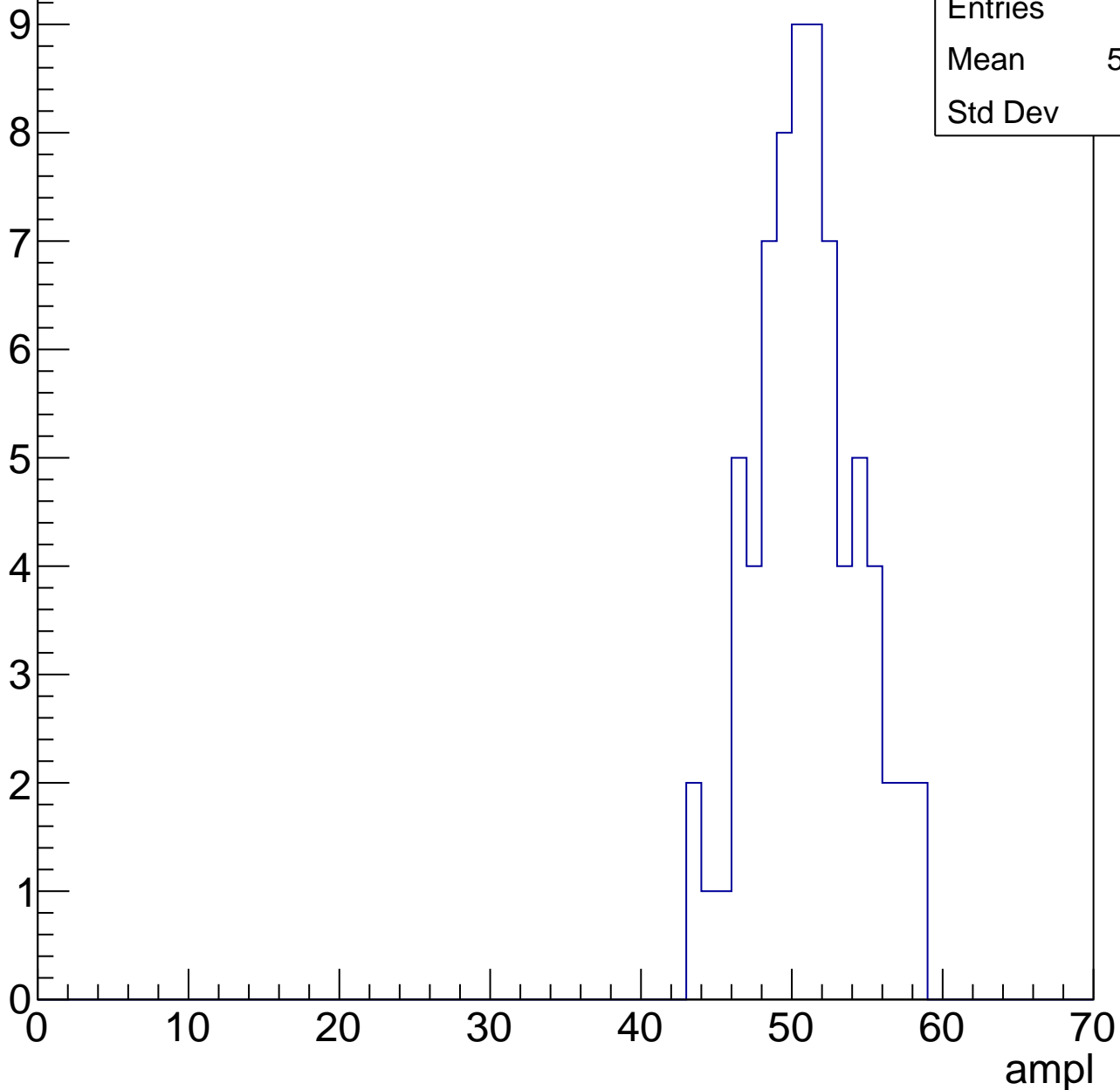
Gaus Width: 3.8354



B0L102S, U12-ch4, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	72
Mean	50.53
Std Dev	3.42

B0L102S, U12-ch4, adc4

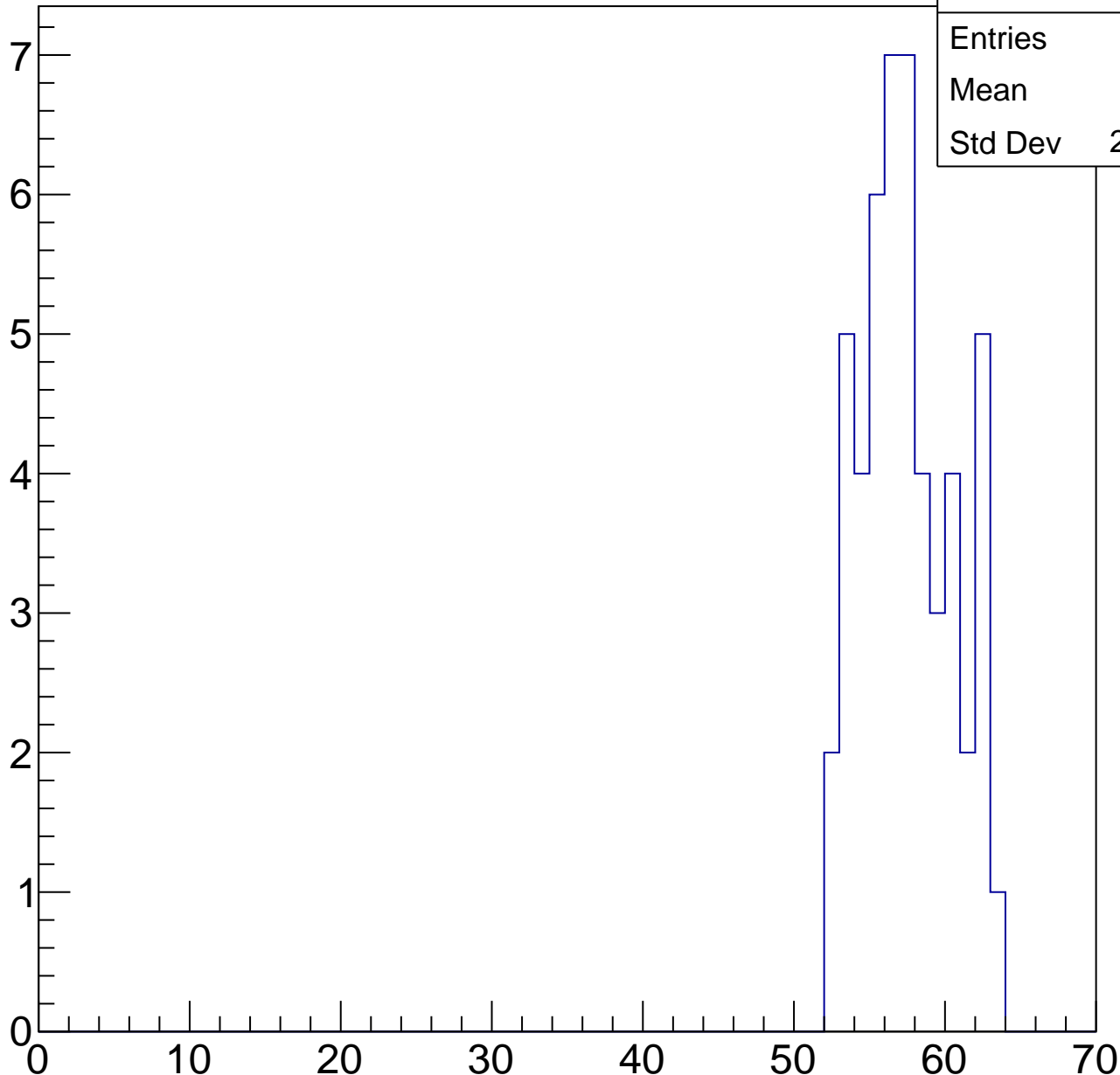
calib_packv5_042523_0143.root, FC#12, port B1

Entry

7
6
5
4
3
2
1
0

Entries	50
Mean	57
Std Dev	2.973

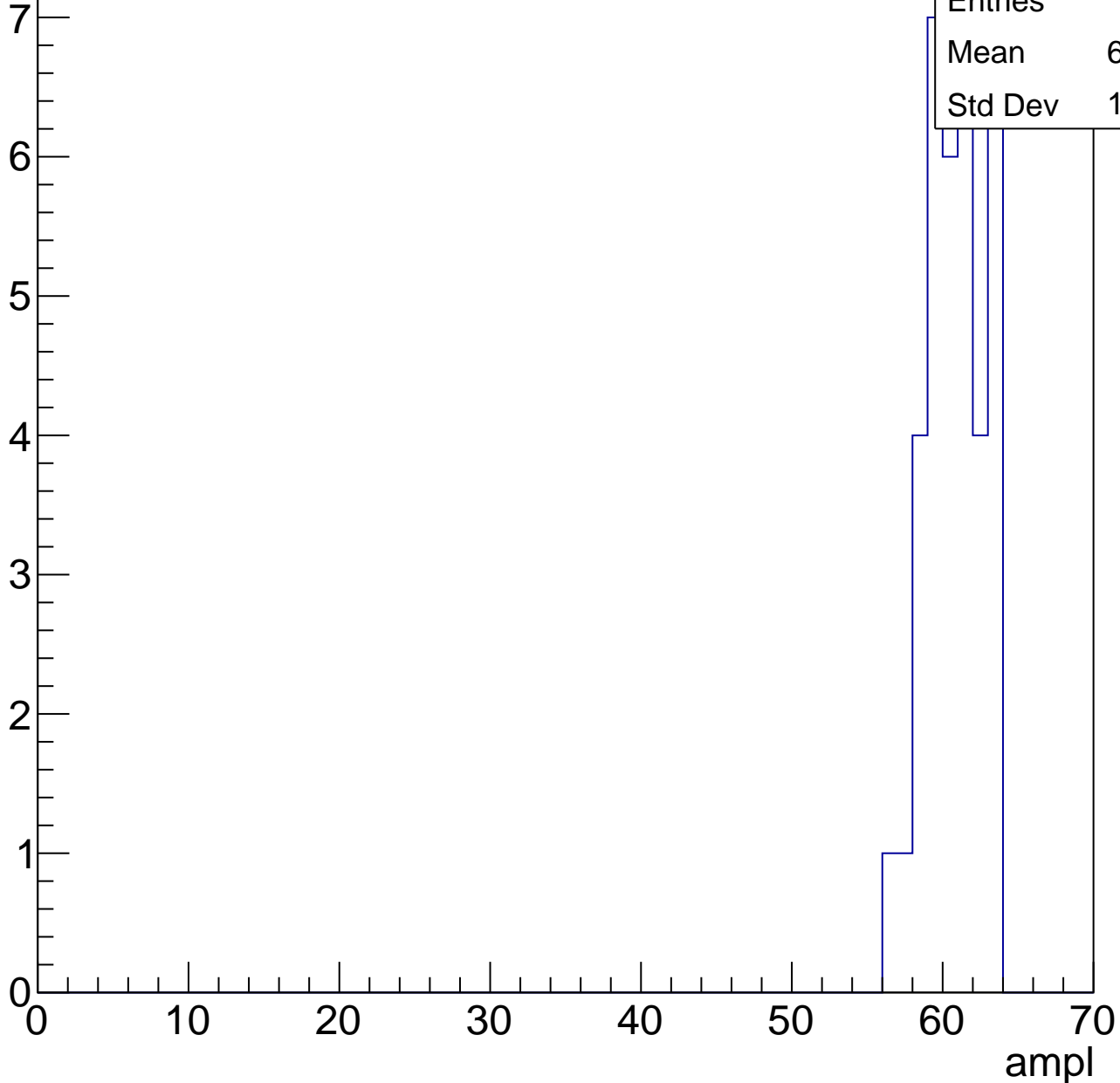
ampl



B0L102S, U12-ch4, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch4, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch4, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch5, adc0

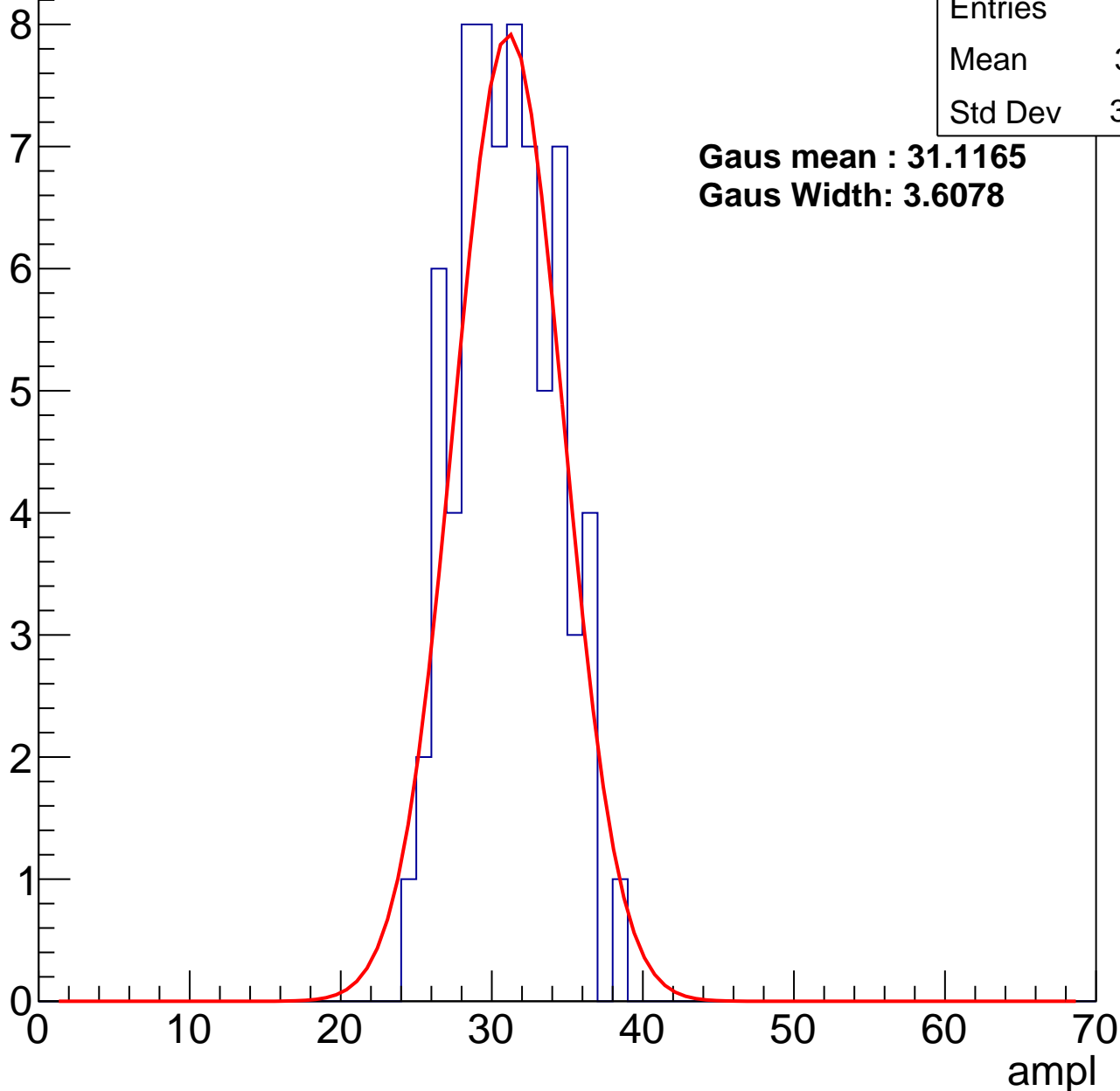
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	30.51
Std Dev	3.175

Gaus mean : 31.1165

Gaus Width: 3.6078



B0L102S, U12-ch5, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	37.22
Std Dev	3.535

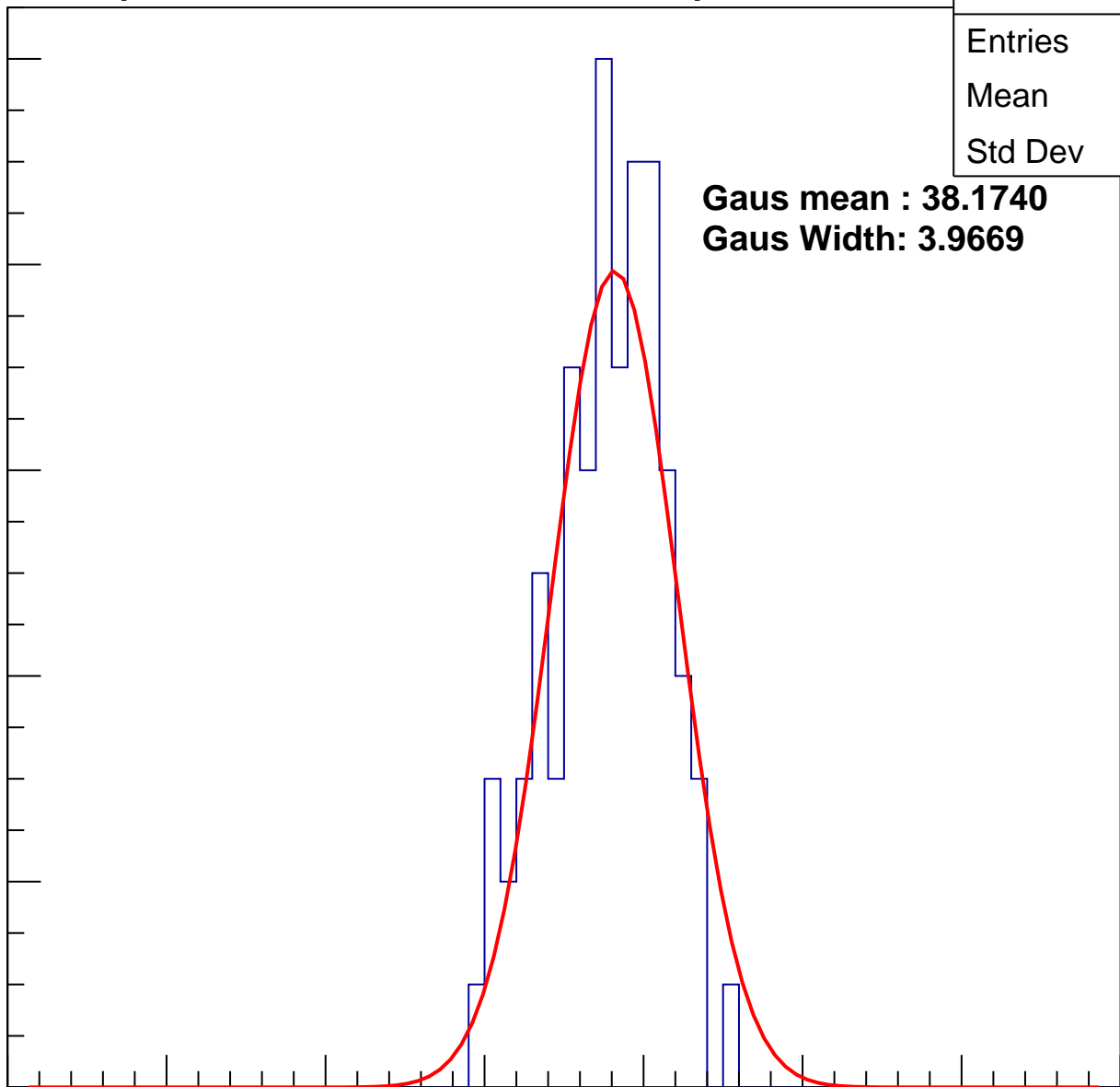
Gaus mean : 38.1740

Gaus Width: 3.9669

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U12-ch5, adc2

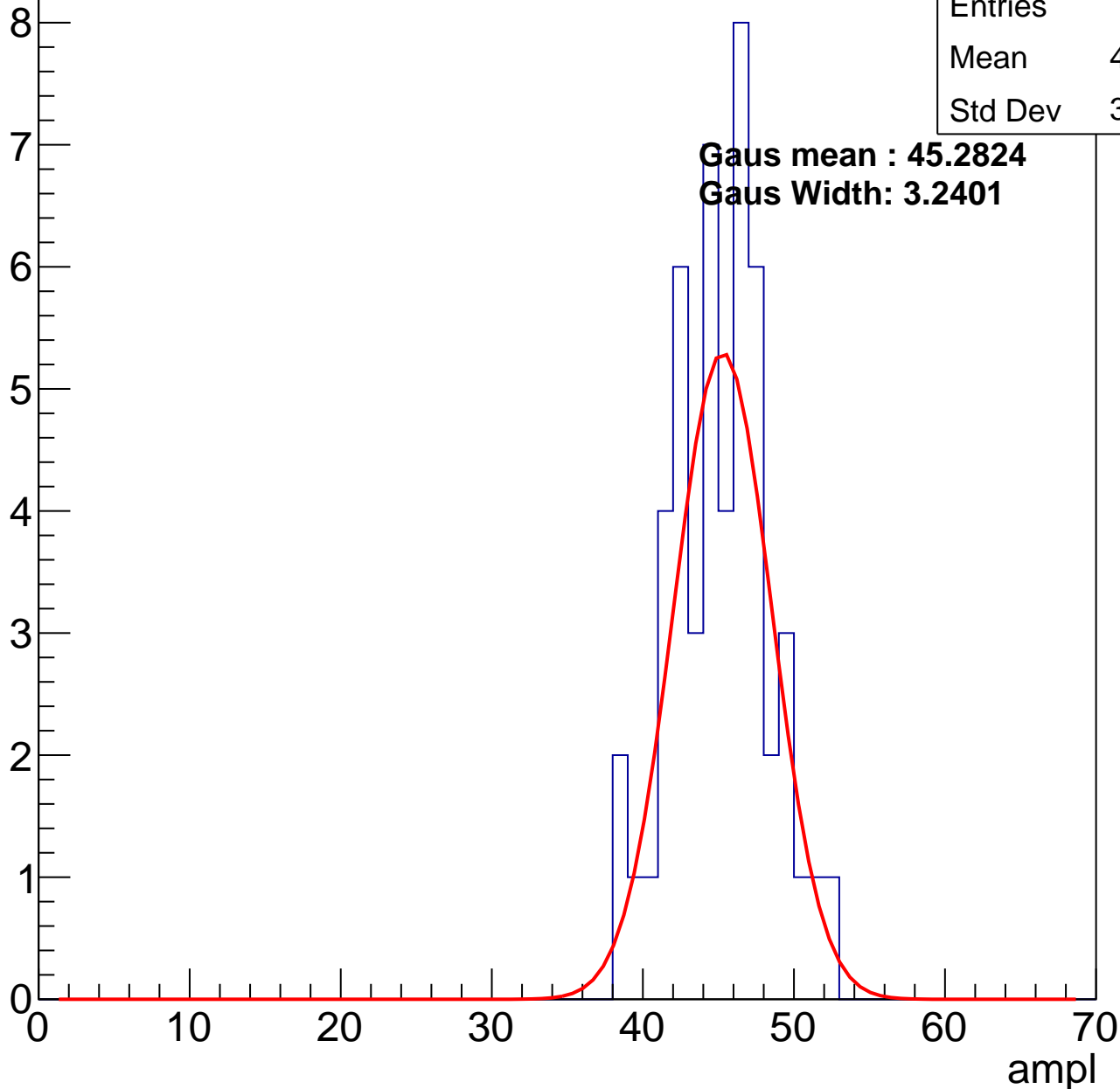
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	44.68
Std Dev	3.159

Gaus mean : 45.2824

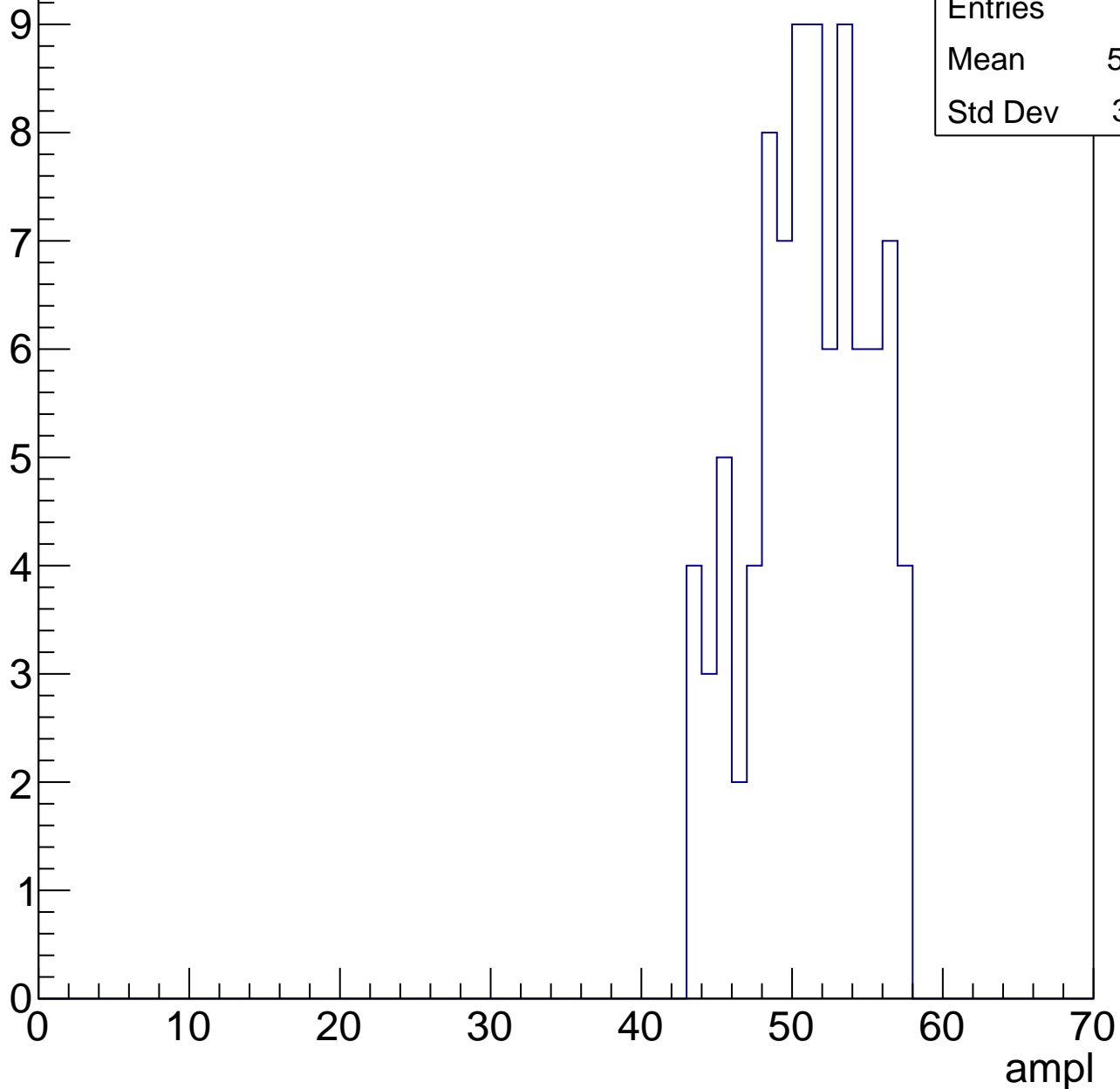
Gaus Width: 3.2401



B0L102S, U12-ch5, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



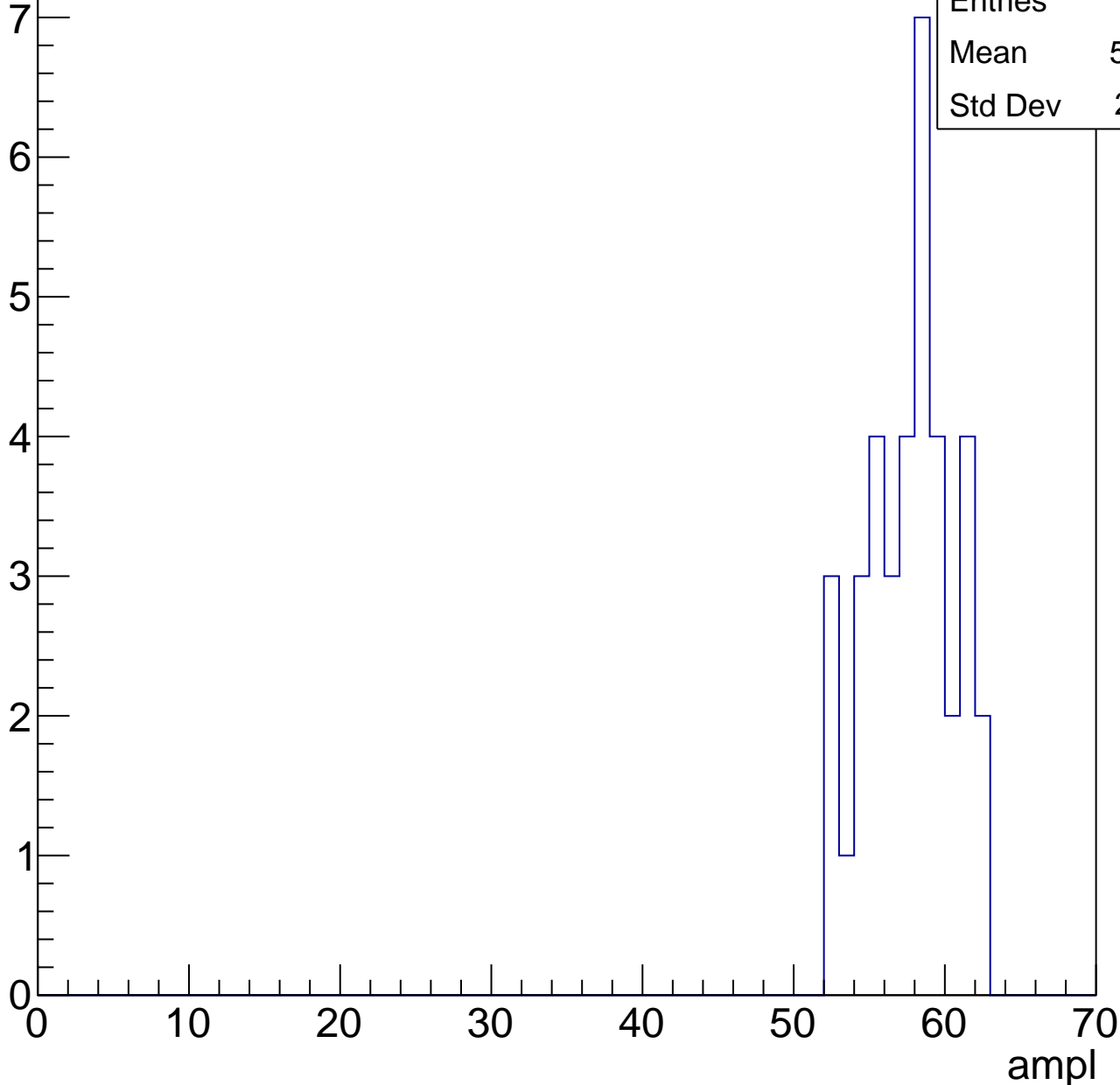
Entries	89
Mean	50.65
Std Dev	3.831

B0L102S, U12-ch5, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	37
Mean	57.22
Std Dev	2.801

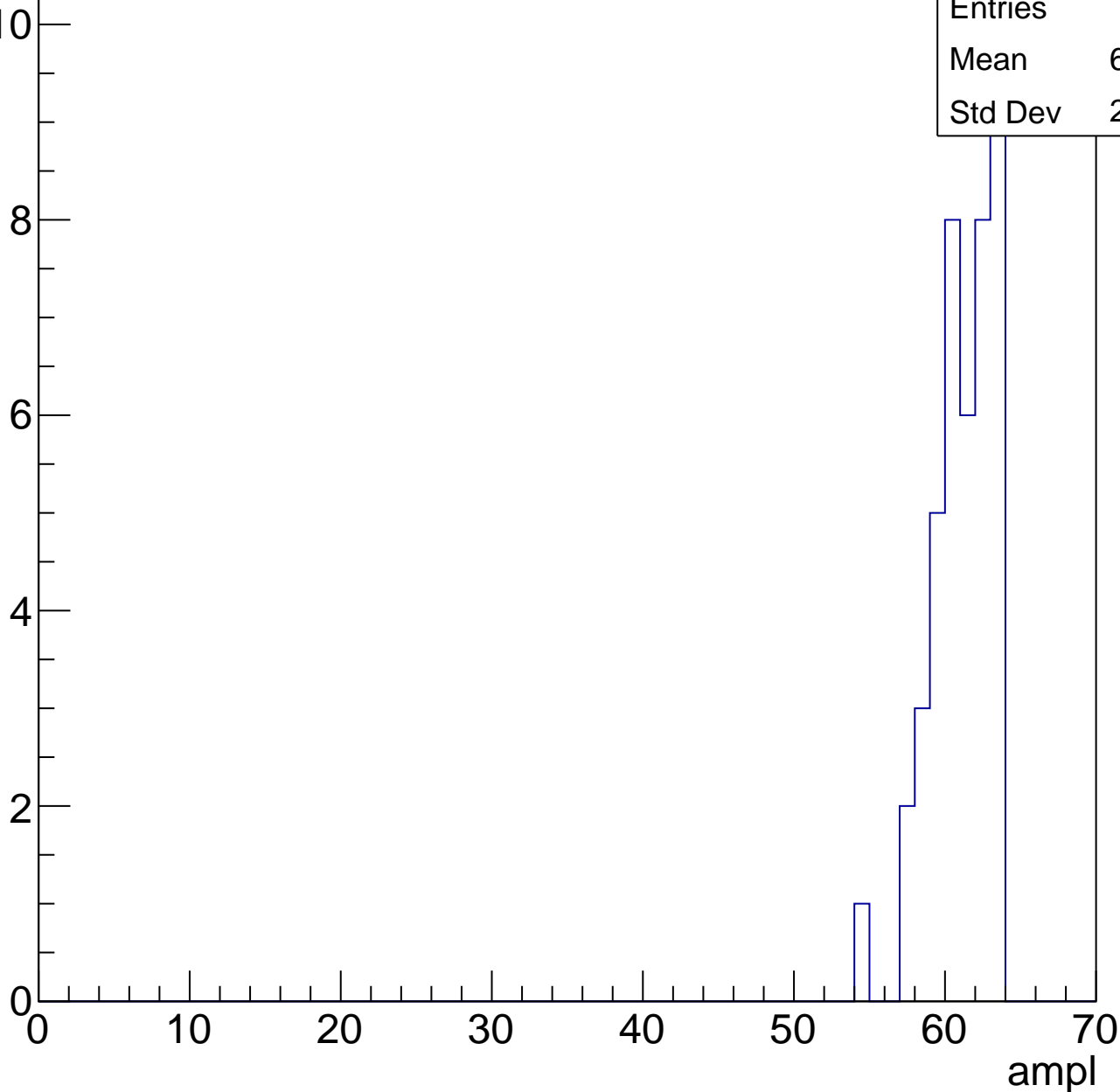


B0L102S, U12-ch5, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

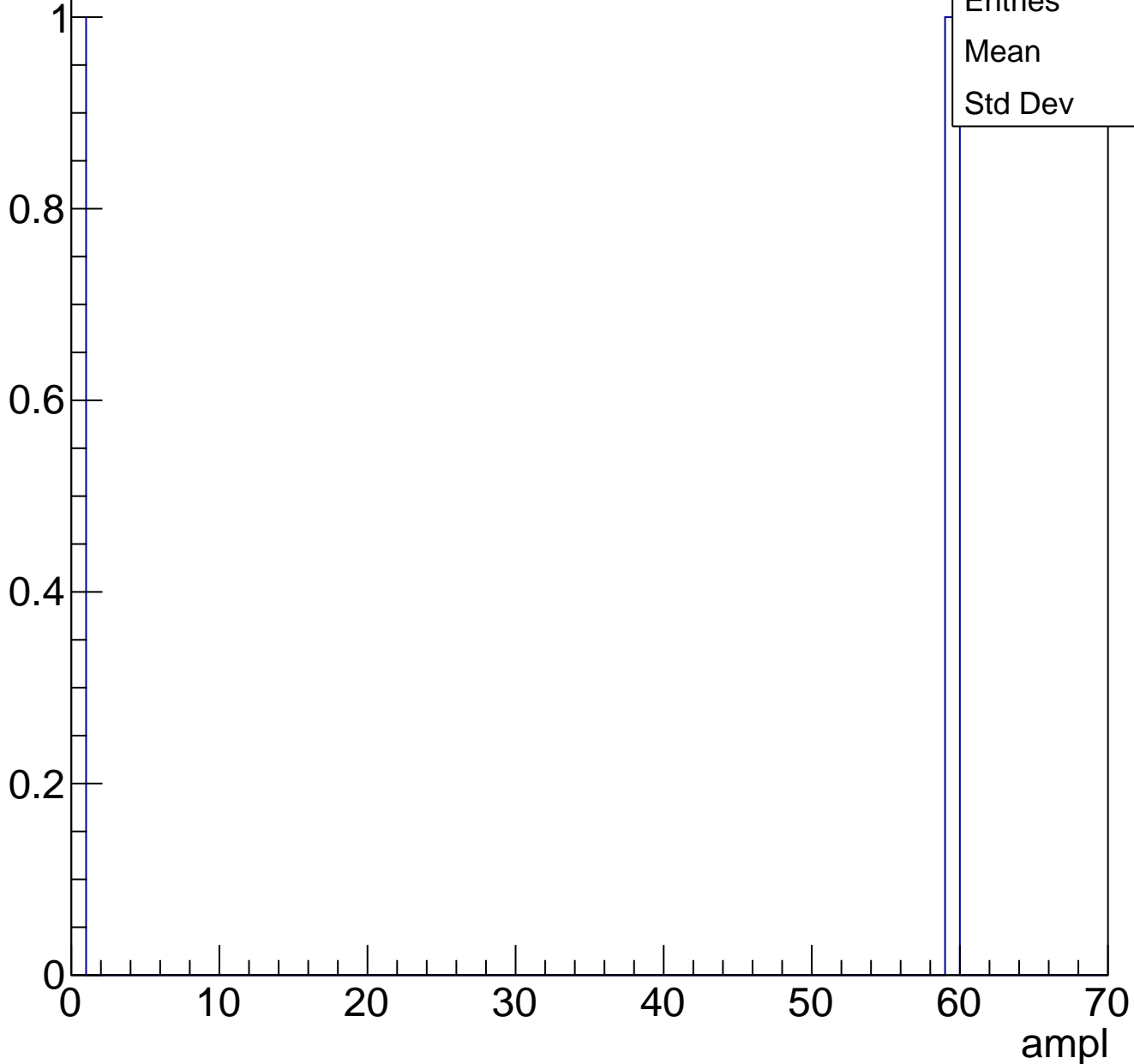
Entries	43
Mean	60.67
Std Dev	2.043



B0L102S, U12-ch5, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch5, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch6, adc0

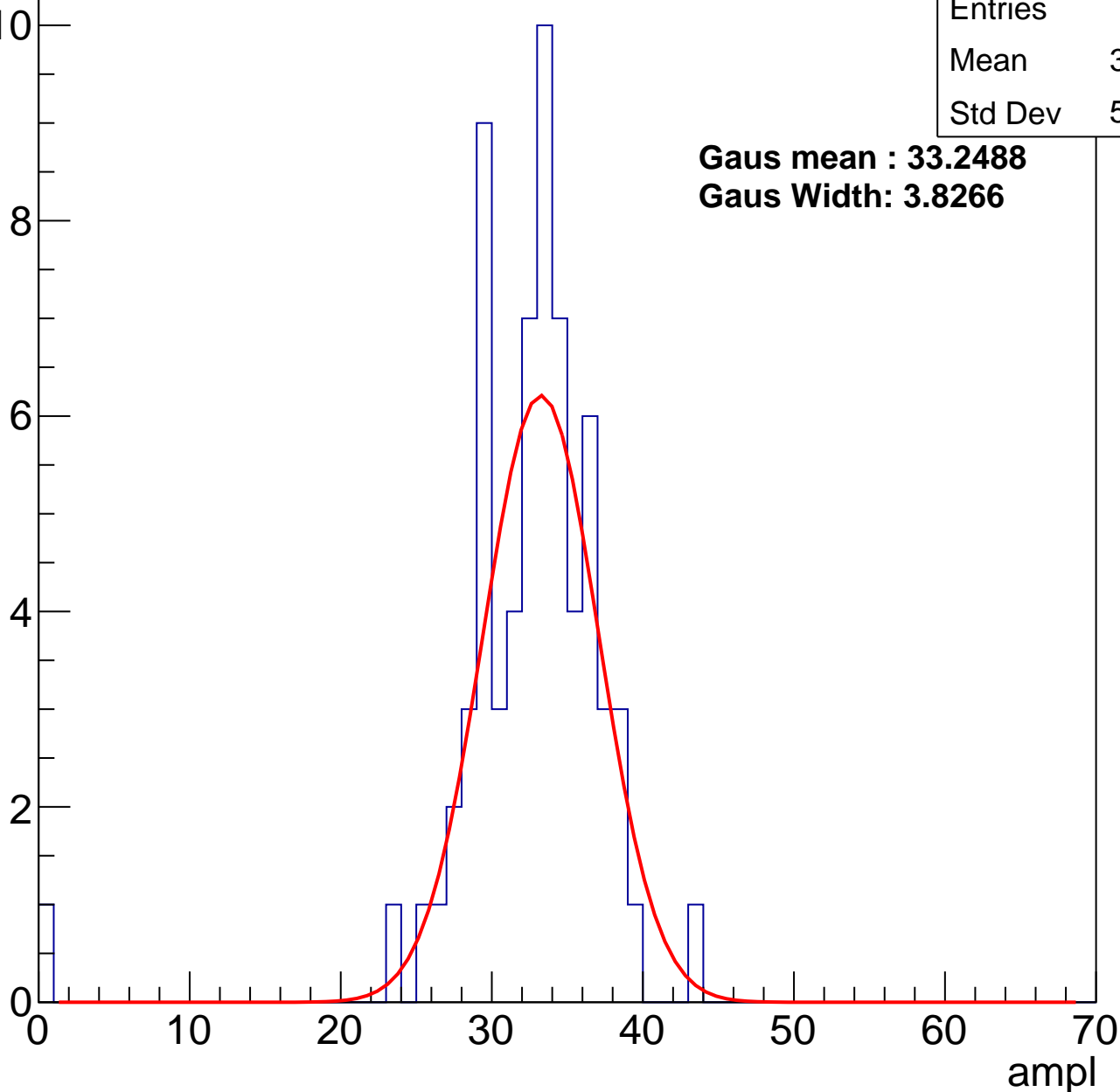
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	31.97
Std Dev	5.336

Gaus mean : 33.2488

Gaus Width: 3.8266



B0L102S, U12-ch6, adc1

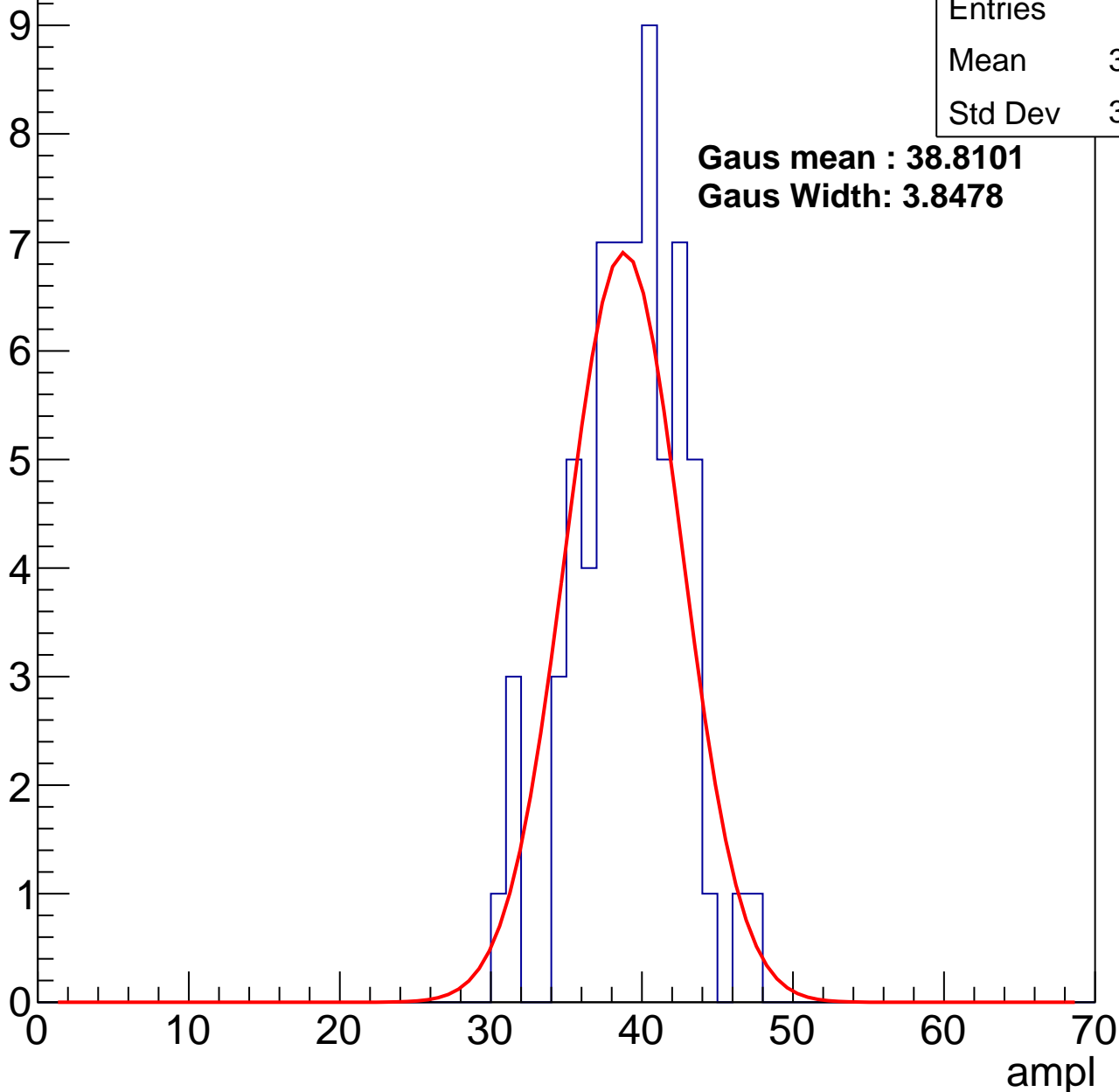
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	38.68
Std Dev	3.487

Gaus mean : 38.8101

Gaus Width: 3.8478



B0L102S, U12-ch6, adc2

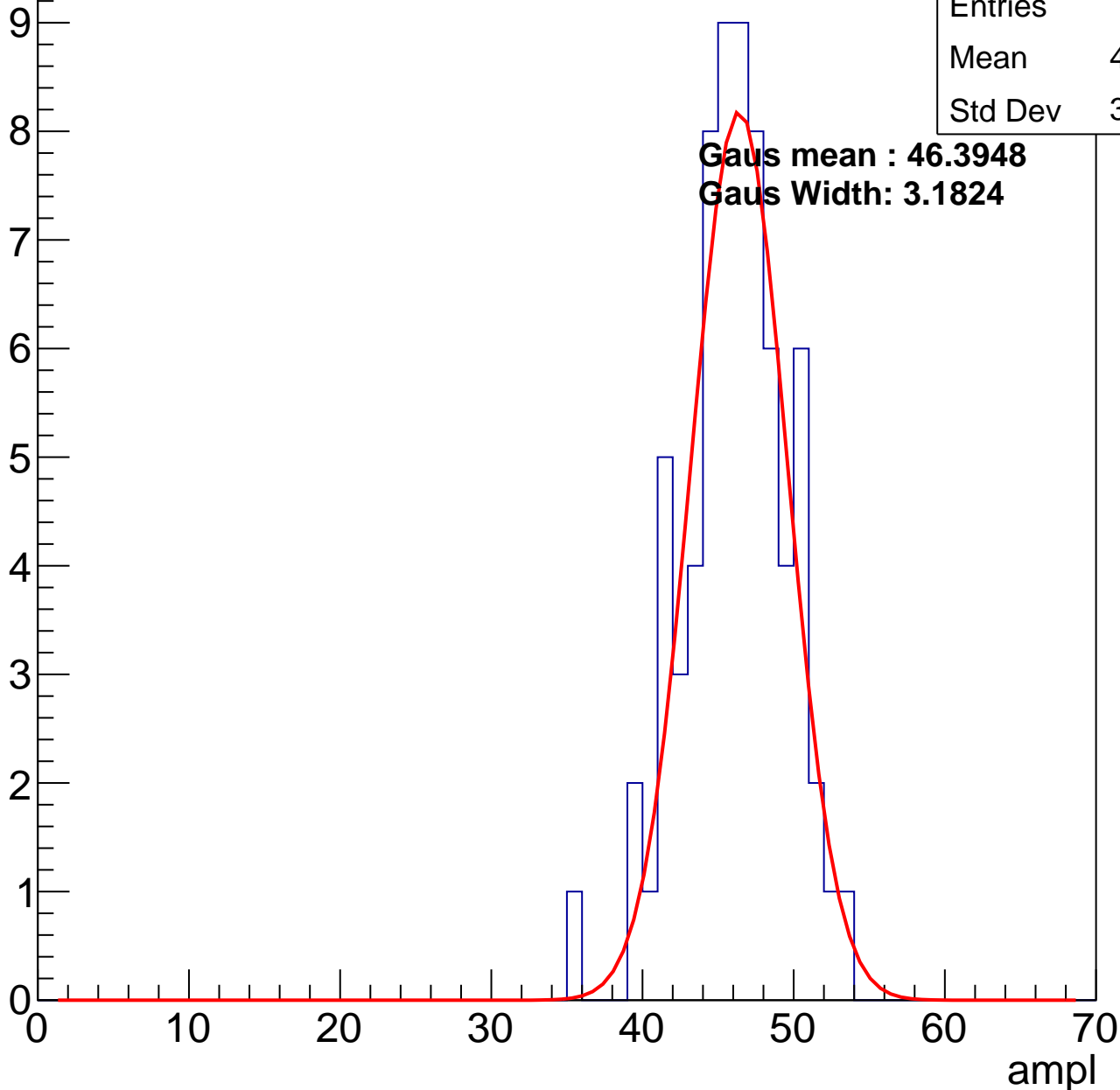
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	45.63
Std Dev	3.369

Gaus mean : 46.3948

Gaus Width: 3.1824

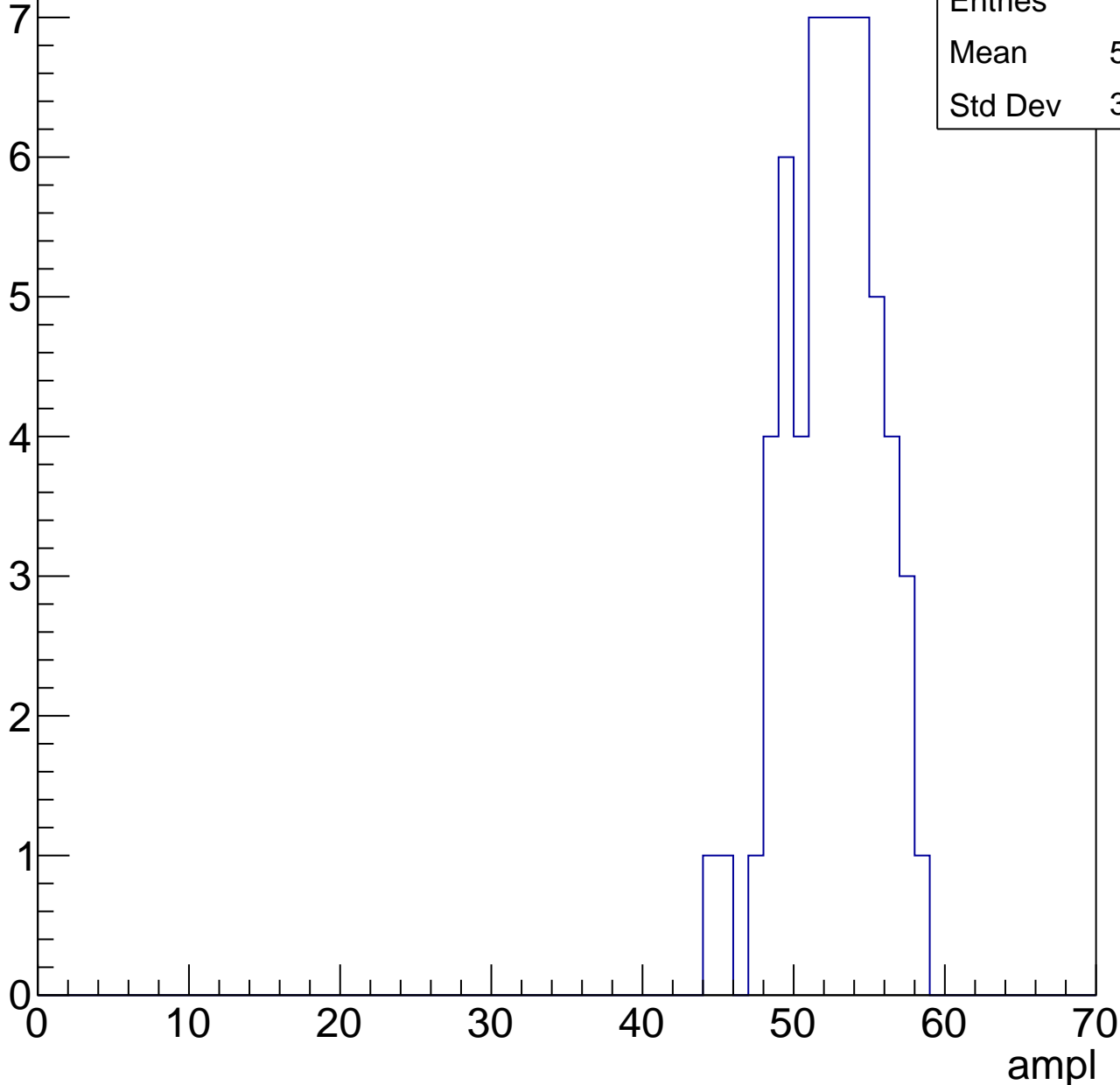


B0L102S, U12-ch6, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	52.07
Std Dev	3.034

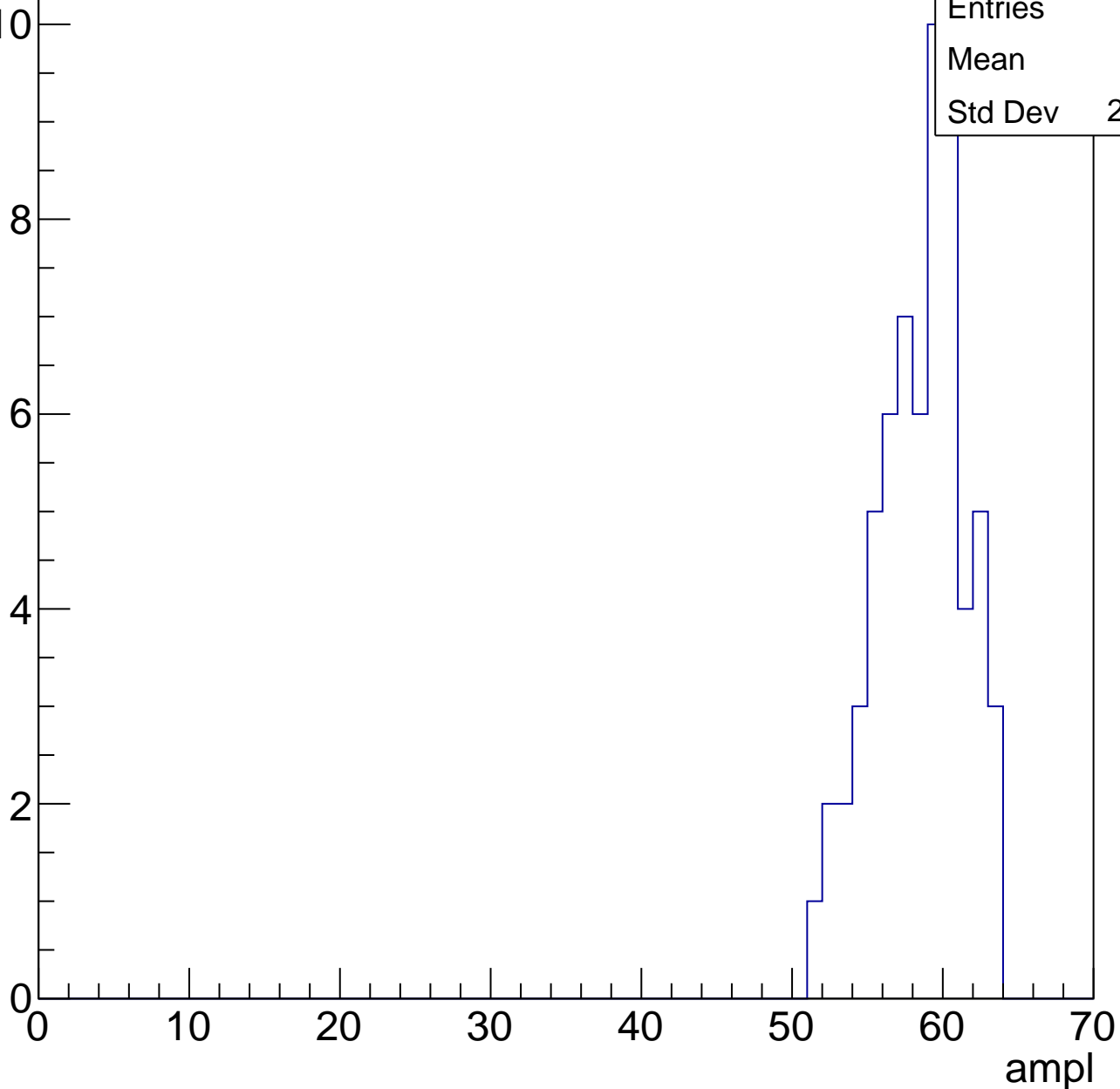


B0L102S, U12-ch6, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	58
Std Dev	2.906

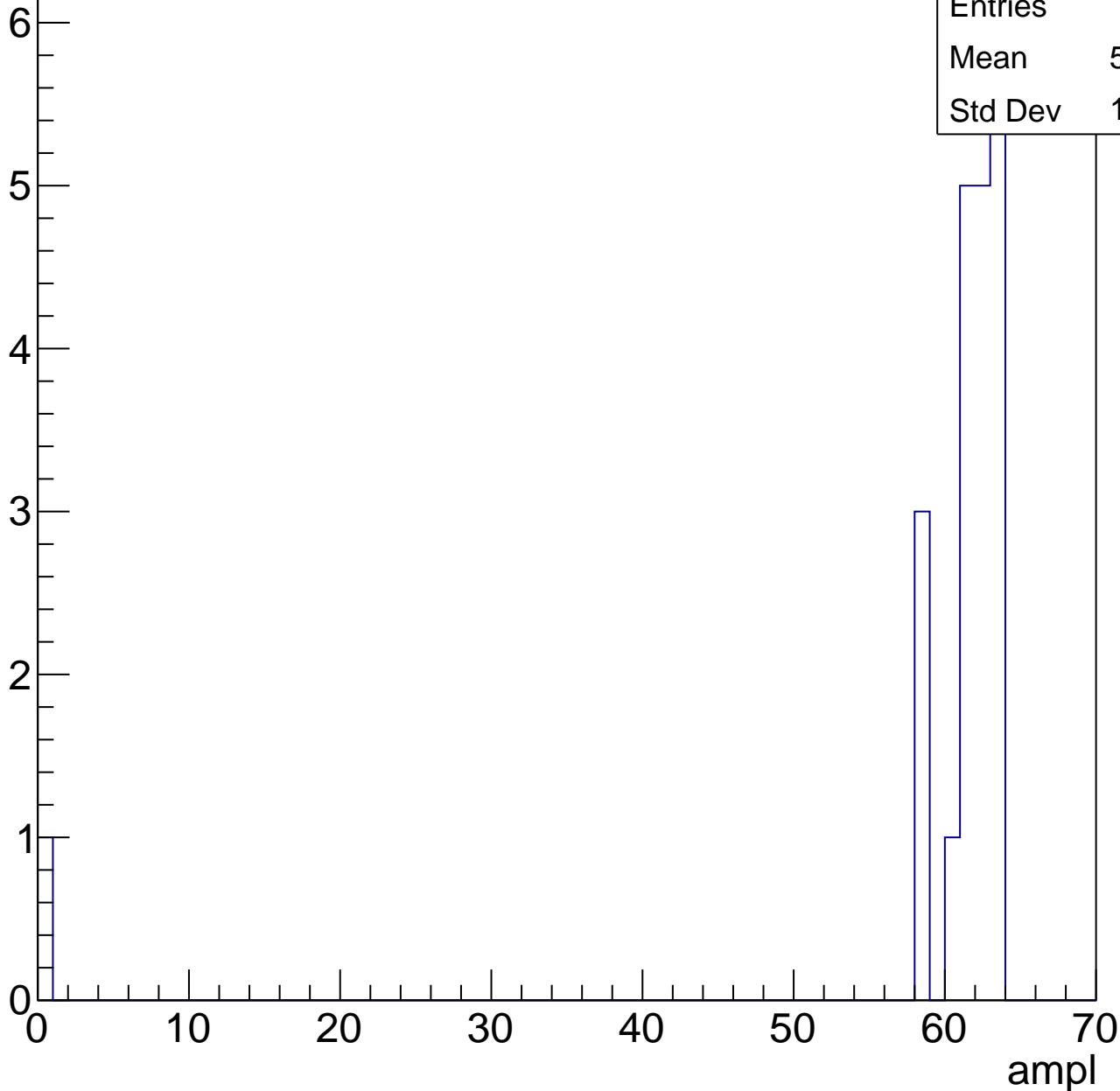


B0L102S, U12-ch6, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	21
Mean	58.43
Std Dev	13.16



B0L102S, U12-ch6, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch6, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch7, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entry

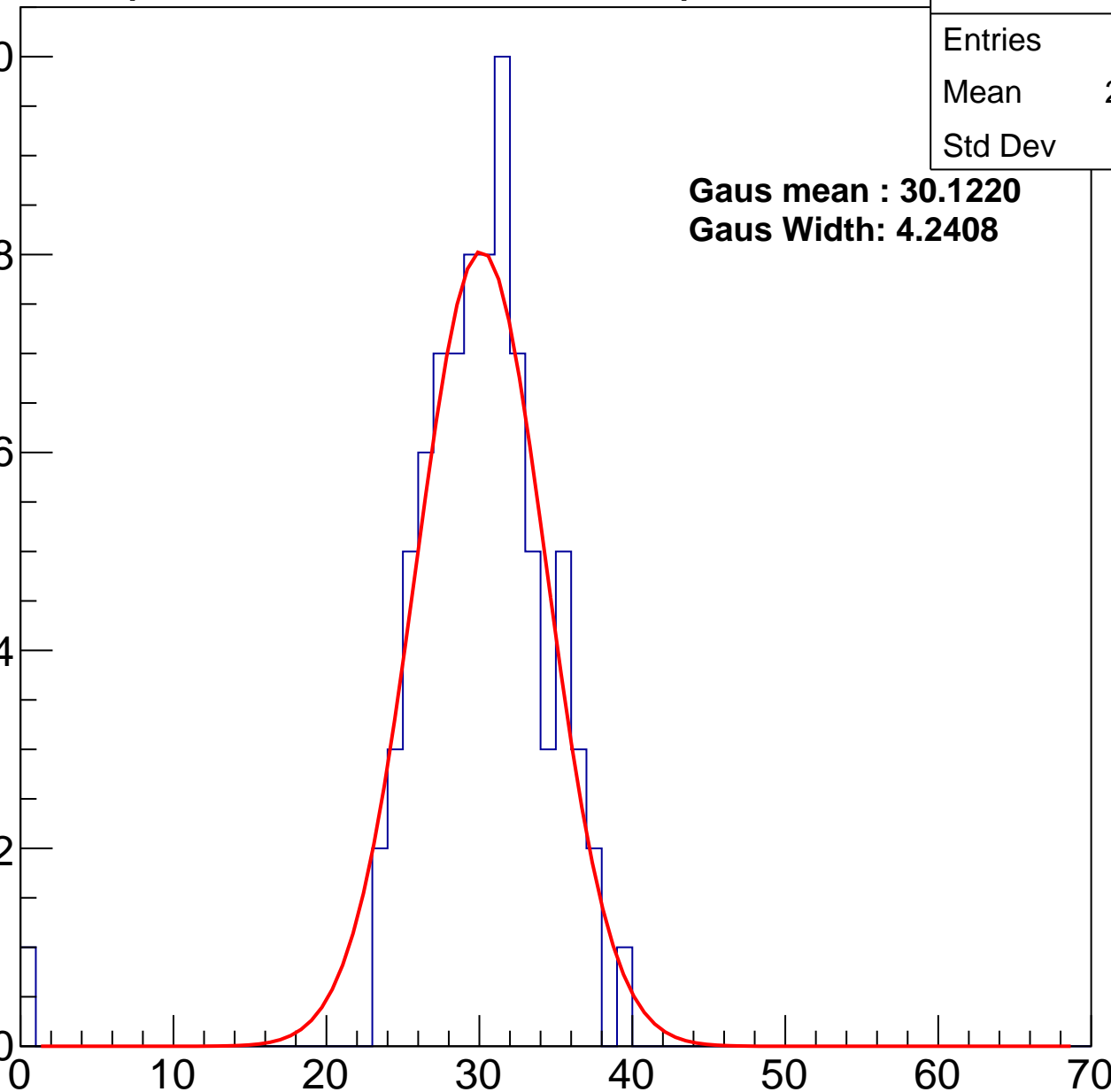
Entries	83
Mean	29.55
Std Dev	4.85

Gaus mean : 30.1220

Gaus Width: 4.2408

10
8
6
4
2
0

ampl



B0L102S, U12-ch7, adc1

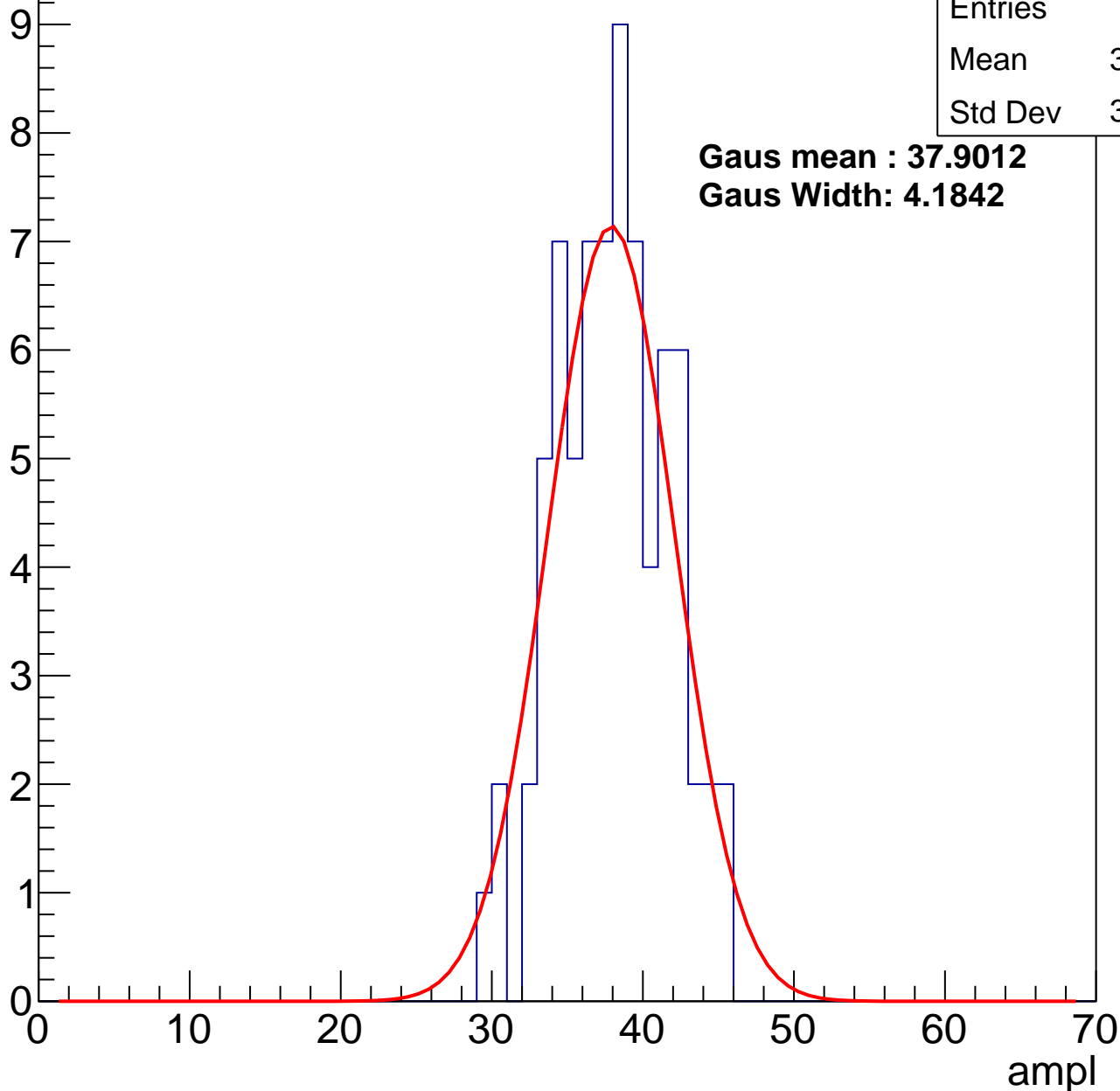
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	37.55
Std Dev	3.632

Gaus mean : 37.9012

Gaus Width: 4.1842



B0L102S, U12-ch7, adc2

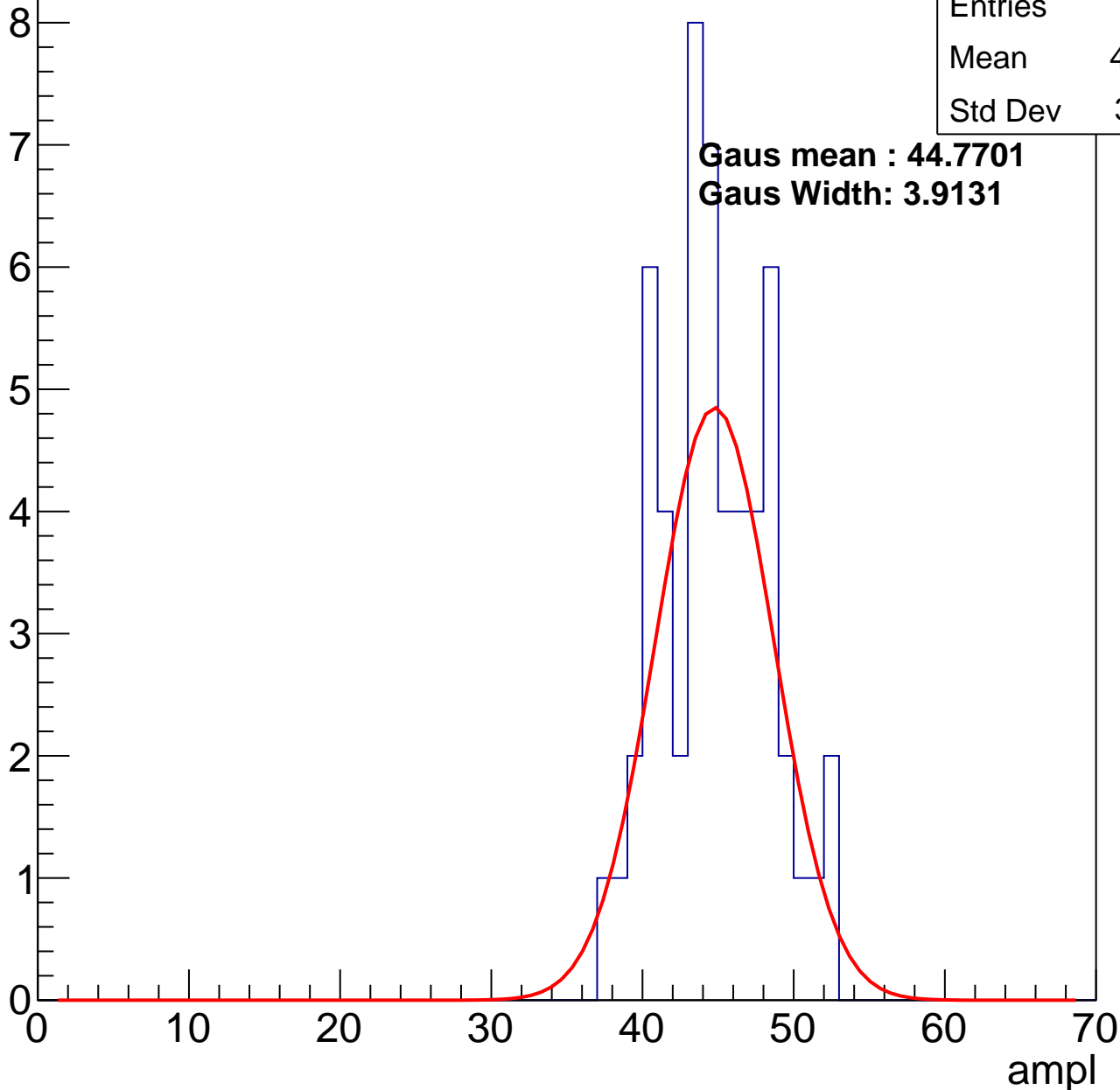
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	44.29
Std Dev	3.551

Gaus mean : 44.7701

Gaus Width: 3.9131

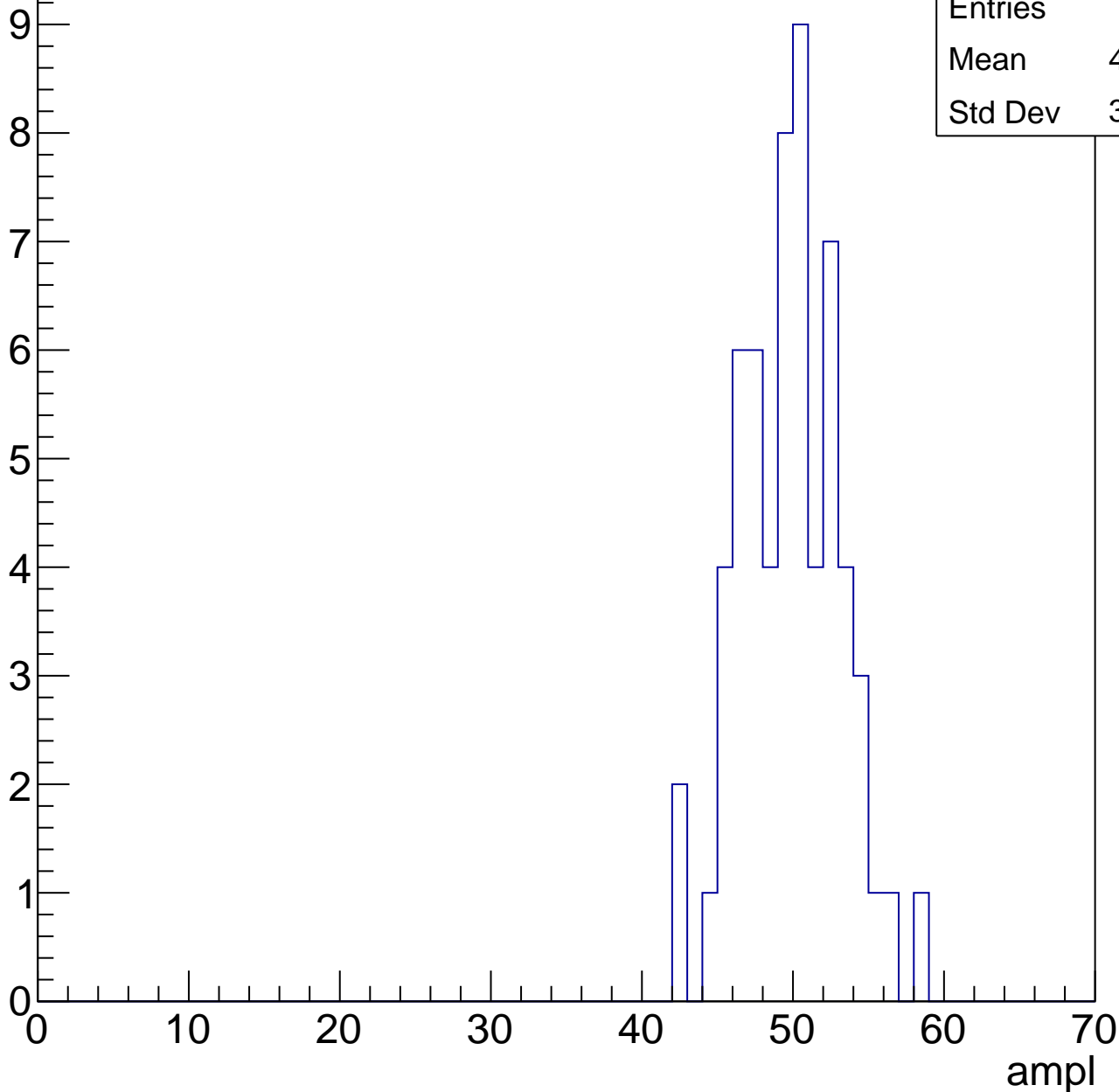


B0L102S, U12-ch7, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

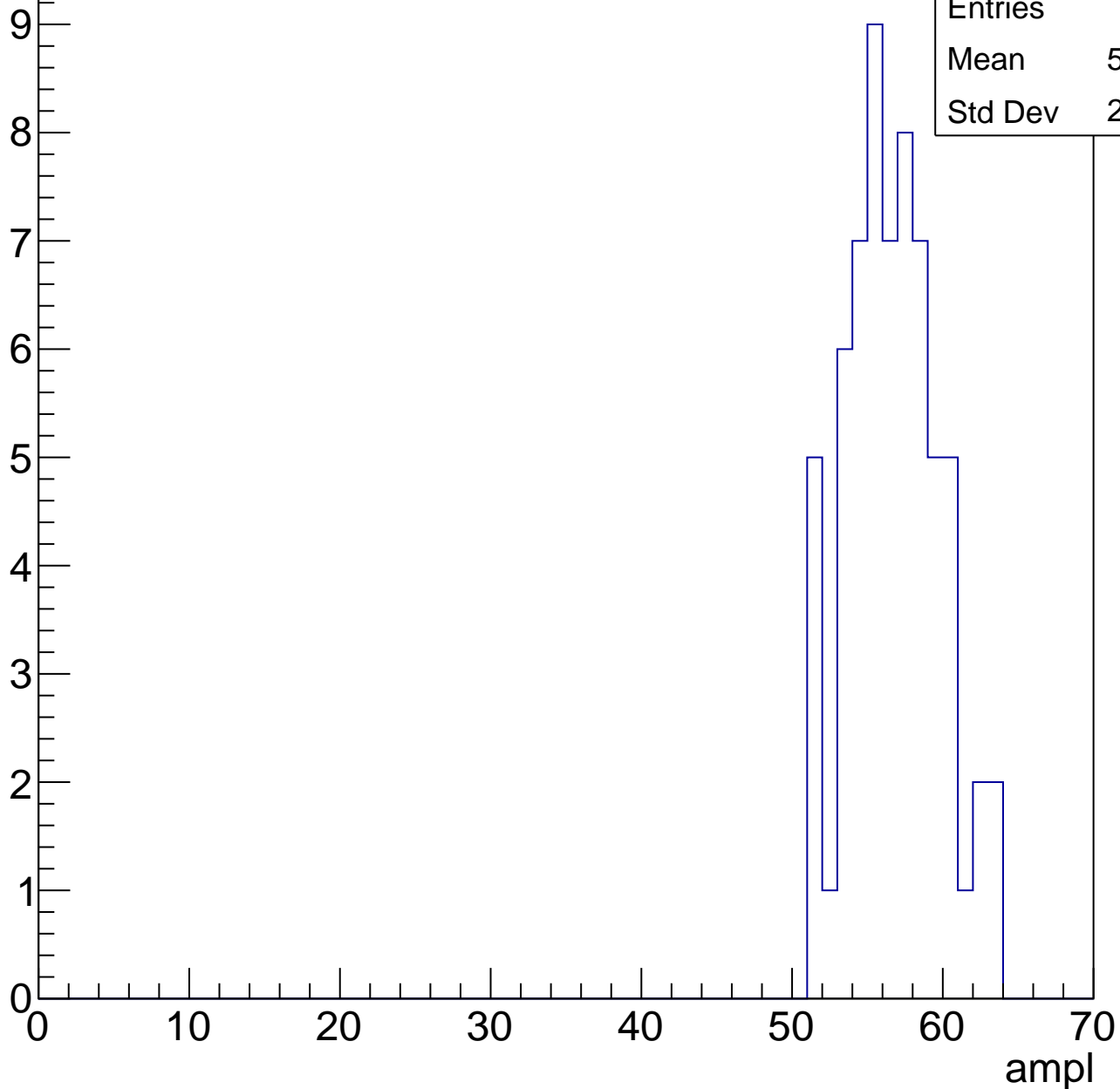
Entries	61
Mean	49.36
Std Dev	3.275



B0L102S, U12-ch7, adc4

calib_packv5_042523_0143.root, FC#12, port B1

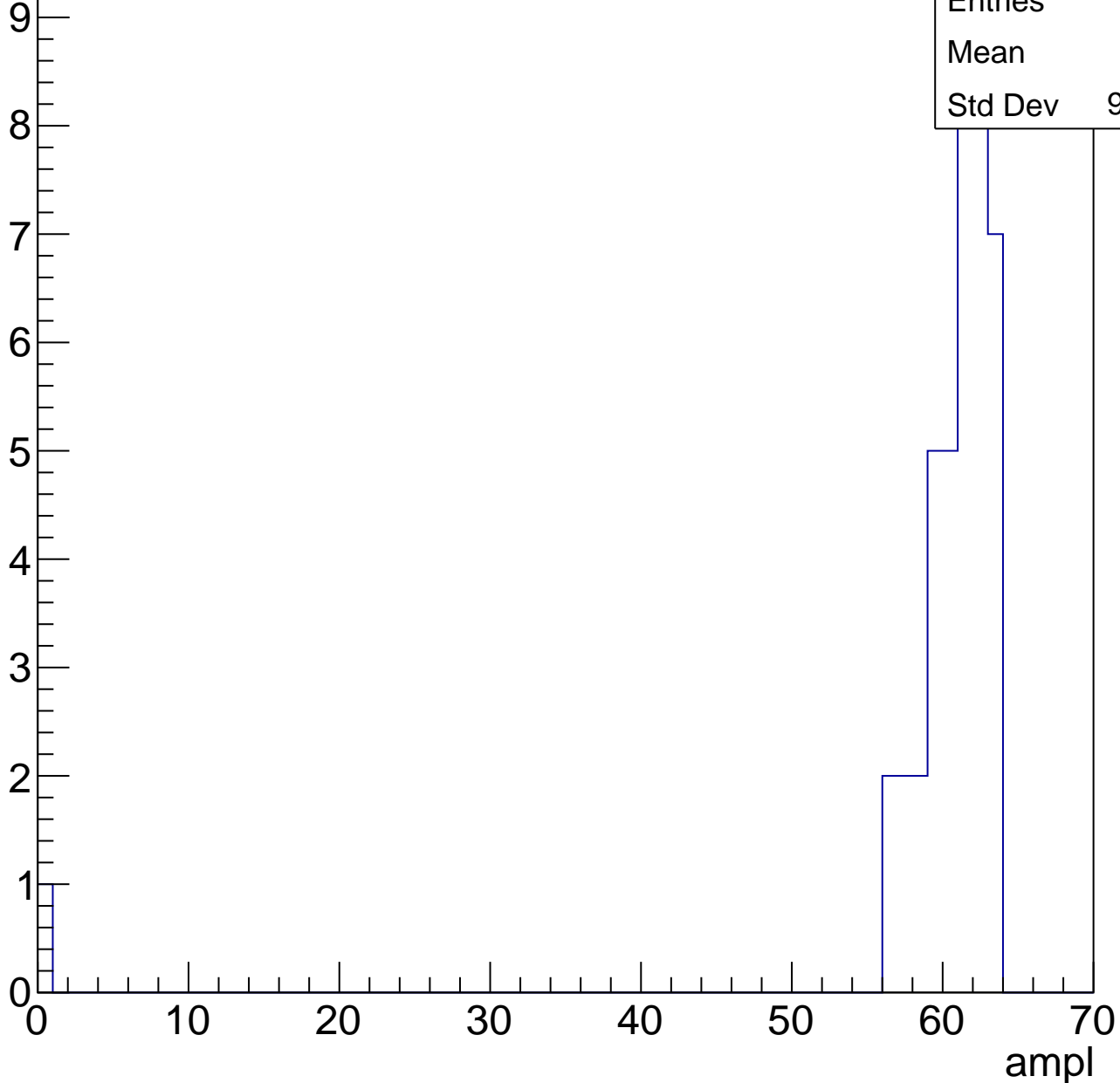
Entry



B0L102S, U12-ch7, adc5

calib_packv5_042523_0143.root, FC#12, port B1

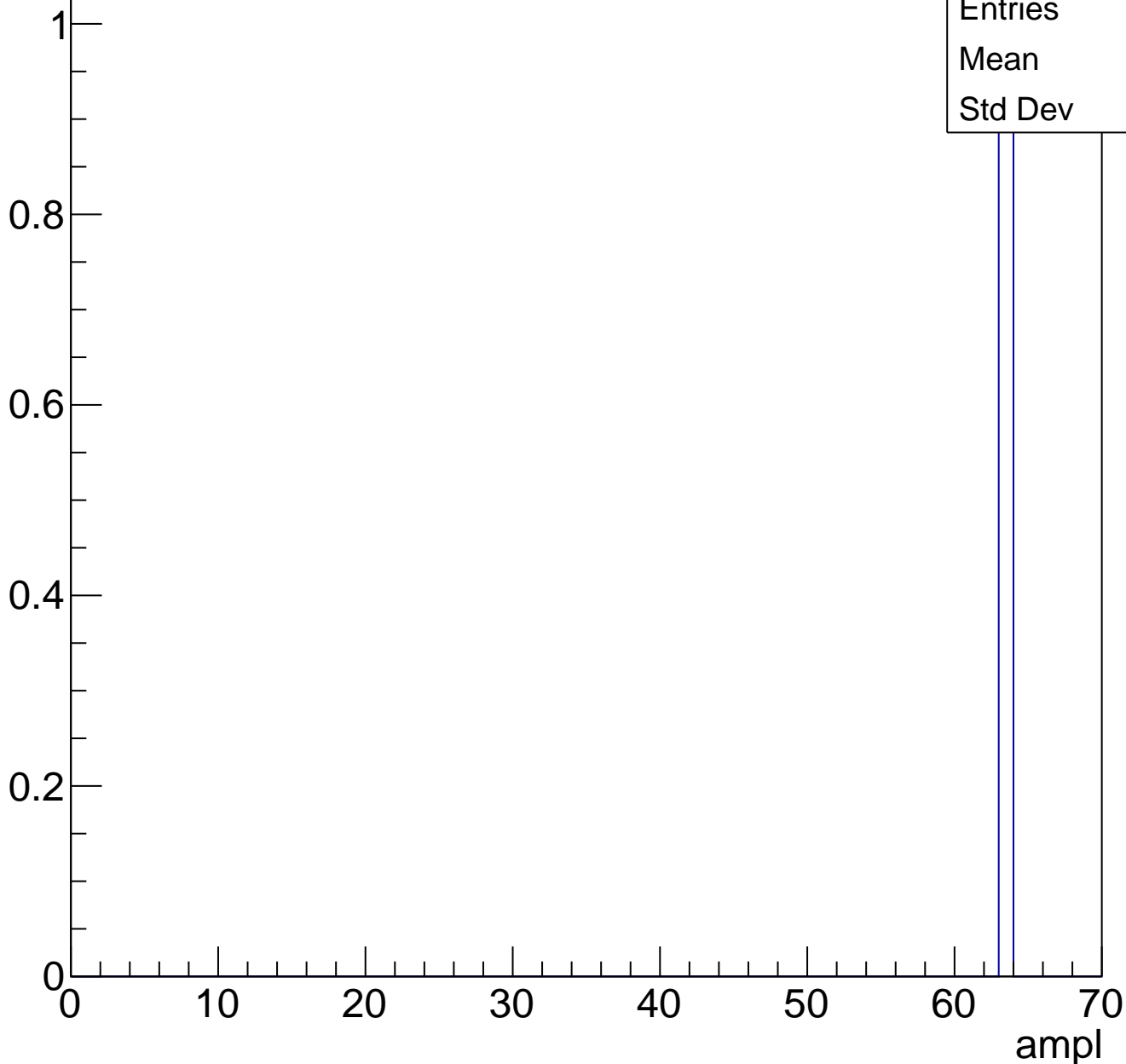
Entry



B0L102S, U12-ch7, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch7, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch8, adc0

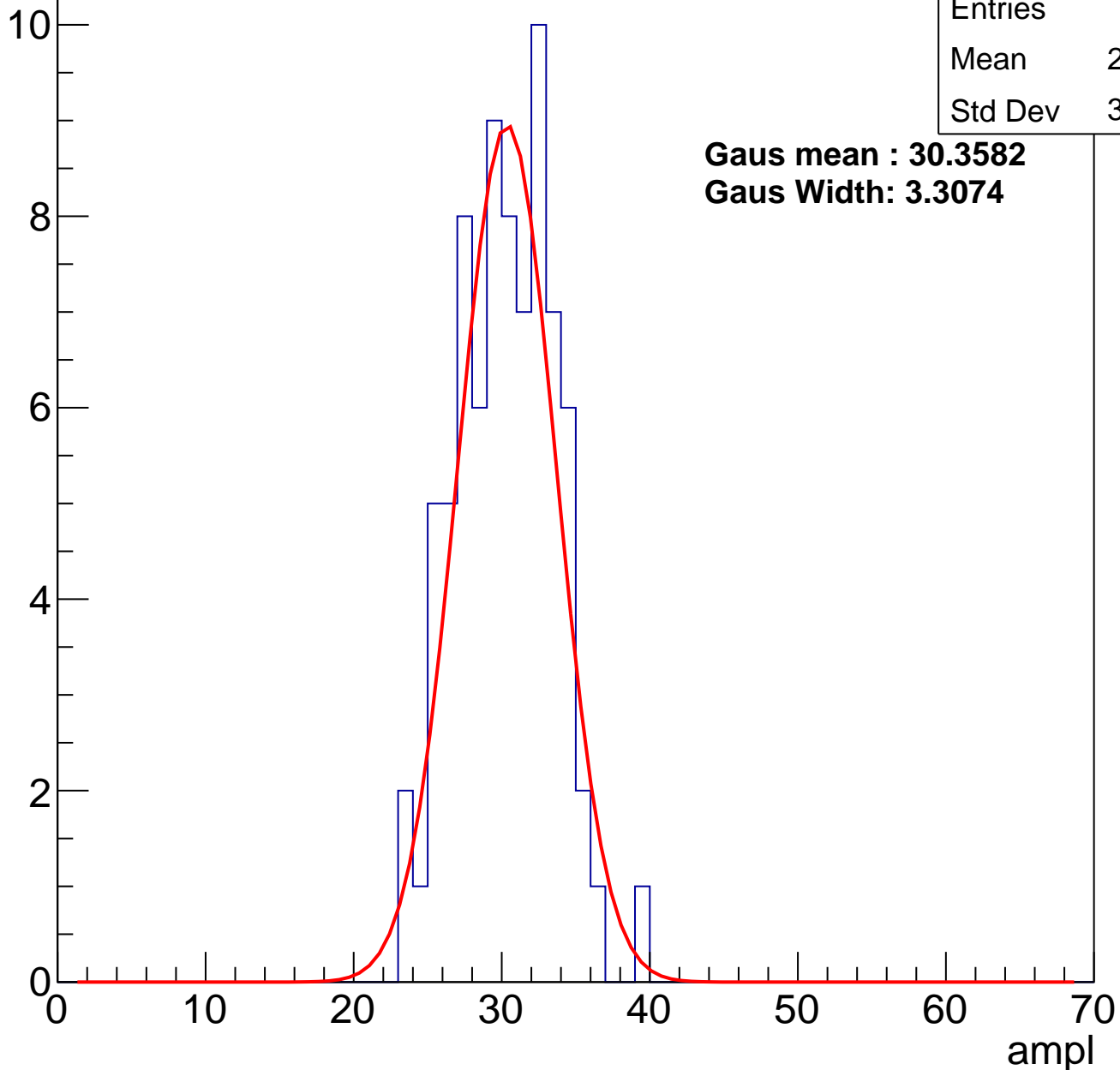
calib_packv5_042523_0143.root, FC#12, port B1

Entries	78
Mean	29.83
Std Dev	3.232

Gaus mean : 30.3582

Gaus Width: 3.3074

Entry



B0L102S, U12-ch8, adc1

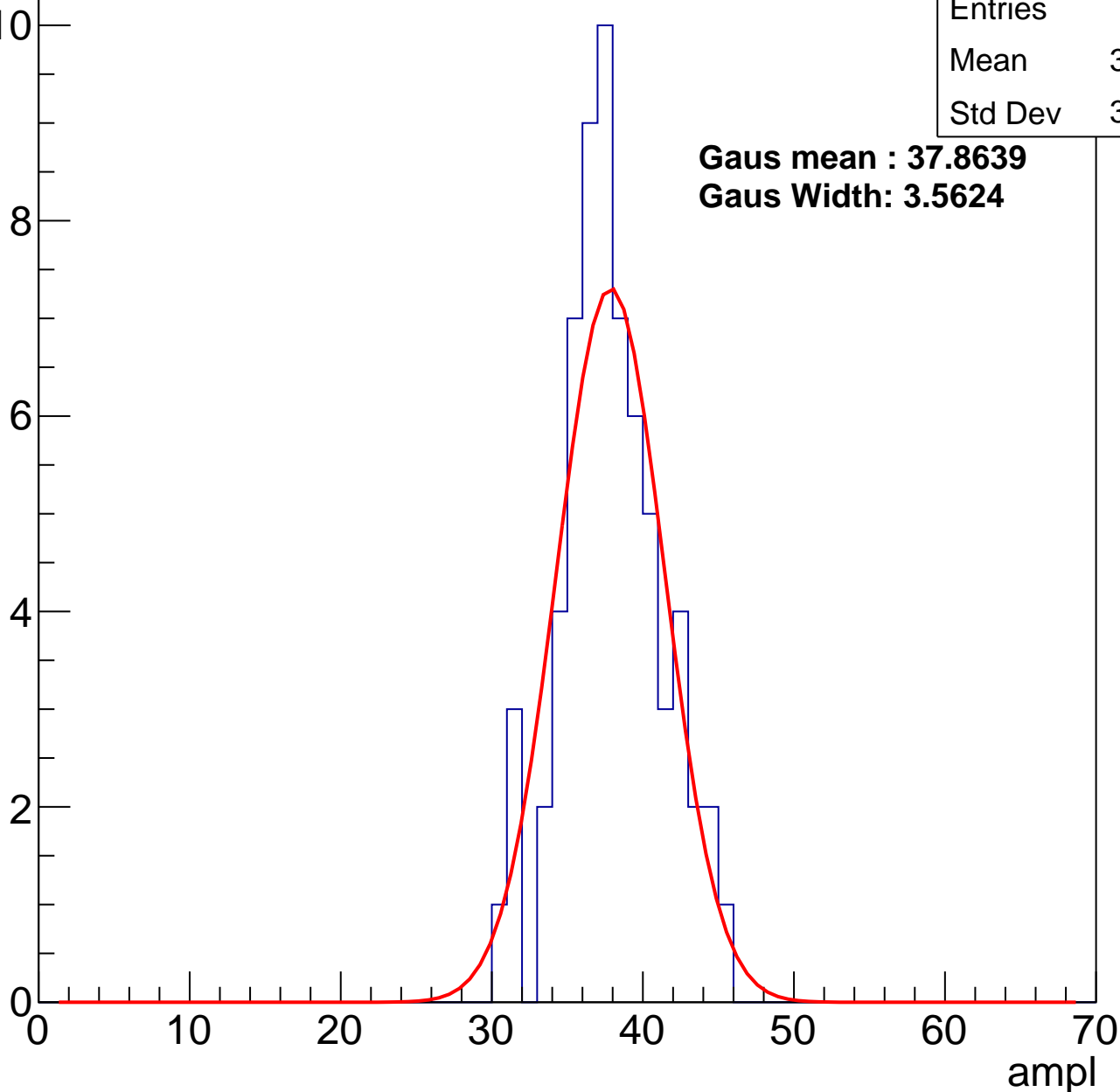
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	37.48
Std Dev	3.267

Gaus mean : 37.8639

Gaus Width: 3.5624

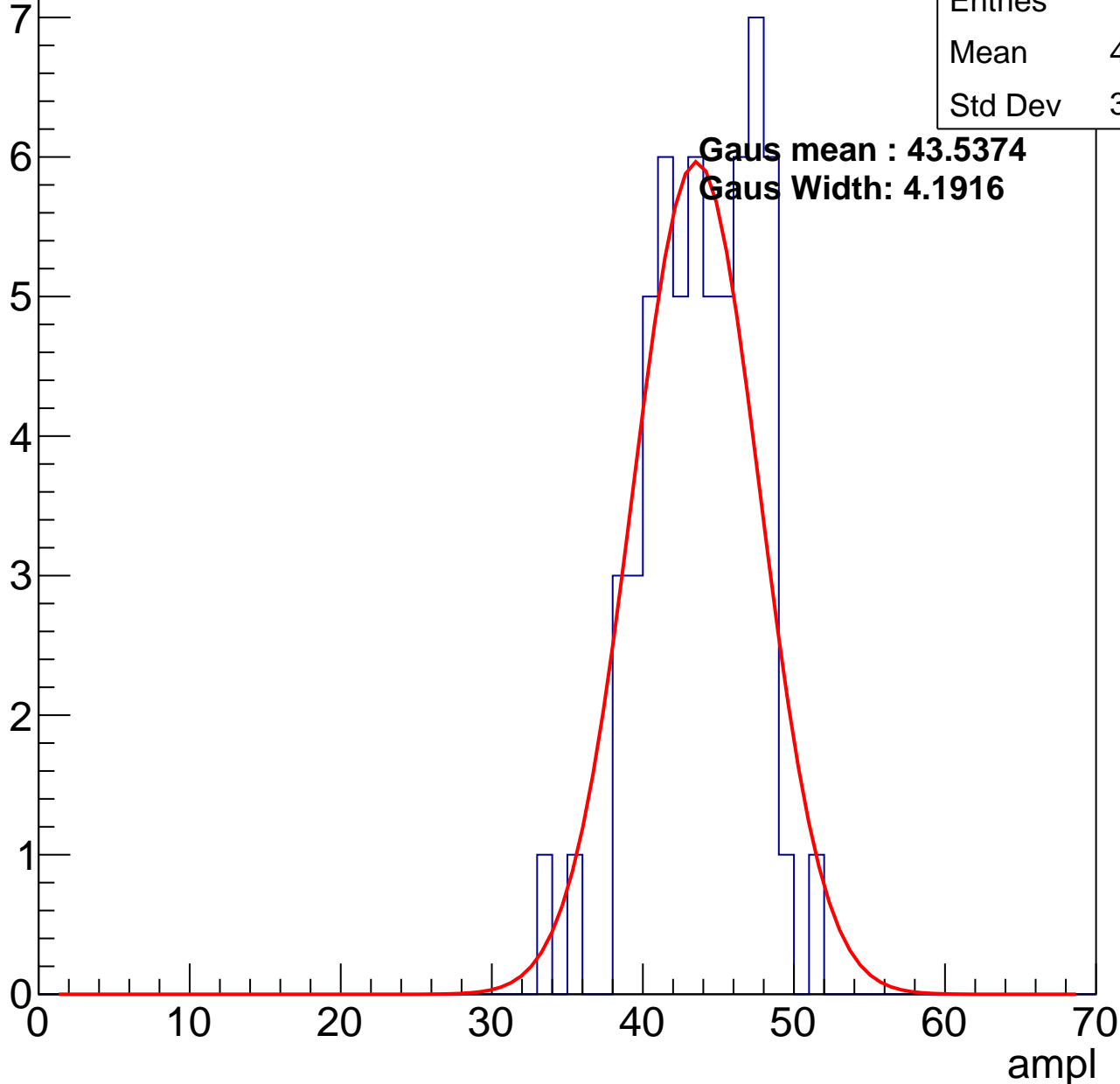


B0L102S, U12-ch8, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	43.46
Std Dev	3.597

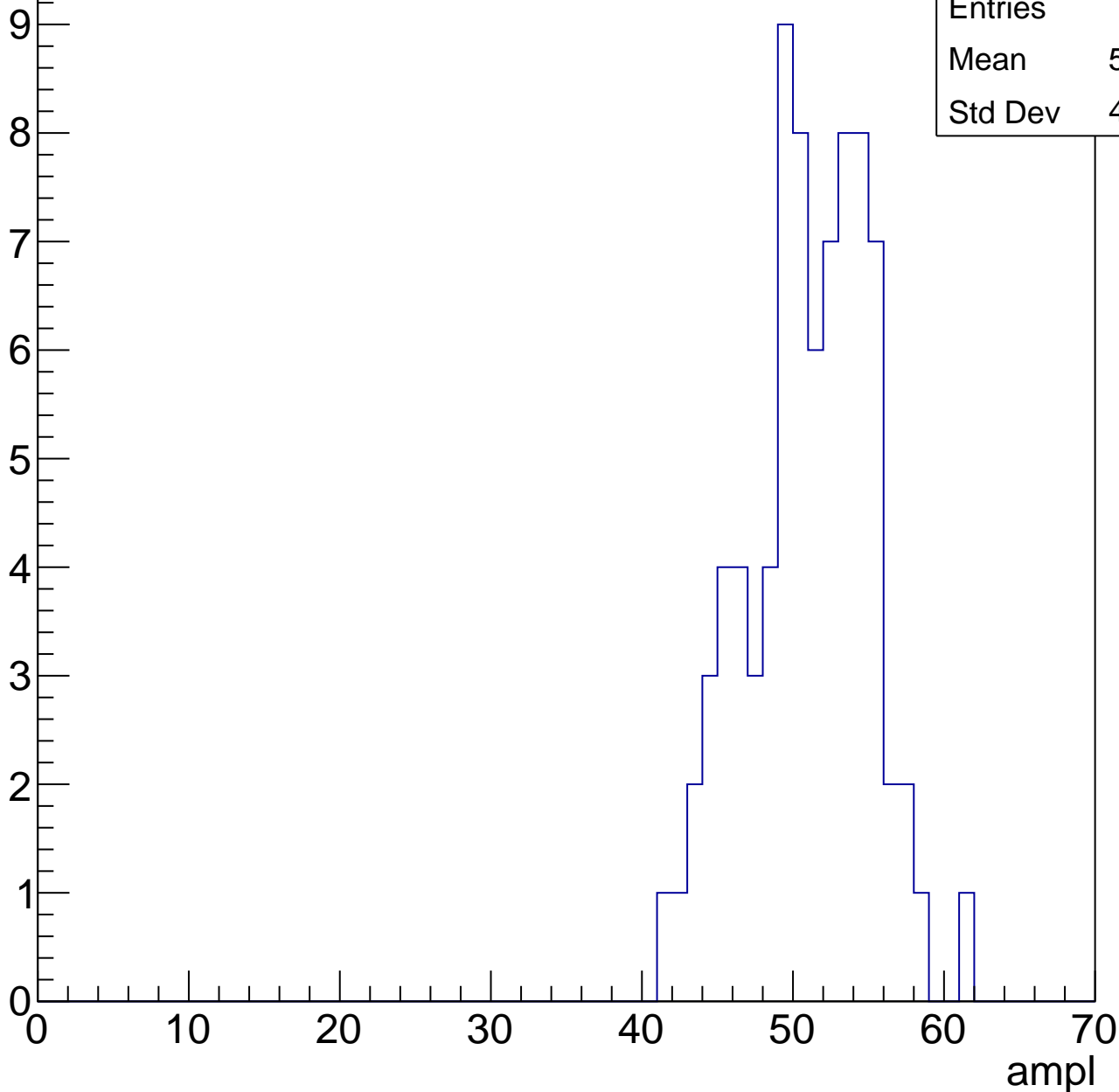


B0L102S, U12-ch8, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	50.56
Std Dev	4.015

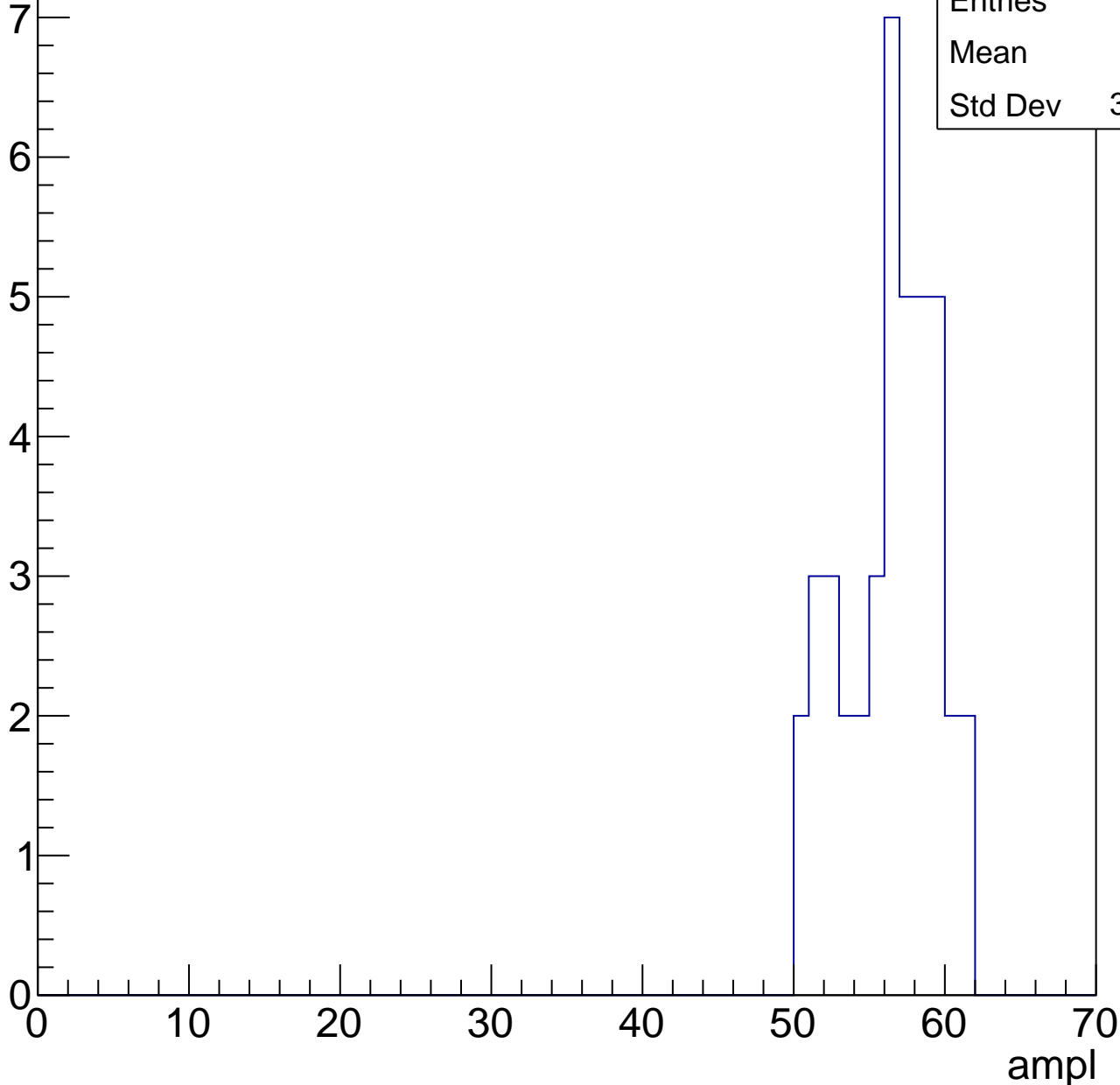


B0L102S, U12-ch8, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	55.9
Std Dev	3.027



B0L102S, U12-ch8, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

Entries 44

Mean 59.27

Std Dev 9.252

8

6

4

2

0

0

10

20

30

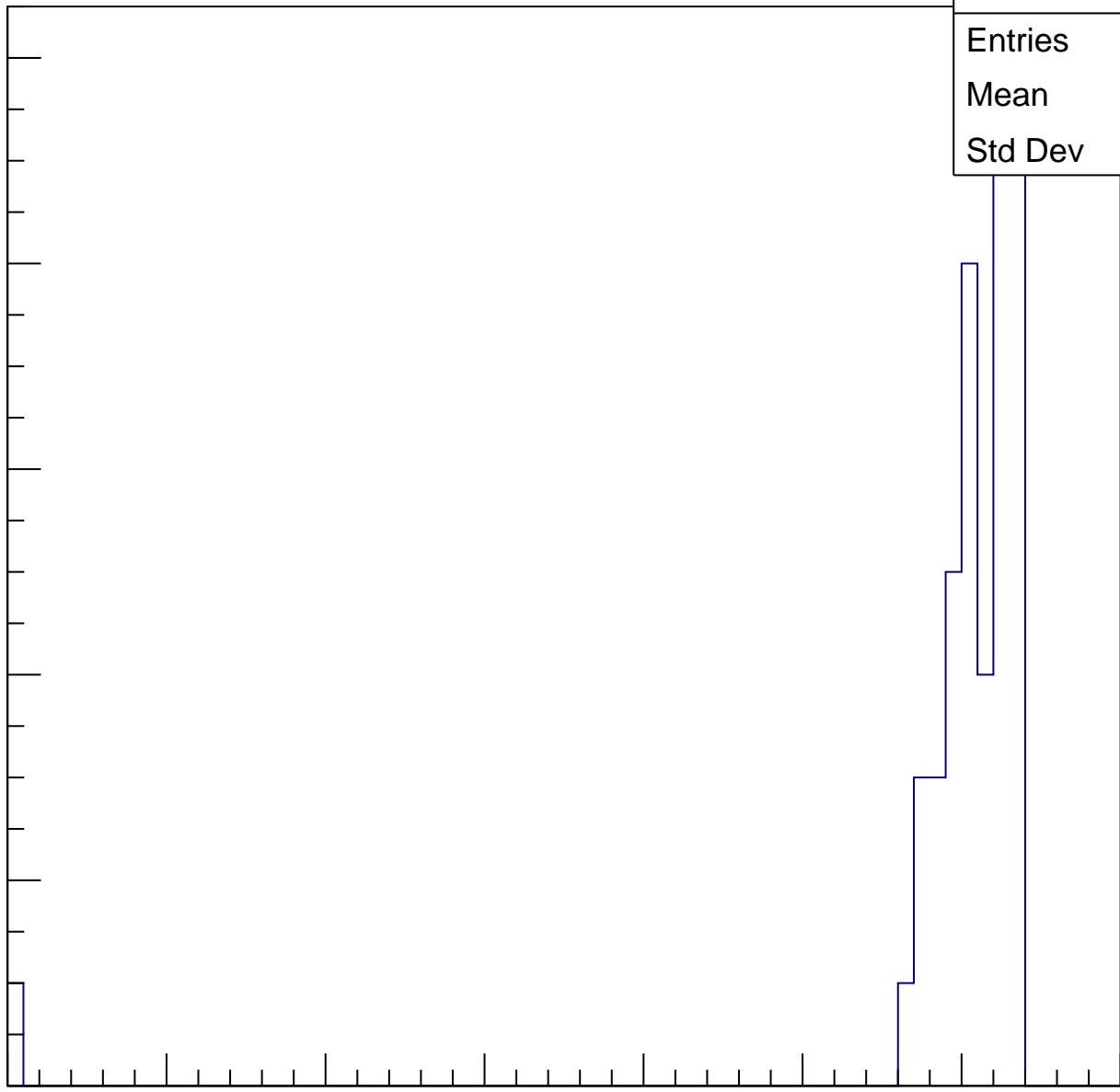
40

50

60

ampl

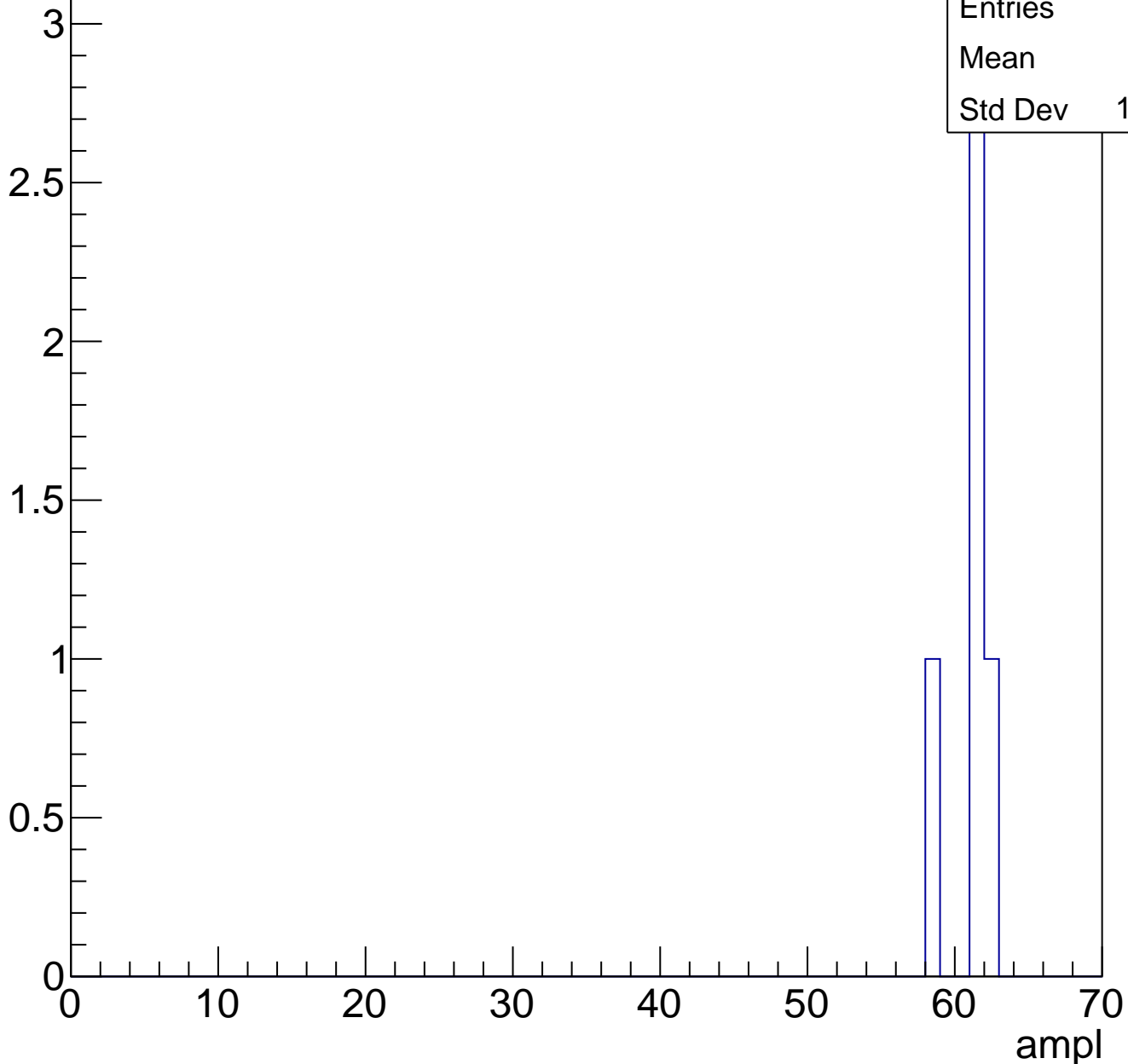
70



B0L102S, U12-ch8, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	60.6
Std Dev	1.356

B0L102S, U12-ch8, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L102S, U12-ch9, adc0

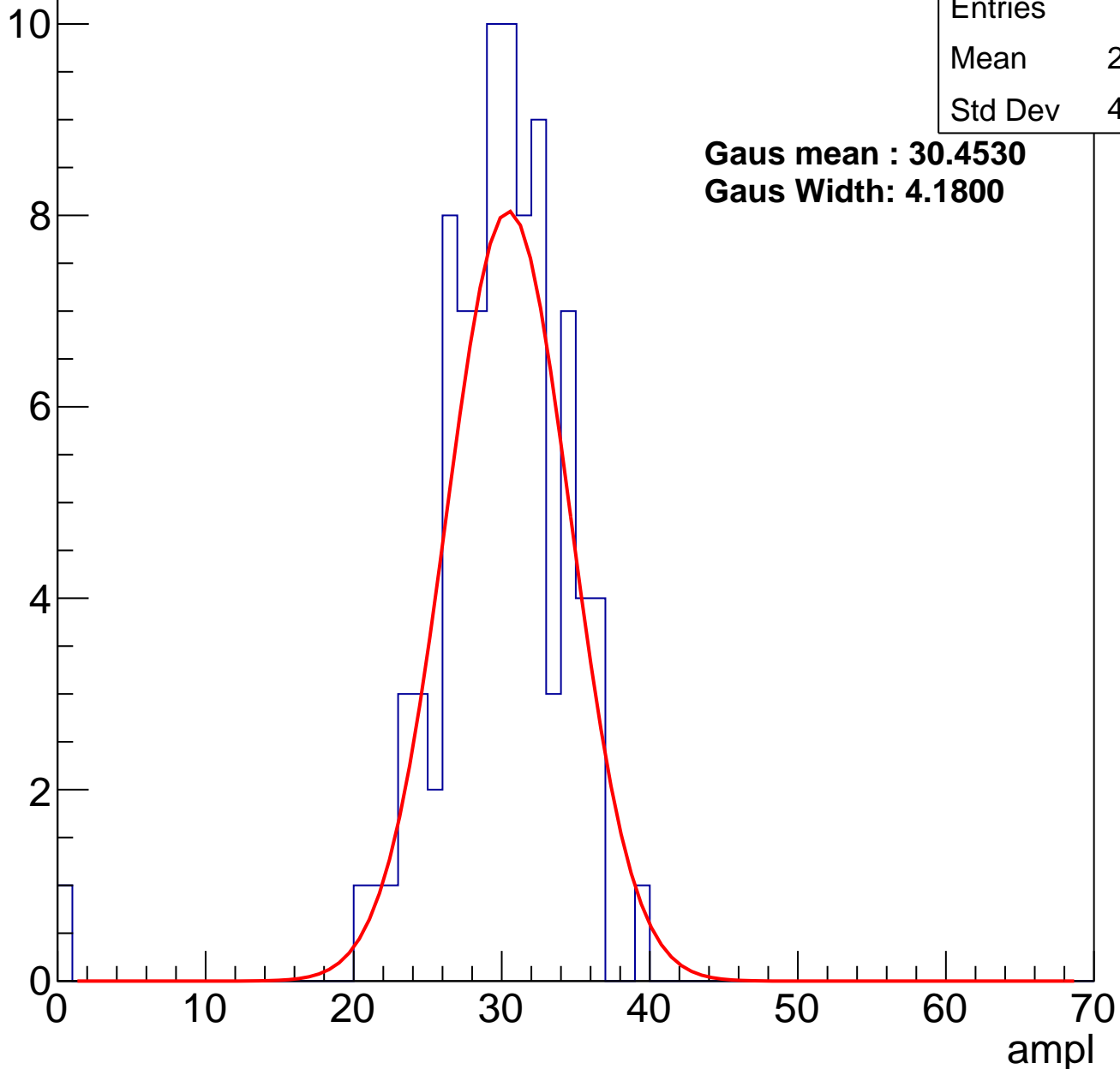
calib_packv5_042523_0143.root, FC#12, port B1

Entries	90
Mean	29.26
Std Dev	4.873

Gaus mean : 30.4530

Gaus Width: 4.1800

Entry



B0L102S, U12-ch9, adc1

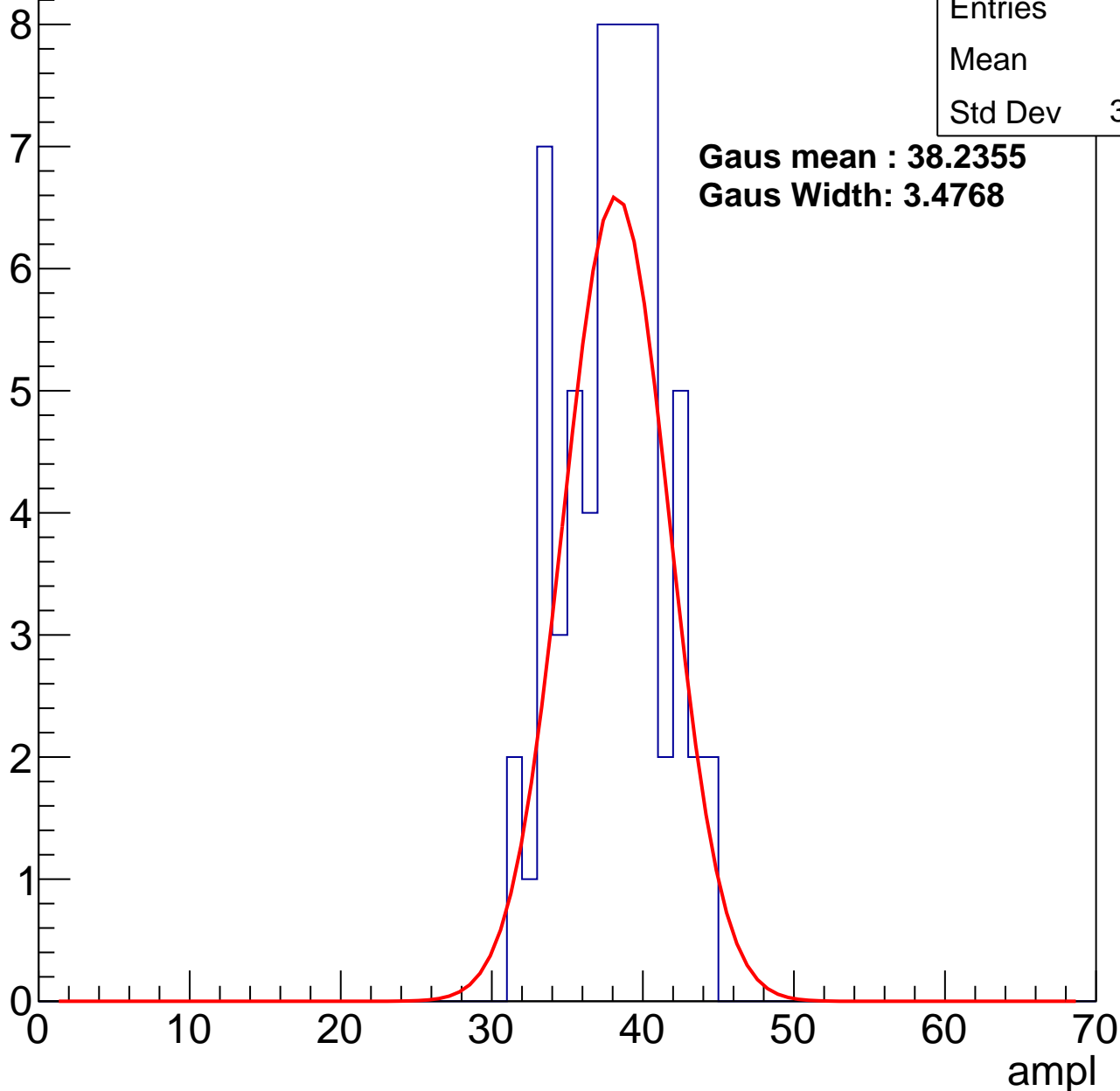
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.6
Std Dev	3.224

Gaus mean : 38.2355

Gaus Width: 3.4768



B0L102S, U12-ch9, adc2

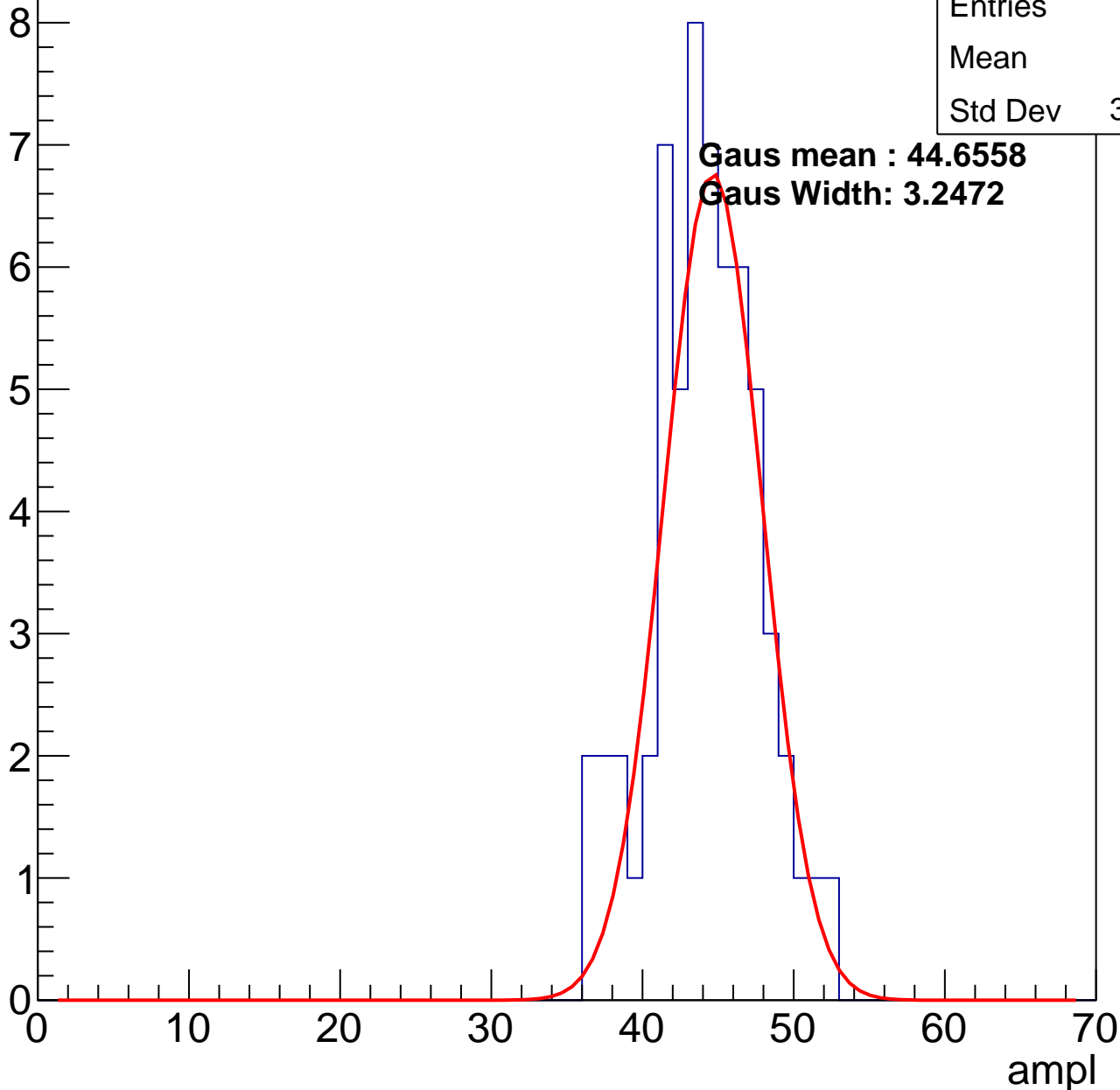
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	43.7
Std Dev	3.536

Gaus mean : 44.6558

Gaus Width: 3.2472

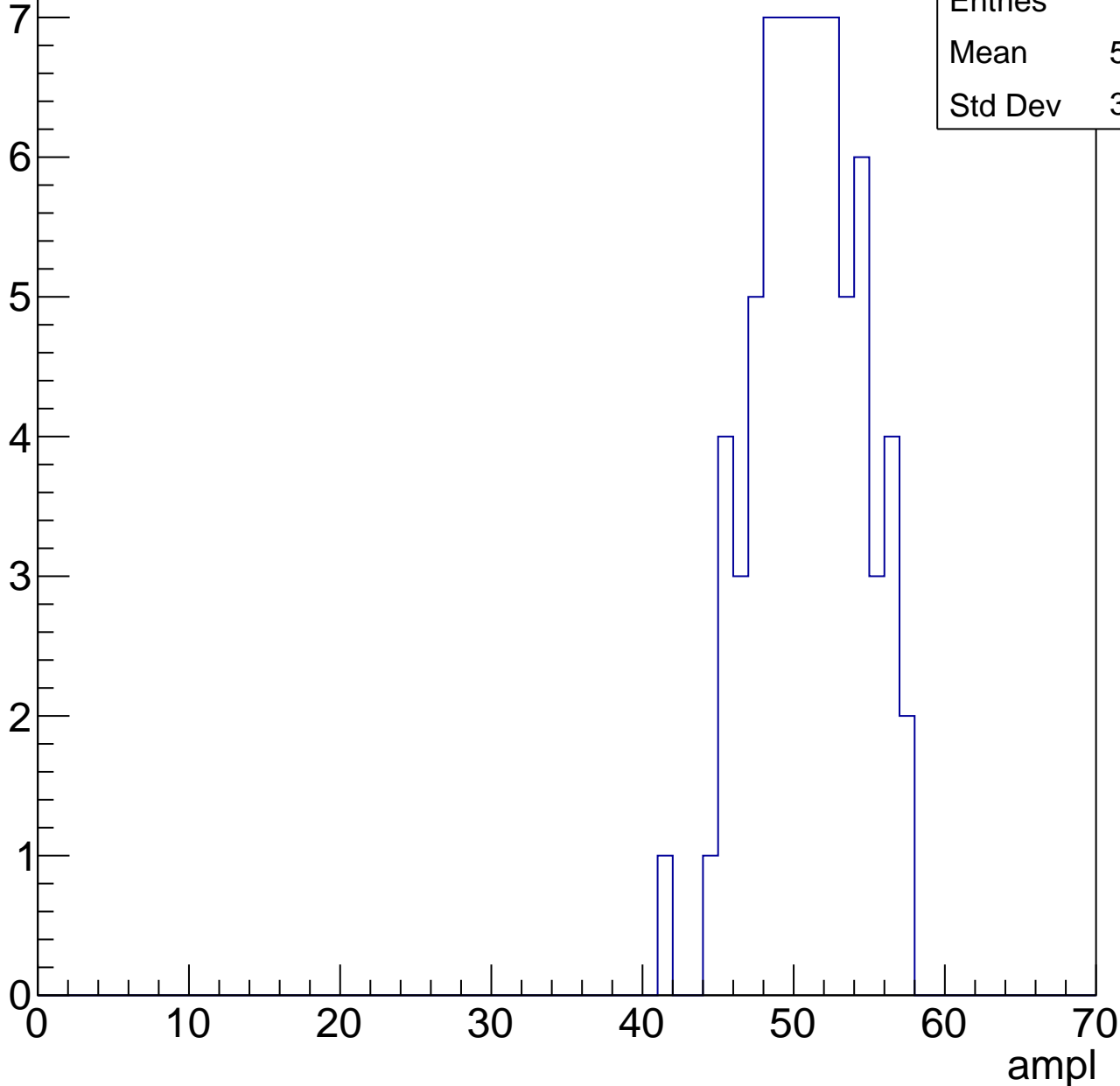


B0L102S, U12-ch9, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	50.43
Std Dev	3.458

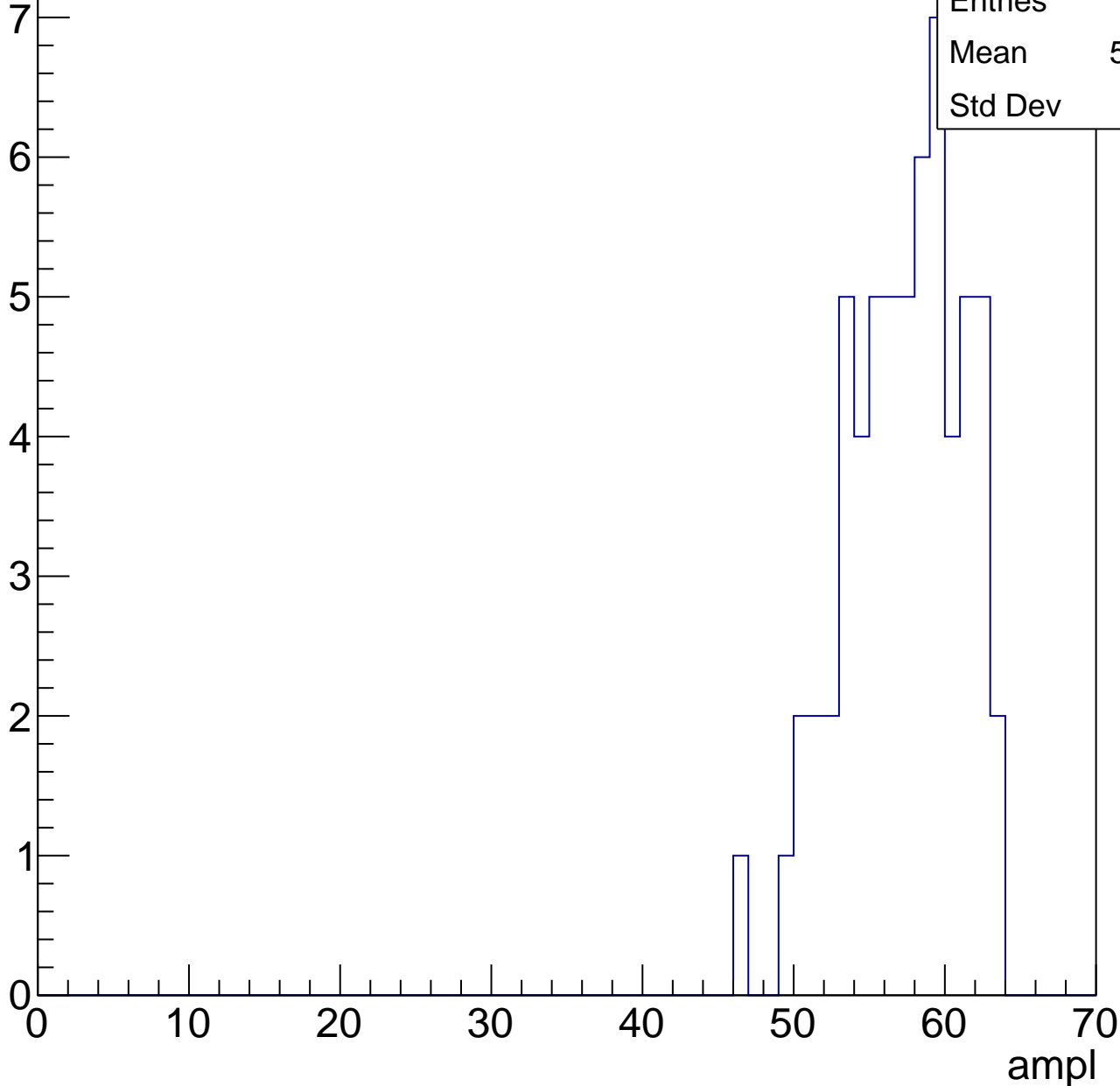


B0L102S, U12-ch9, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	56.79
Std Dev	3.82

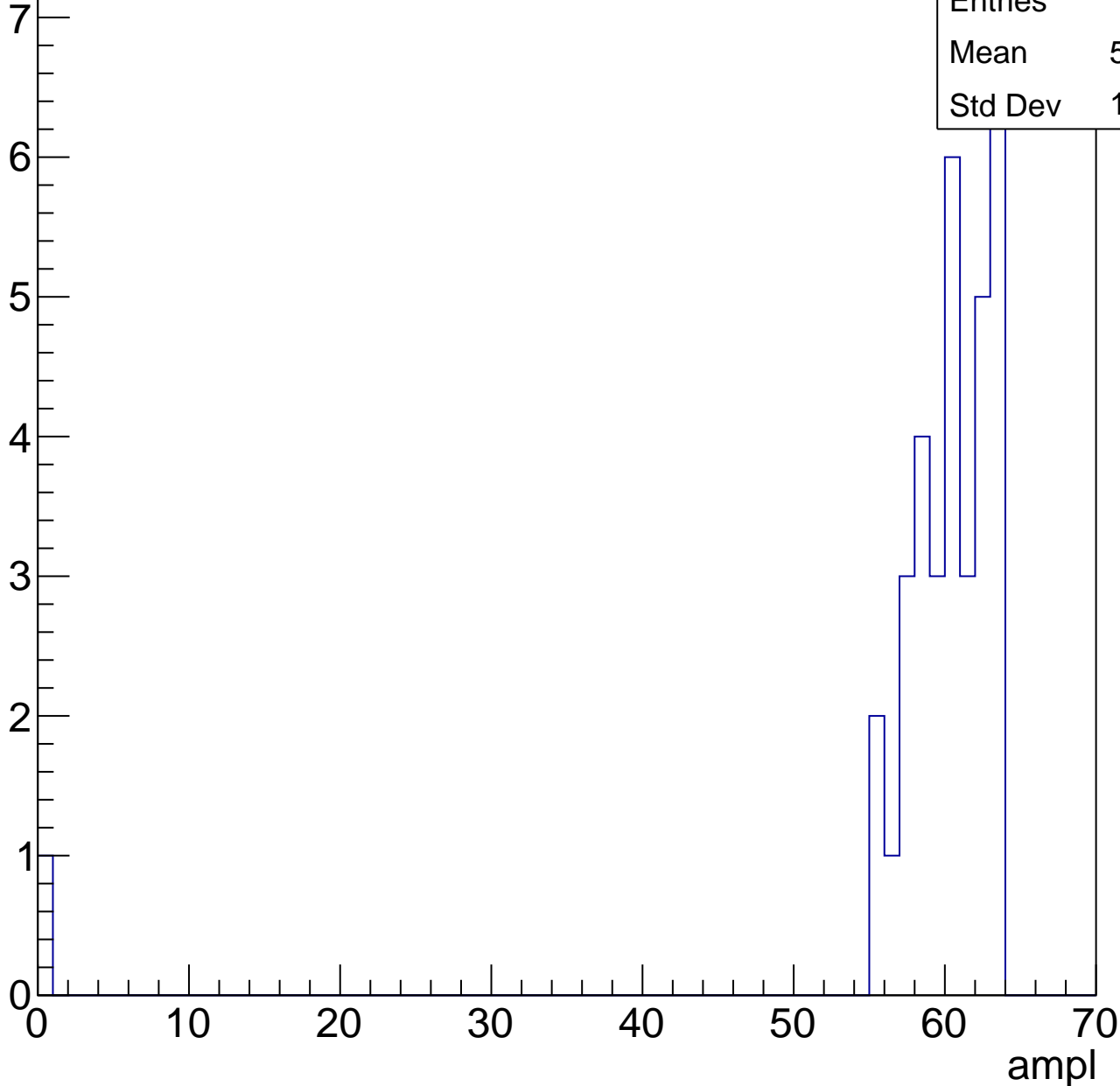


B0L102S, U12-ch9, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	35
Mean	58.29
Std Dev	10.27



B0L102S, U12-ch9, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	61.67
Std Dev	0.9428

B0L102S, U12-ch9, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	19
Std Dev	0

ampl

B0L102S, U12-ch10, adc0

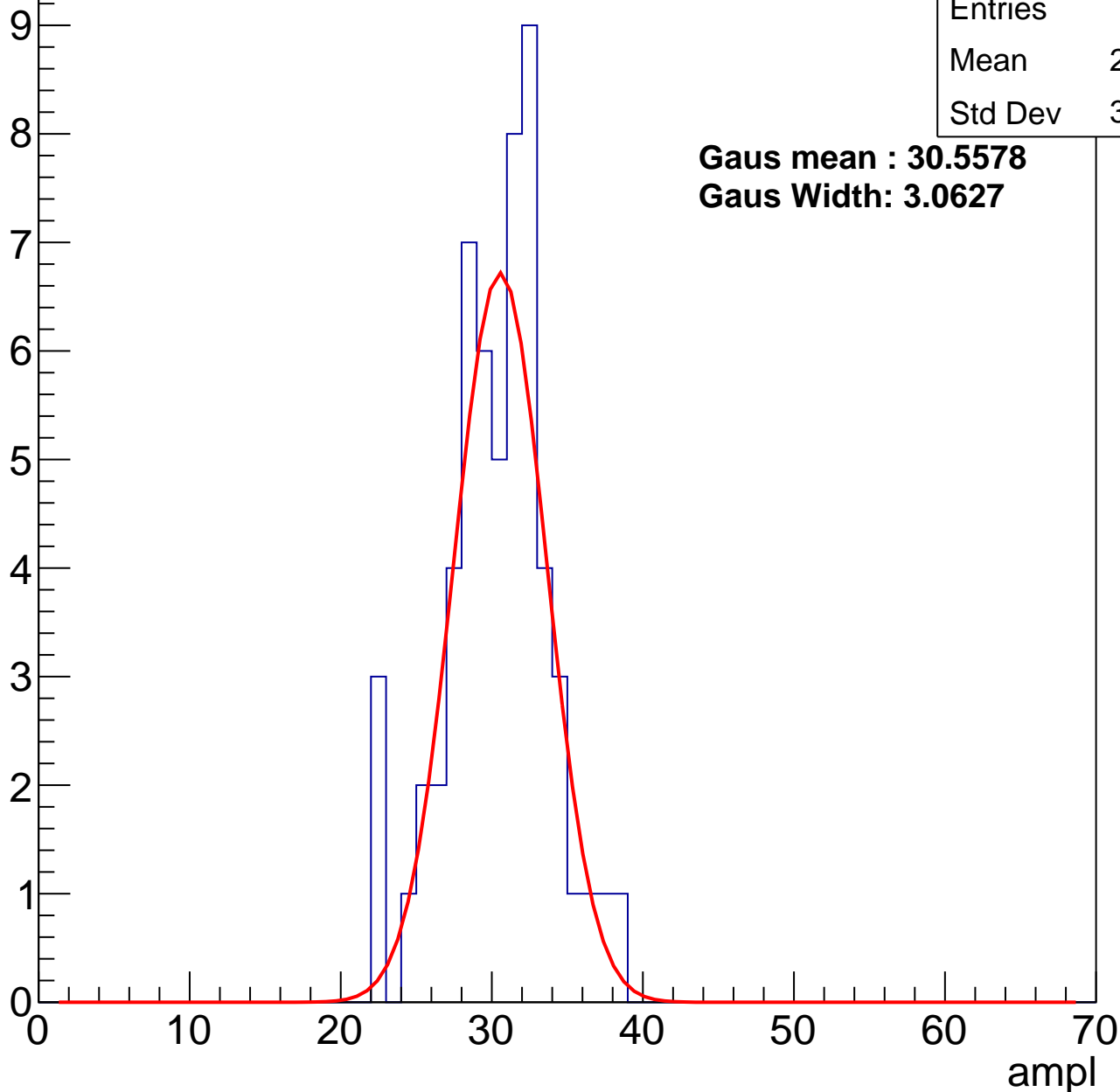
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	29.93
Std Dev	3.428

Gaus mean : 30.5578

Gaus Width: 3.0627



B0L102S, U12-ch10, adc1

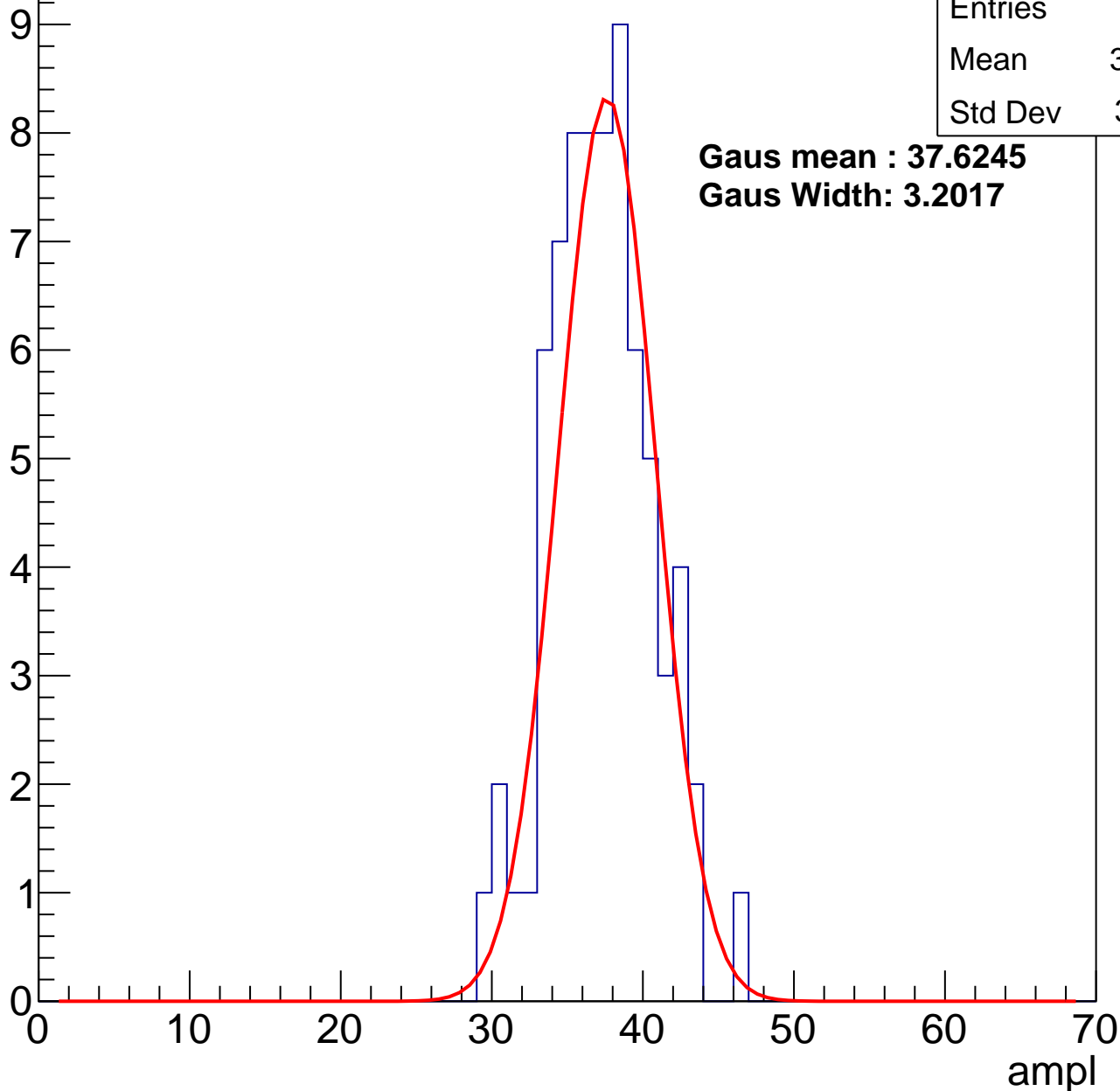
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	36.82
Std Dev	3.331

Gaus mean : 37.6245

Gaus Width: 3.2017



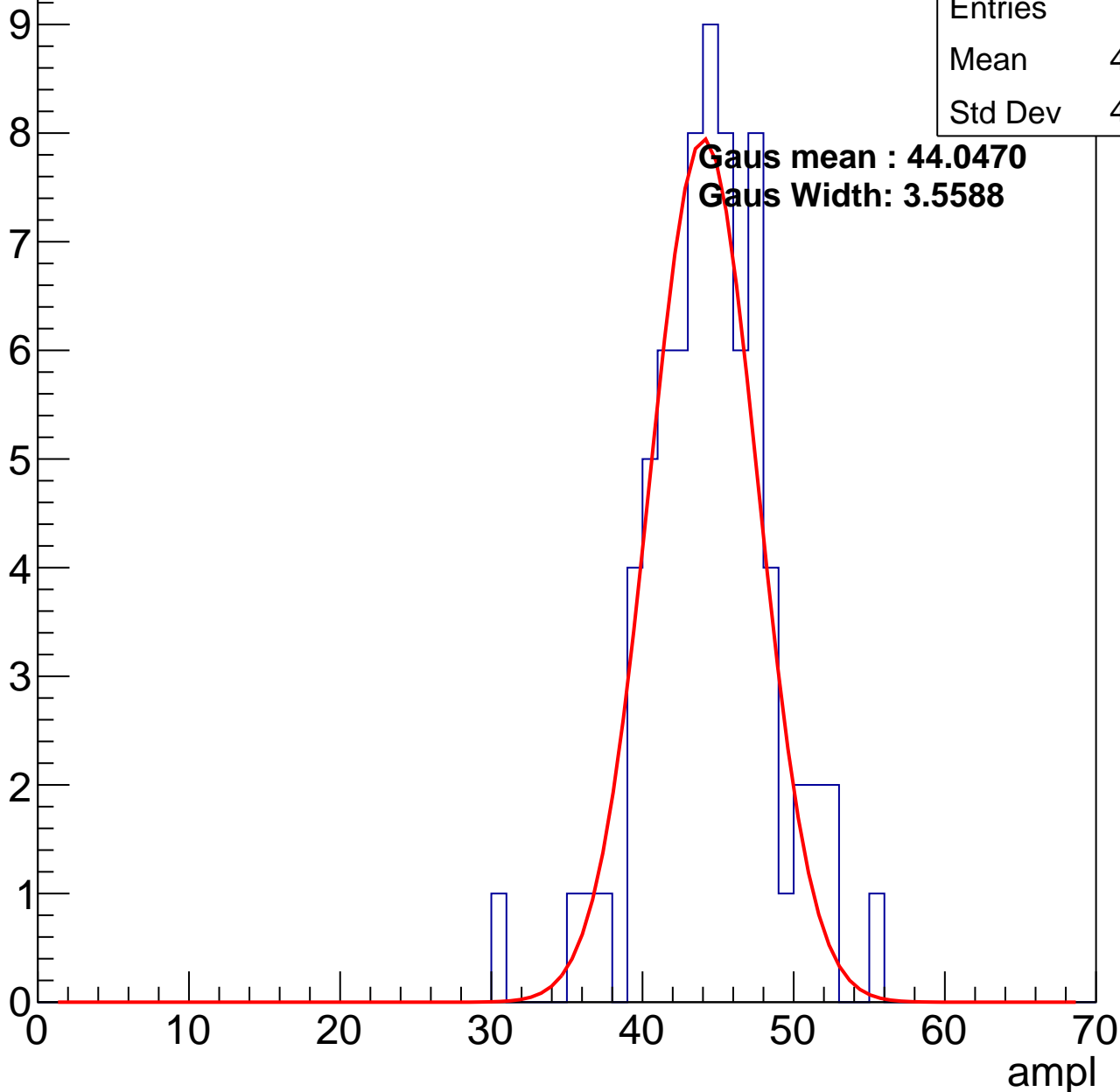
B0L102S, U12-ch10, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	44.03
Std Dev	4.085

Gaus mean : 44.0470
Gaus Width: 3.5588

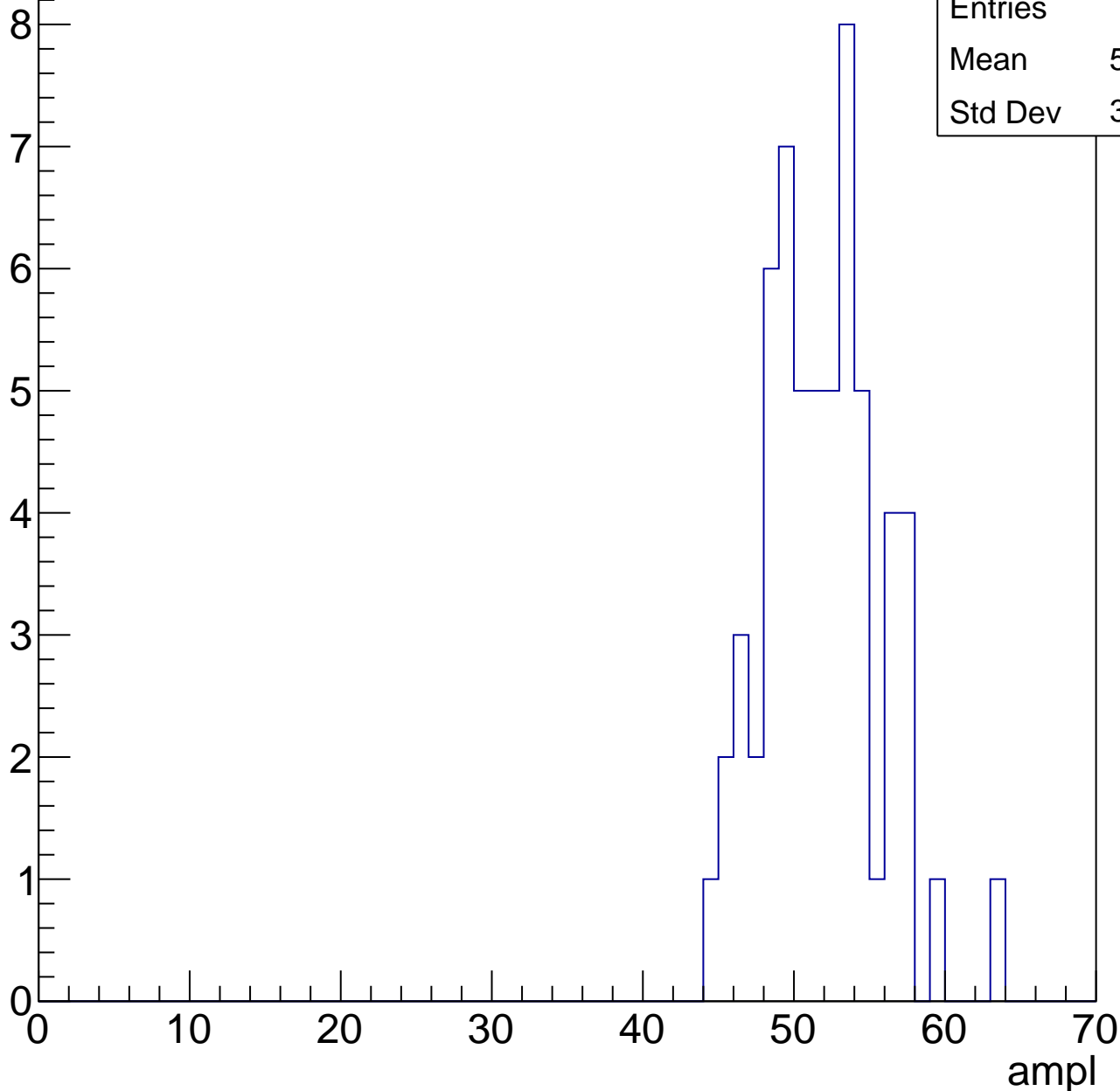


B0L102S, U12-ch10, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

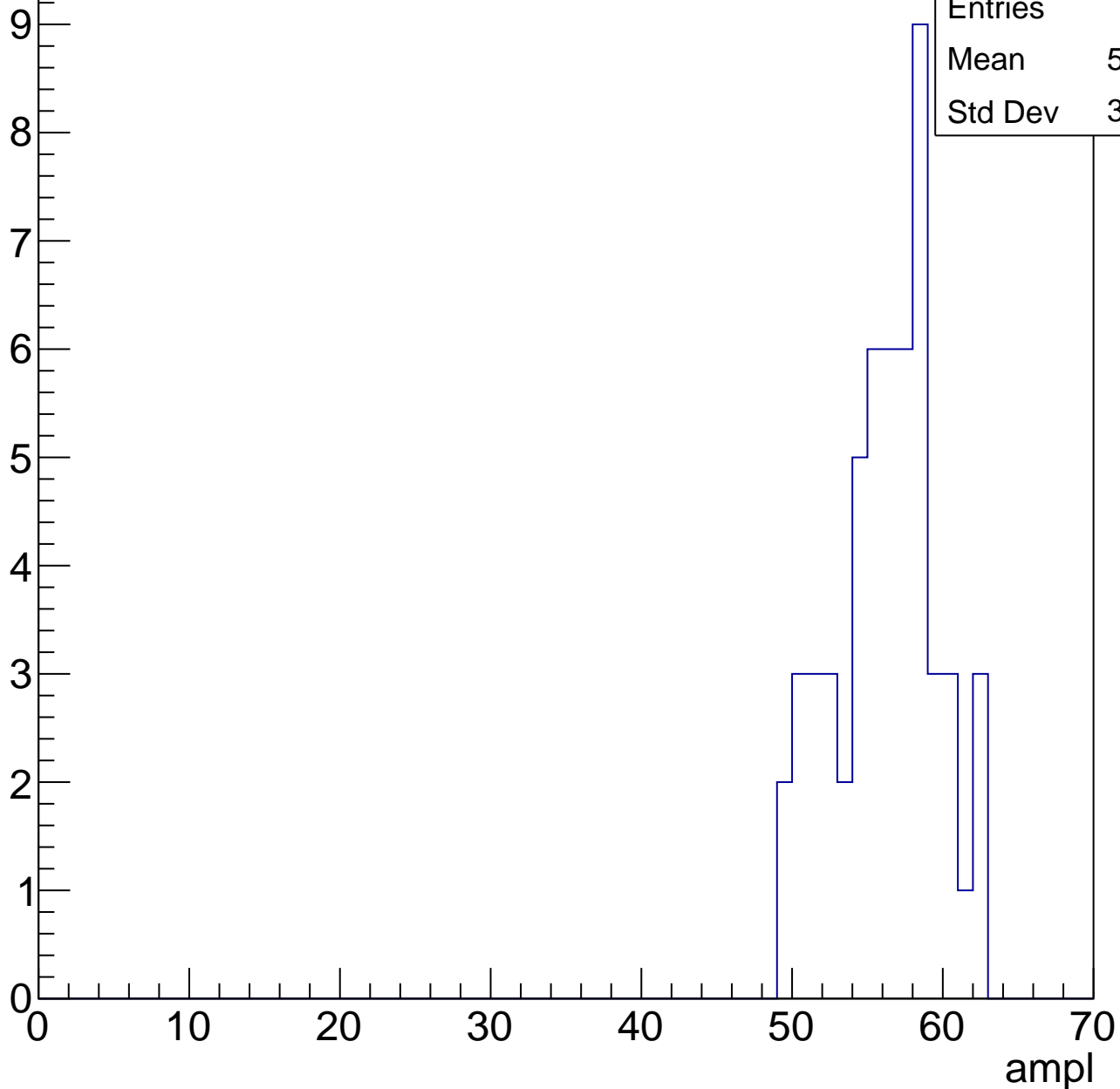
Entries	60
Mean	51.42
Std Dev	3.774



B0L102S, U12-ch10, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

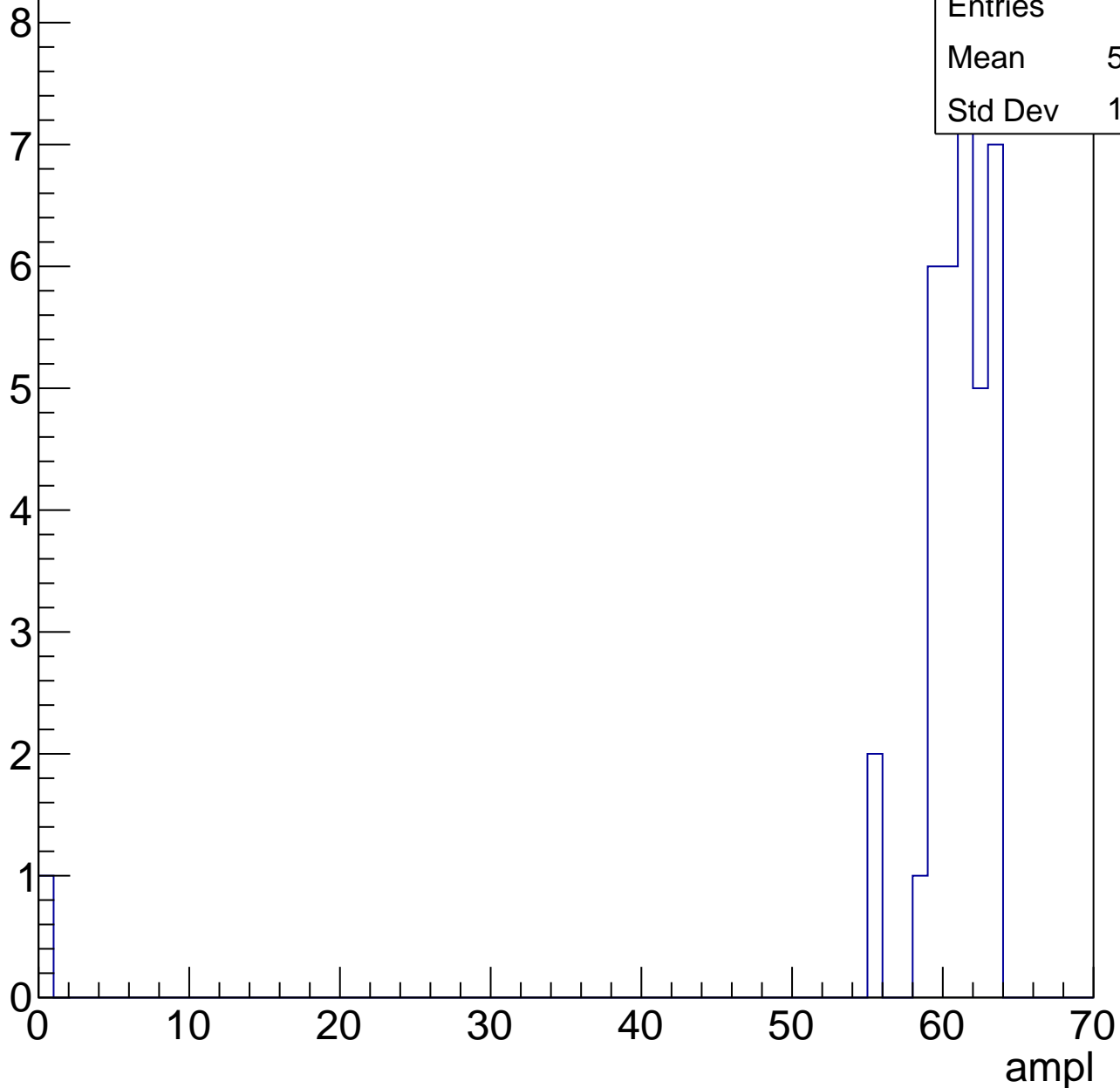


B0L102S, U12-ch10, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	36
Mean	58.92
Std Dev	10.15



B0L102S, U12-ch10, adc6

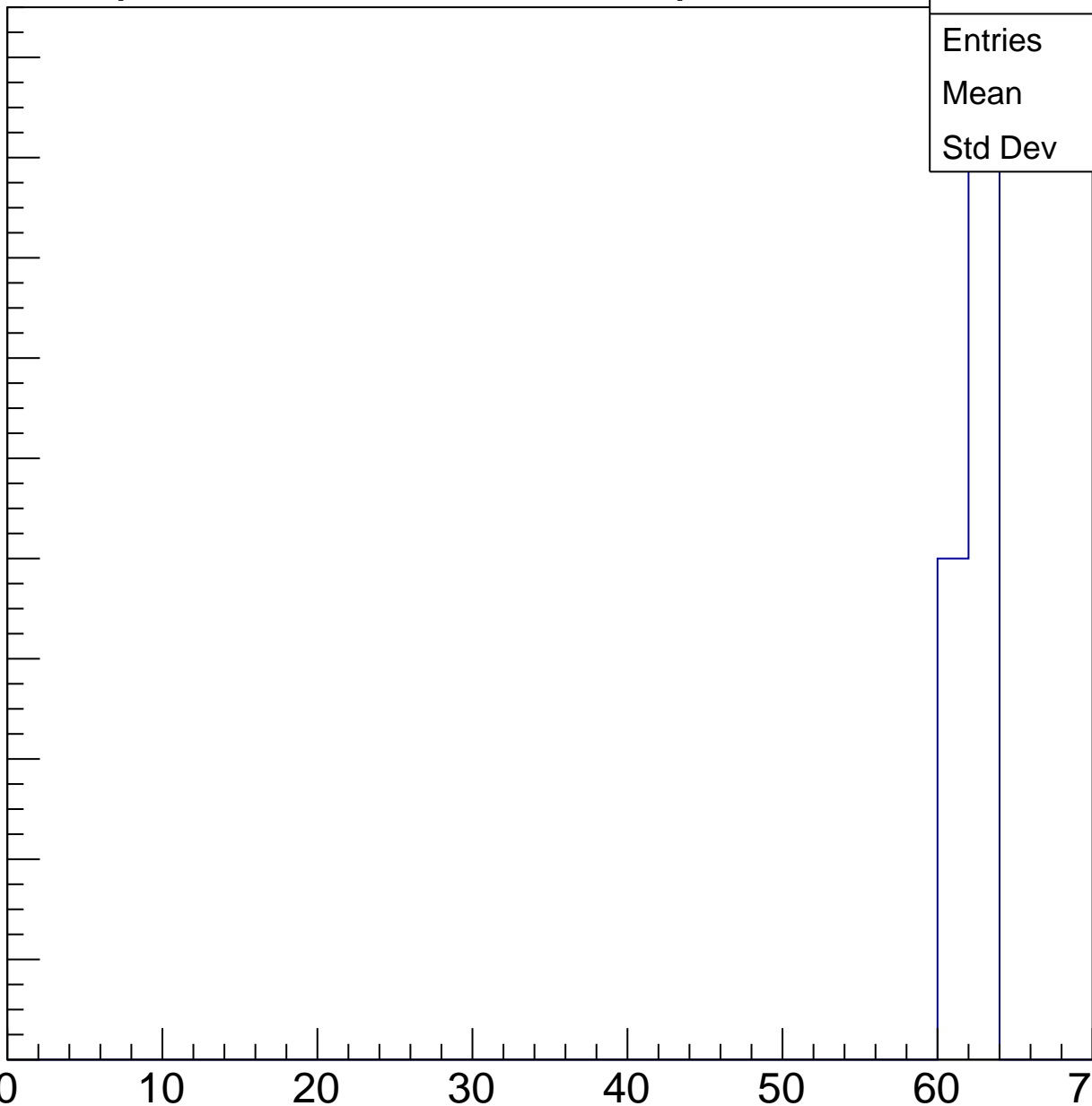
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl



B0L102S, U12-ch10, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch11, adc0

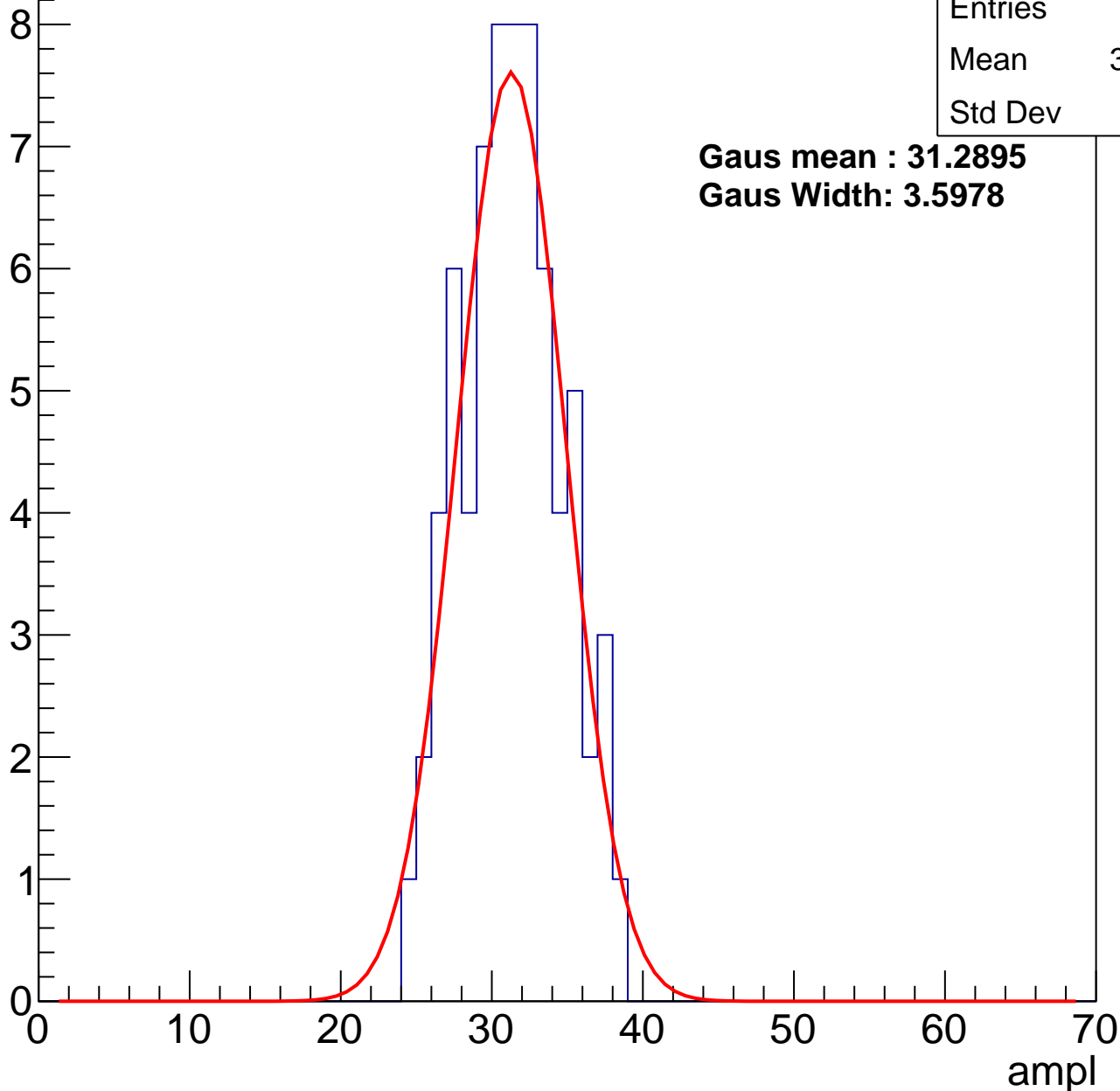
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	30.86
Std Dev	3.28

Gaus mean : 31.2895

Gaus Width: 3.5978



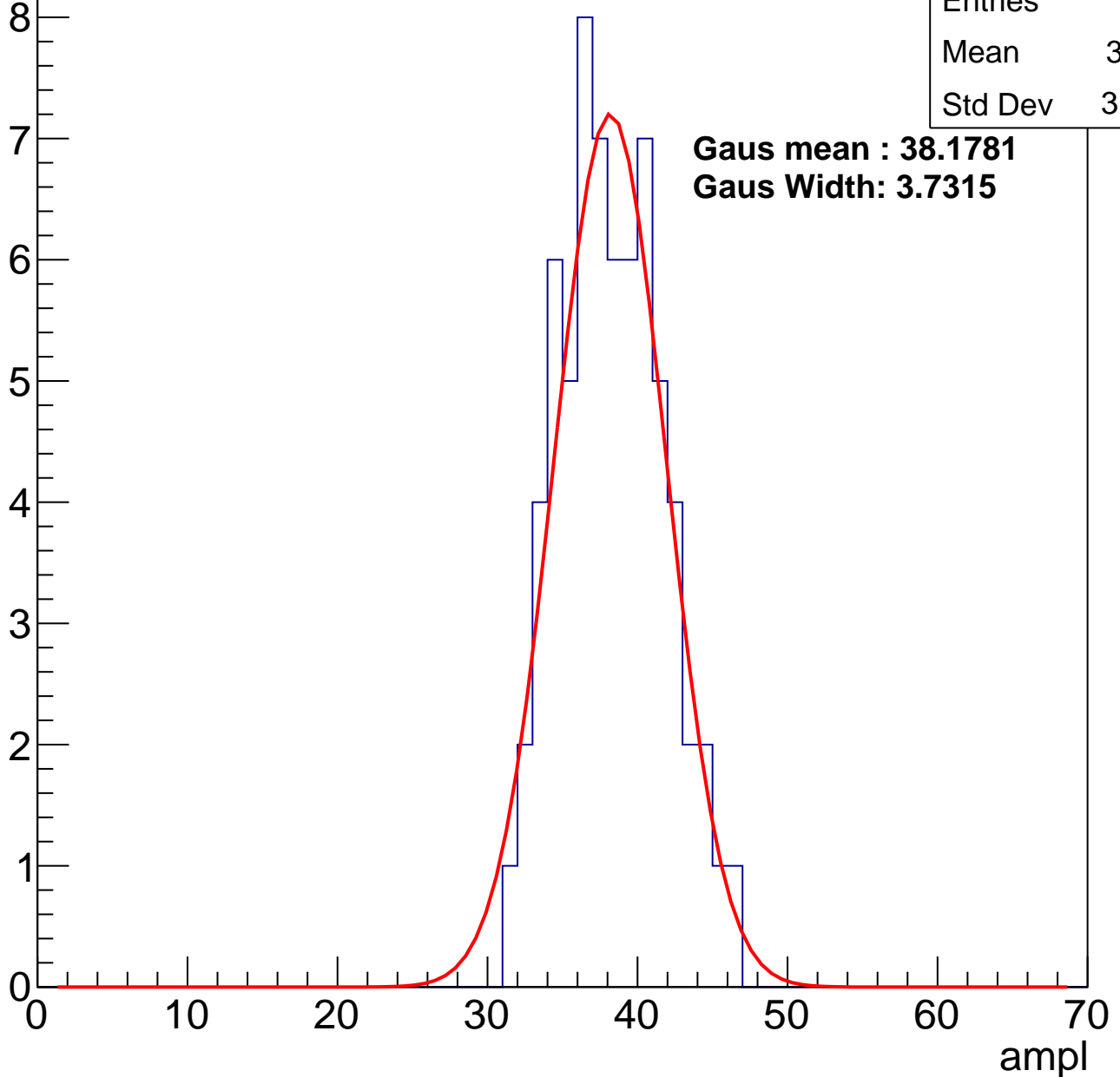
B0L102S, U12-ch11, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	37.81
Std Dev	3.404

Gaus mean : 38.1781
Gaus Width: 3.7315



B0L102S, U12-ch11, adc2

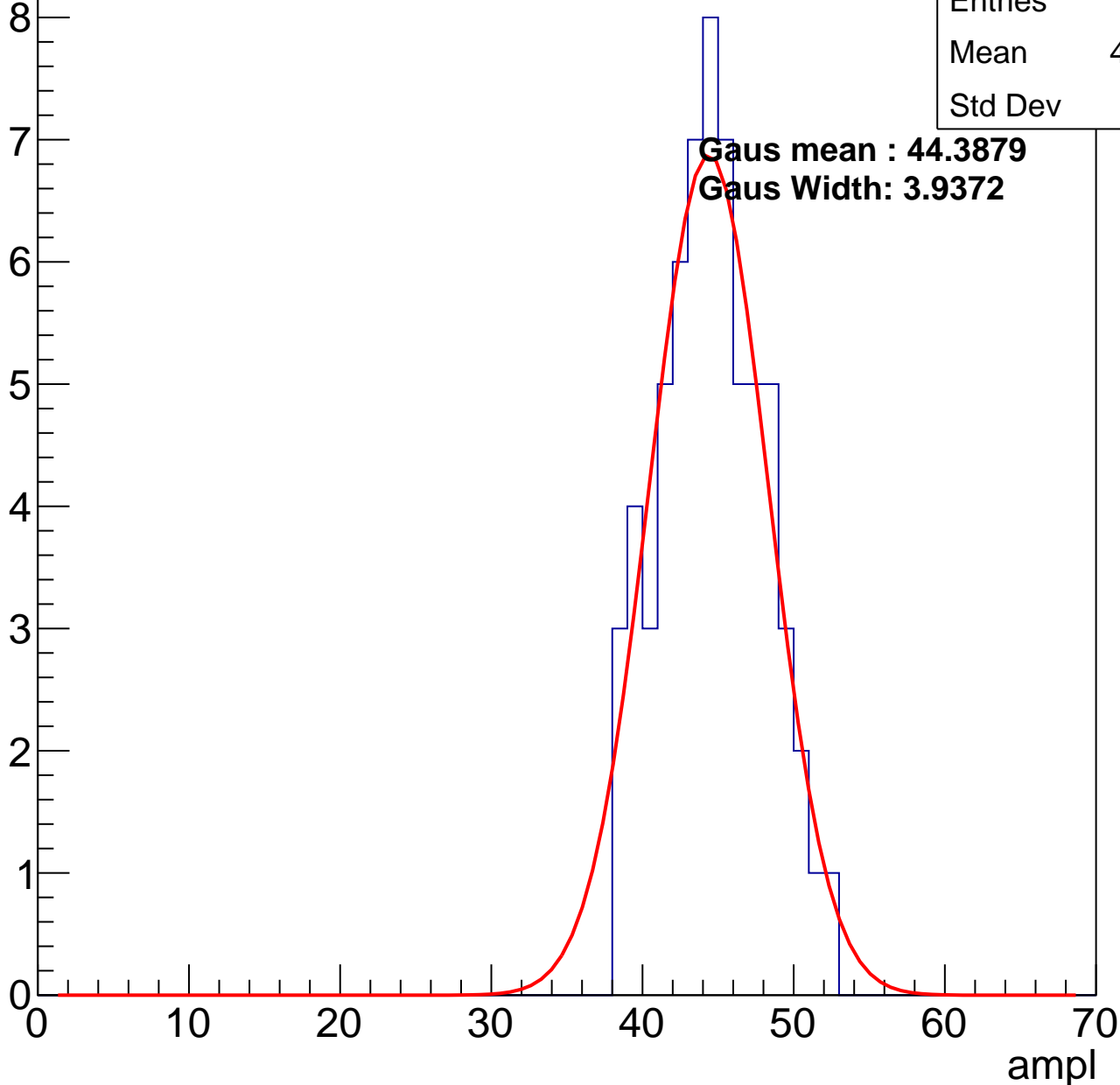
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	44.15
Std Dev	3.38

Gaus mean : 44.3879

Gaus Width: 3.9372

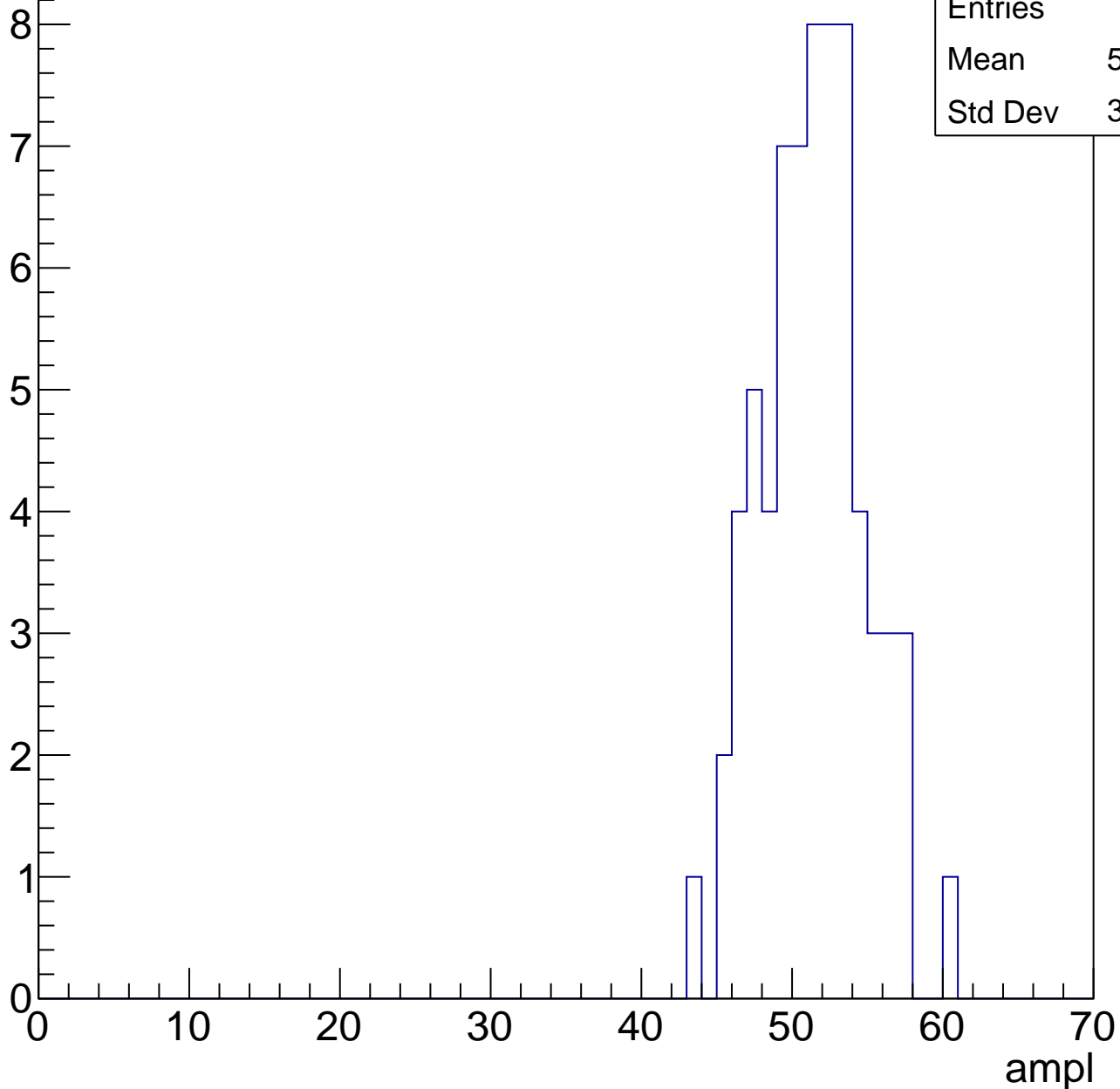


B0L102S, U12-ch11, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

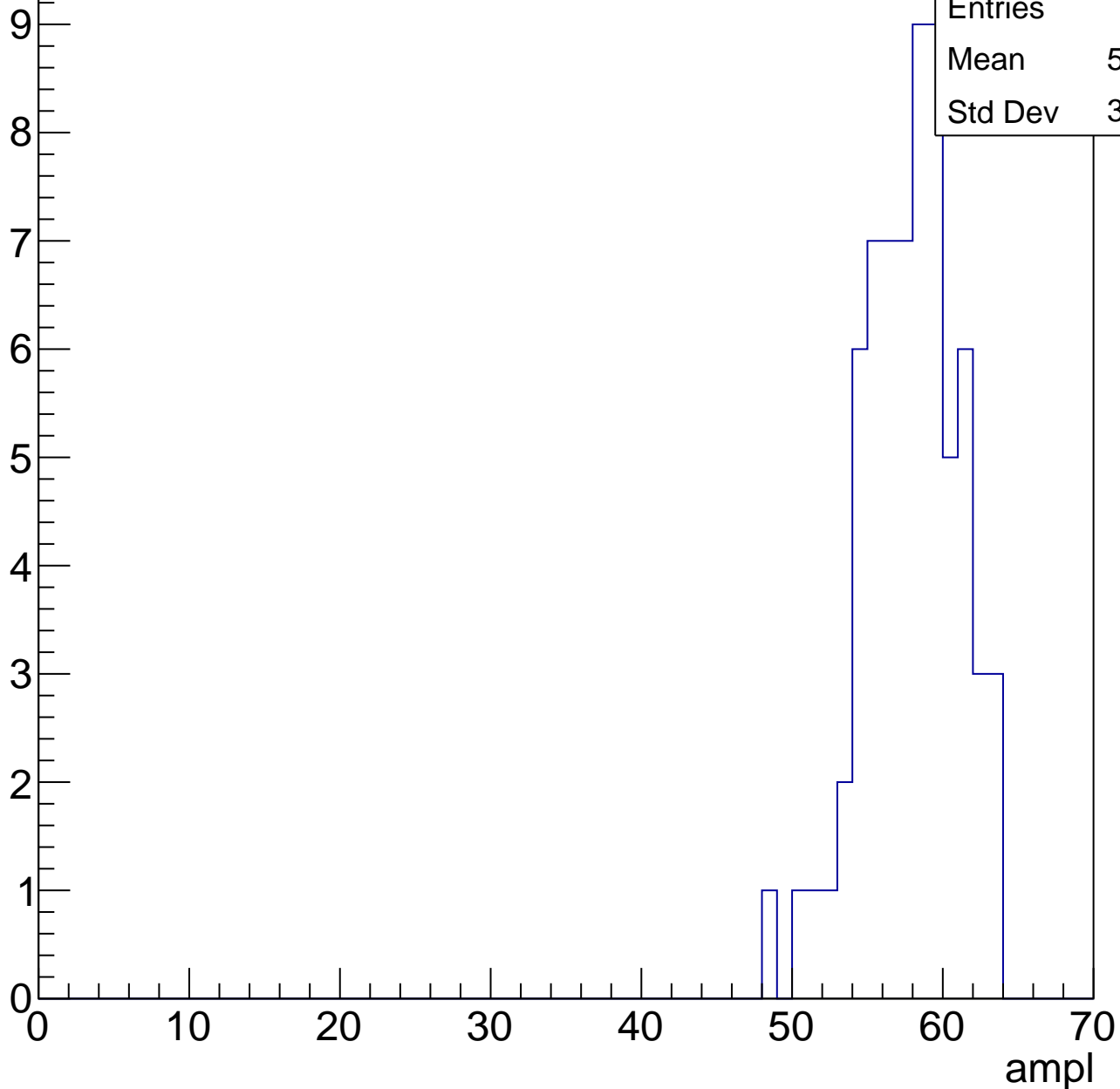
Entries	68
Mean	50.96
Std Dev	3.376



B0L102S, U12-ch11, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

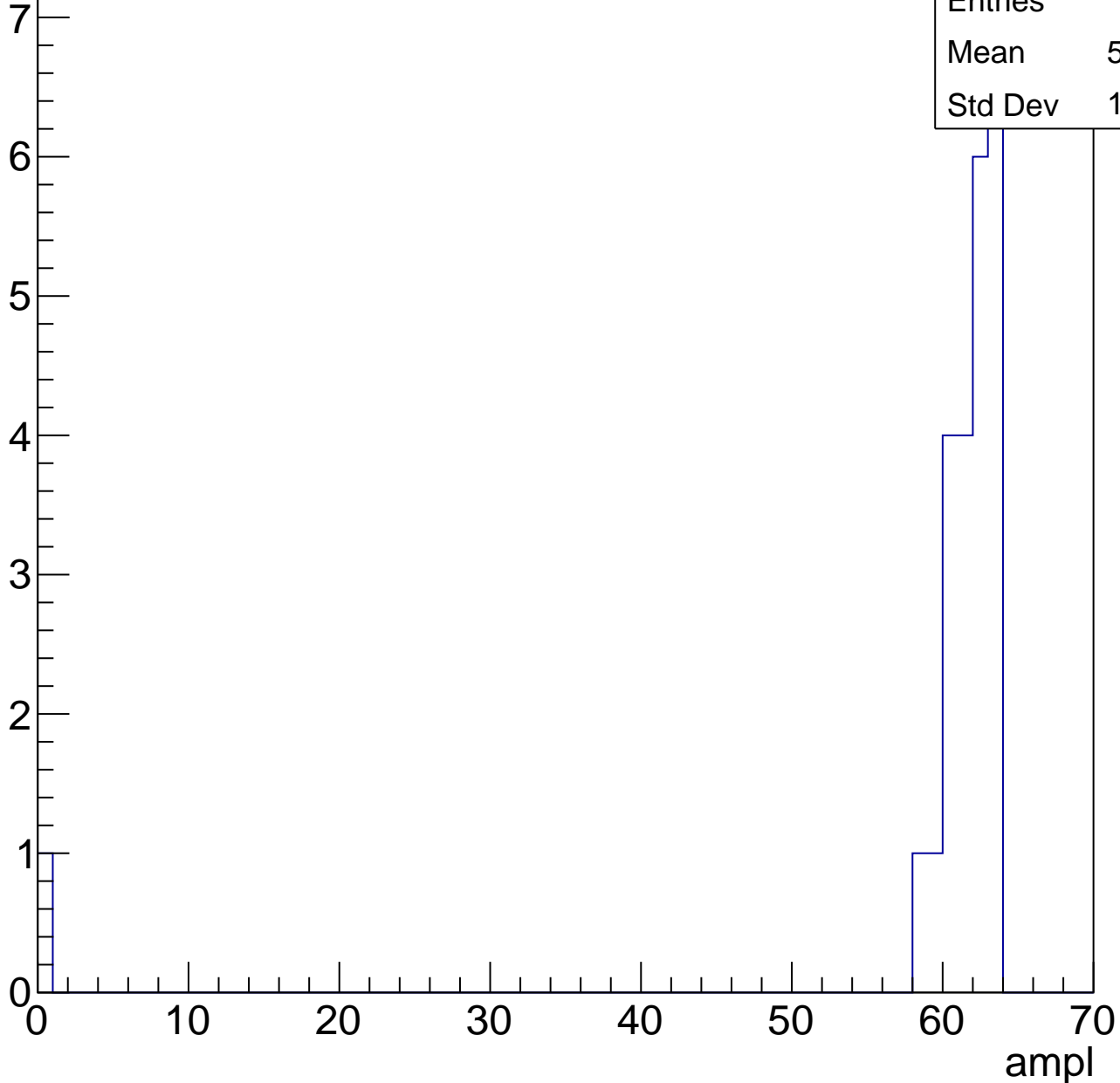


B0L102S, U12-ch11, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	24
Mean	58.92
Std Dev	12.36



B0L102S, U12-ch11, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch11, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch12, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	83
Mean	29.53
Std Dev	5.055

Gaus mean : 29.5752

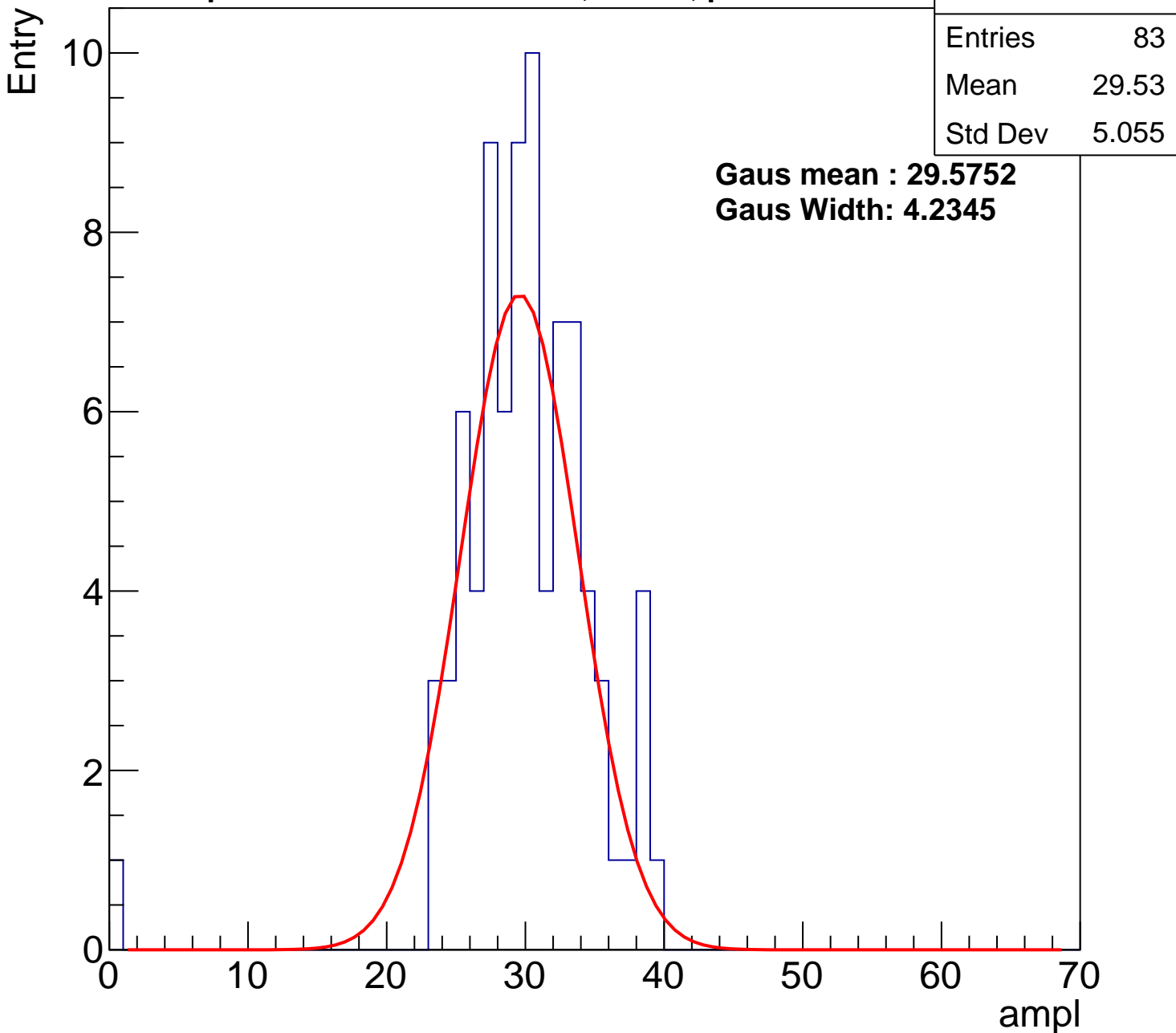
Gaus Width: 4.2345

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch12, adc1

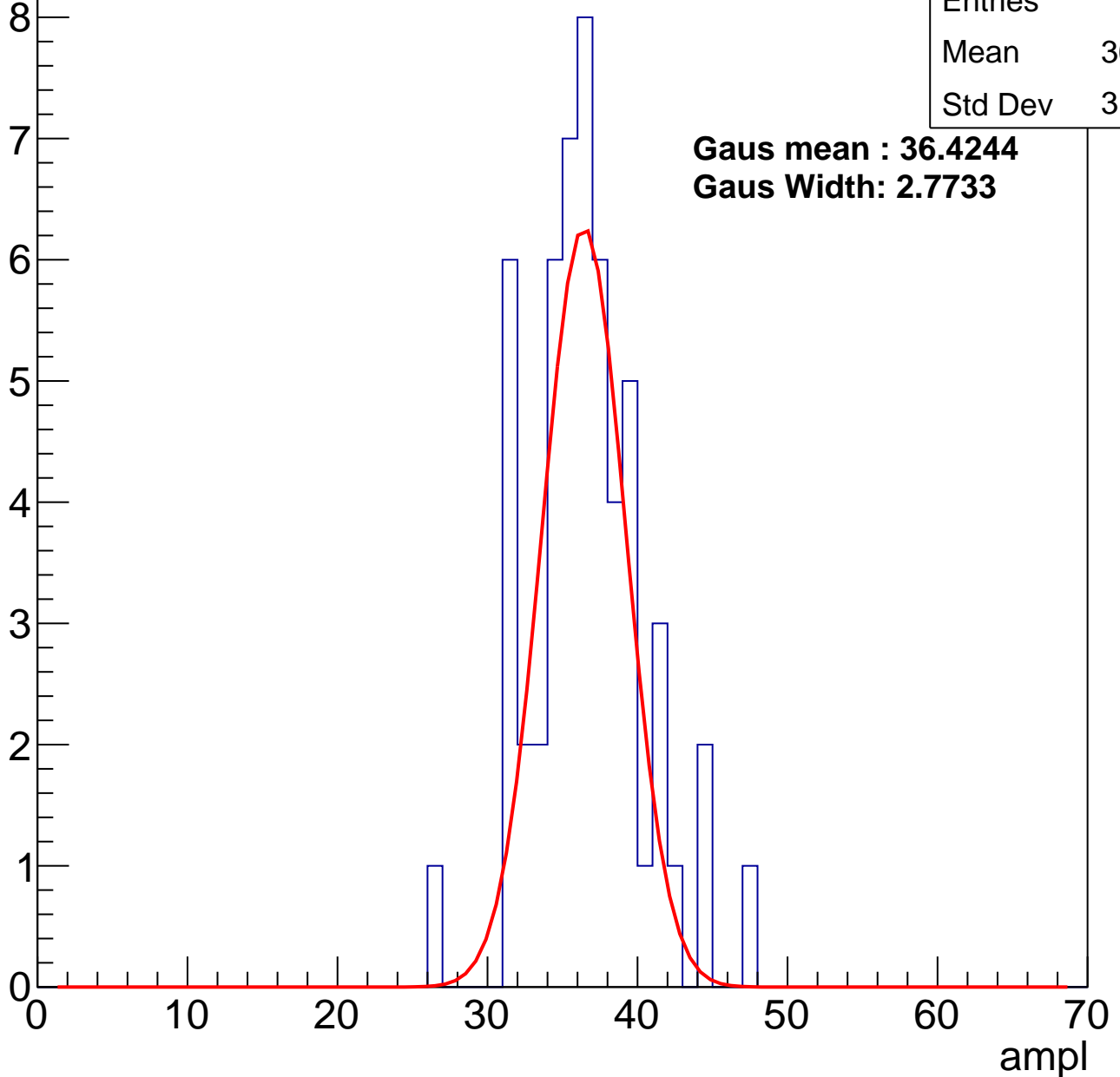
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	36.15
Std Dev	3.749

Gaus mean : 36.4244

Gaus Width: 2.7733



B0L102S, U12-ch12, adc2

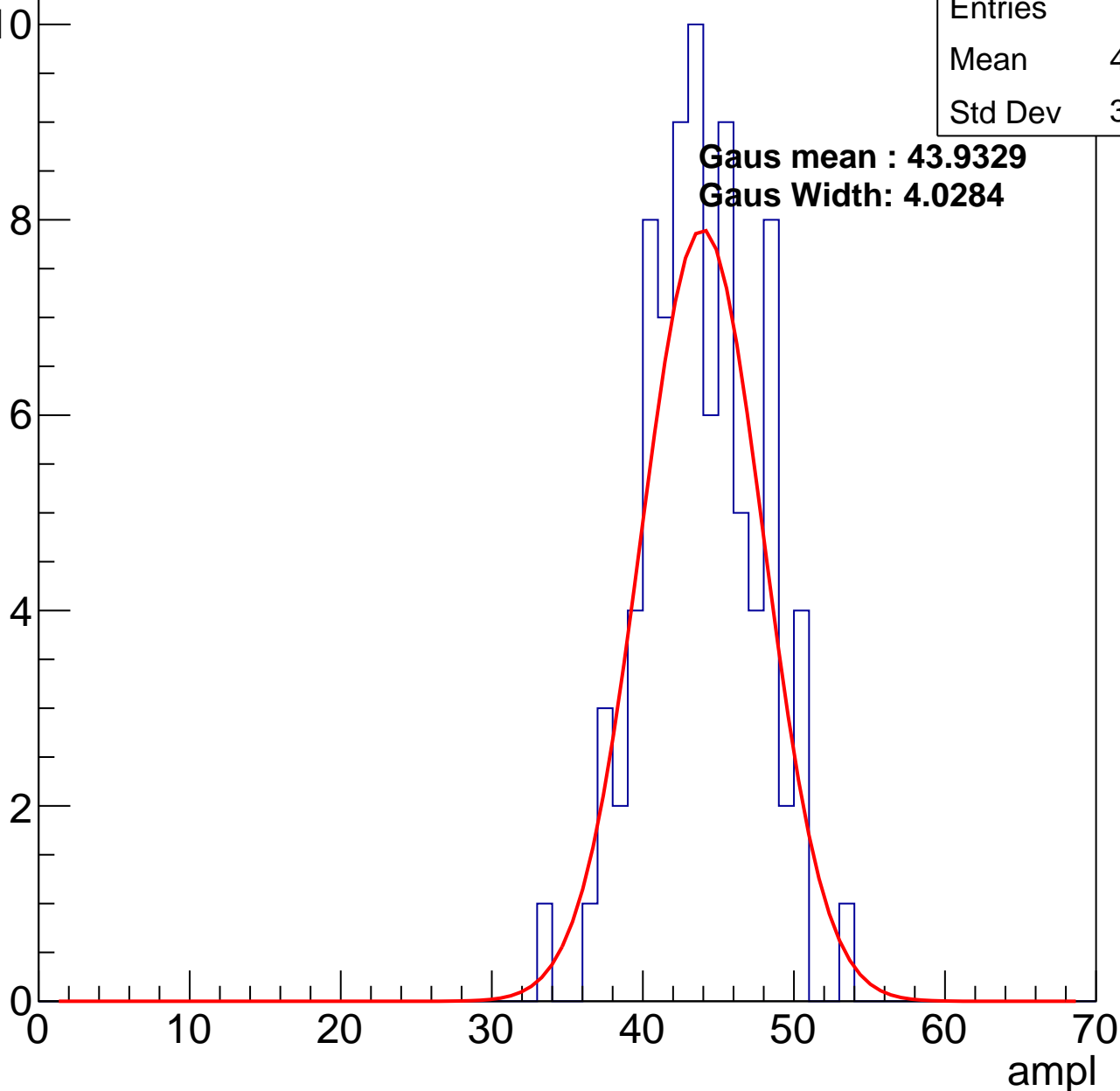
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	43.44
Std Dev	3.752

Gaus mean : 43.9329

Gaus Width: 4.0284

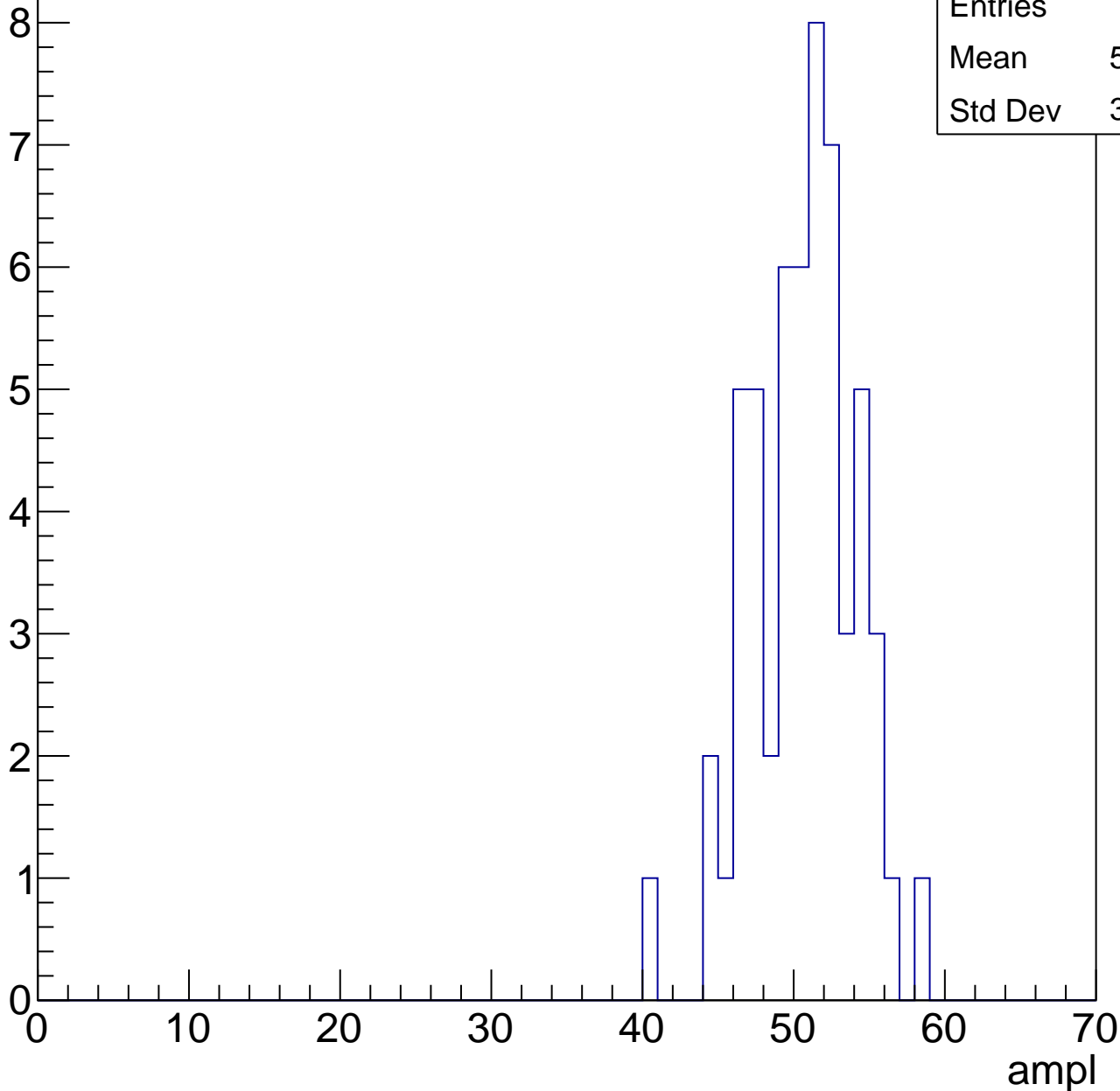


B0L102S, U12-ch12, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	50.14
Std Dev	3.414

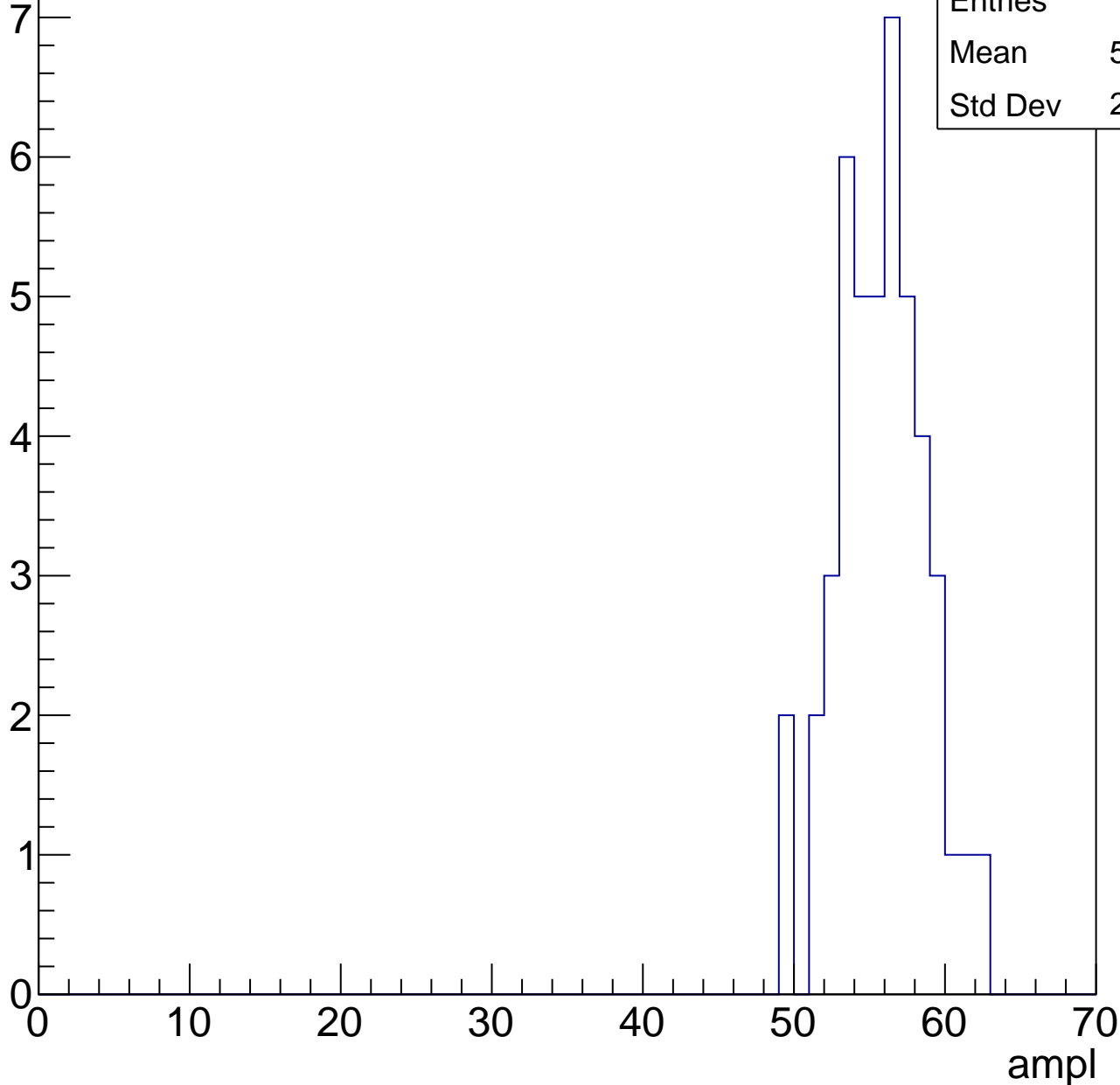


B0L102S, U12-ch12, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

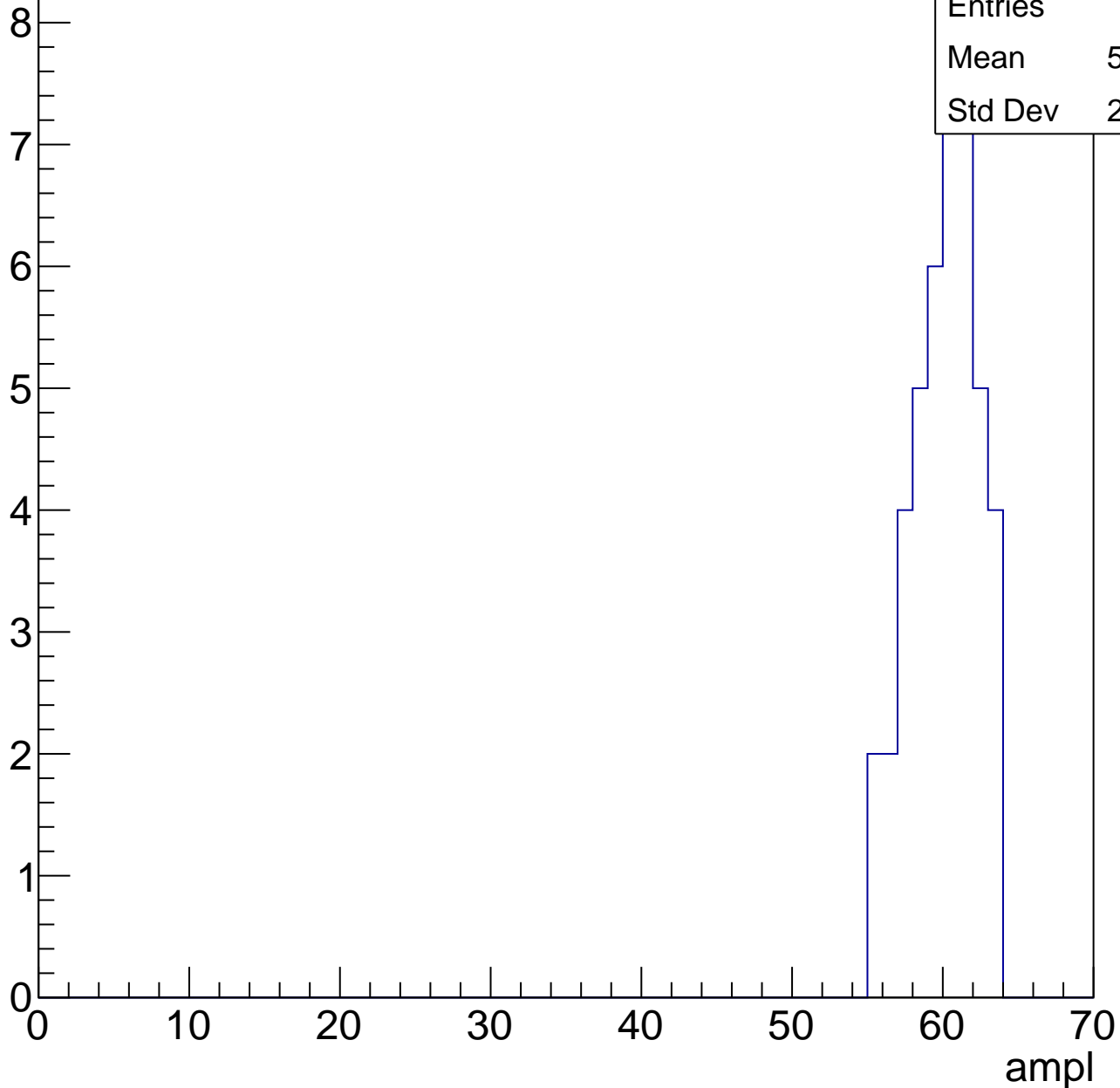
Entries	45
Mean	55.29
Std Dev	2.895



B0L102S, U12-ch12, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

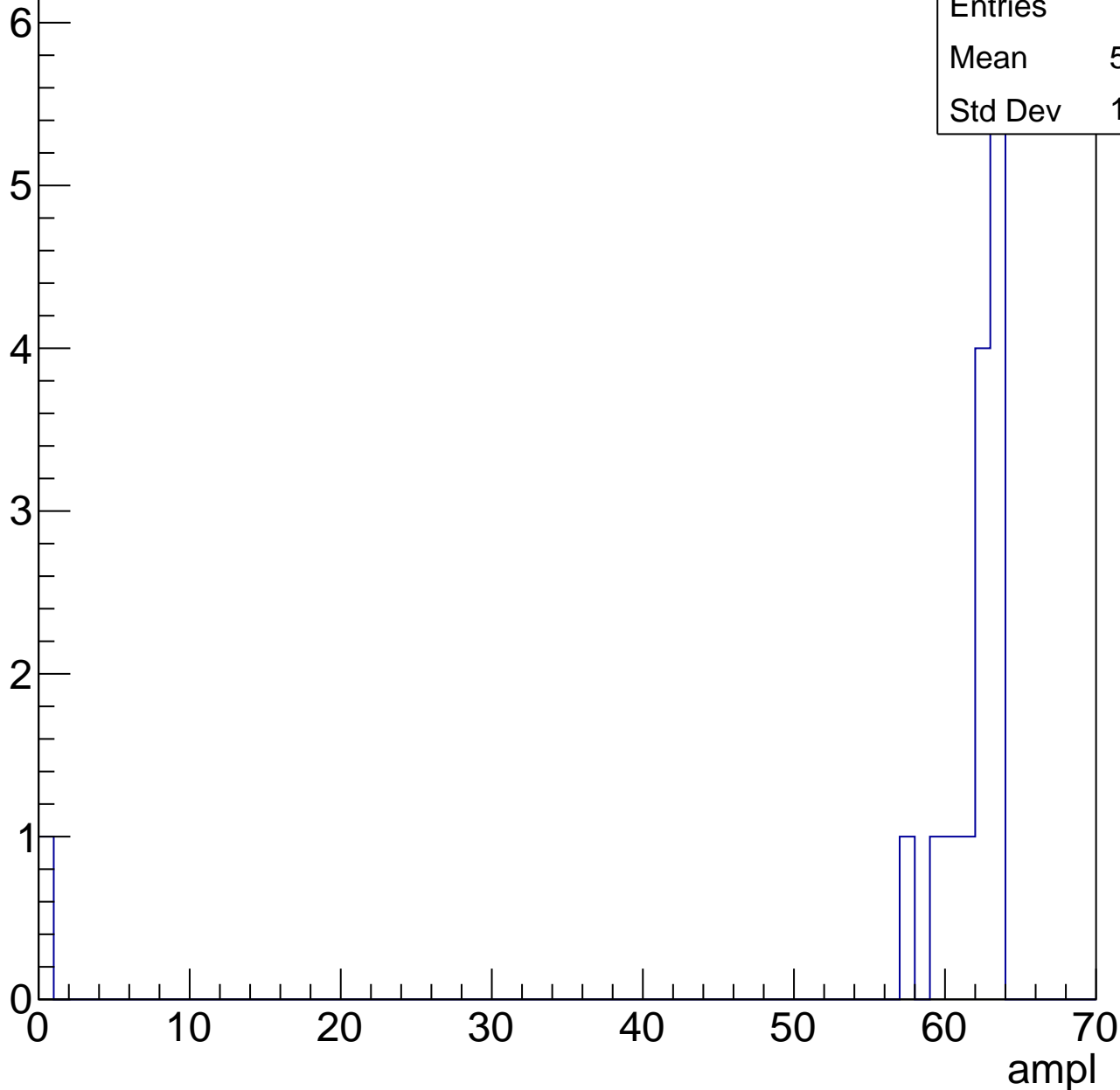


B0L102S, U12-ch12, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.53
Std Dev	15.47



B0L102S, U12-ch12, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch13, adc0

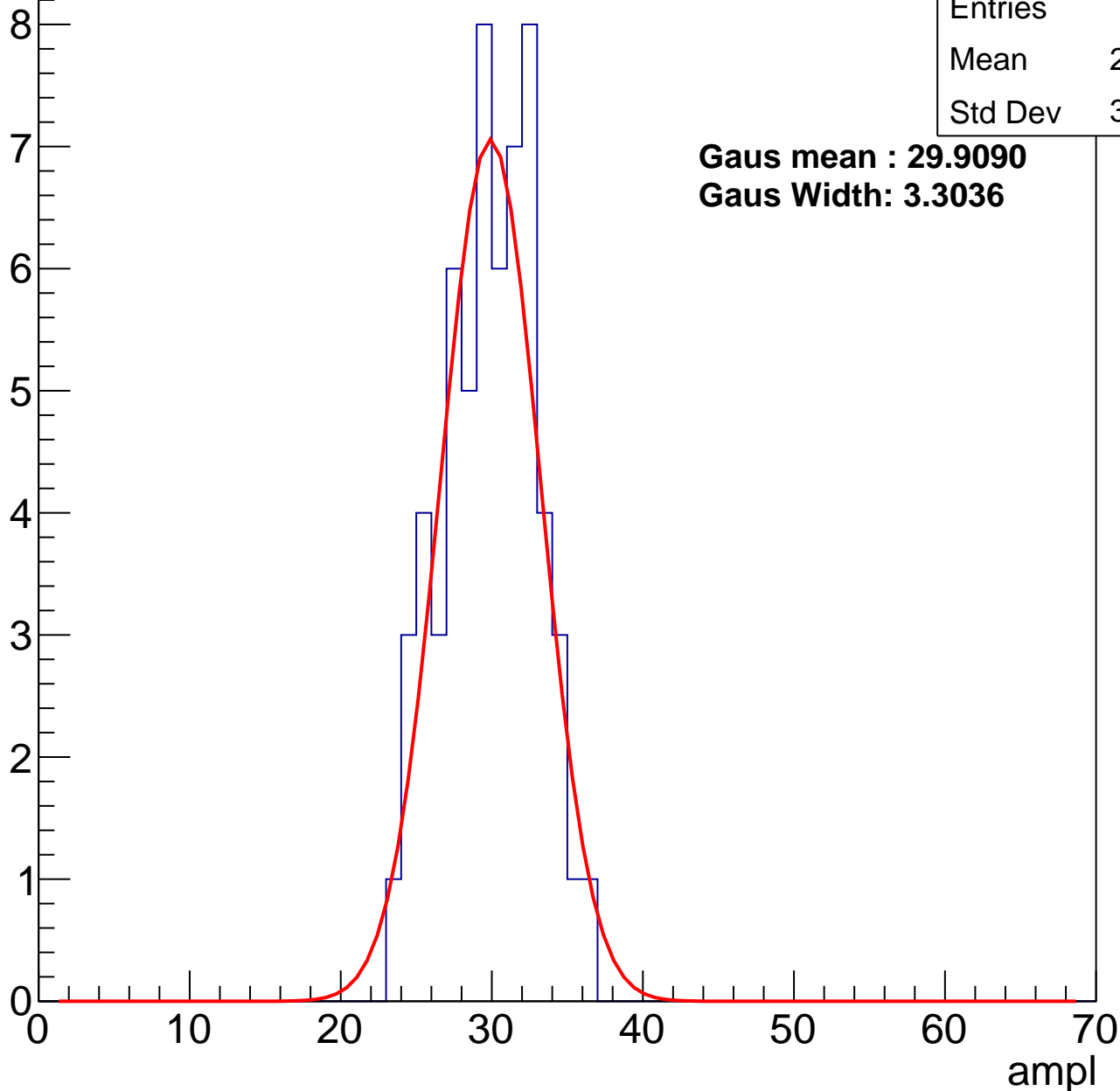
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	29.42
Std Dev	3.029

Gaus mean : 29.9090

Gaus Width: 3.3036



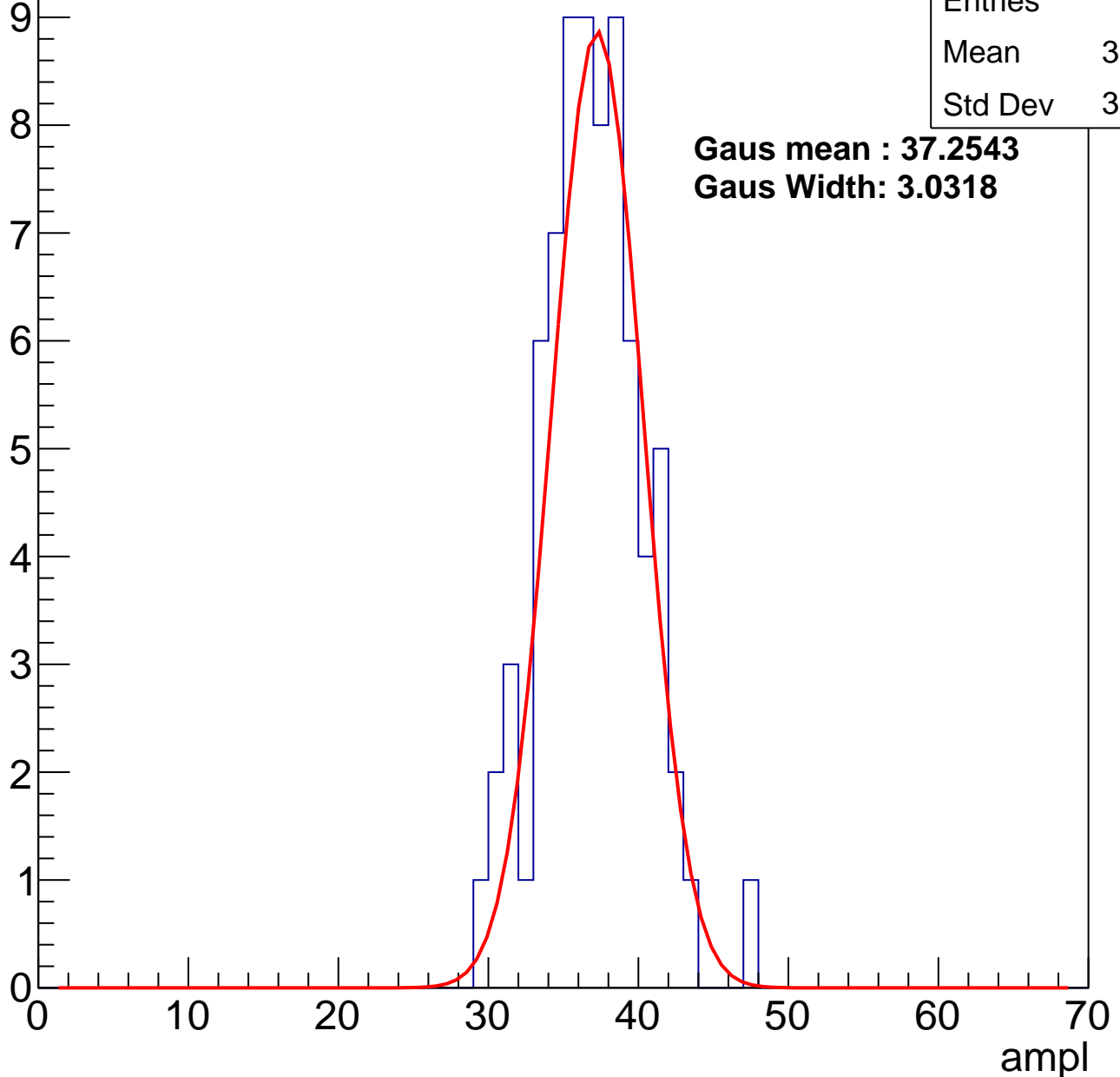
B0L102S, U12-ch13, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	36.49
Std Dev	3.318

Gaus mean : 37.2543
Gaus Width: 3.0318



B0L102S, U12-ch13, adc2

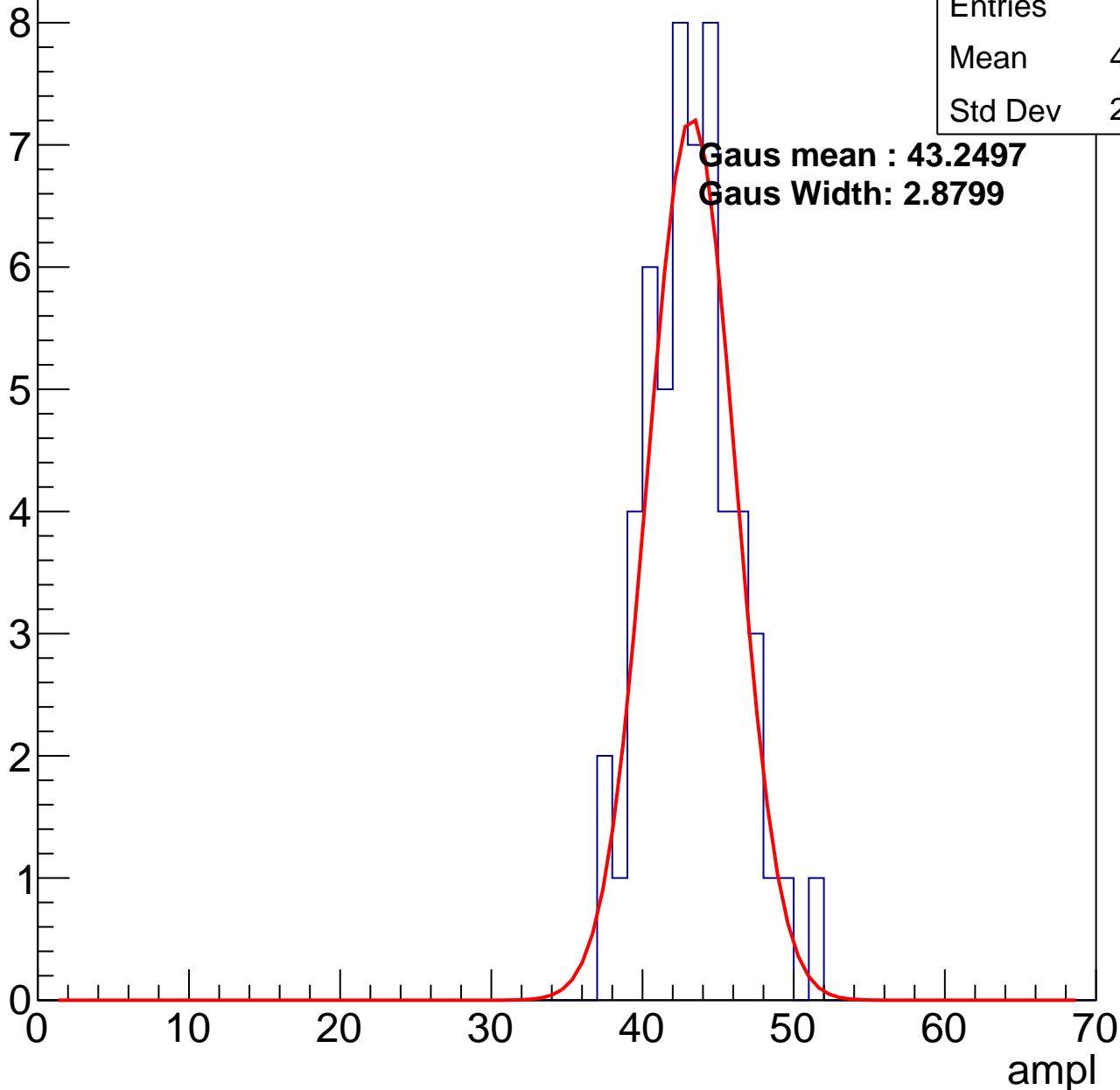
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	42.82
Std Dev	2.936

Gaus mean : 43.2497

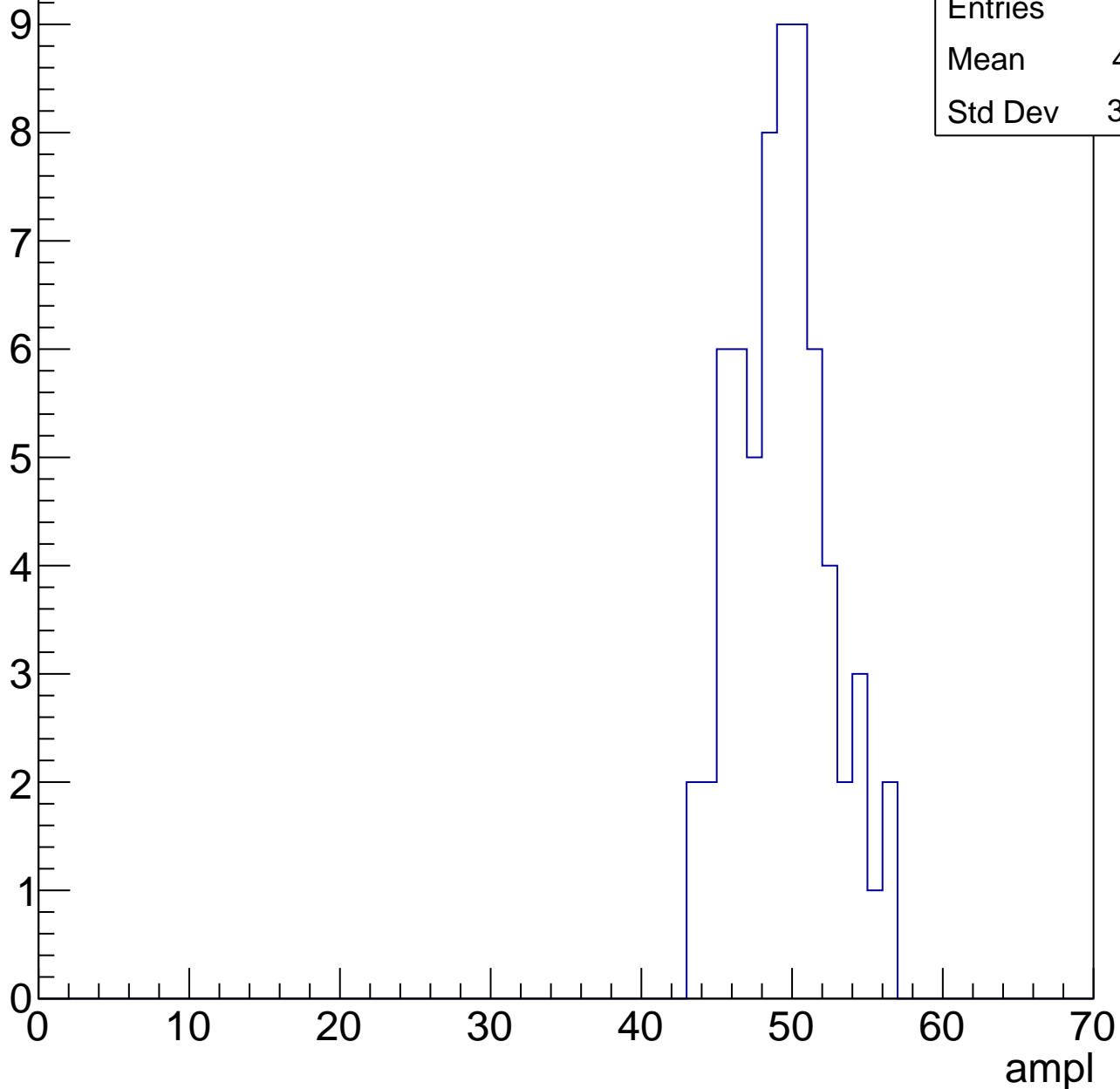
Gaus Width: 2.8799



B0L102S, U12-ch13, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	65
Mean	48.91
Std Dev	3.062

B0L102S, U12-ch13, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	73
Mean	55.45
Std Dev	3.012

Entry

10

8

6

4

2

0

0

10

20

30

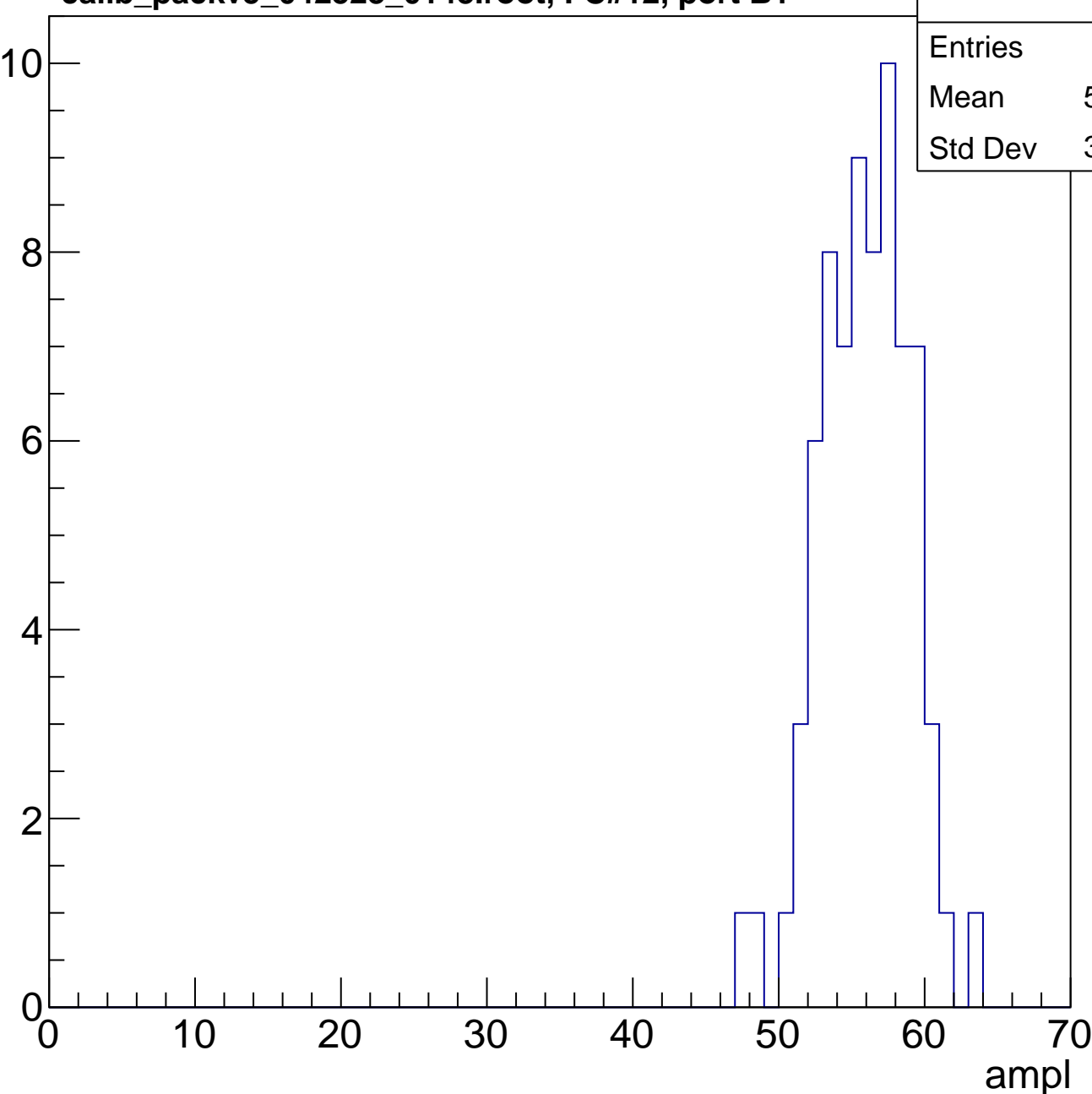
40

50

60

70

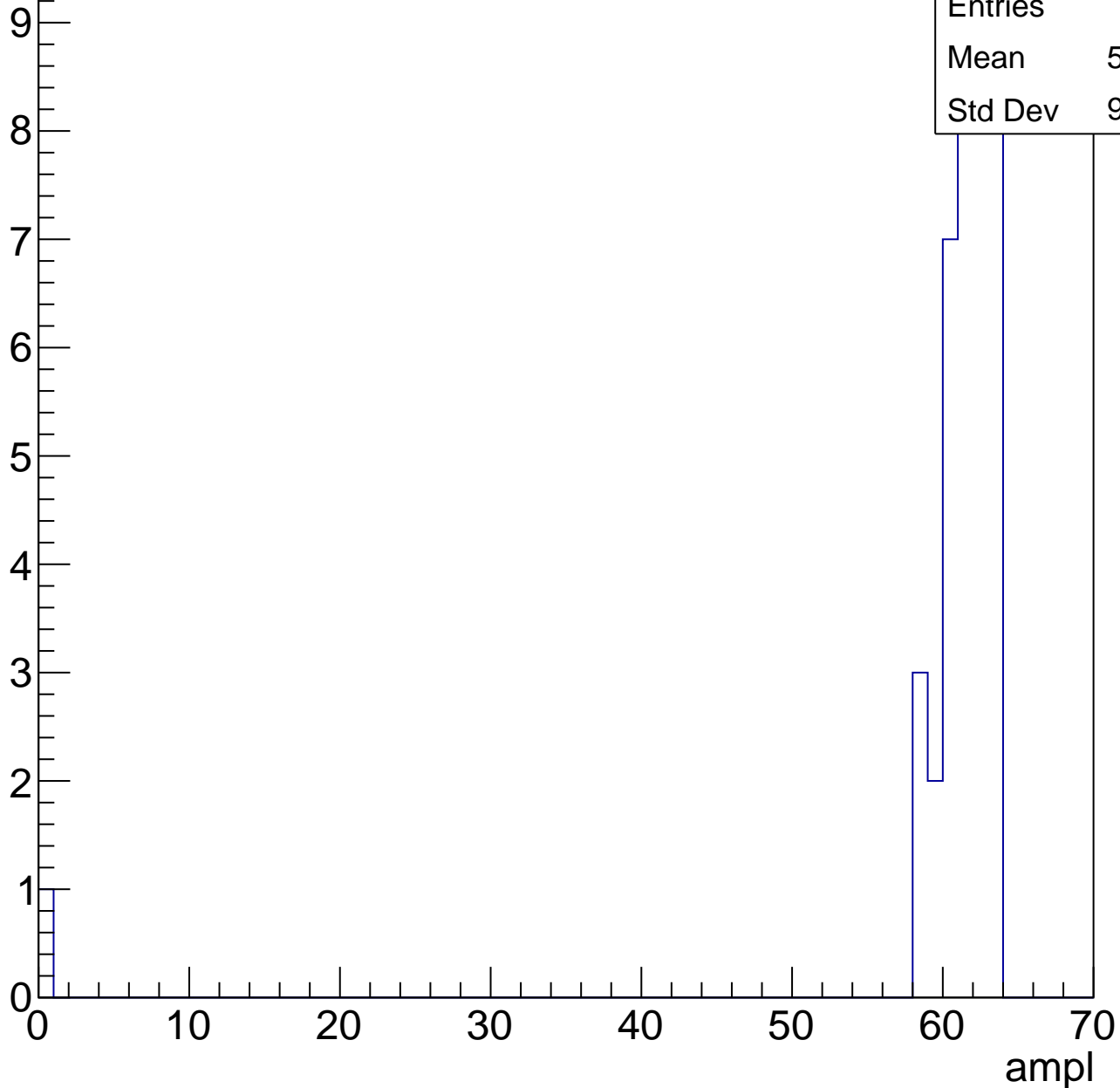
ampl



B0L102S, U12-ch13, adc5

calib_packv5_042523_0143.root, FC#12, port B1

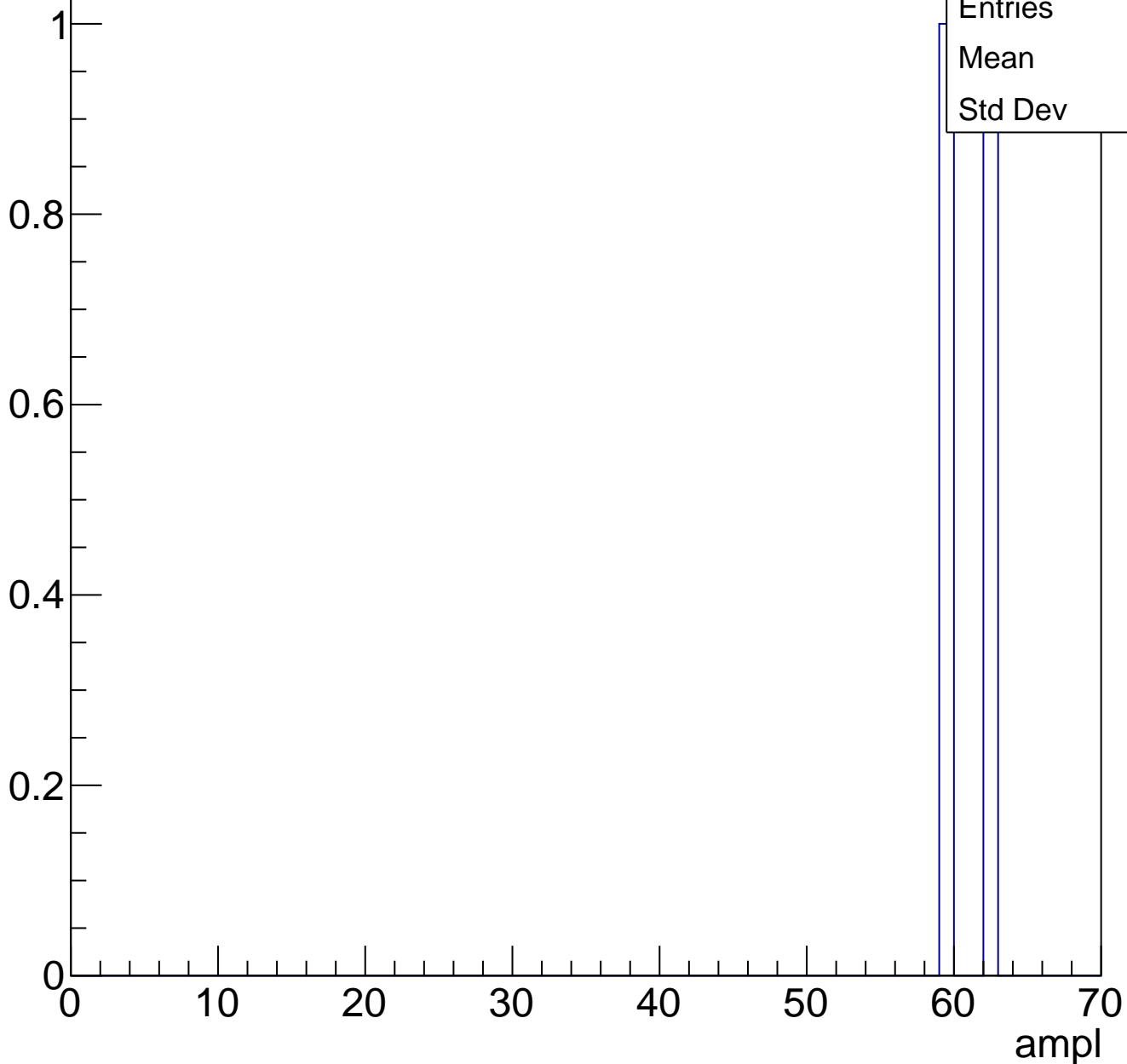
Entry



B0L102S, U12-ch13, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch13, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch14, adc0

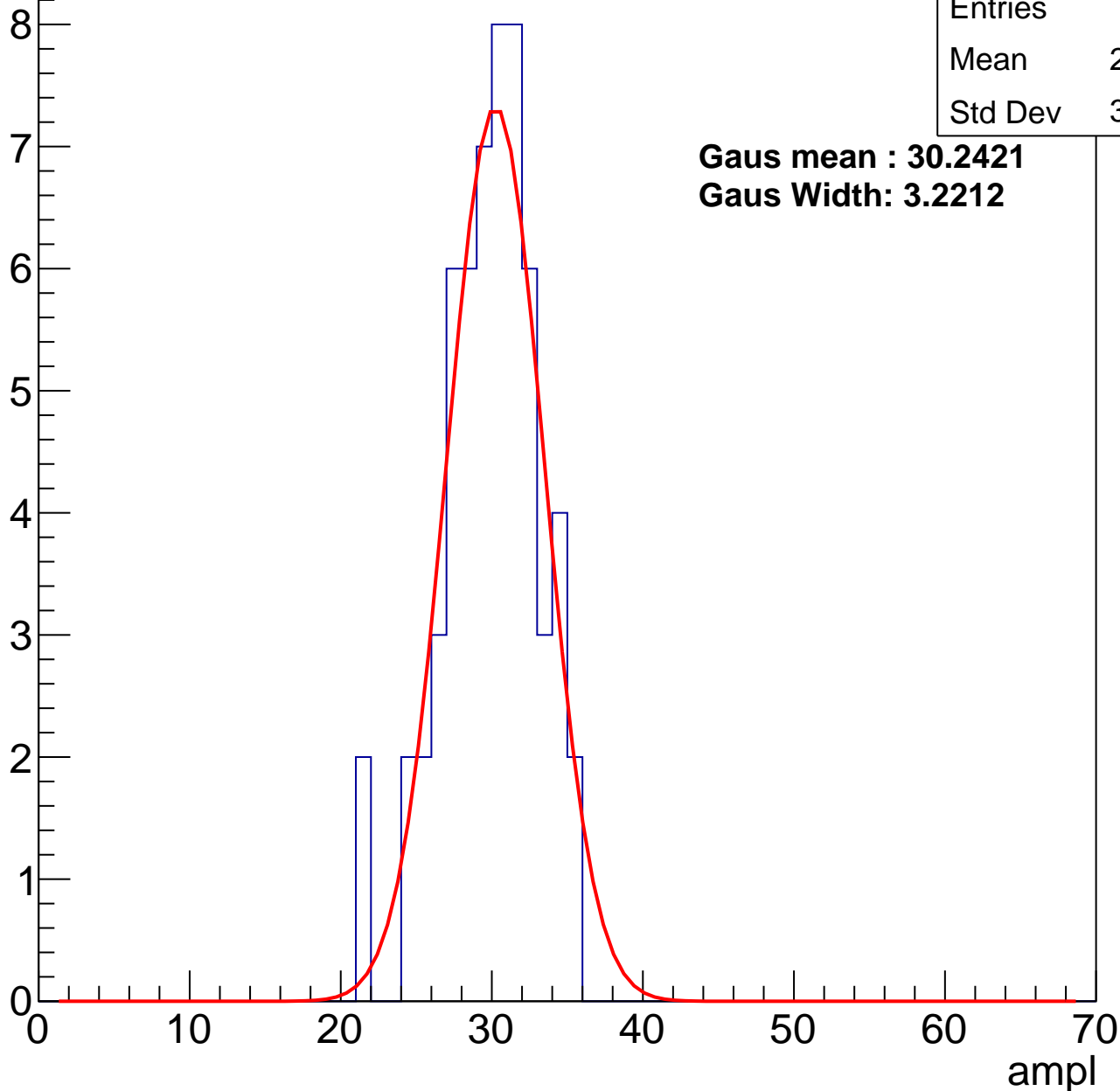
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	29.42
Std Dev	3.109

Gaus mean : 30.2421

Gaus Width: 3.2212



B0L102S, U12-ch14, adc1

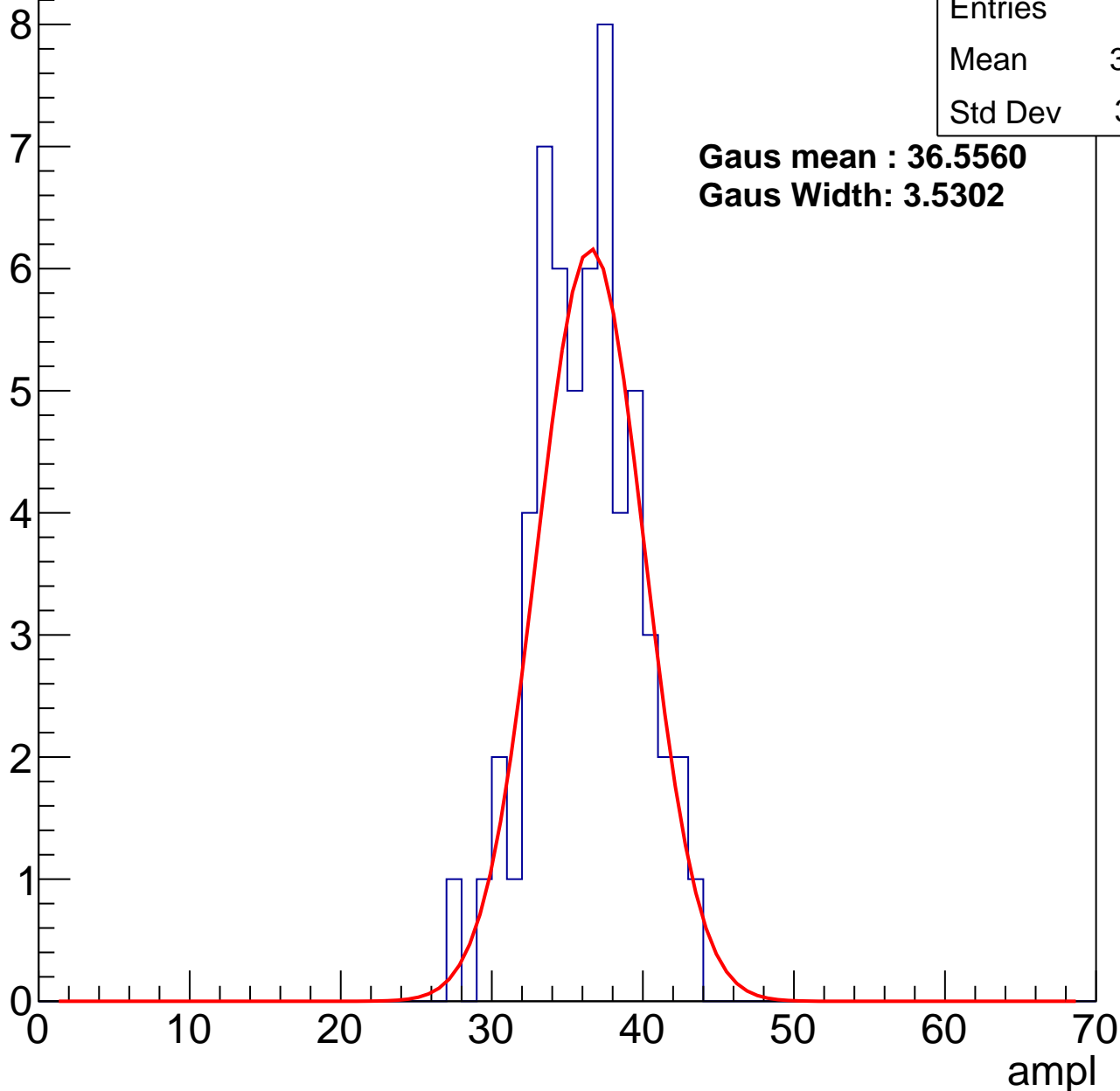
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	35.74
Std Dev	3.391

Gaus mean : 36.5560

Gaus Width: 3.5302



B0L102S, U12-ch14, adc2

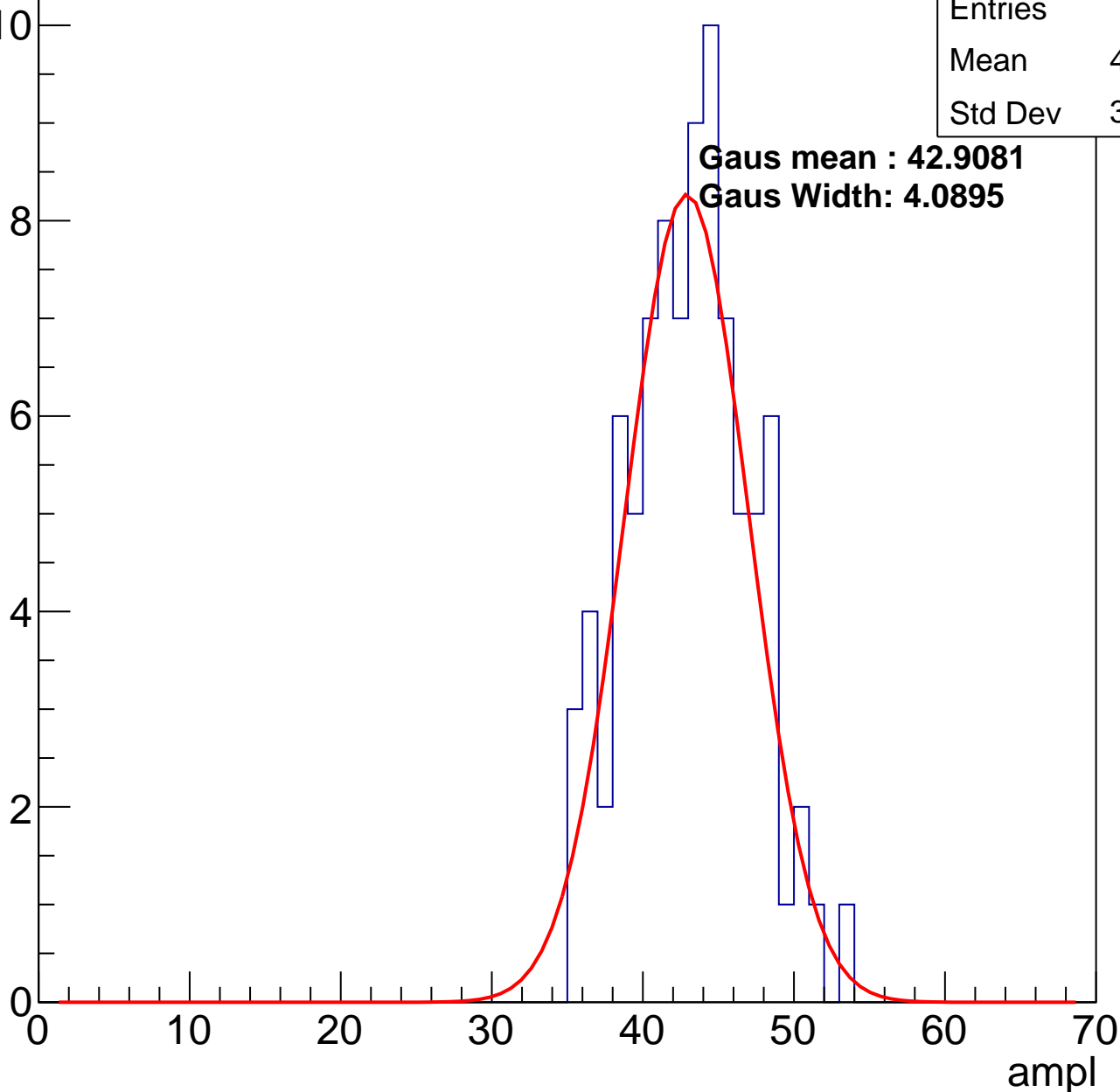
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	42.65
Std Dev	3.955

Gaus mean : 42.9081

Gaus Width: 4.0895

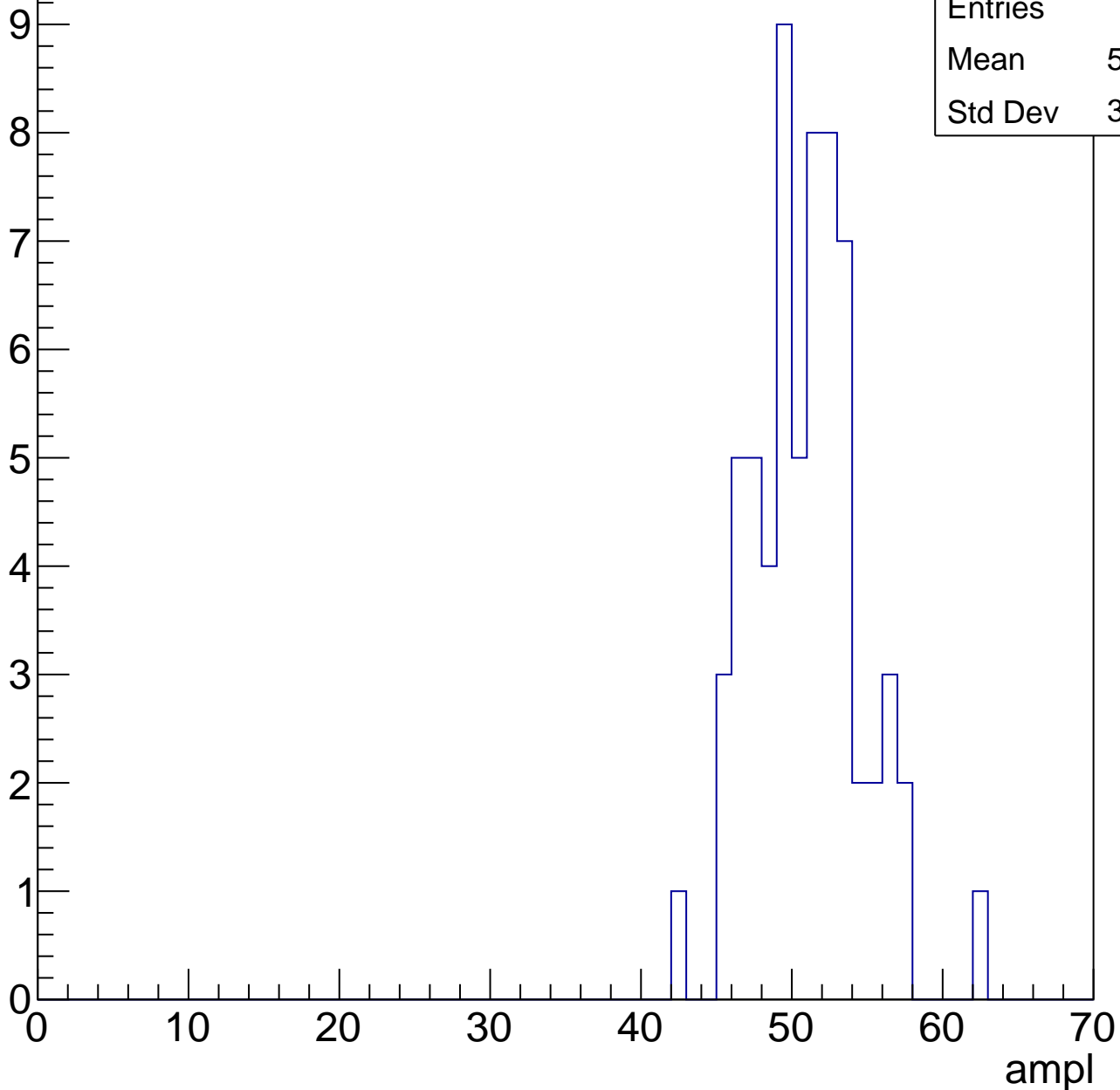


B0L102S, U12-ch14, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	50.49
Std Dev	3.526



B0L102S, U12-ch14, adc4

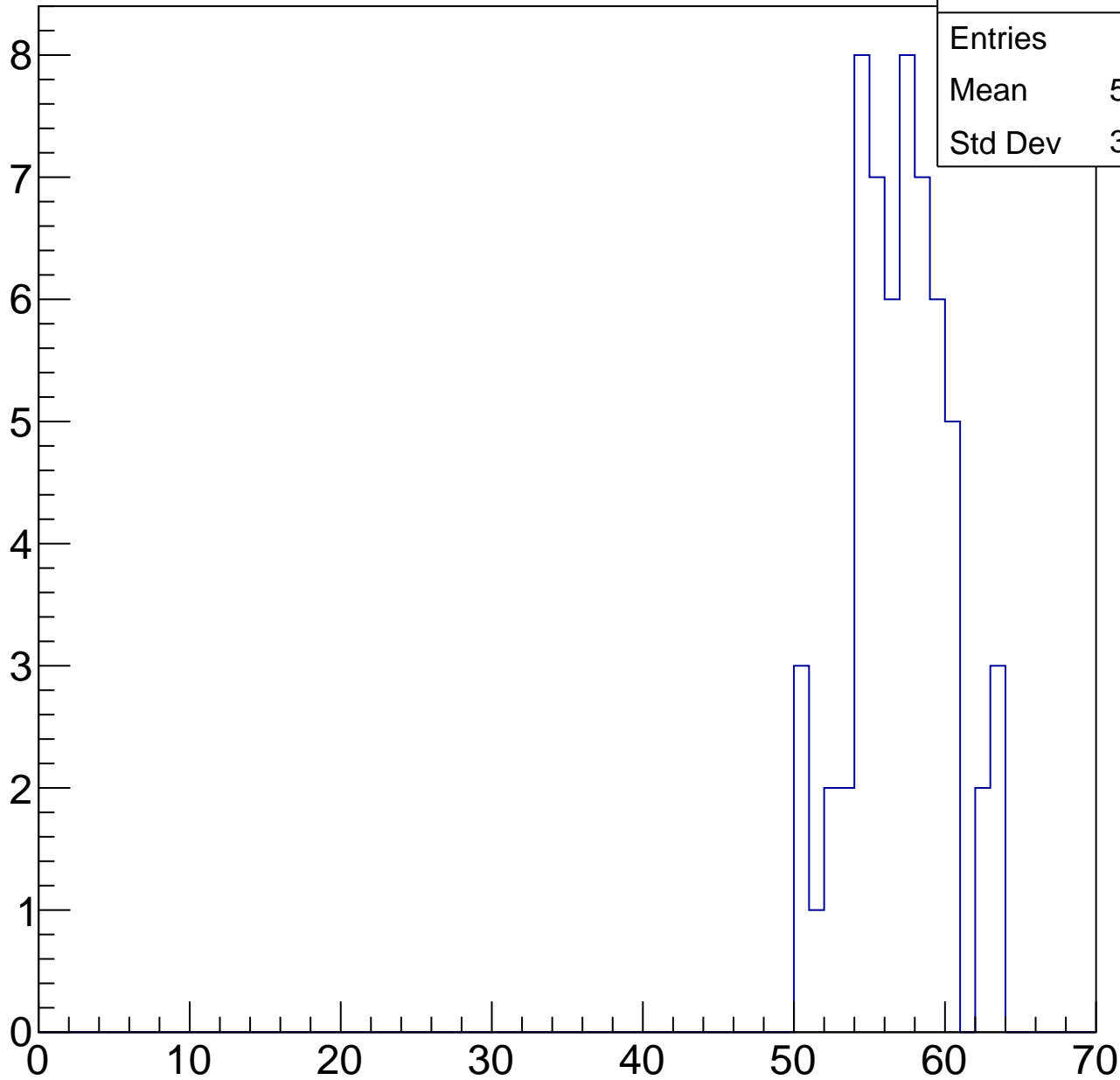
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	60
Mean	56.55
Std Dev	3.143

ampl

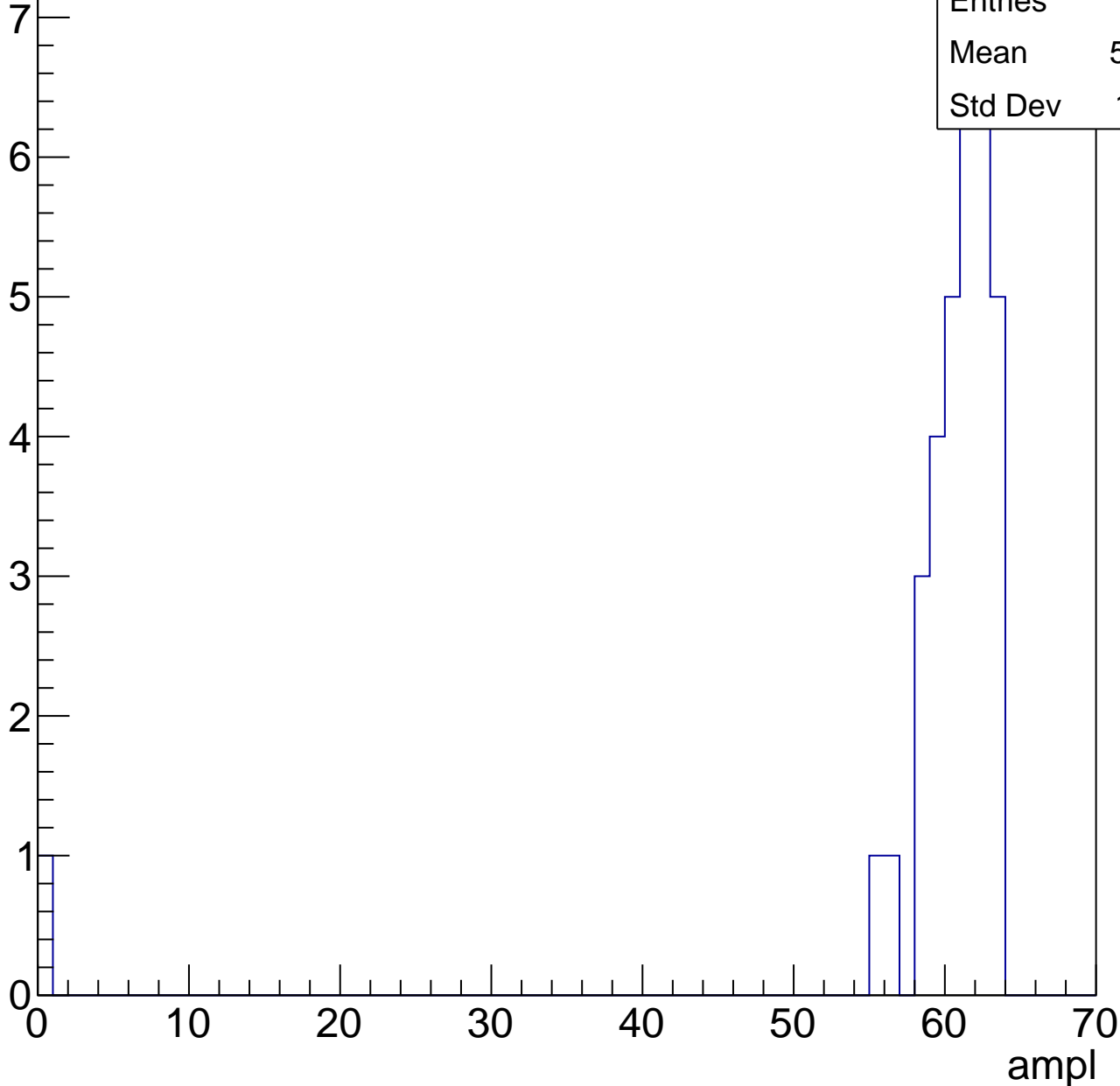


B0L102S, U12-ch14, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	34
Mean	58.74
Std Dev	10.41



B0L102S, U12-ch14, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

61.8

Std Dev

0.9798

B0L102S, U12-ch14, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch15, adc0

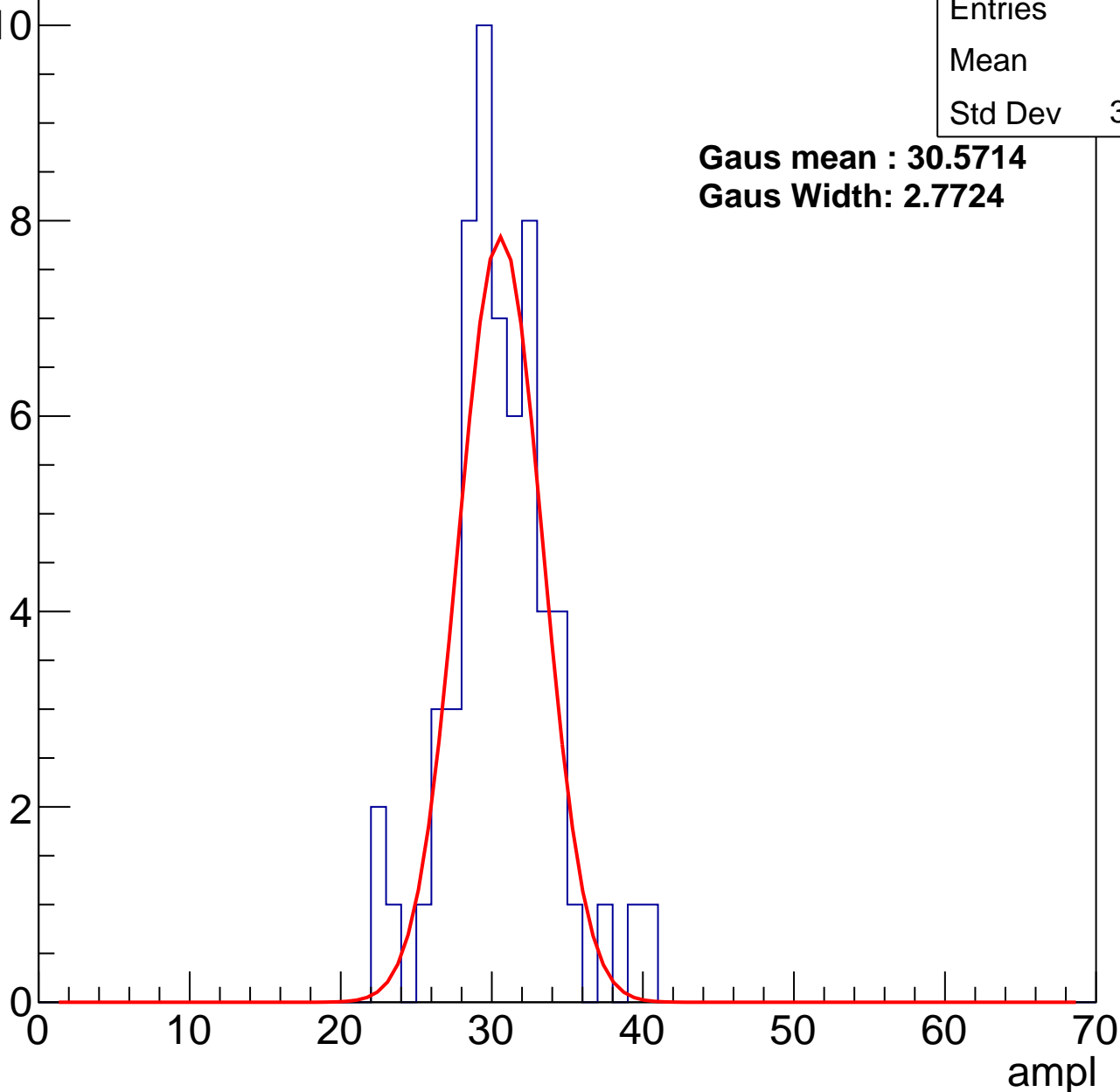
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	30.1
Std Dev	3.415

Gaus mean : 30.5714

Gaus Width: 2.7724



B0L102S, U12-ch15, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	61
Mean	35.95
Std Dev	3.005

Gaus mean : 36.7093

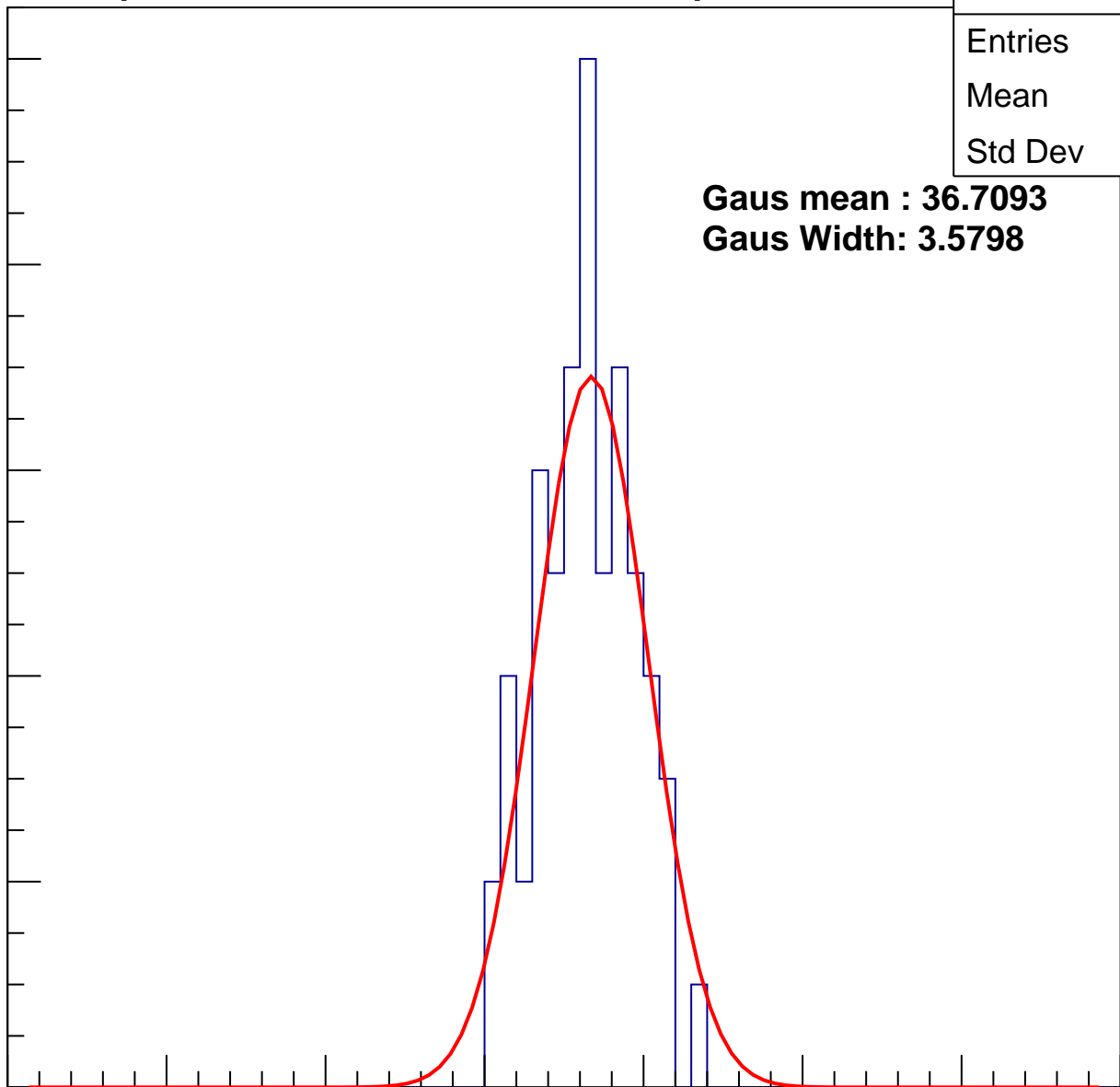
Gaus Width: 3.5798

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch15, adc2

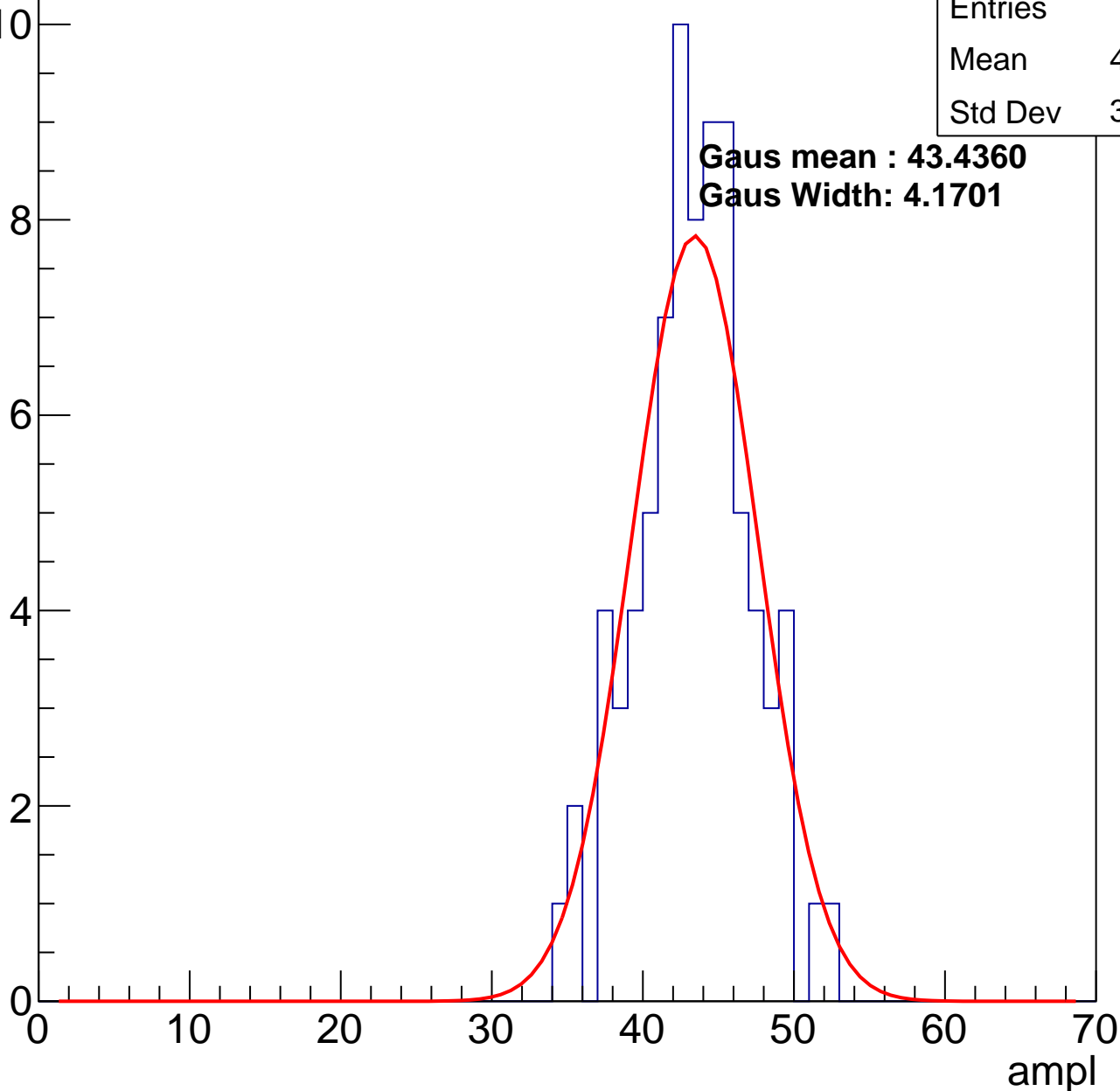
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	42.94
Std Dev	3.696

Gaus mean : 43.4360

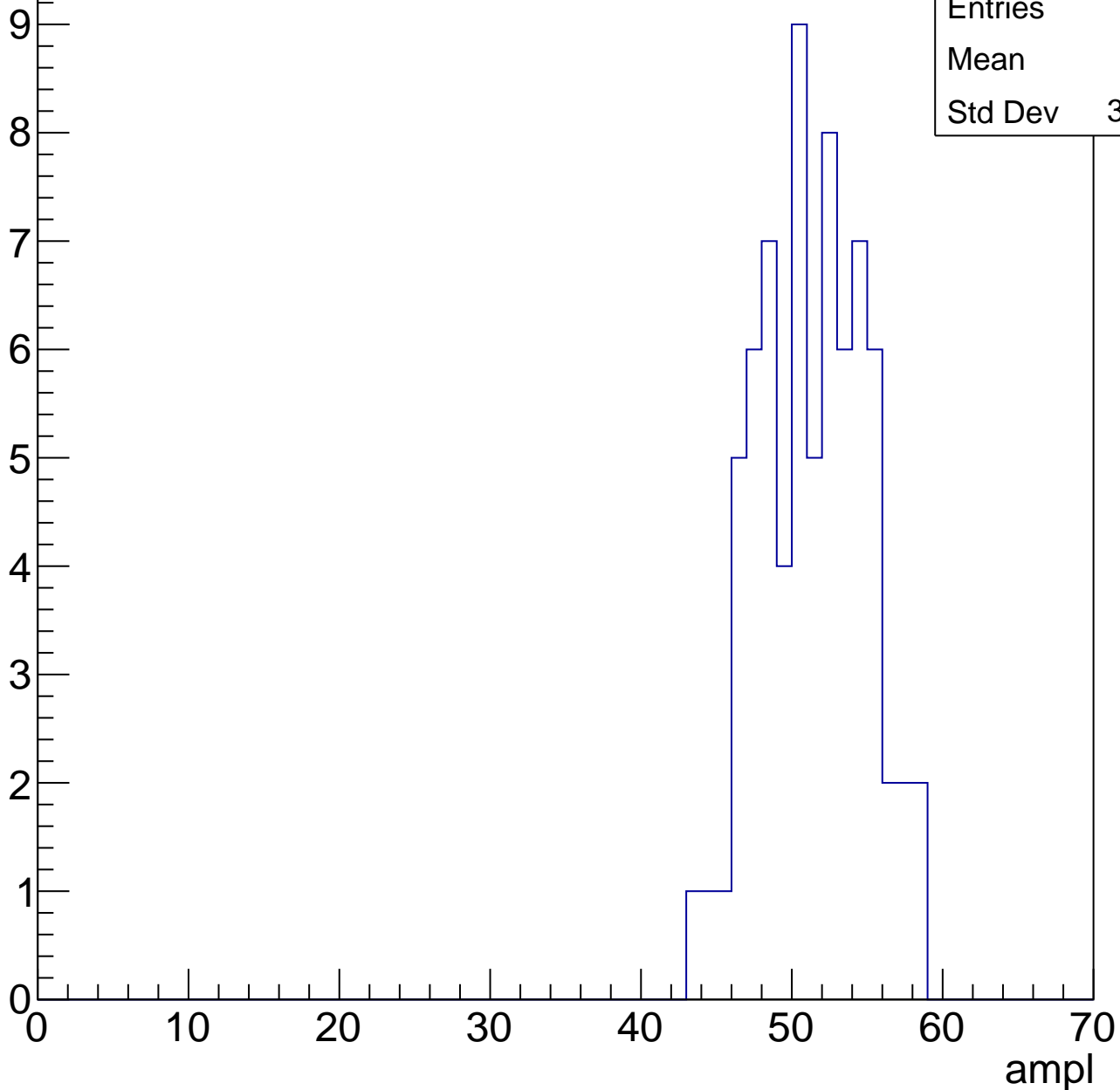
Gaus Width: 4.1701



B0L102S, U12-ch15, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

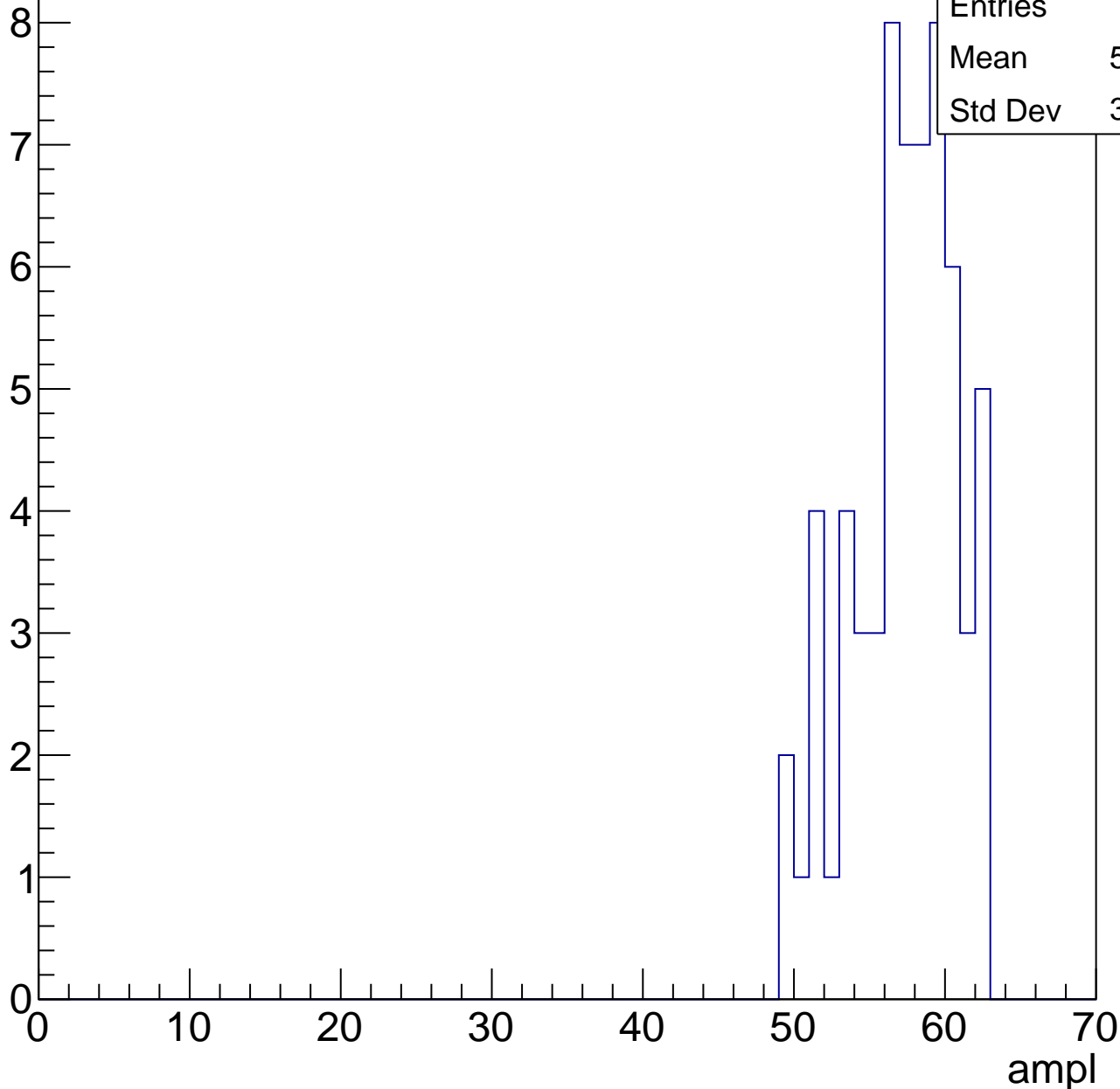


B0L102S, U12-ch15, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	56.79
Std Dev	3.408

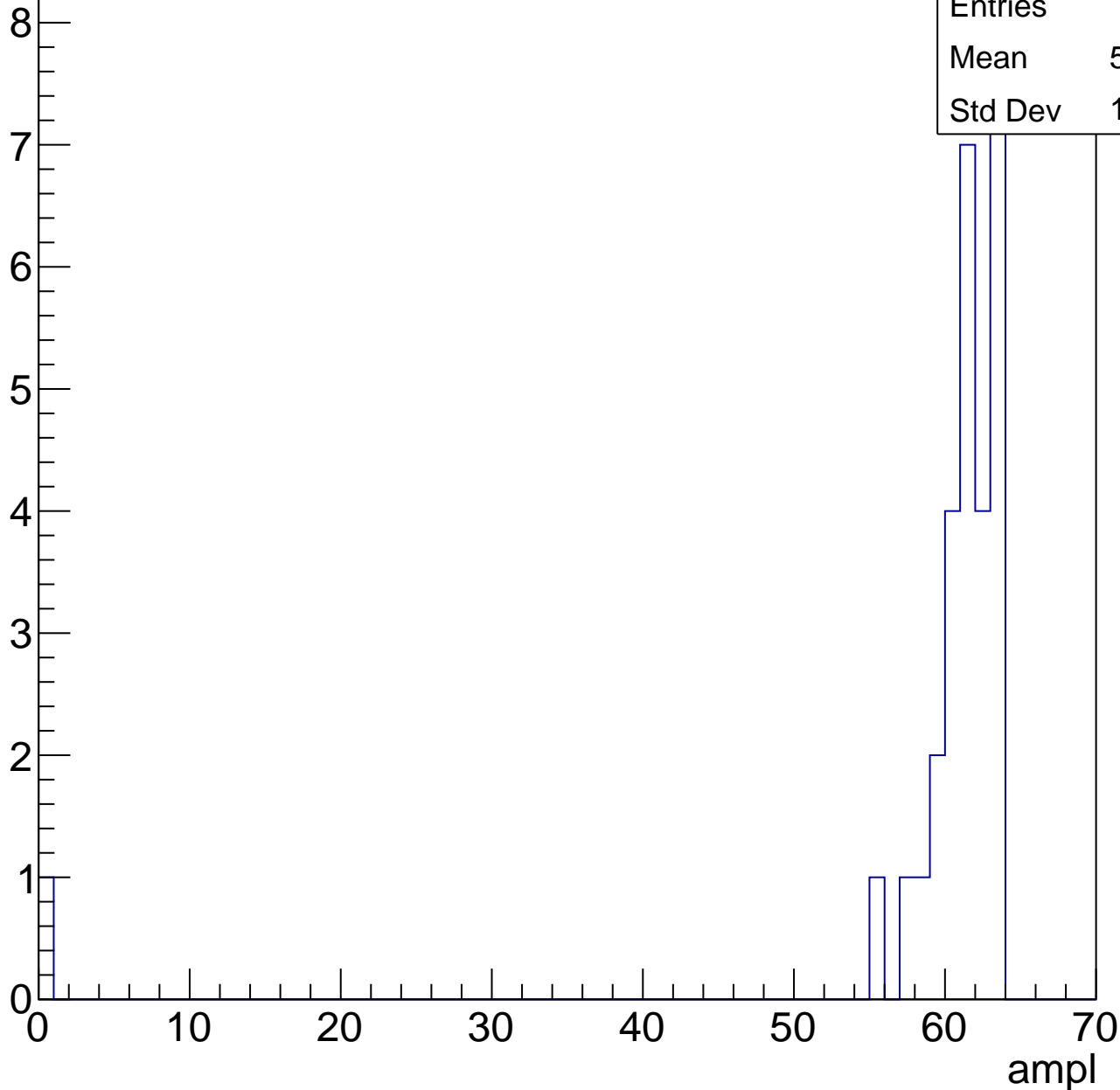


B0L102S, U12-ch15, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	29
Mean	58.86
Std Dev	11.29



B0L102S, U12-ch15, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch15, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch16, adc0

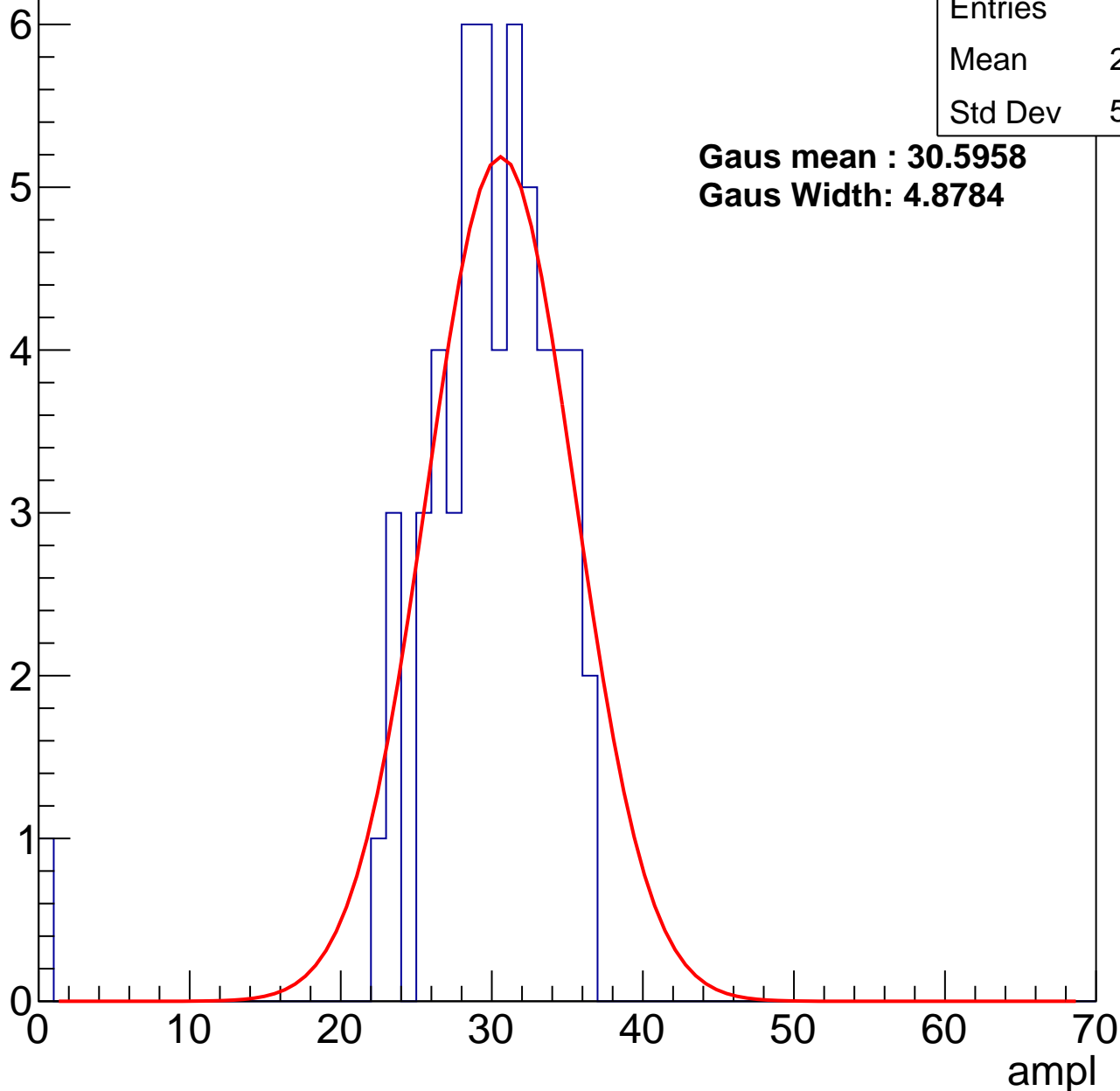
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	29.27
Std Dev	5.303

Gaus mean : 30.5958

Gaus Width: 4.8784



B0L102S, U12-ch16, adc1

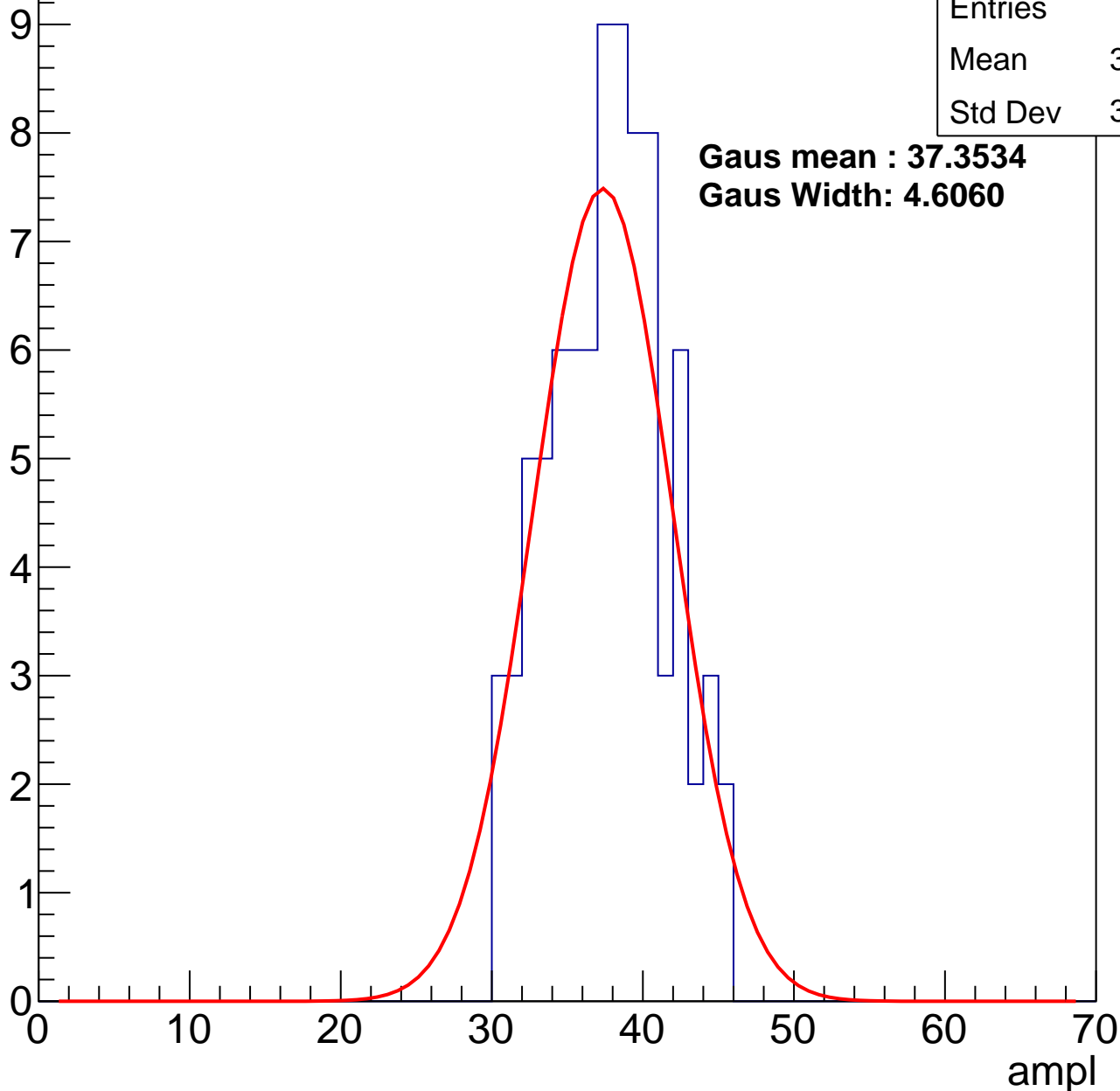
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	37.24
Std Dev	3.775

Gaus mean : 37.3534

Gaus Width: 4.6060



B0L102S, U12-ch16, adc2

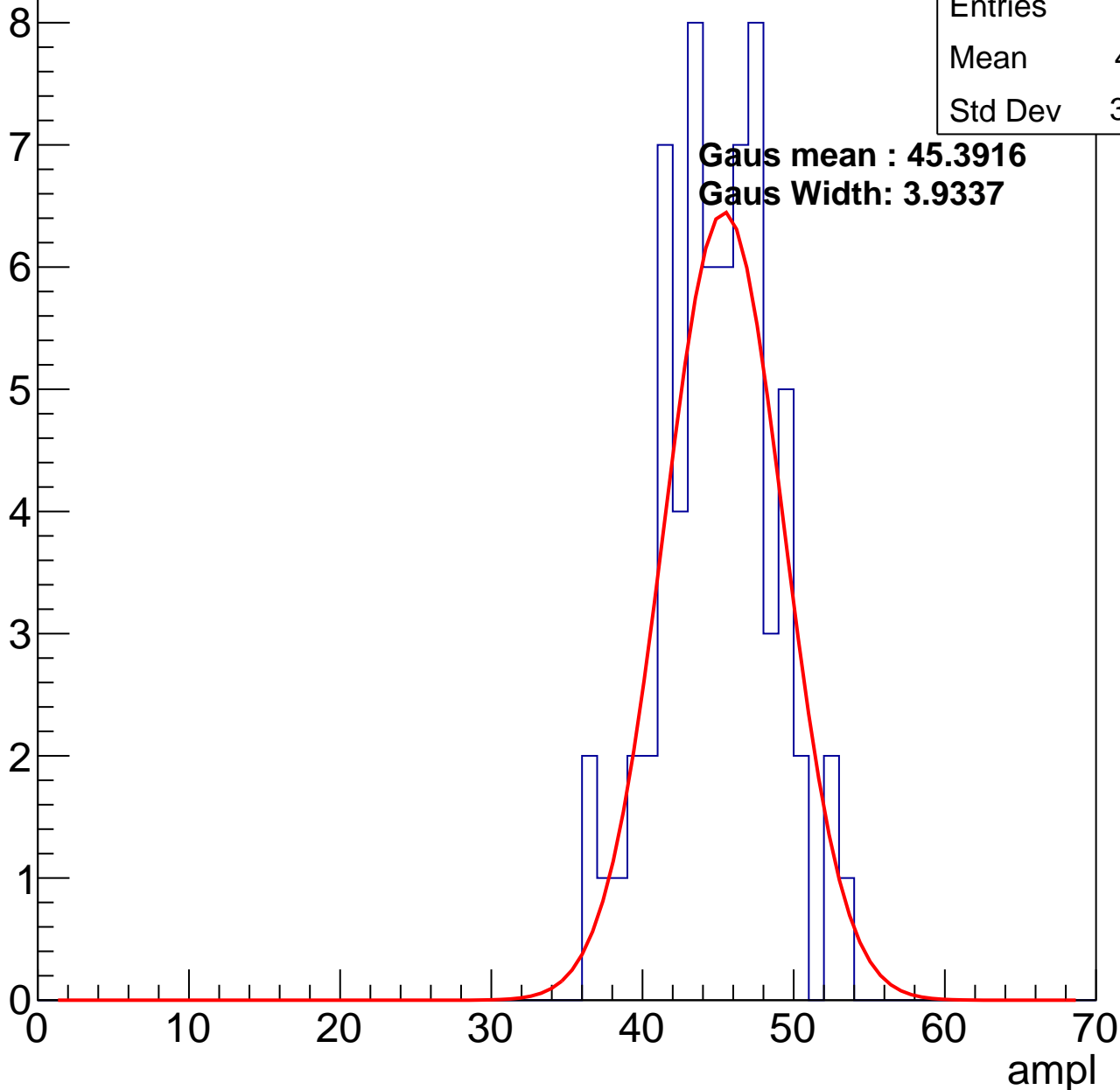
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	44.51
Std Dev	3.715

Gaus mean : 45.3916

Gaus Width: 3.9337

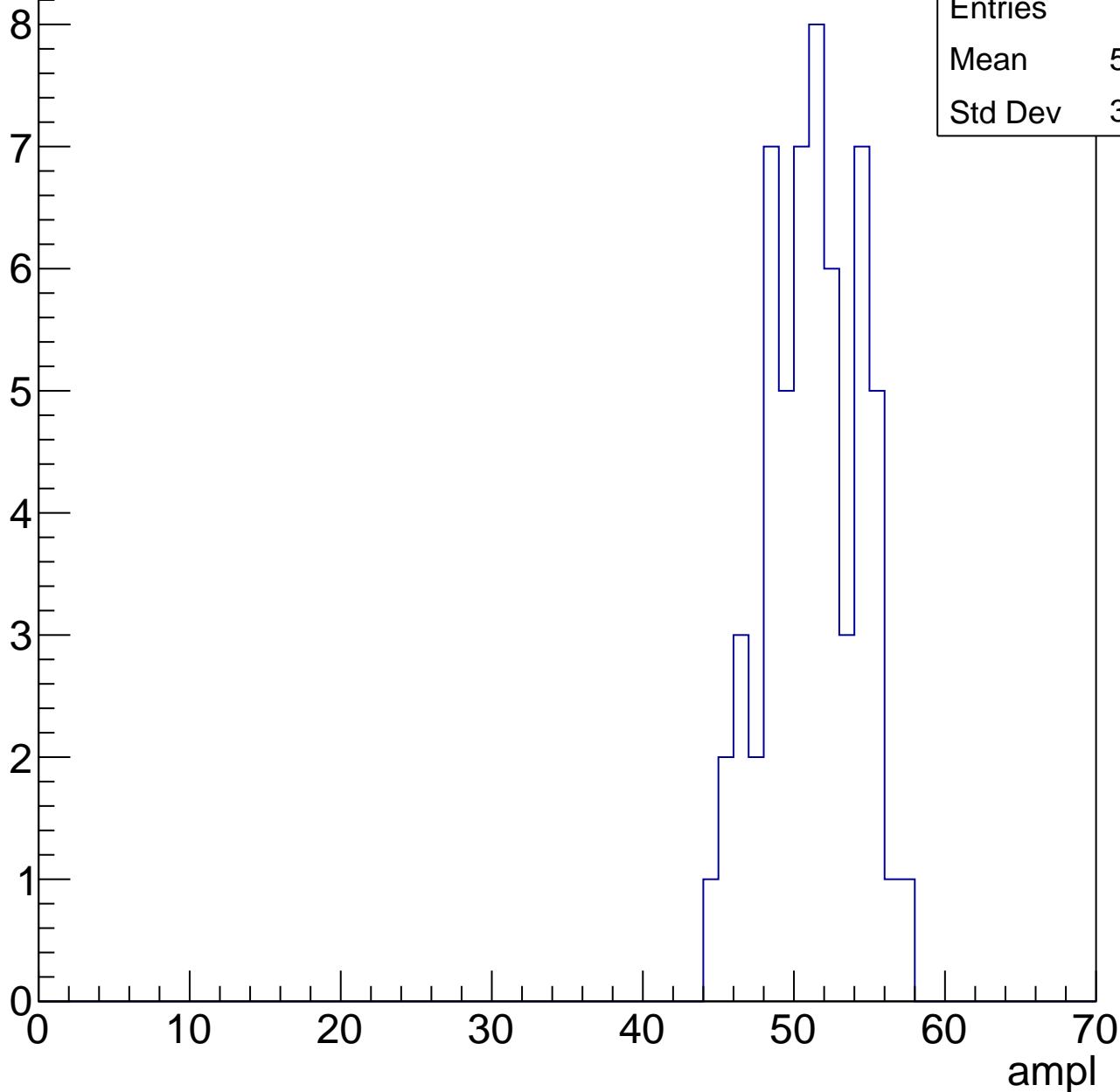


B0L102S, U12-ch16, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	50.72
Std Dev	3.039

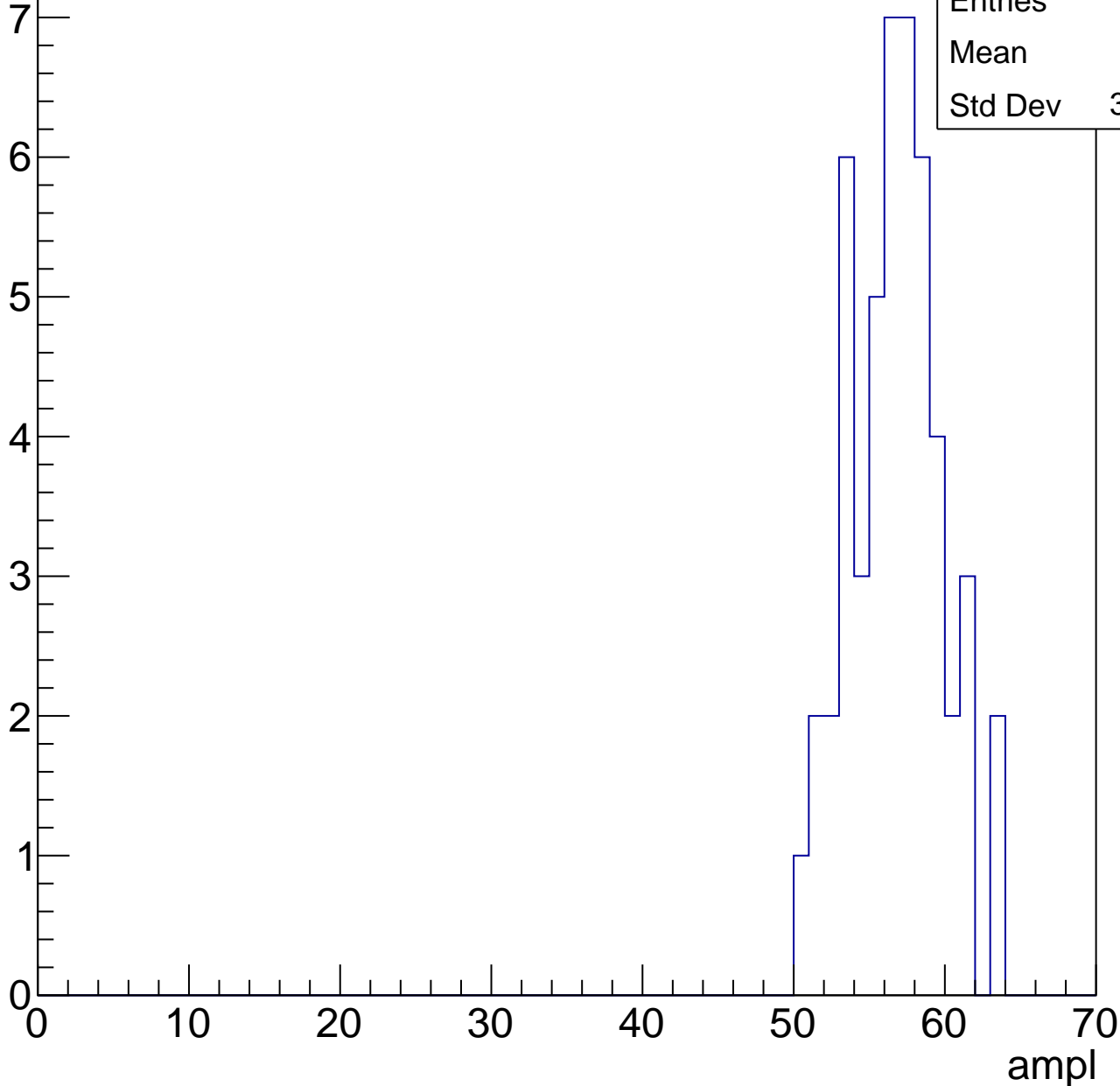


B0L102S, U12-ch16, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	56.3
Std Dev	3.022

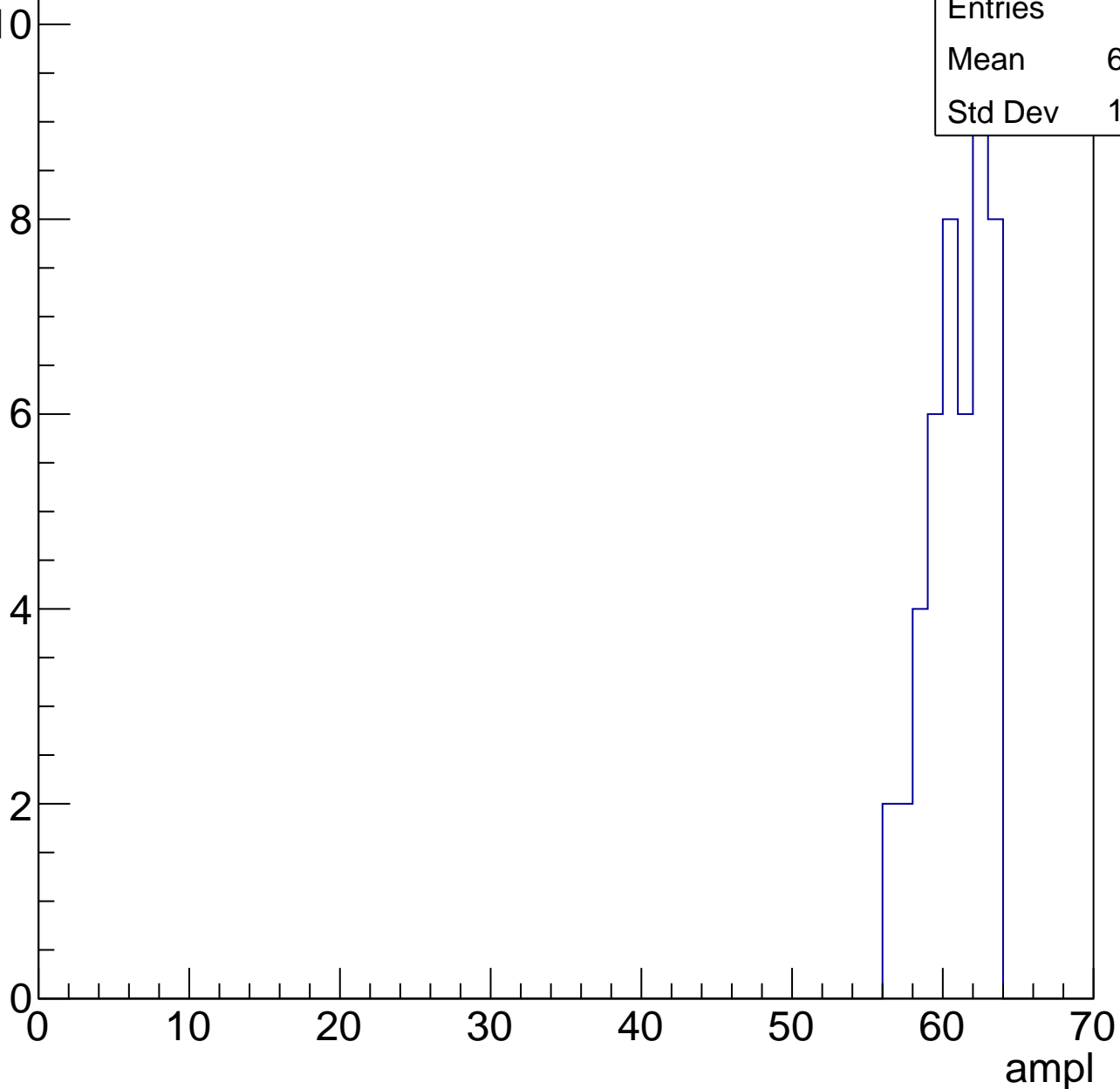


B0L102S, U12-ch16, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

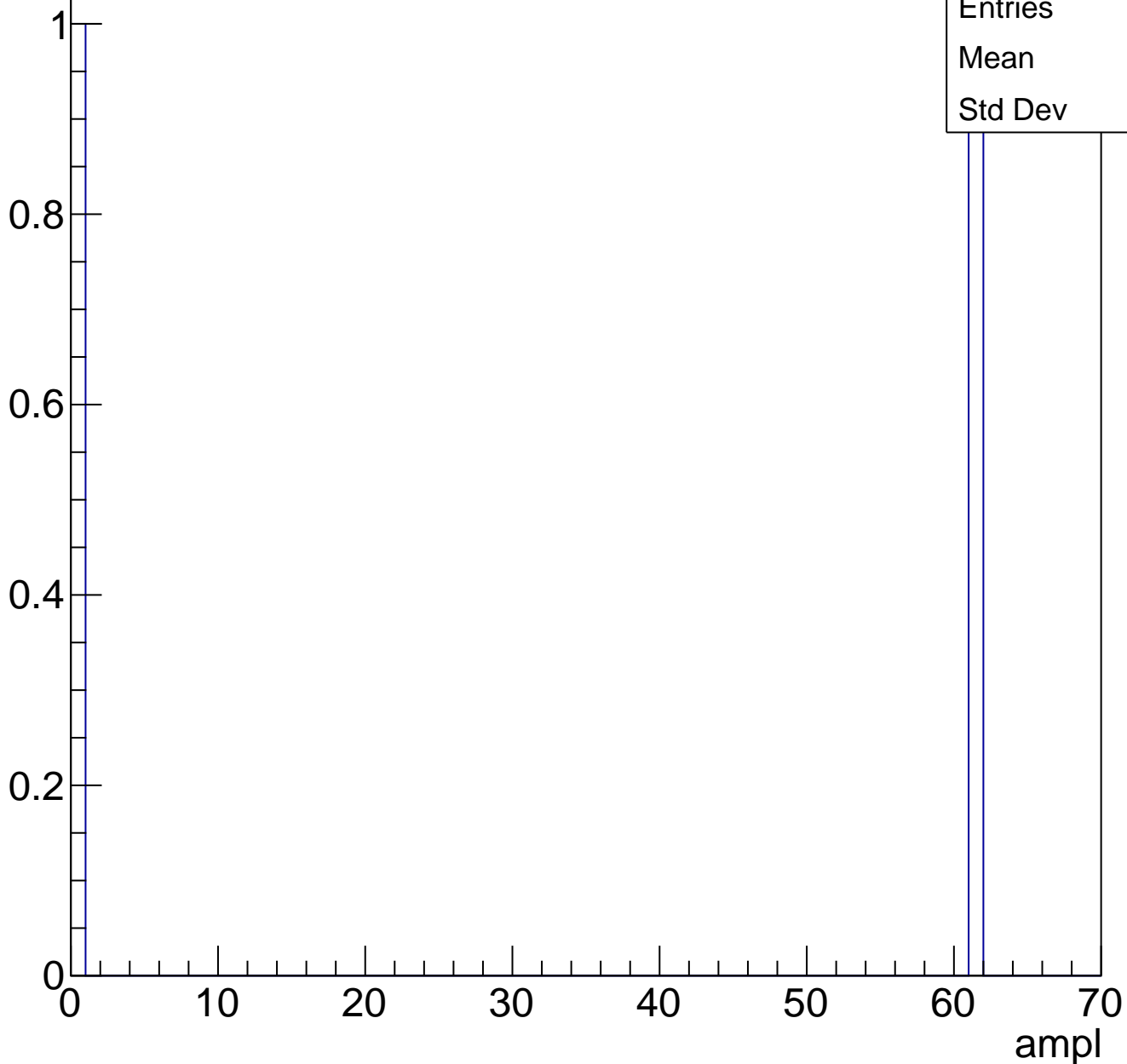
Entries	46
Mean	60.48
Std Dev	1.975



B0L102S, U12-ch16, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch16, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L102S, U12-ch17, adc0

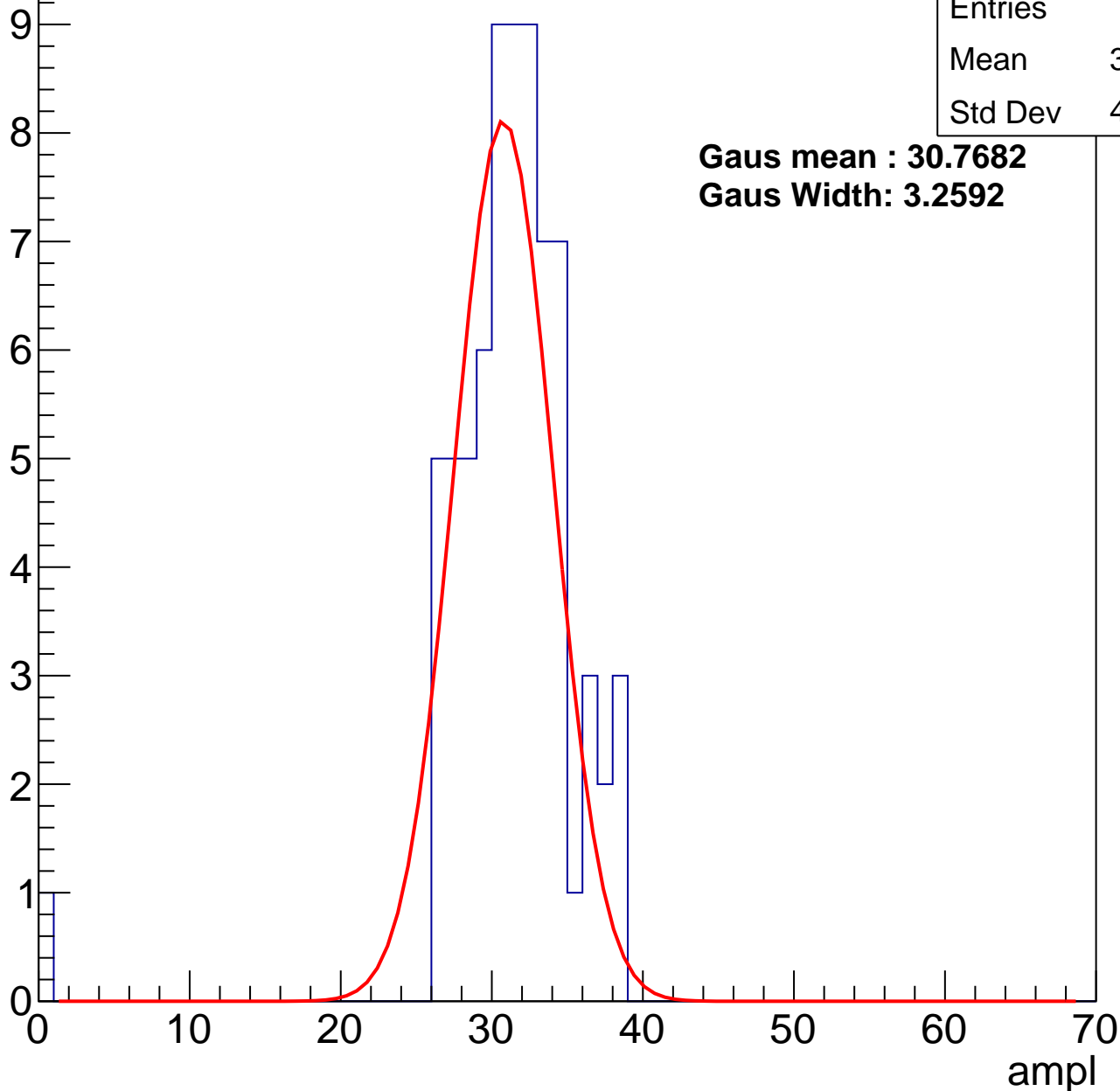
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	30.78
Std Dev	4.788

Gaus mean : 30.7682

Gaus Width: 3.2592



B0L102S, U12-ch17, adc1

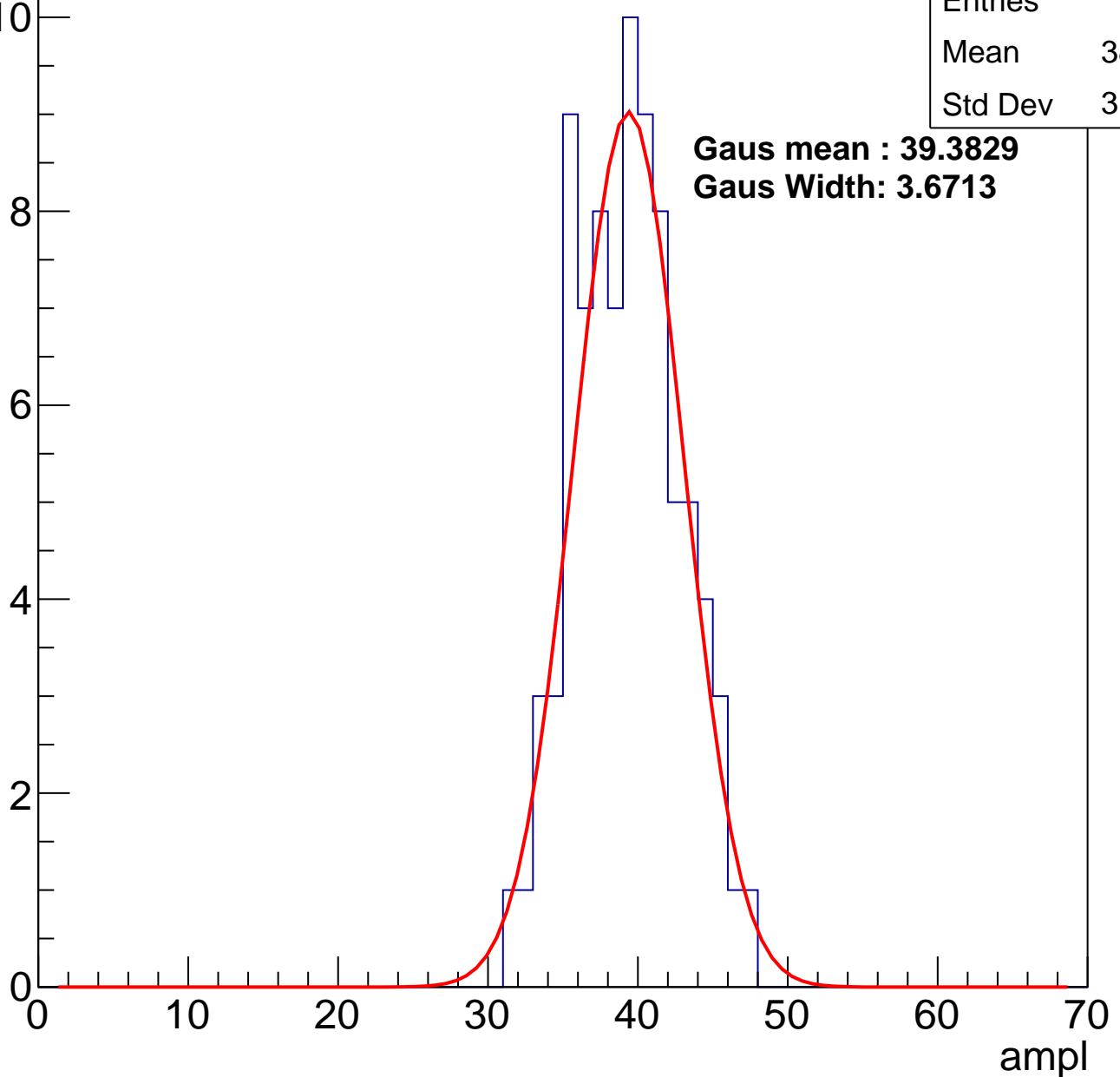
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	38.82
Std Dev	3.475

Gaus mean : 39.3829

Gaus Width: 3.6713



B0L102S, U12-ch17, adc2

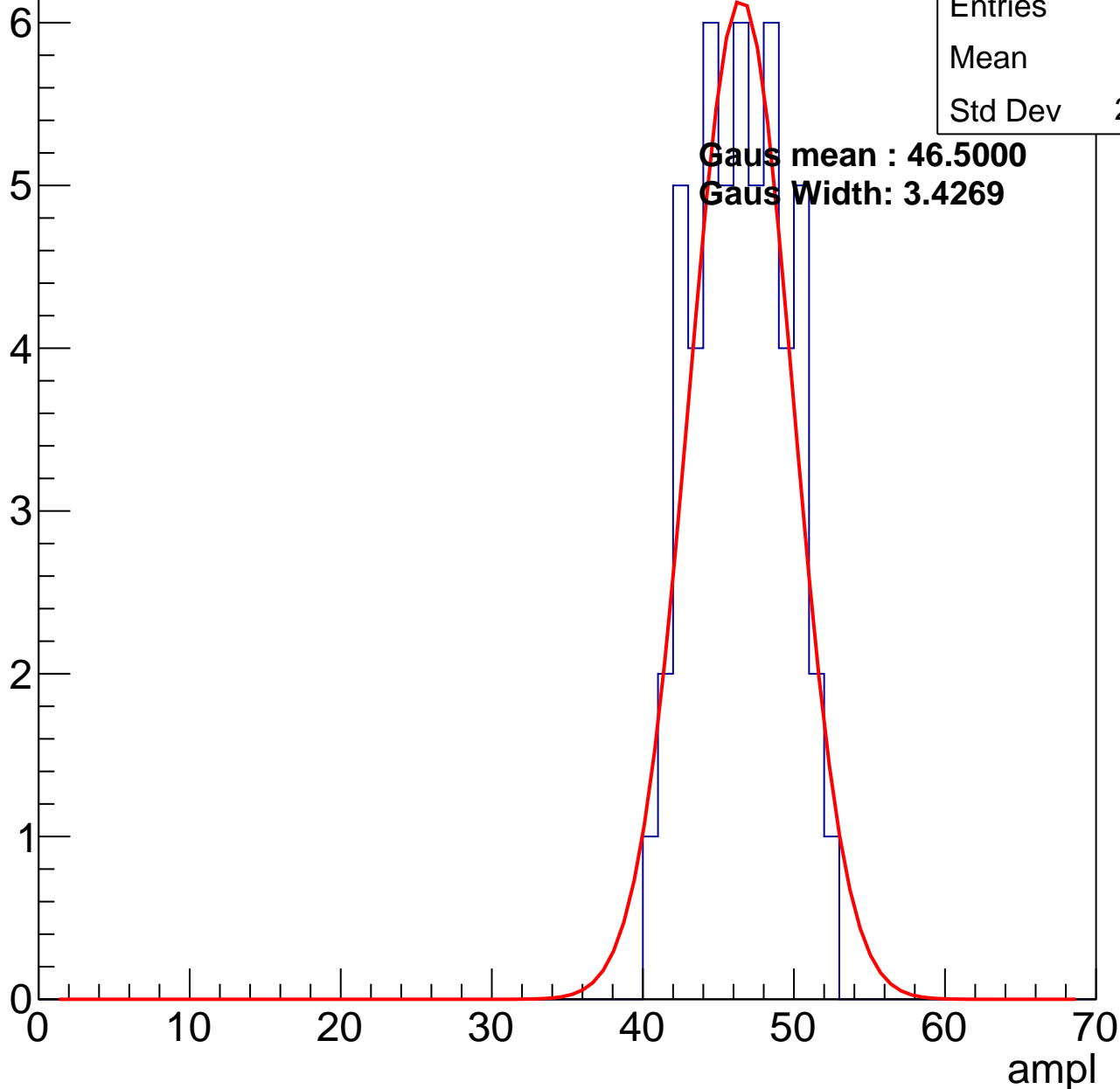
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	46
Std Dev	2.981

Gaus mean : 46.5000

Gaus Width: 3.4269

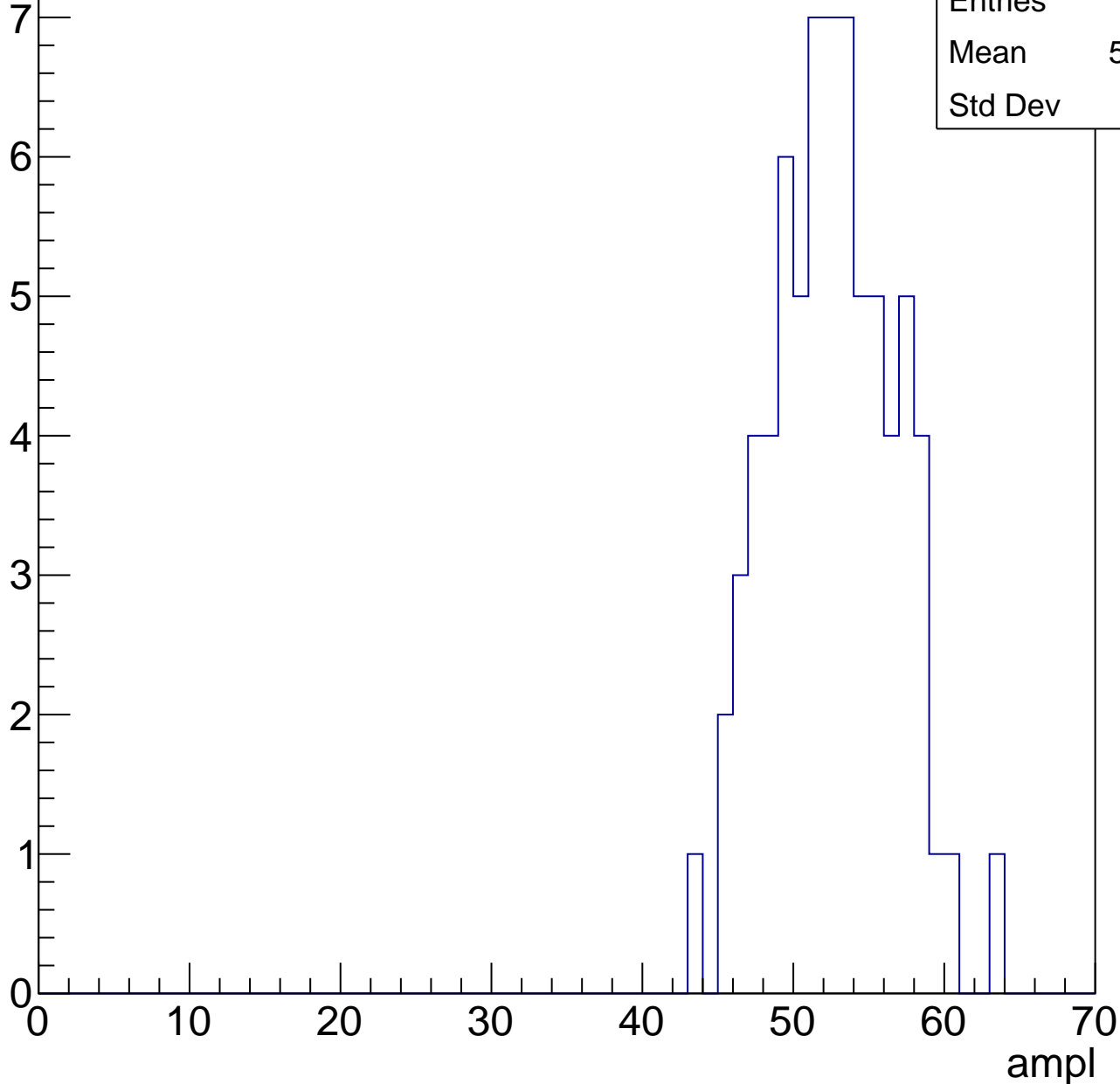


B0L102S, U12-ch17, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

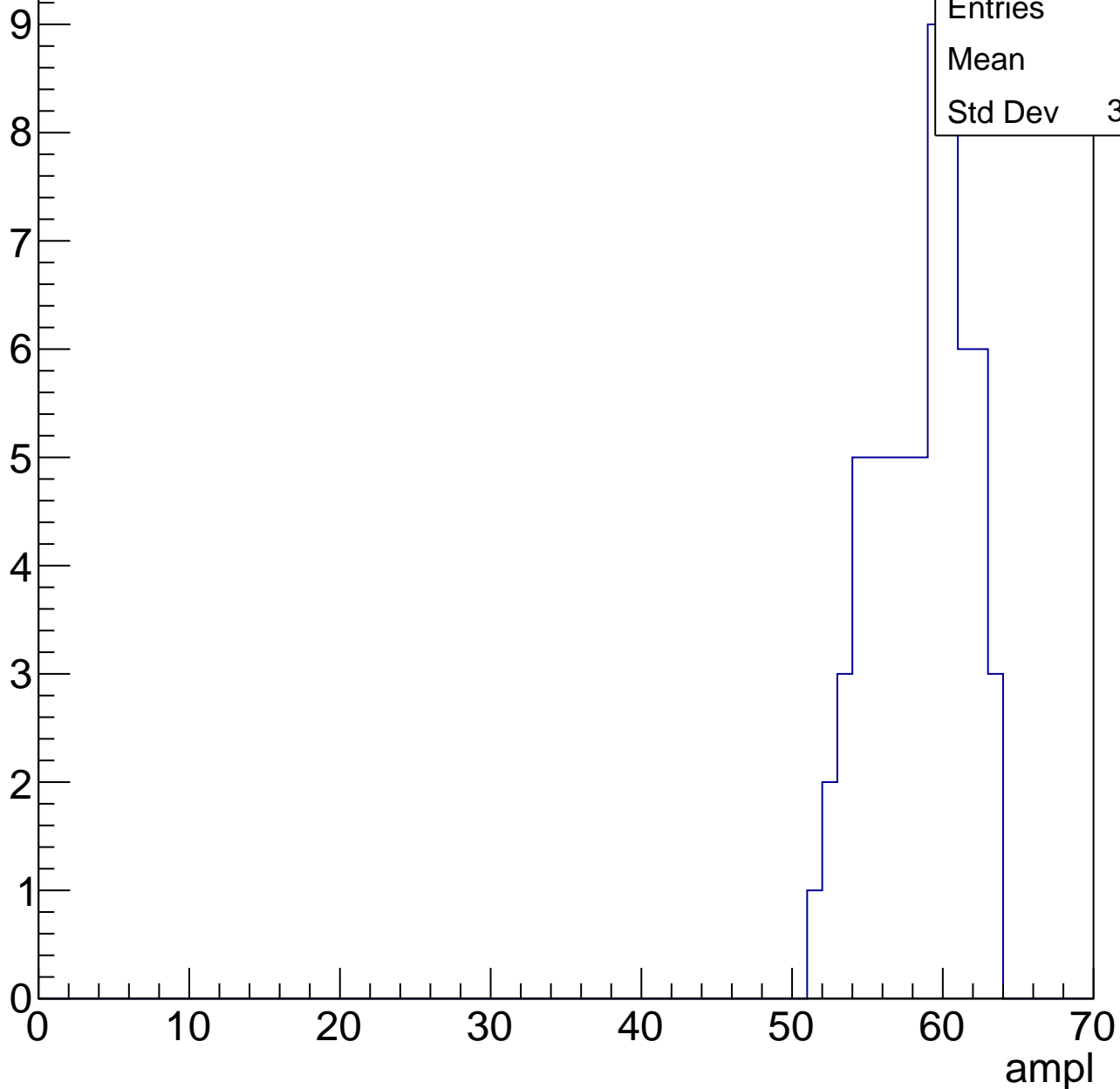
Entries	72
Mean	52.15
Std Dev	4.03



B0L102S, U12-ch17, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

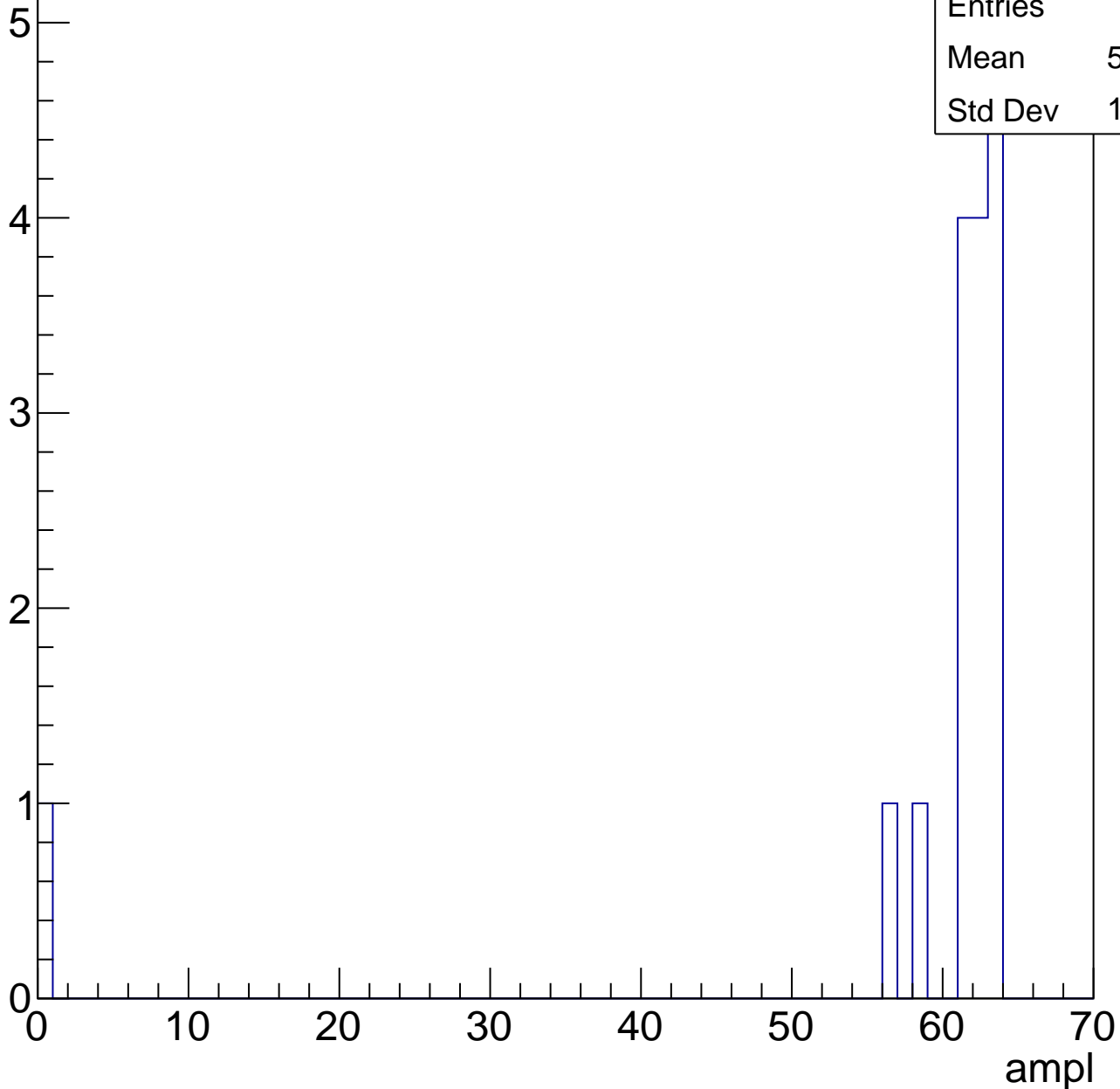


B0L102S, U12-ch17, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	16
Mean	57.56
Std Dev	14.98



B0L102S, U12-ch17, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch17, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B0L102S, U12-ch18, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	70
Mean	29.47
Std Dev	3.401

Gaus mean : 30.0326

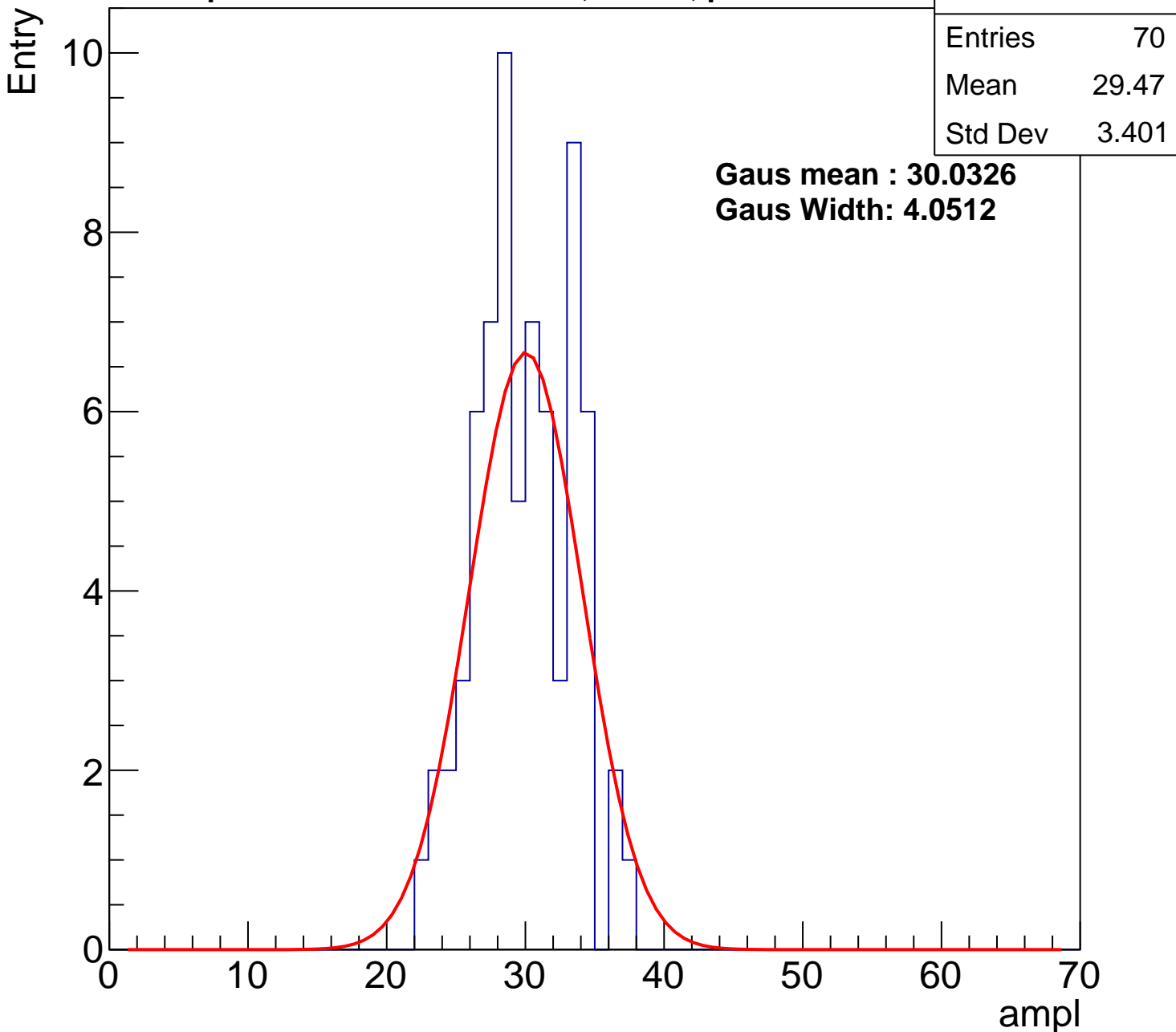
Gaus Width: 4.0512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

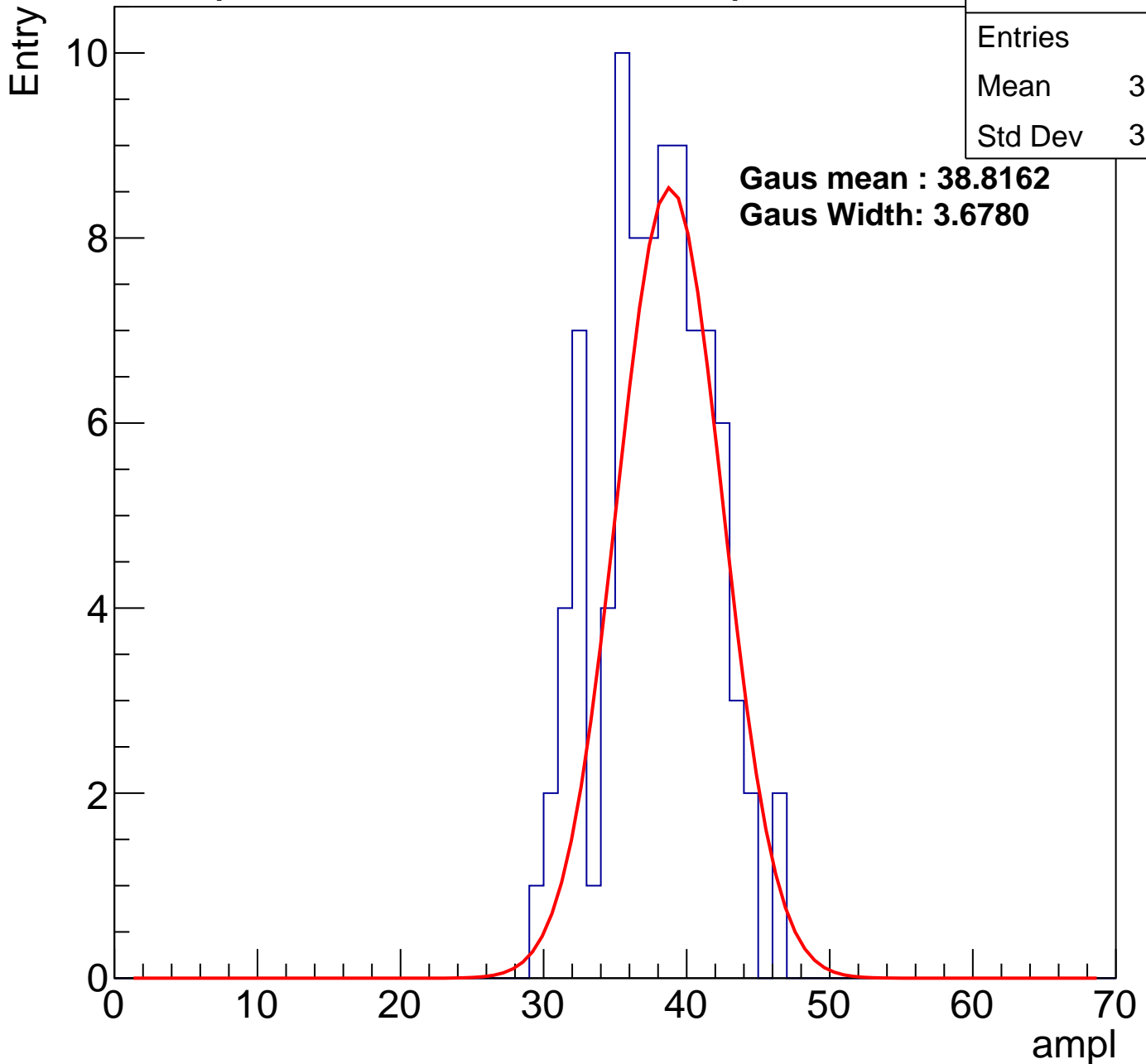


B0L102S, U12-ch18, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	90
Mean	37.34
Std Dev	3.813

Gaus mean : 38.8162
Gaus Width: 3.6780



B0L102S, U12-ch18, adc2

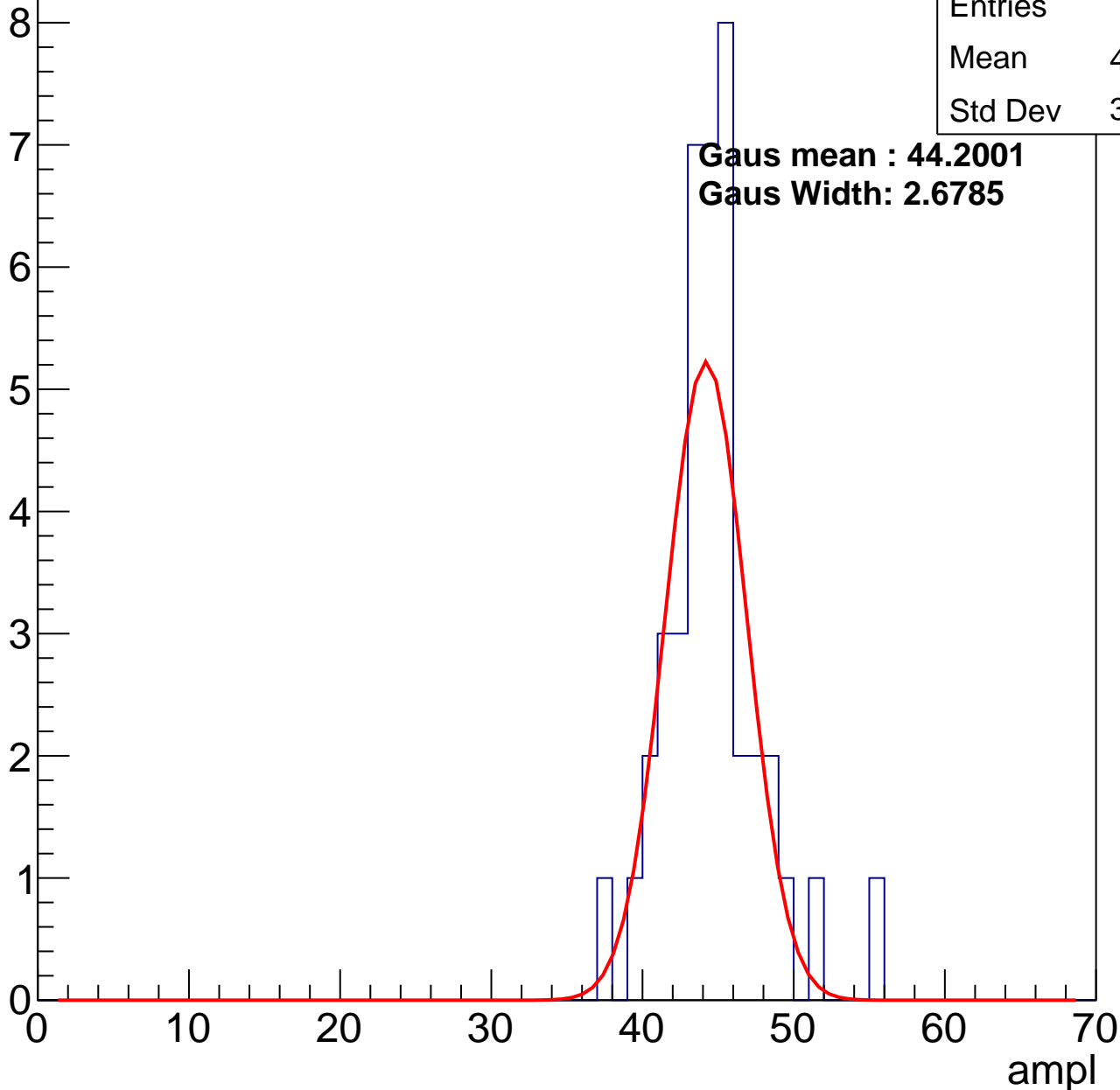
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	44.17
Std Dev	3.169

Gaus mean : 44.2001

Gaus Width: 2.6785

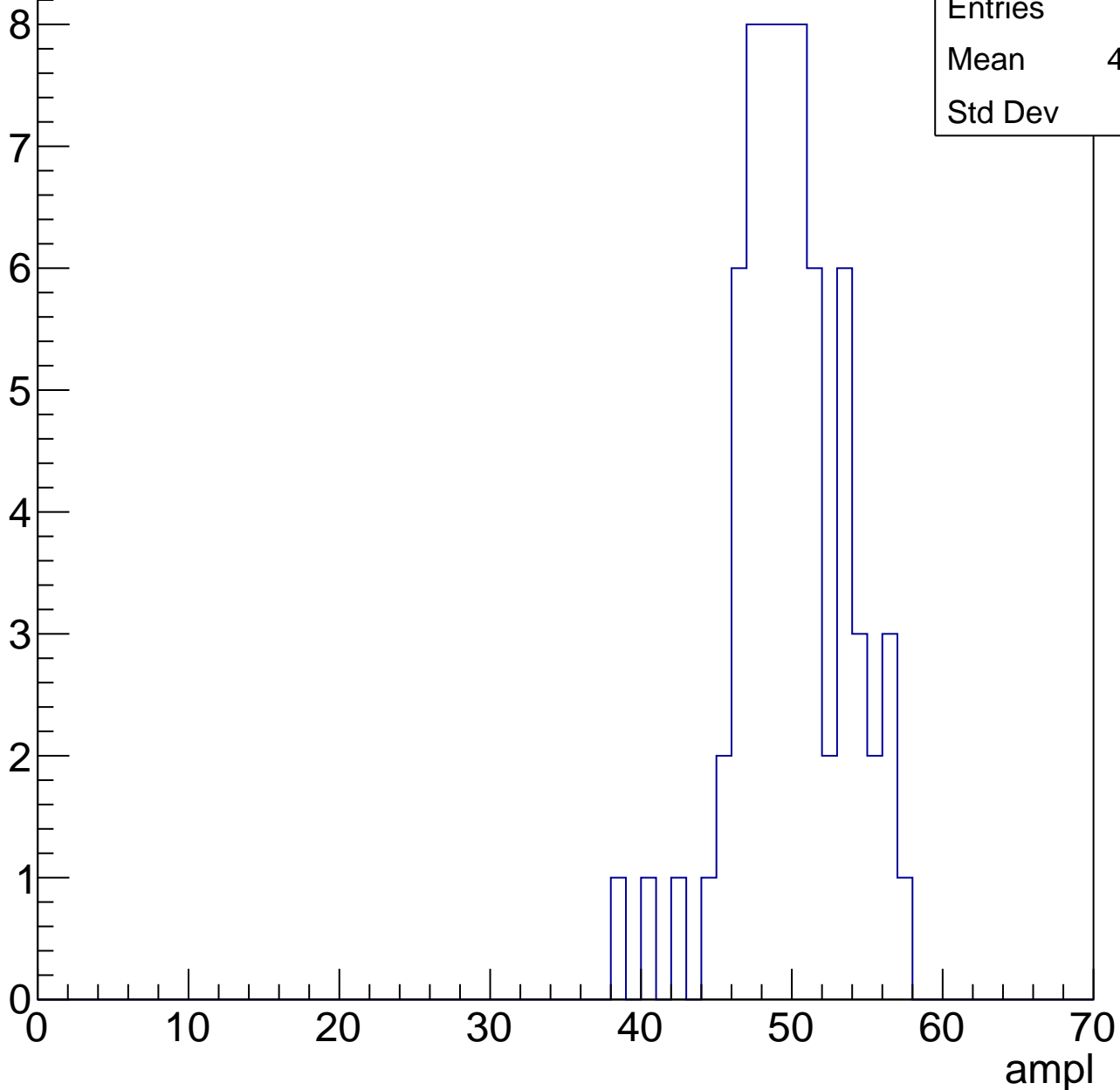


B0L102S, U12-ch18, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	49.36
Std Dev	3.66

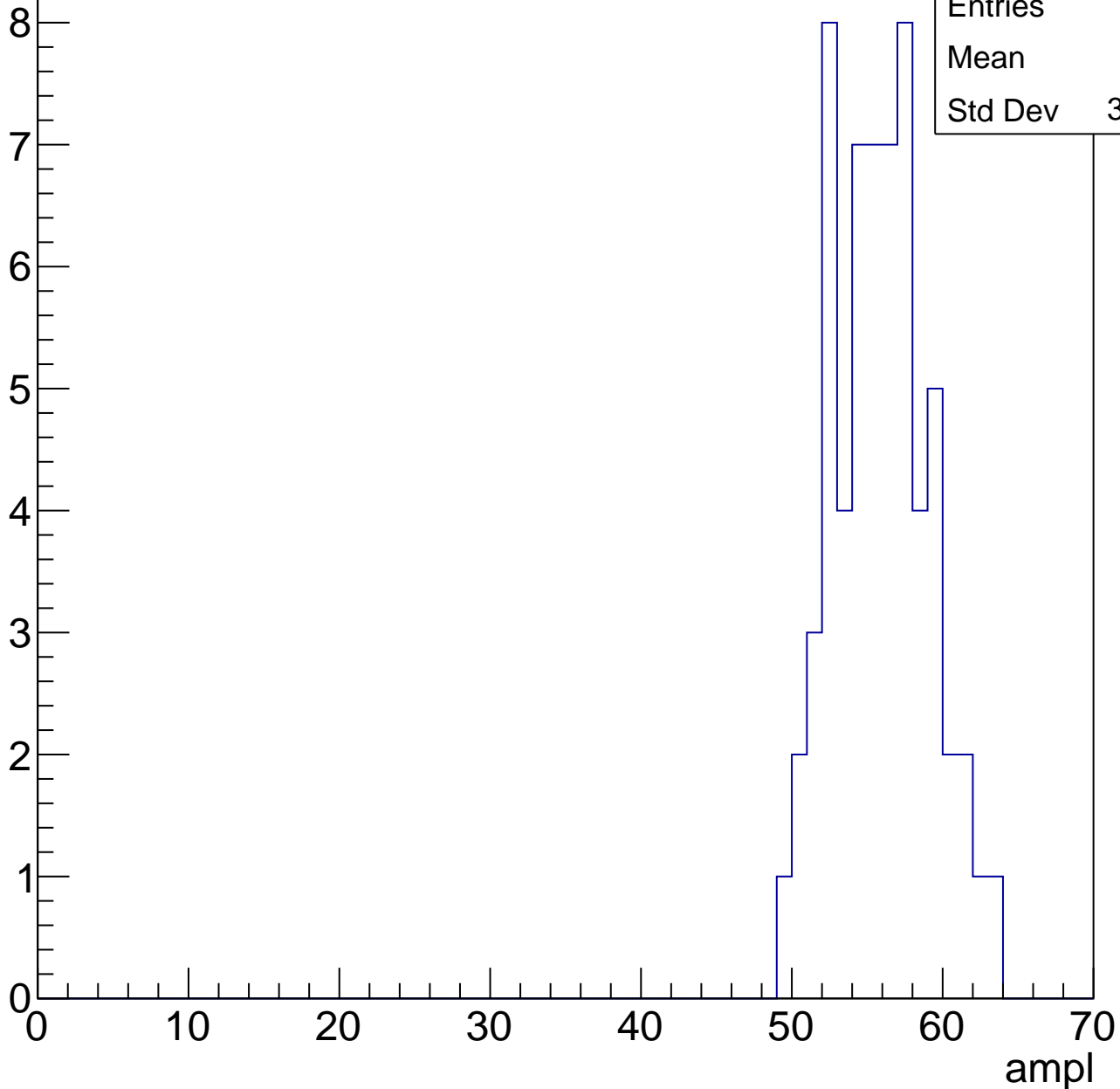


B0L102S, U12-ch18, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

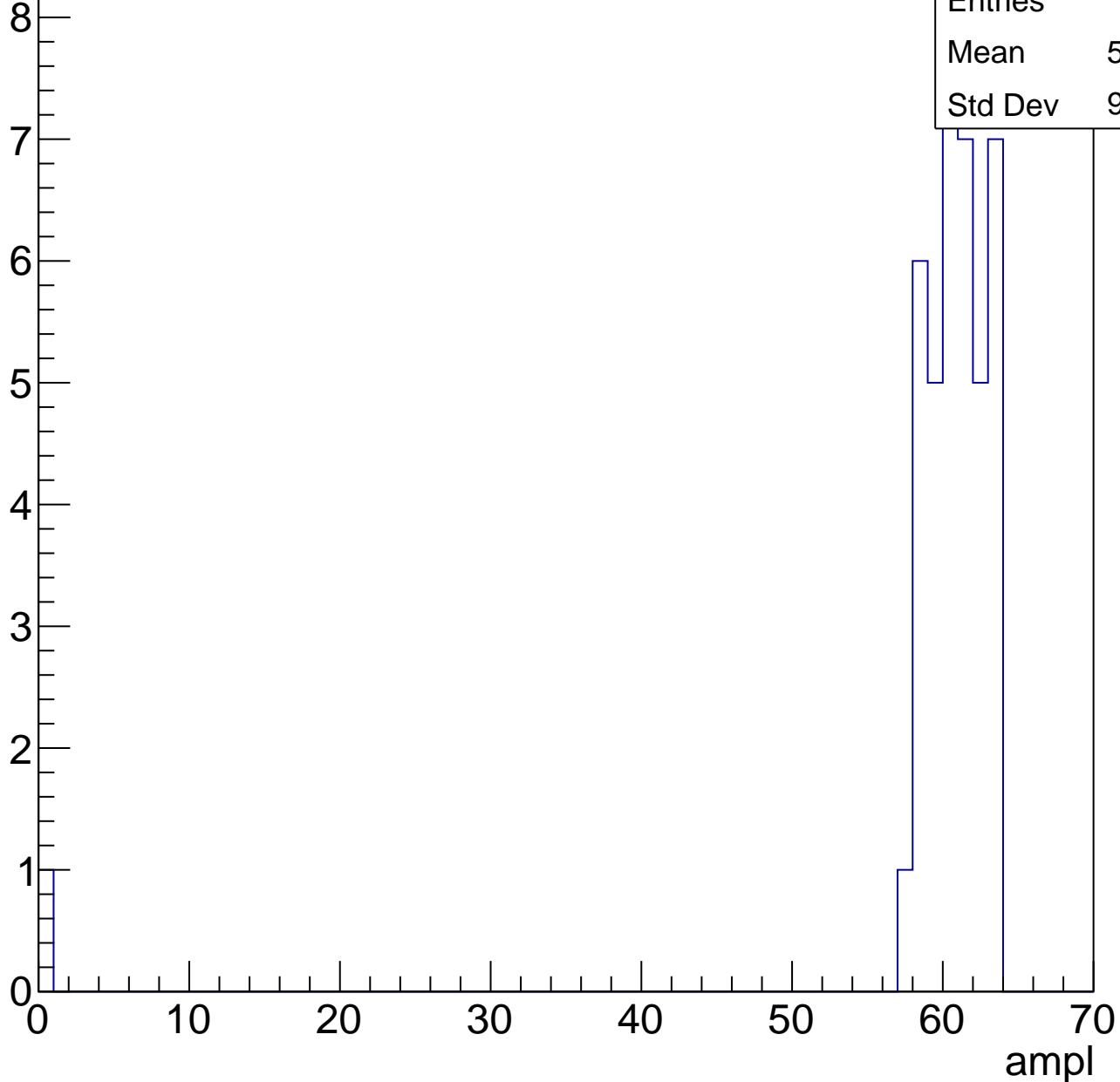
Entries	62
Mean	55.4
Std Dev	3.134



B0L102S, U12-ch18, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch18, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch18, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch19, adc0

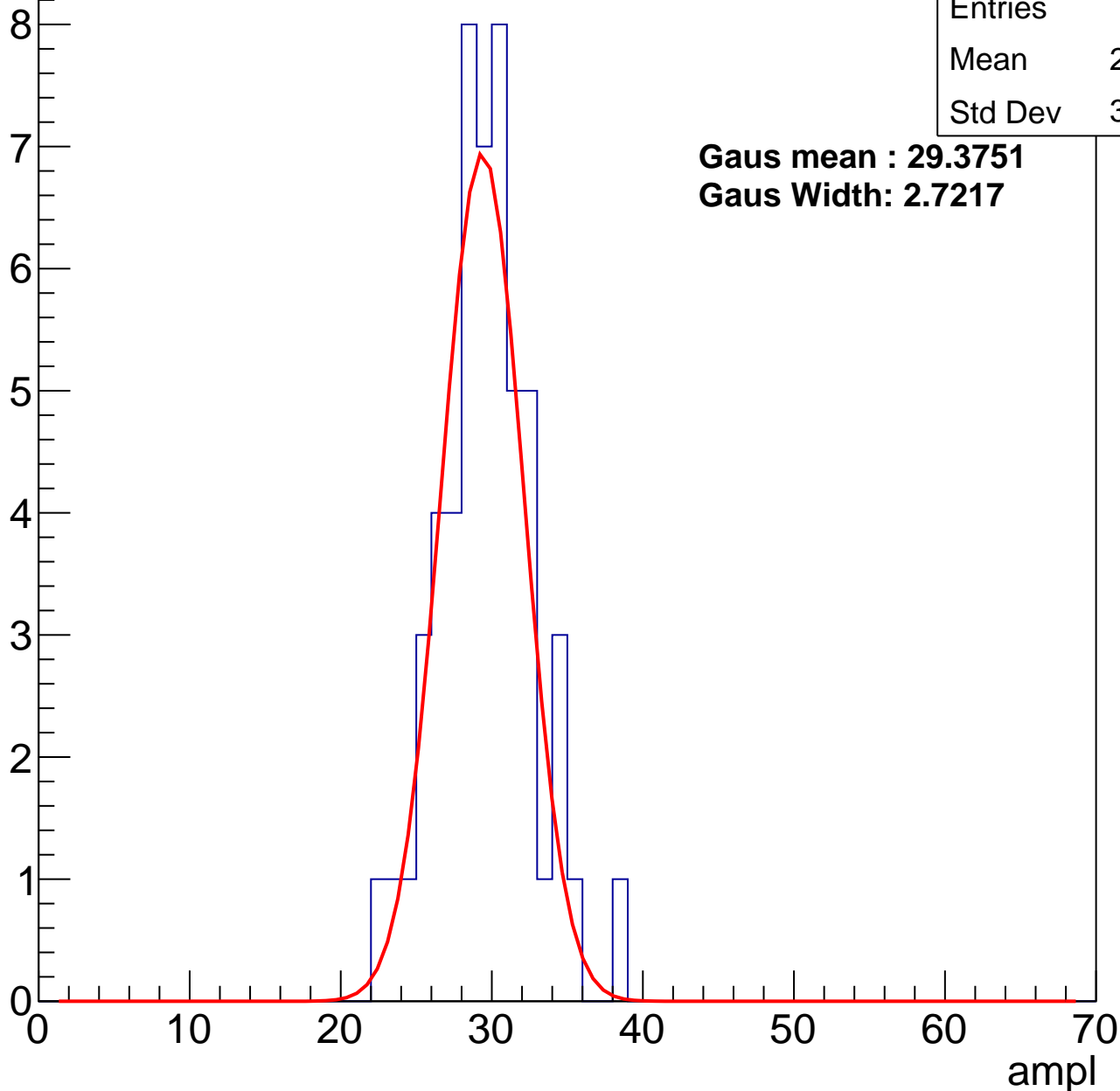
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	29.17
Std Dev	3.064

Gaus mean : 29.3751

Gaus Width: 2.7217



B0L102S, U12-ch19, adc1

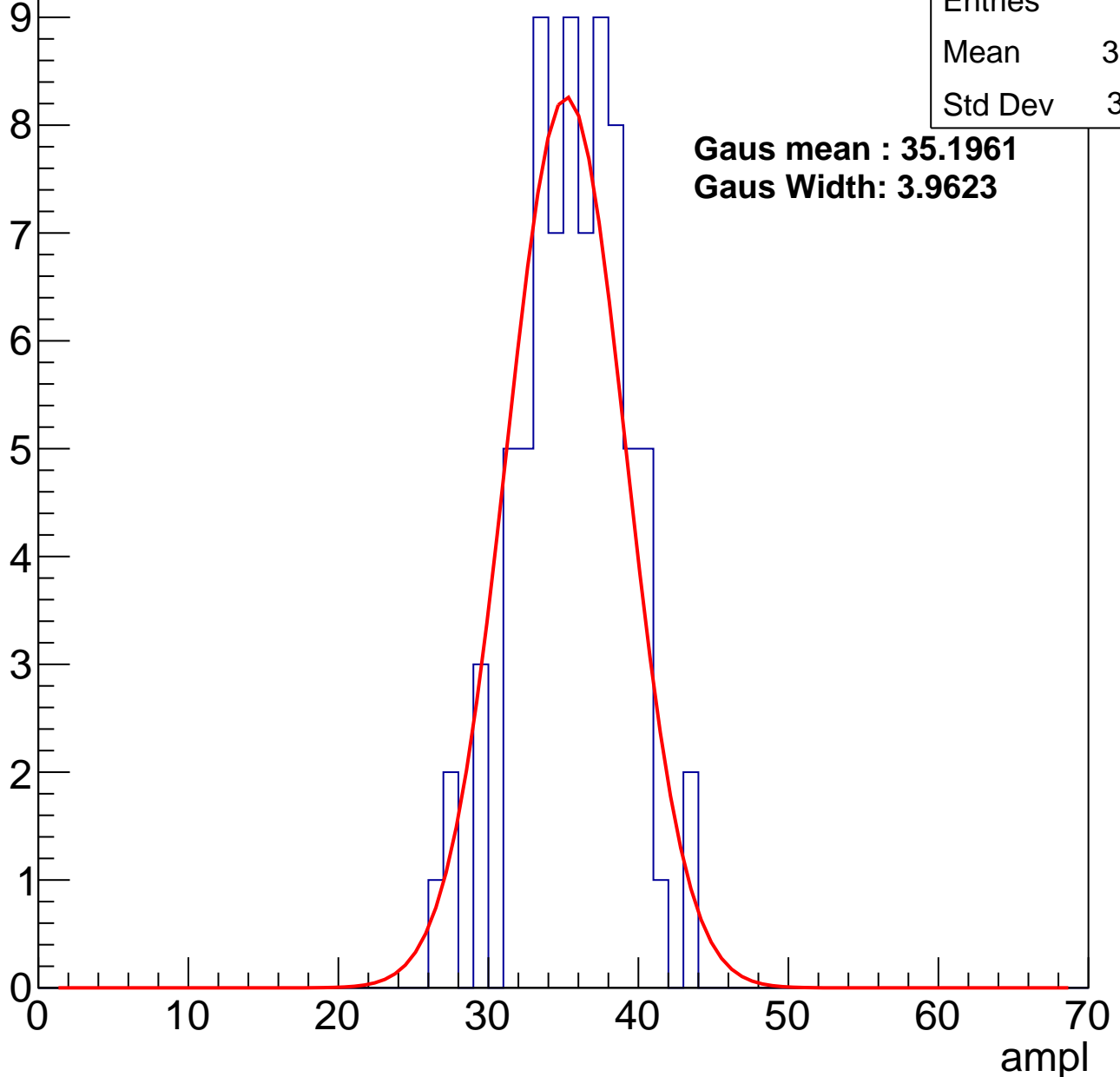
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	35.17
Std Dev	3.521

Gaus mean : 35.1961

Gaus Width: 3.9623



B0L102S, U12-ch19, adc2

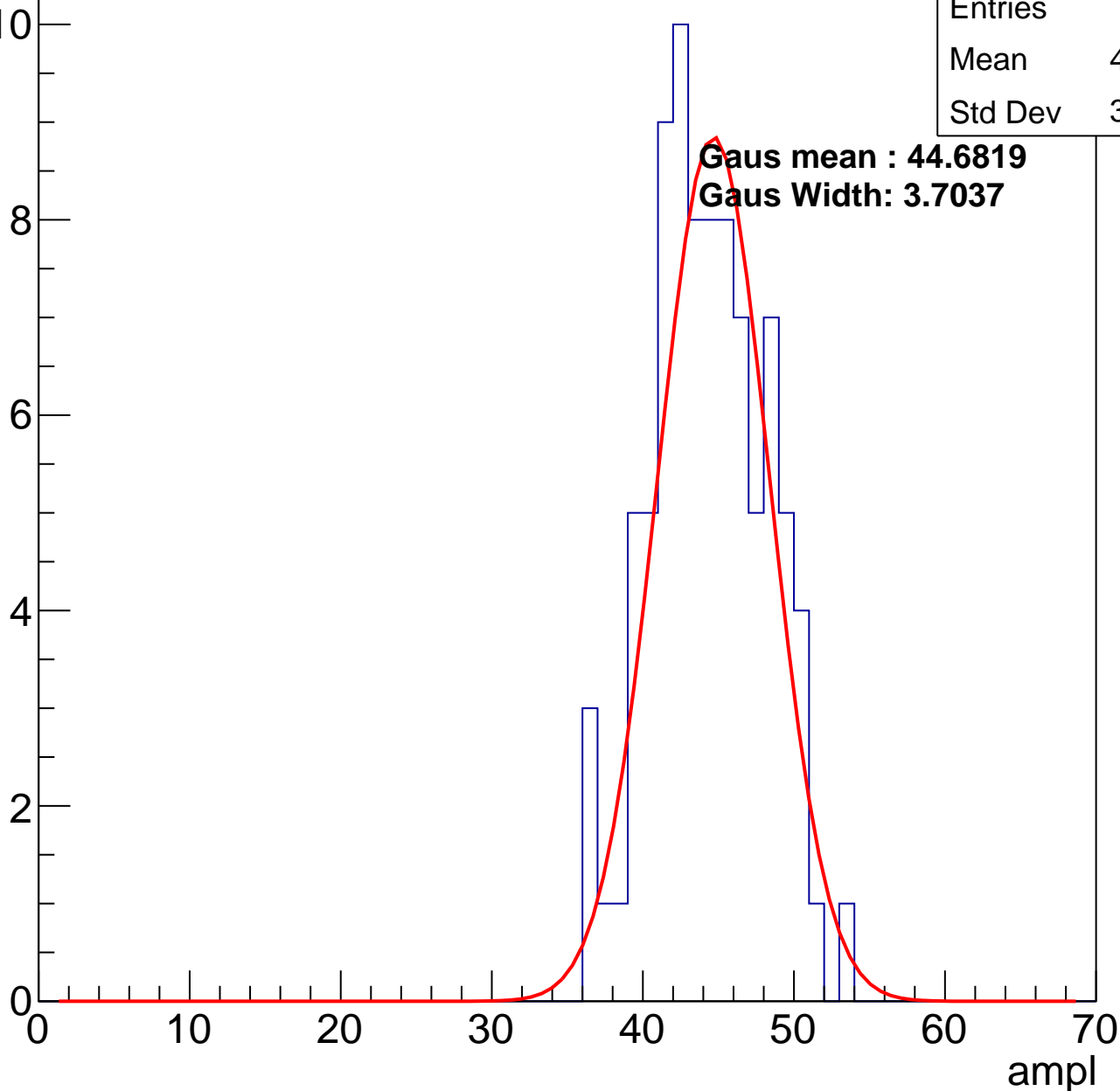
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	88
Mean	43.92
Std Dev	3.697

Gaus mean : 44.6819

Gaus Width: 3.7037

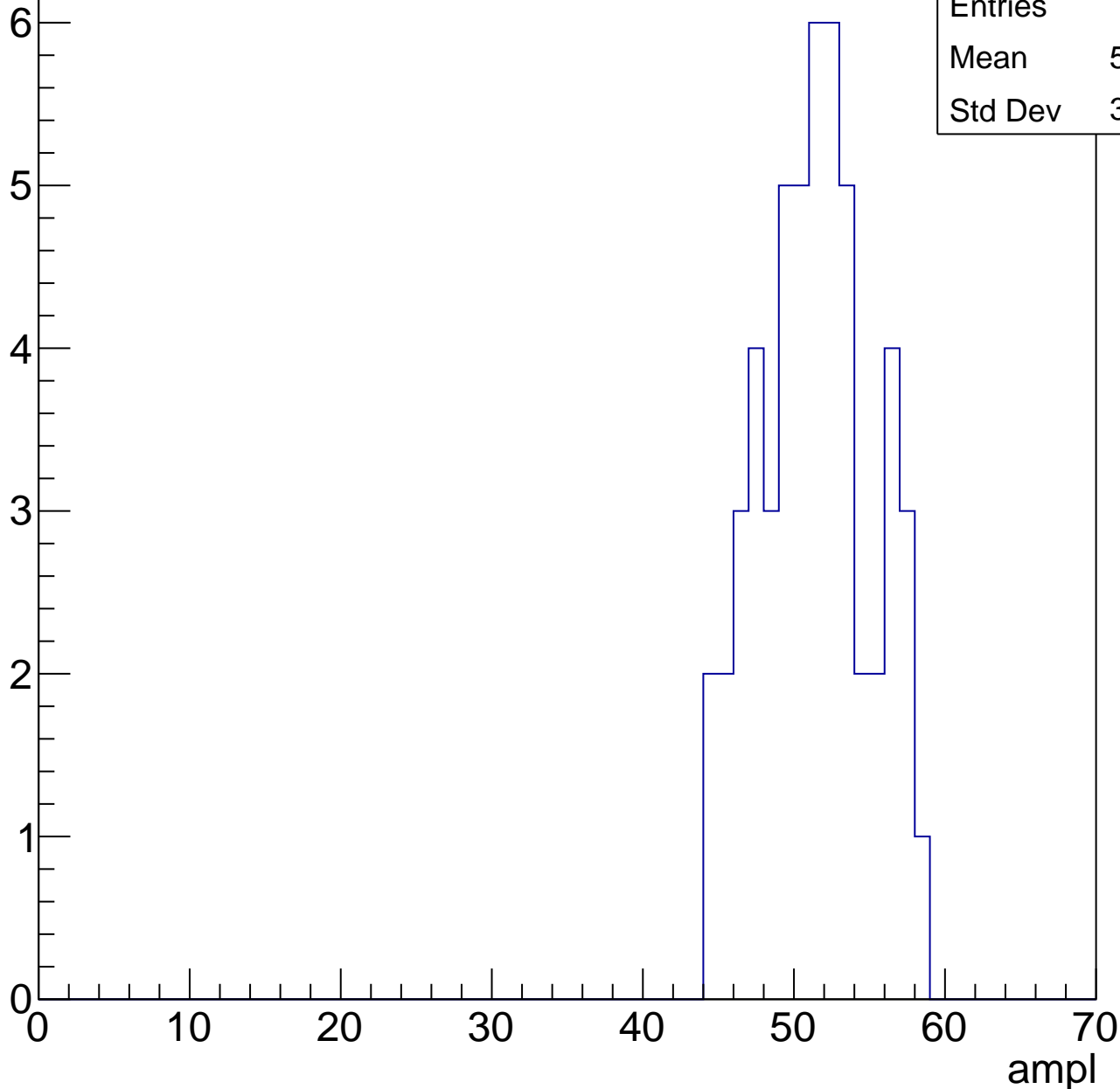


B0L102S, U12-ch19, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	50.89
Std Dev	3.617

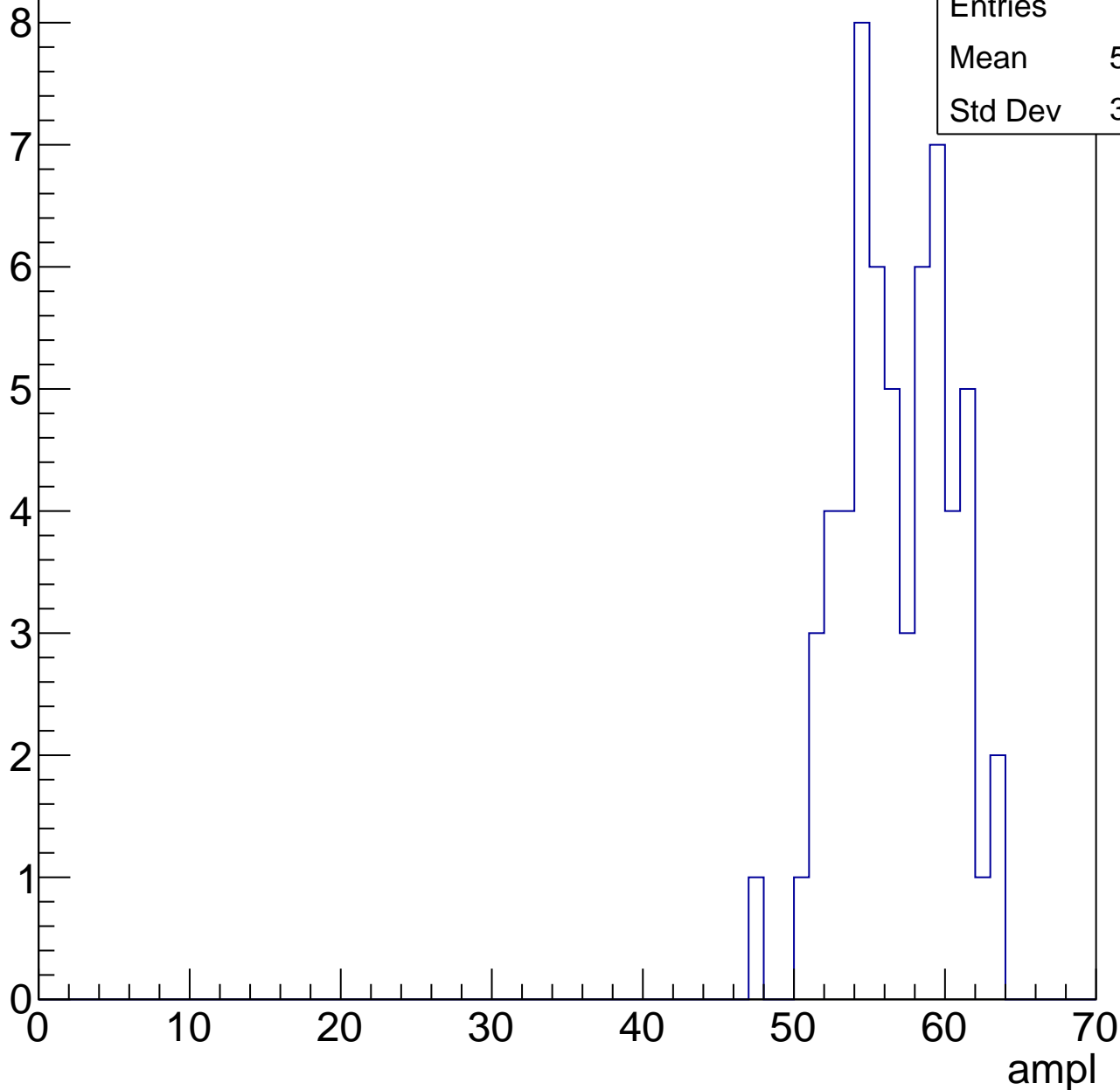


B0L102S, U12-ch19, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	56.28
Std Dev	3.508

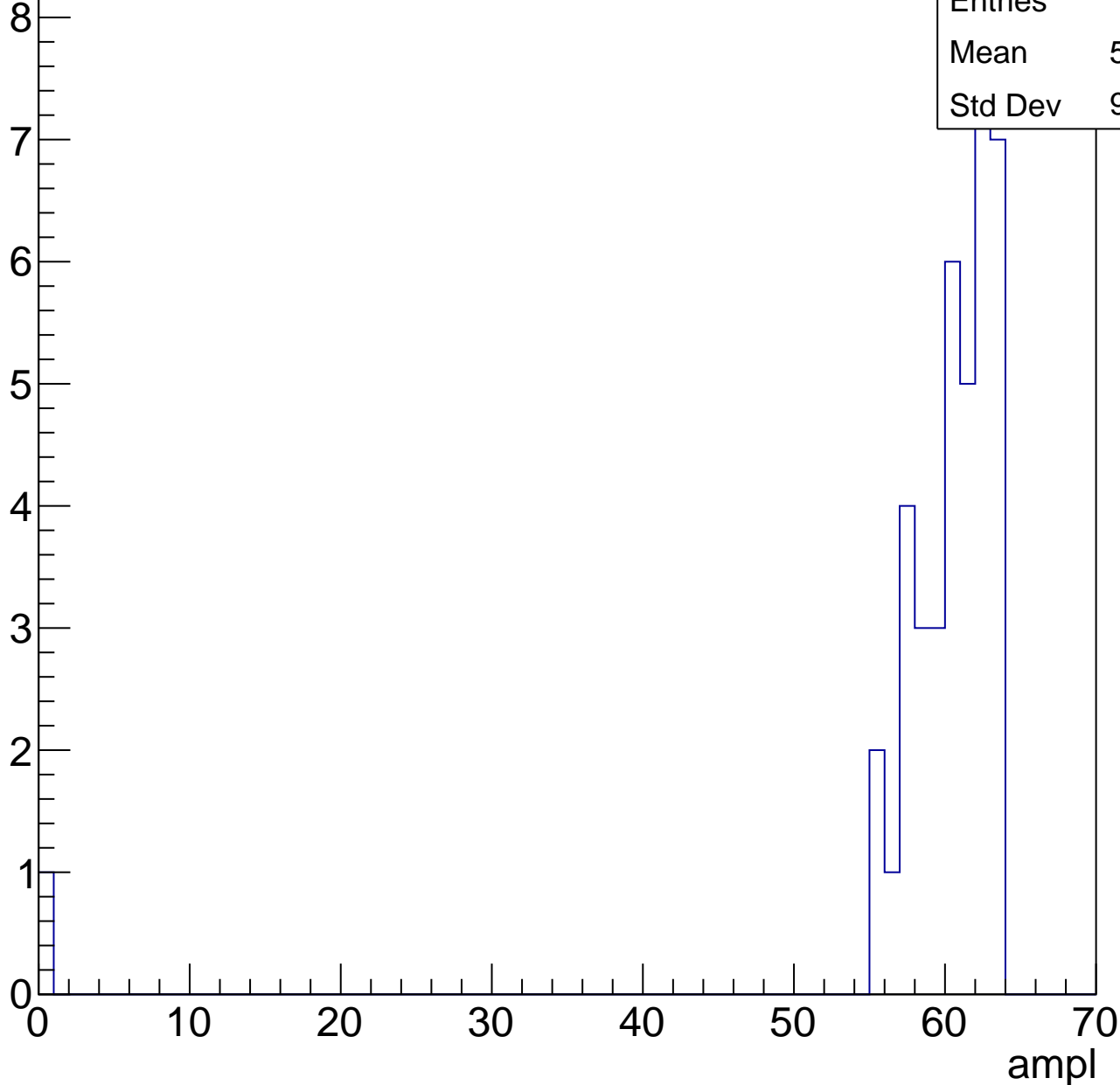


B0L102S, U12-ch19, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	40
Mean	58.67
Std Dev	9.678



B0L102S, U12-ch19, adc6

calib_packv5_042523_0143.root, FC#12, port B1

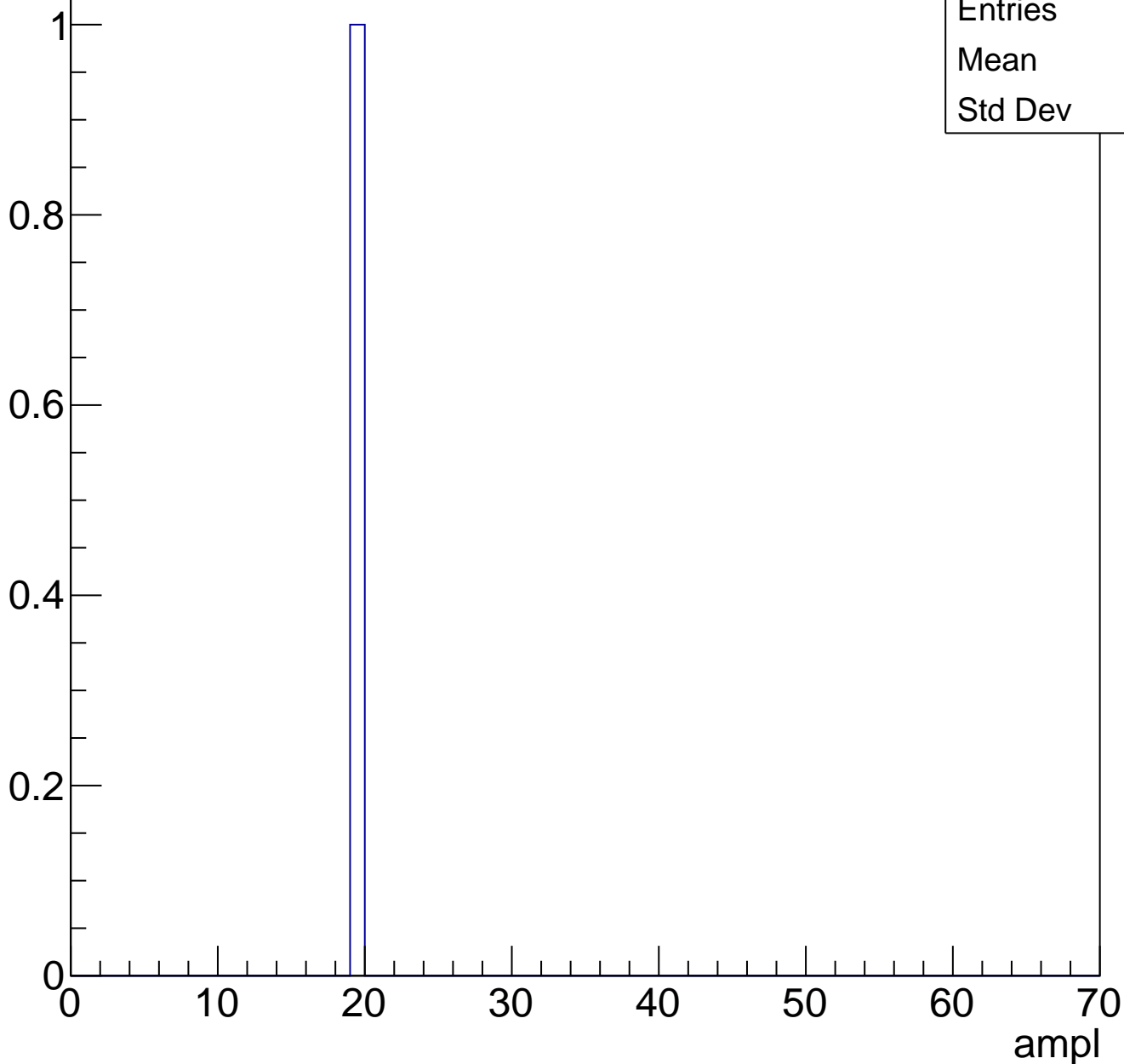
Entry



B0L102S, U12-ch19, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch20, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	30.43
Std Dev	3.445

Gaus mean : 30.9973

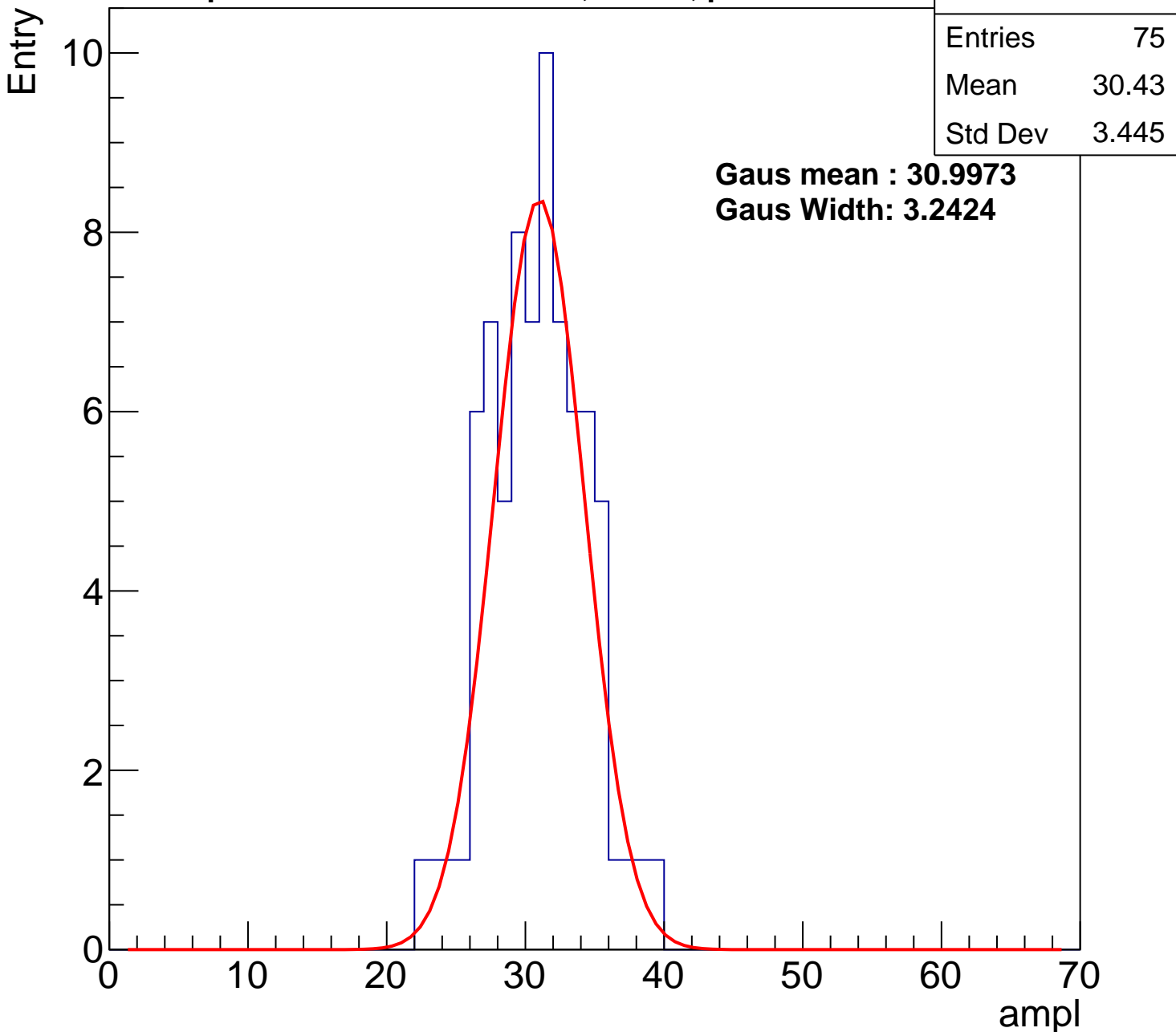
Gaus Width: 3.2424

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch20, adc1

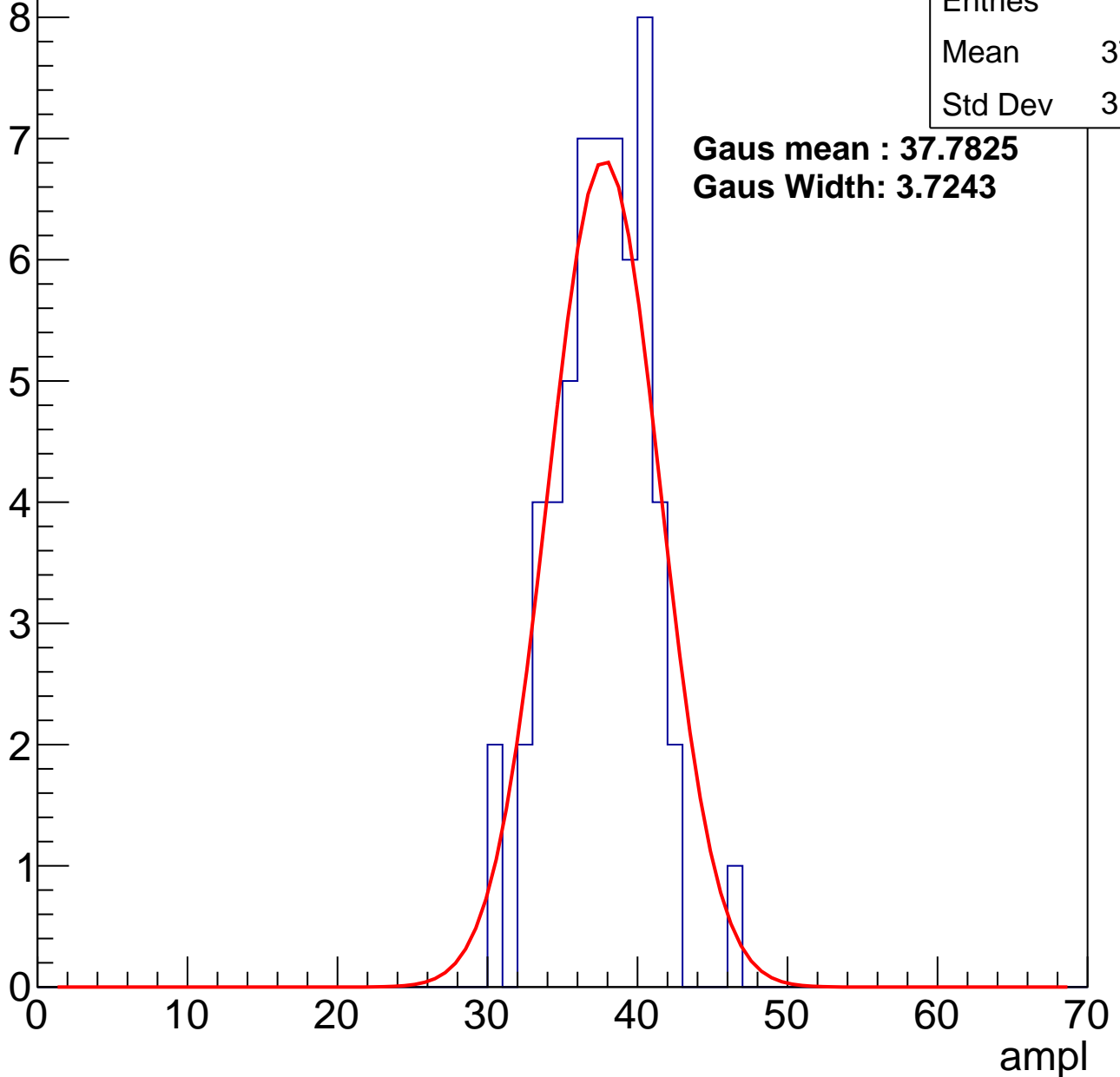
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	37.15
Std Dev	3.113

Gaus mean : 37.7825

Gaus Width: 3.7243



B0L102S, U12-ch20, adc2

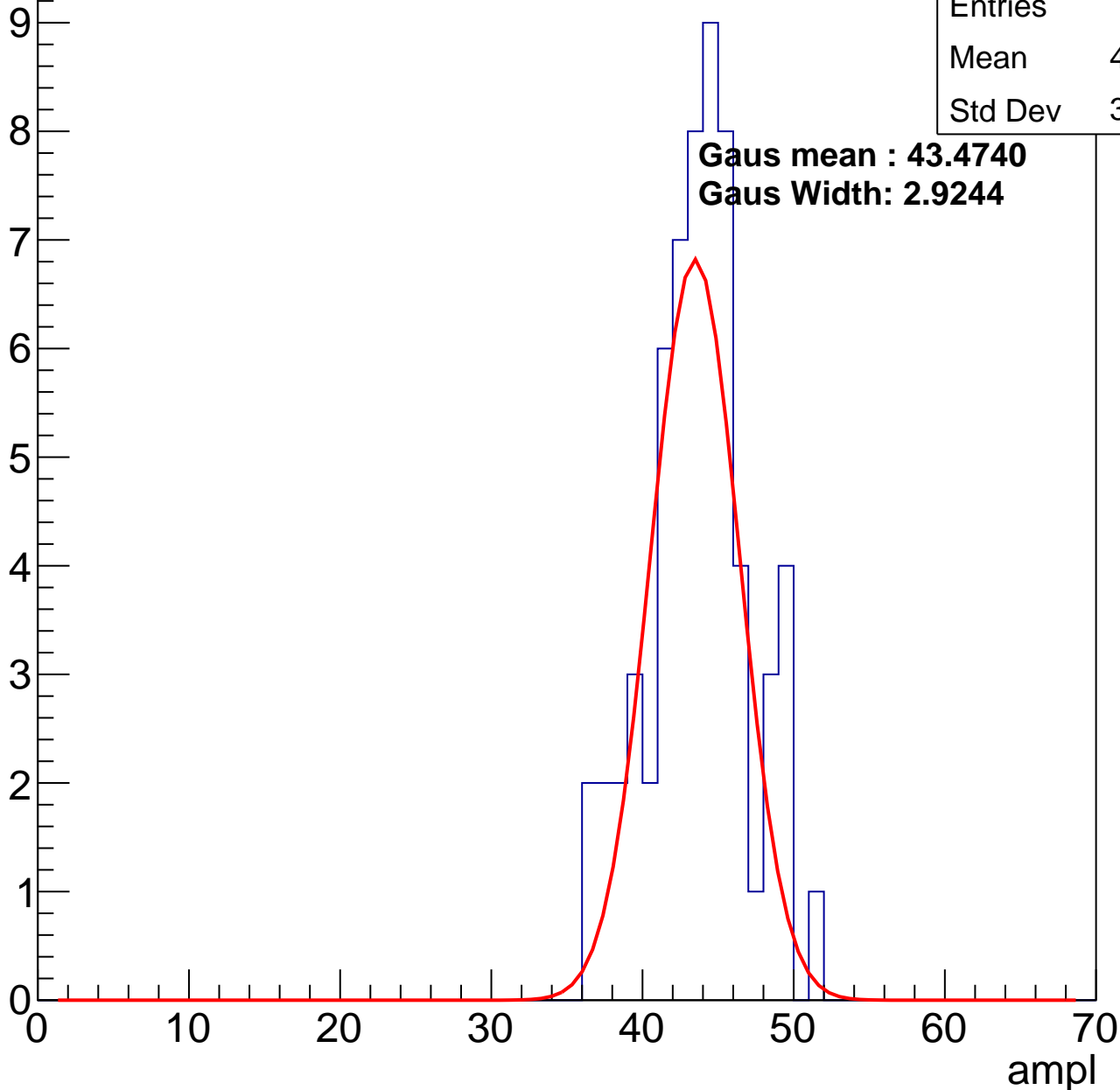
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	43.24
Std Dev	3.334

Gaus mean : 43.4740

Gaus Width: 2.9244

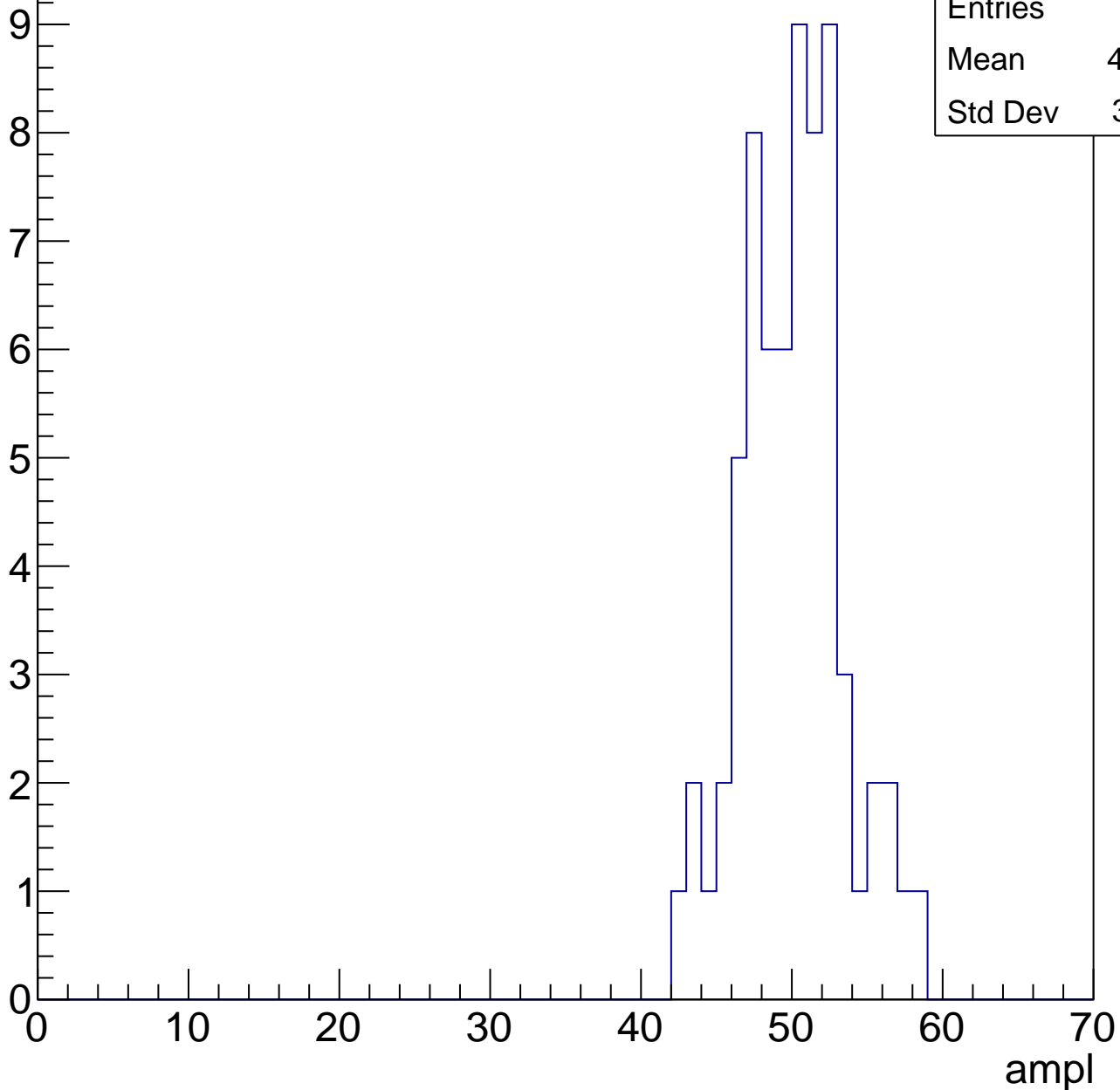


B0L102S, U12-ch20, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	49.64
Std Dev	3.331

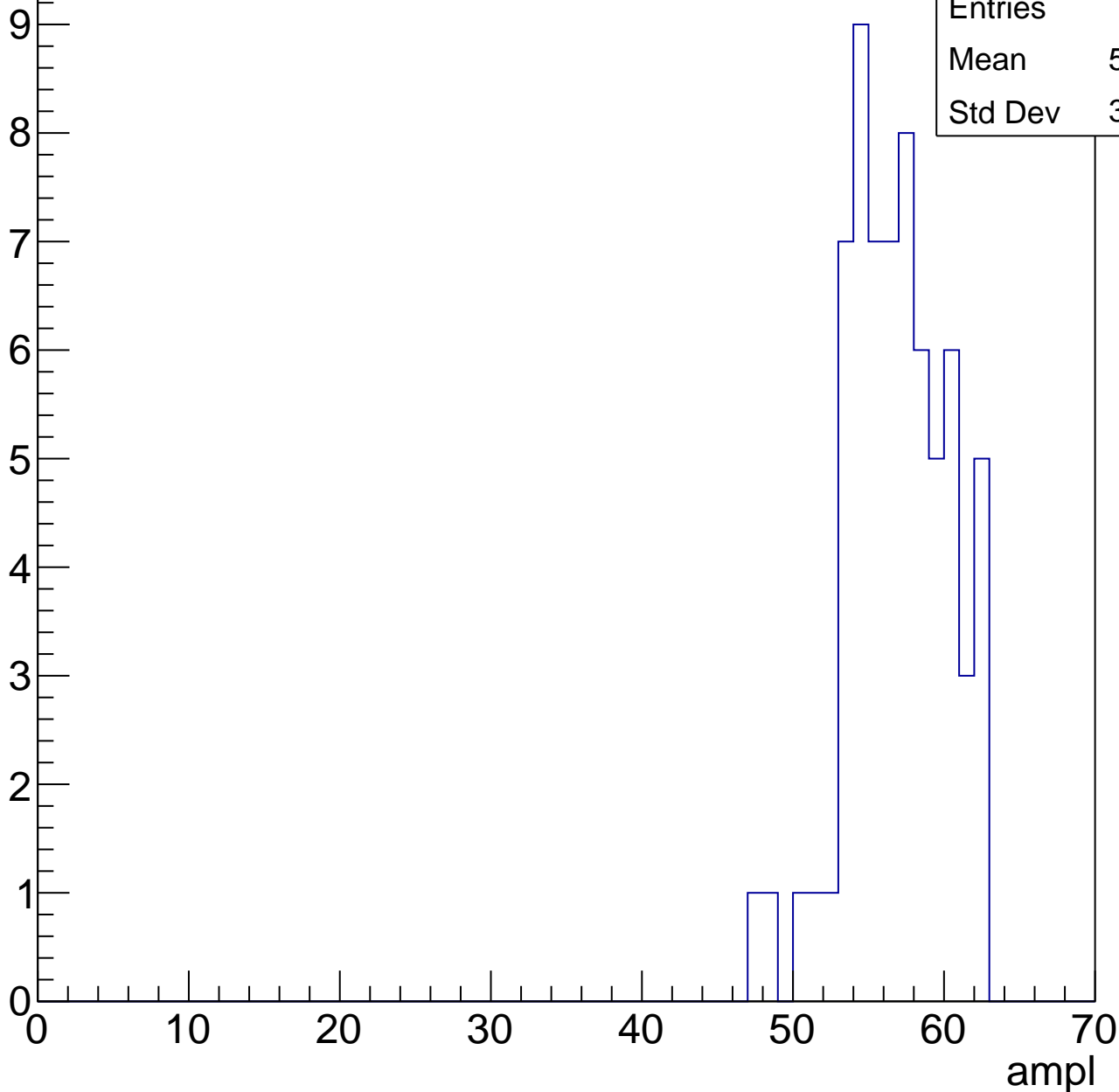


B0L102S, U12-ch20, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	56.38
Std Dev	3.312

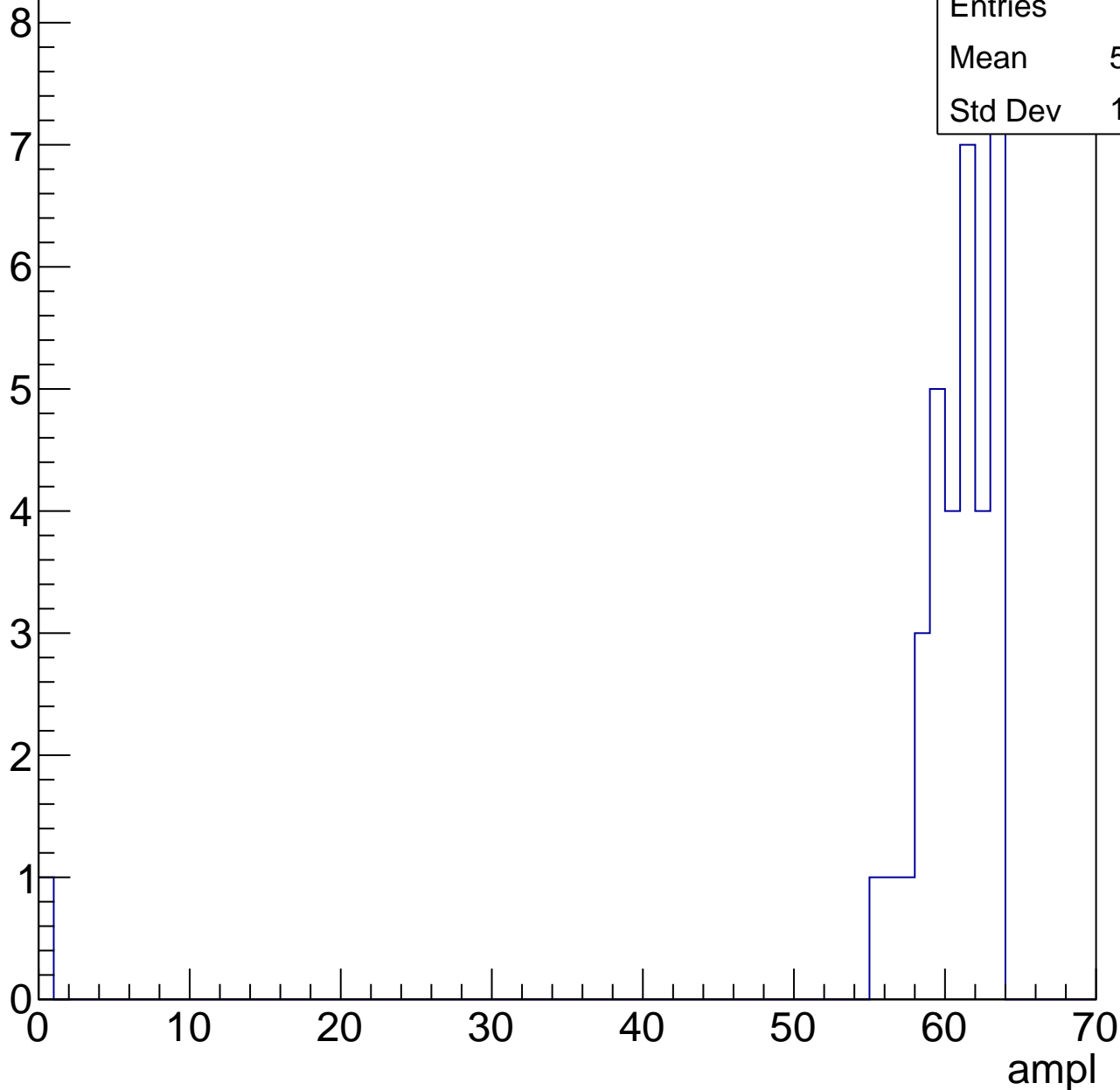


B0L102S, U12-ch20, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	35
Mean	58.74
Std Dev	10.29



B0L102S, U12-ch20, adc6

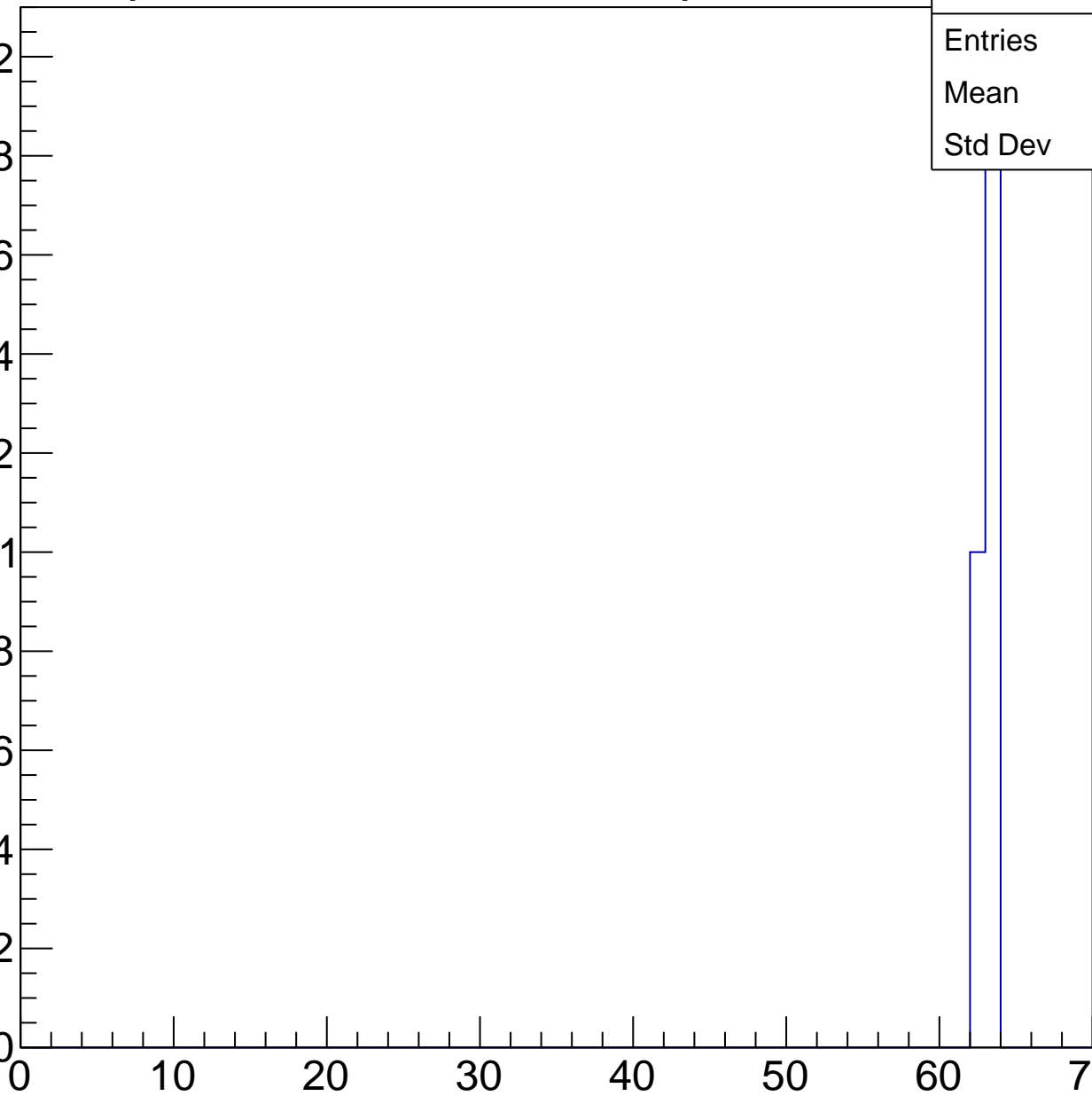
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B0L102S, U12-ch20, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch21, adc0

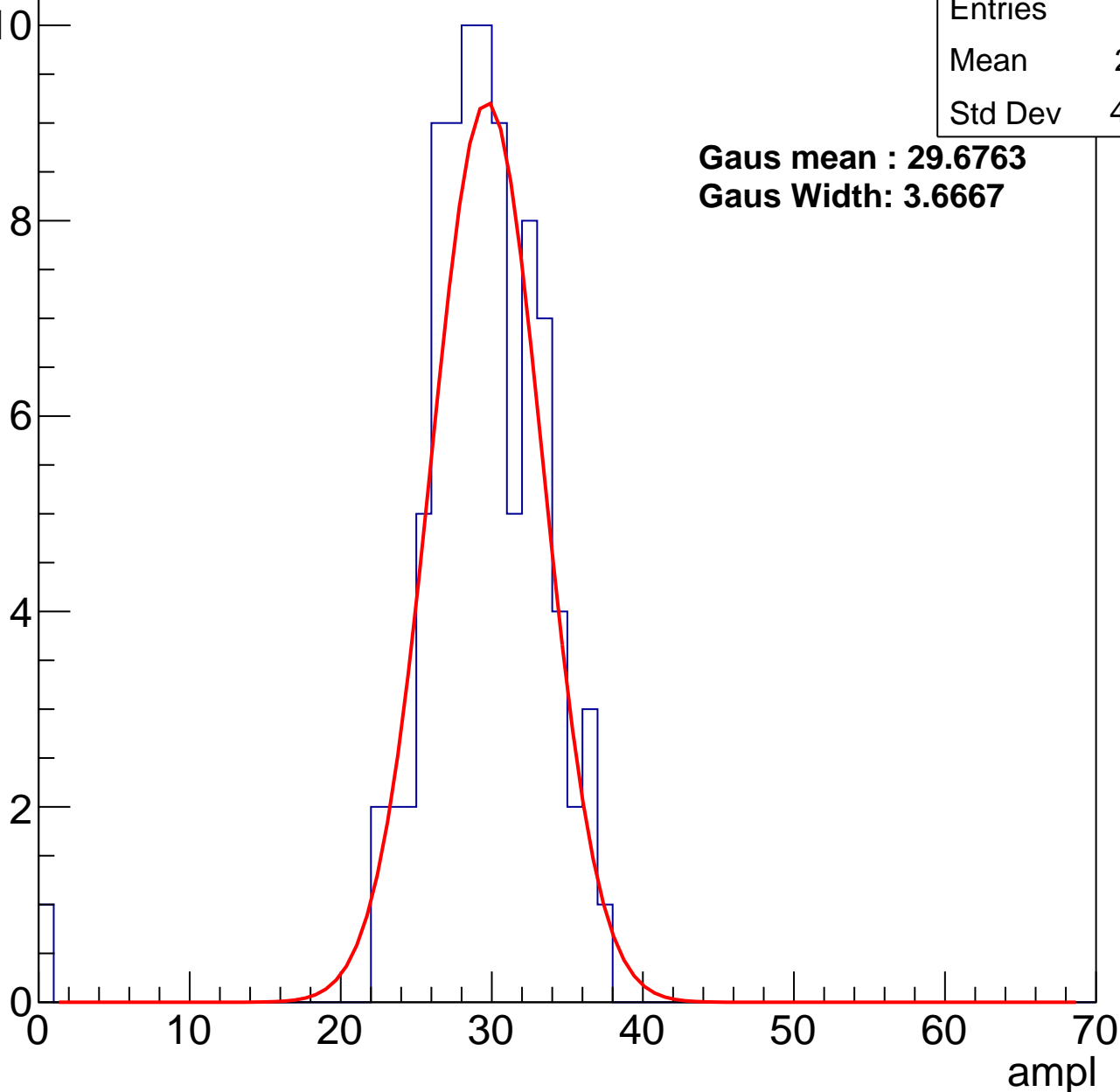
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	28.91
Std Dev	4.576

Gaus mean : 29.6763

Gaus Width: 3.6667



B0L102S, U12-ch21, adc1

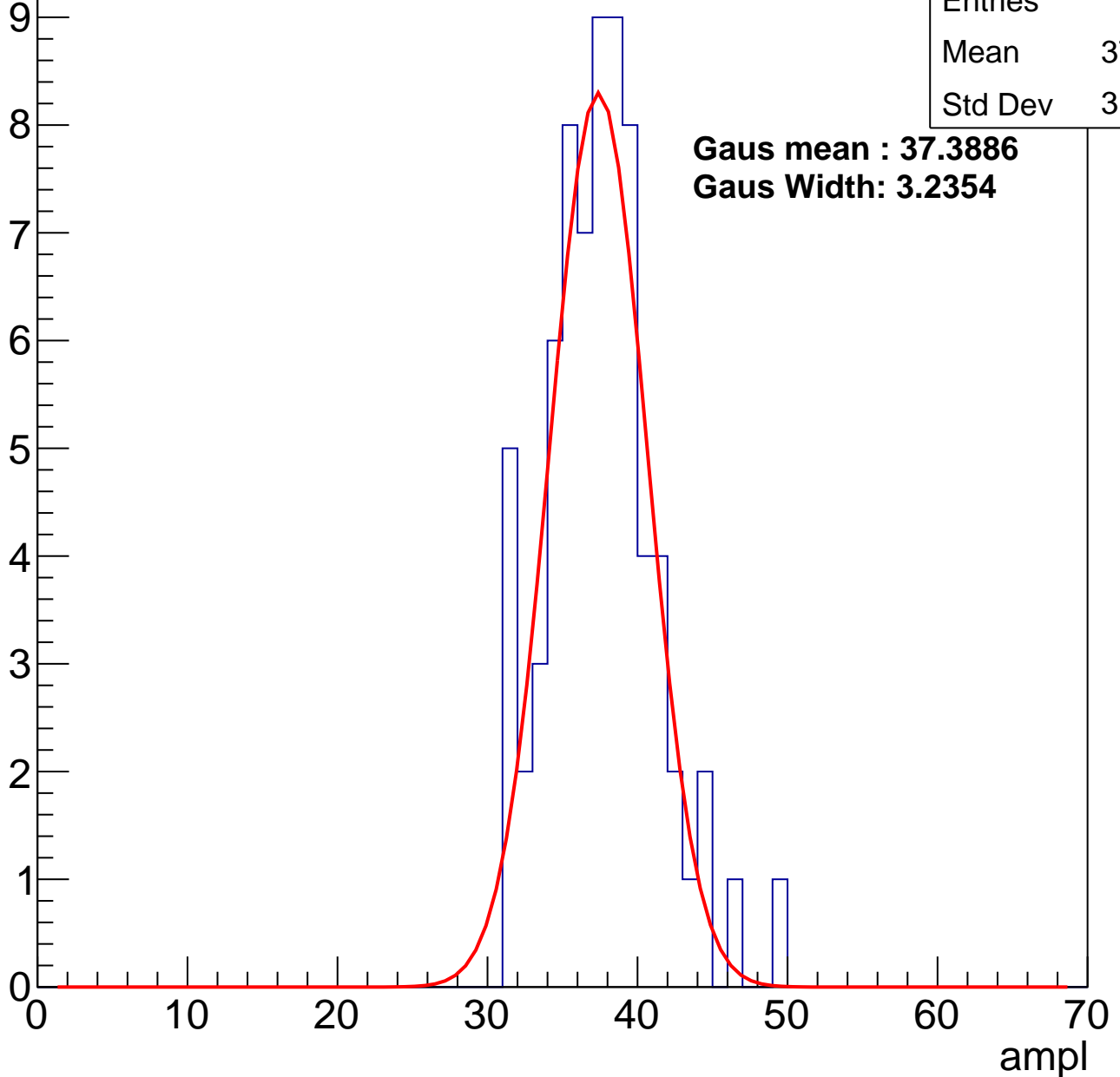
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	37.15
Std Dev	3.573

Gaus mean : 37.3886

Gaus Width: 3.2354



B0L102S, U12-ch21, adc2

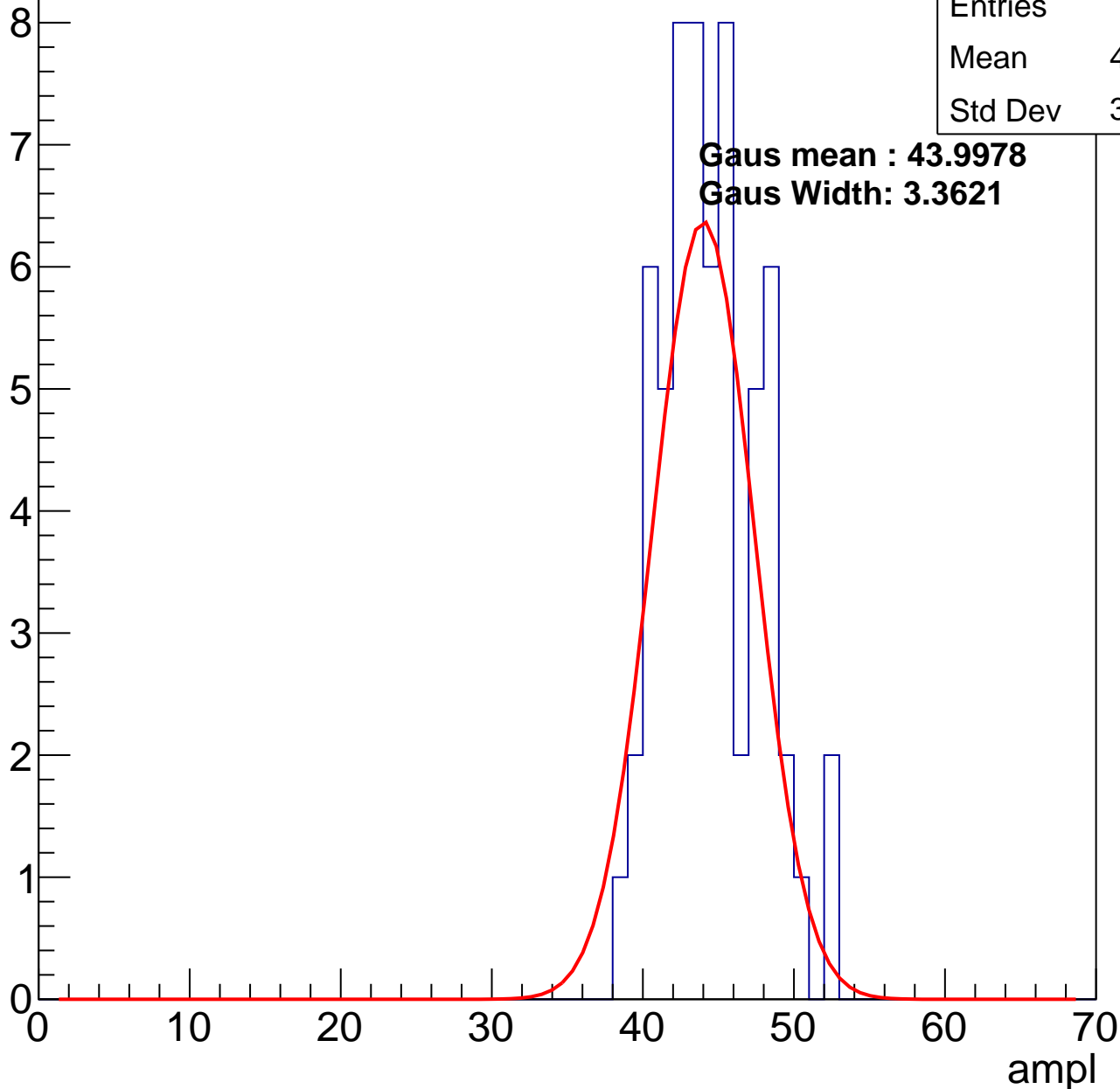
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	44.06
Std Dev	3.207

Gaus mean : 43.9978

Gaus Width: 3.3621

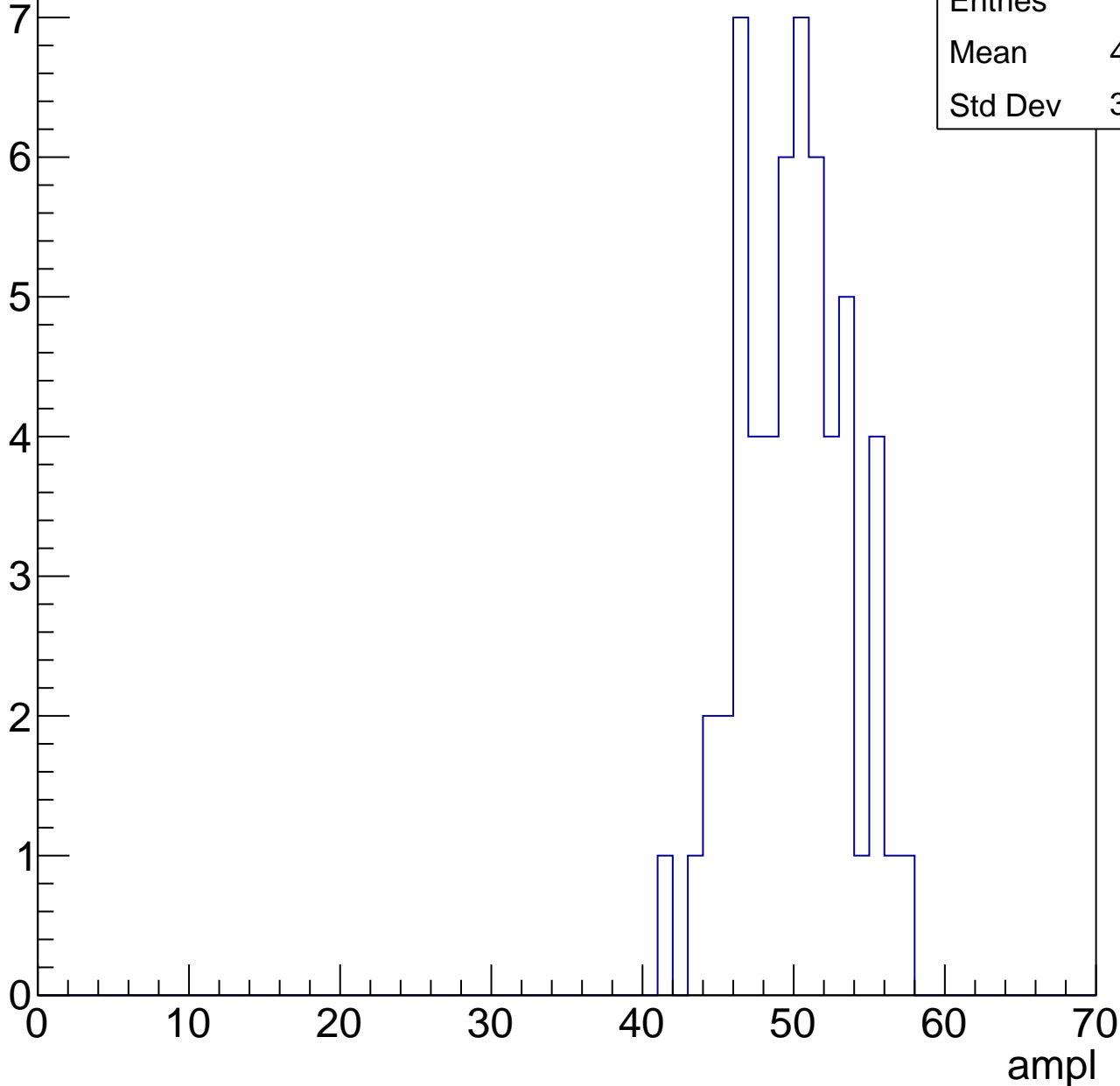


B0L102S, U12-ch21, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	49.54
Std Dev	3.479

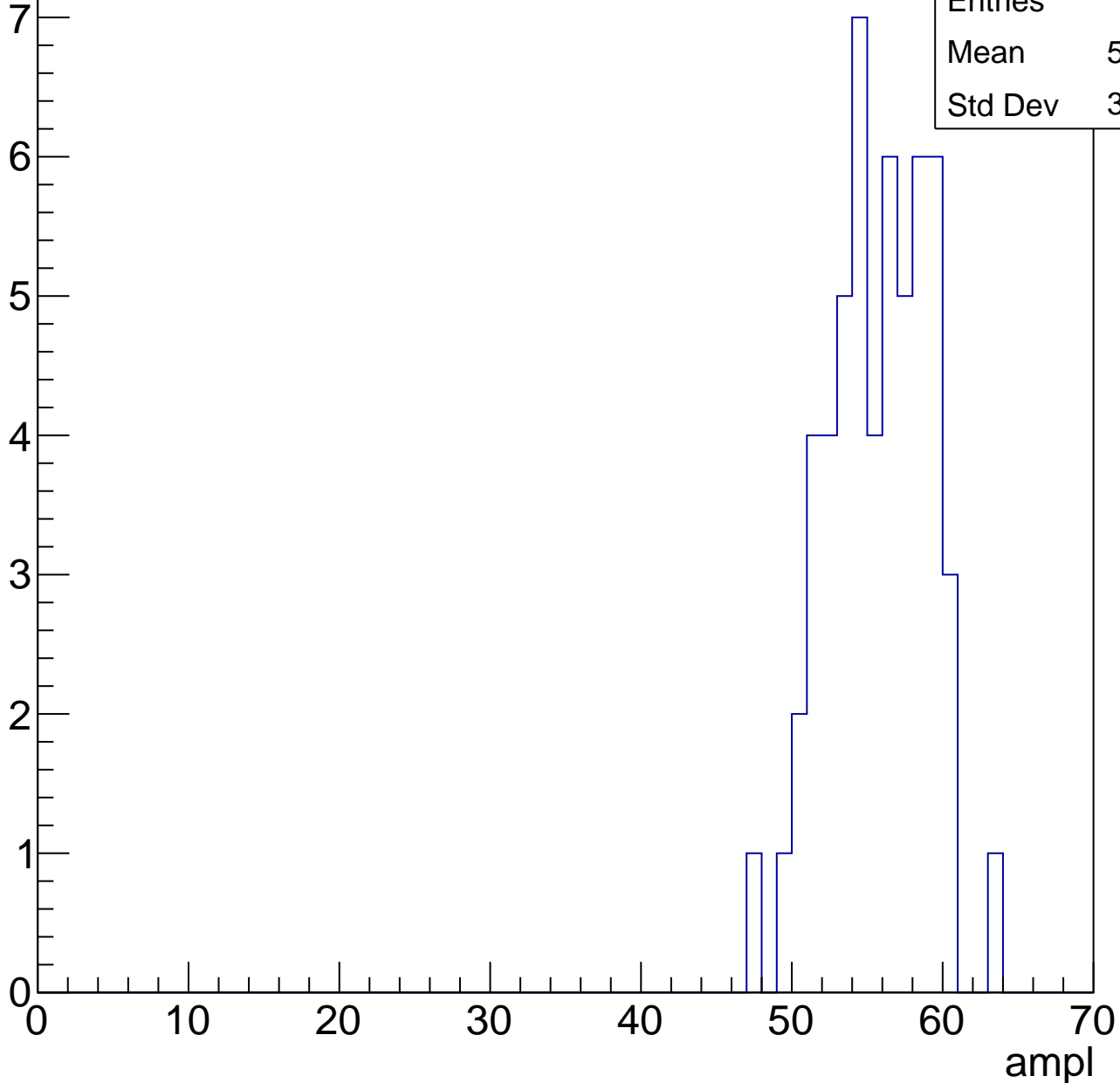


B0L102S, U12-ch21, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	55.22
Std Dev	3.274

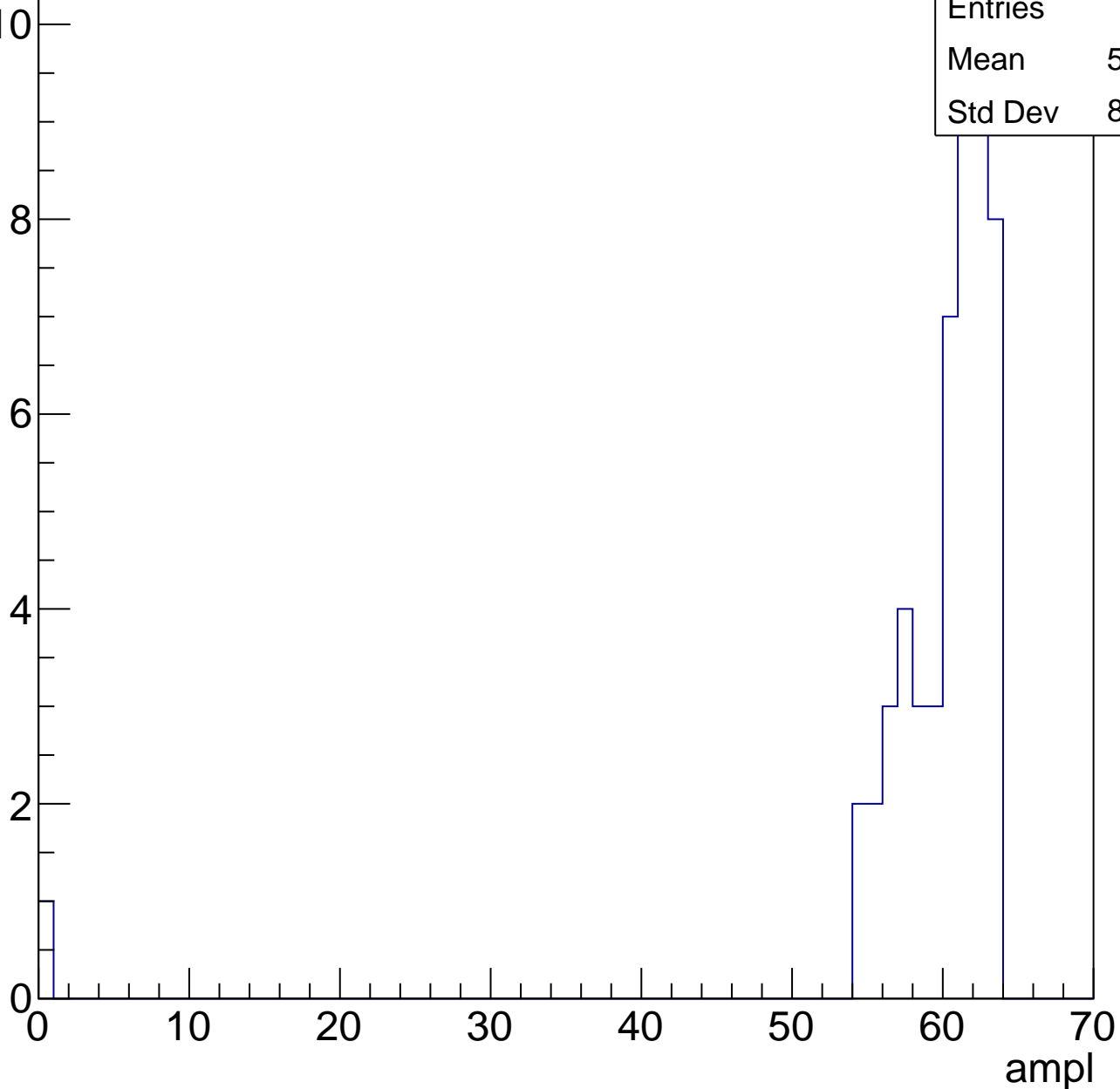


B0L102S, U12-ch21, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

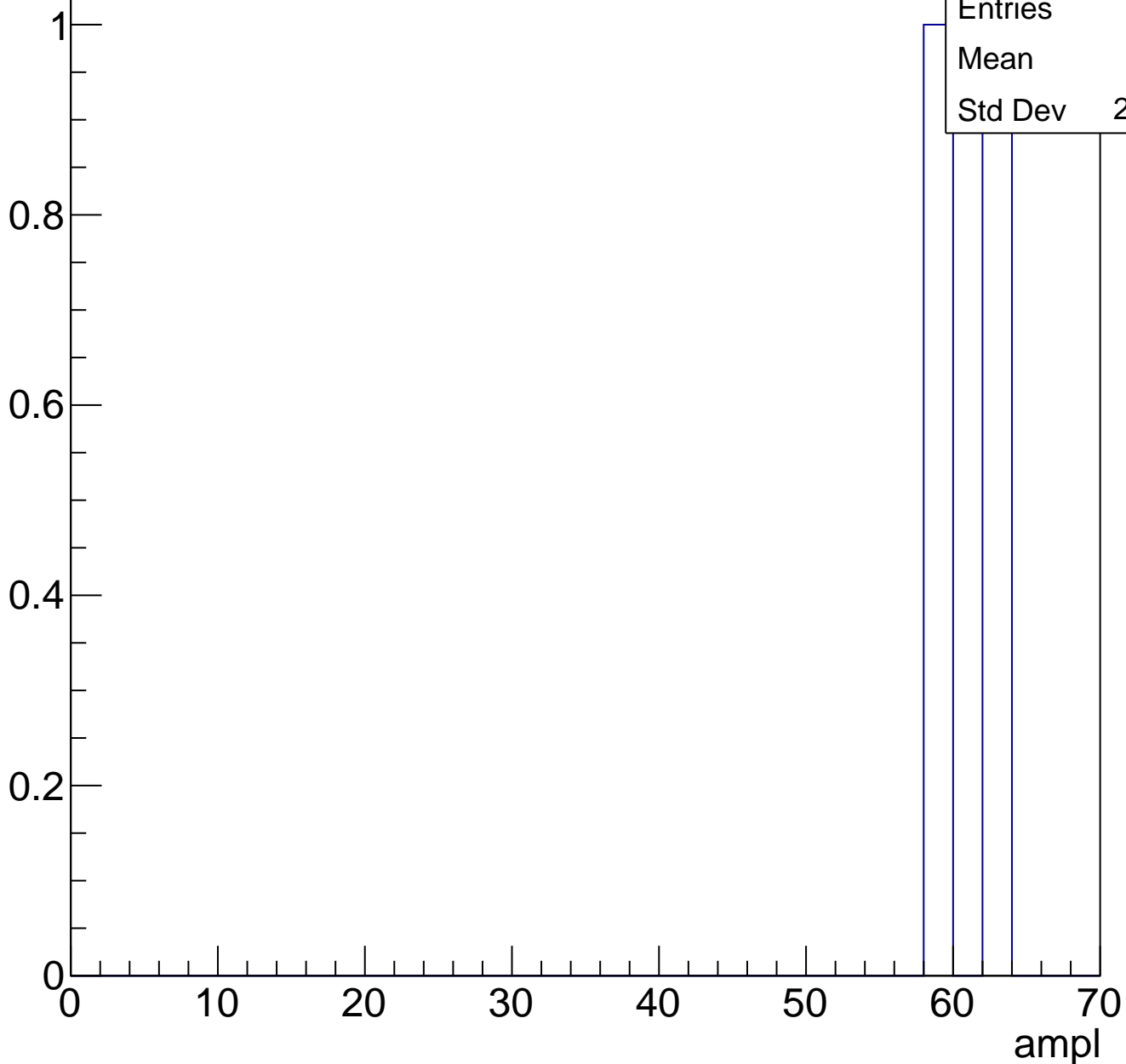
Entries	52
Mean	58.79
Std Dev	8.619



B0L102S, U12-ch21, adc6

calib_packv5_042523_0143.root, FC#12, port B1

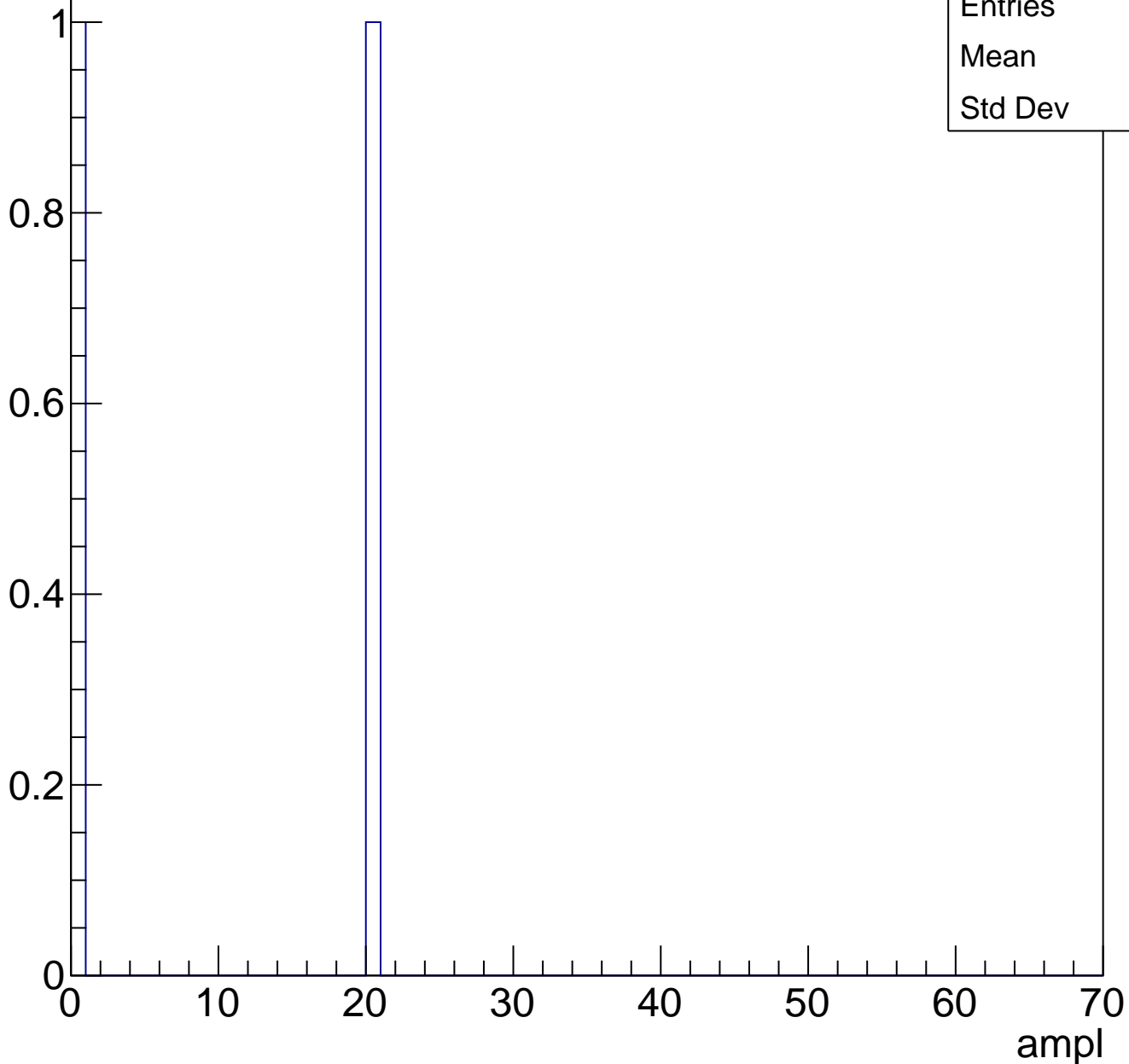
Entry



B0L102S, U12-ch21, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10
Std Dev	10

B0L102S, U12-ch22, adc0

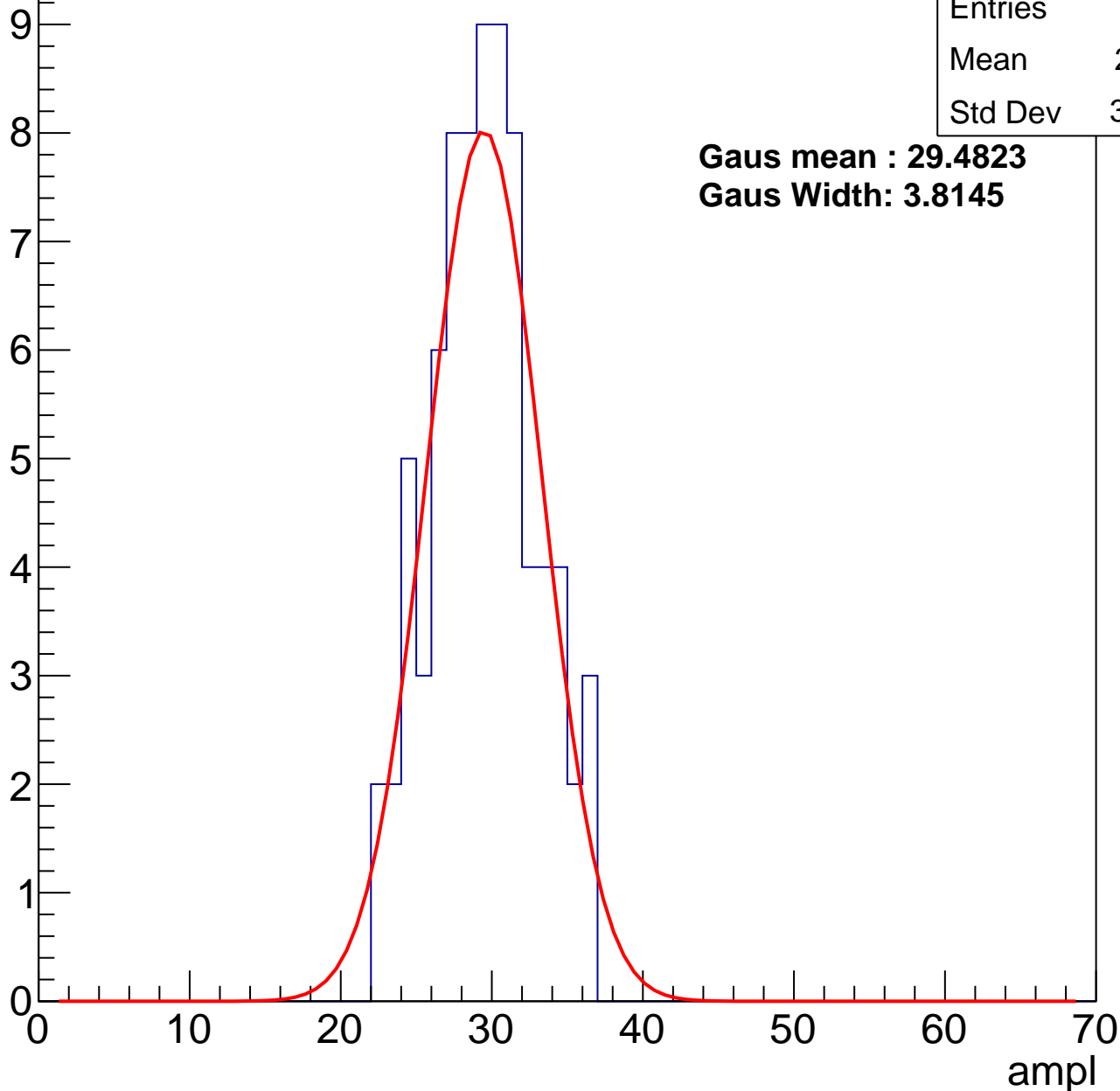
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	29.01
Std Dev	3.413

Gaus mean : 29.4823

Gaus Width: 3.8145



B0L102S, U12-ch22, adc1

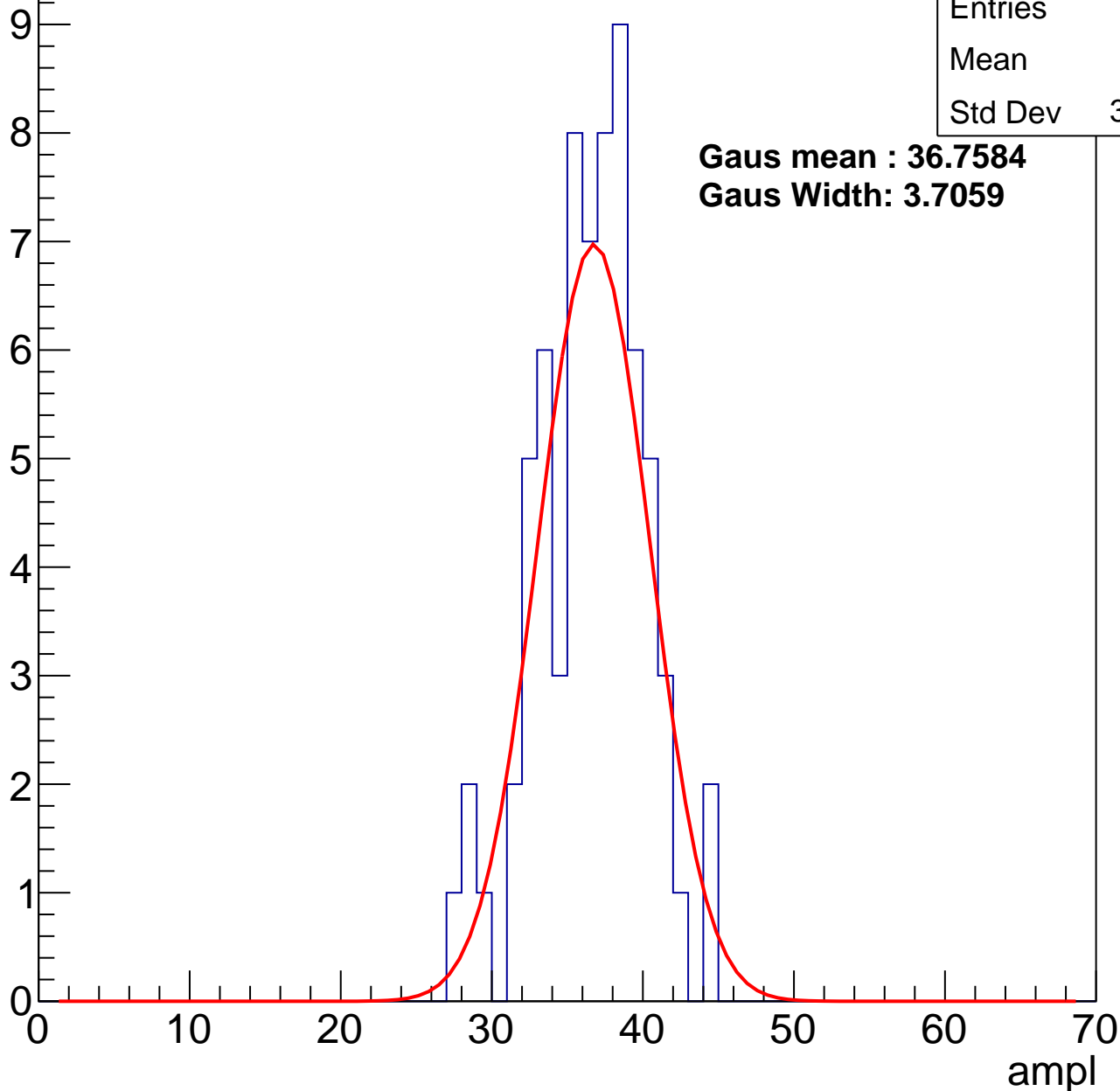
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.1
Std Dev	3.568

Gaus mean : 36.7584

Gaus Width: 3.7059



B0L102S, U12-ch22, adc2

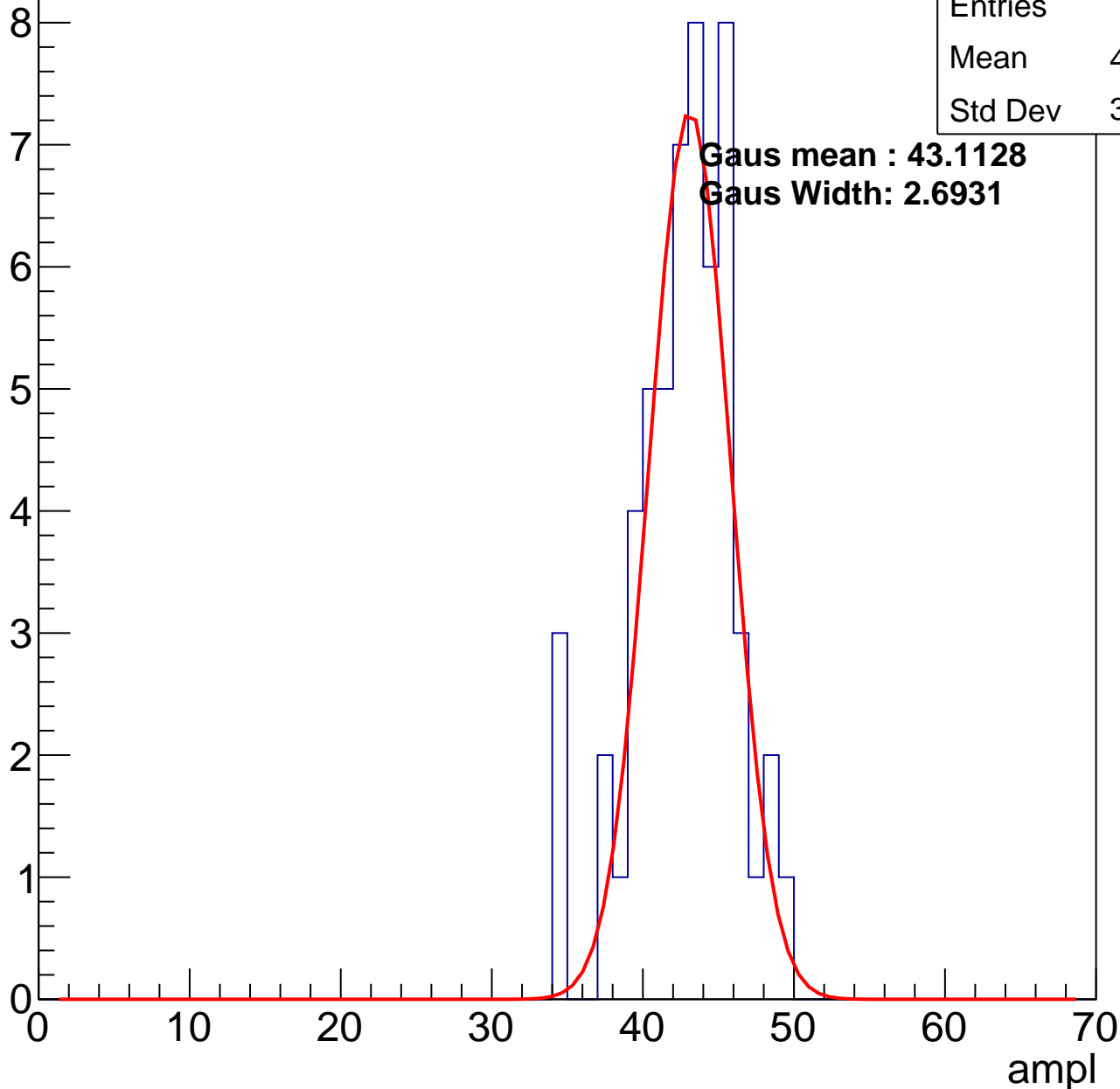
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	42.27
Std Dev	3.308

Gaus mean : 43.1128

Gaus Width: 2.6931

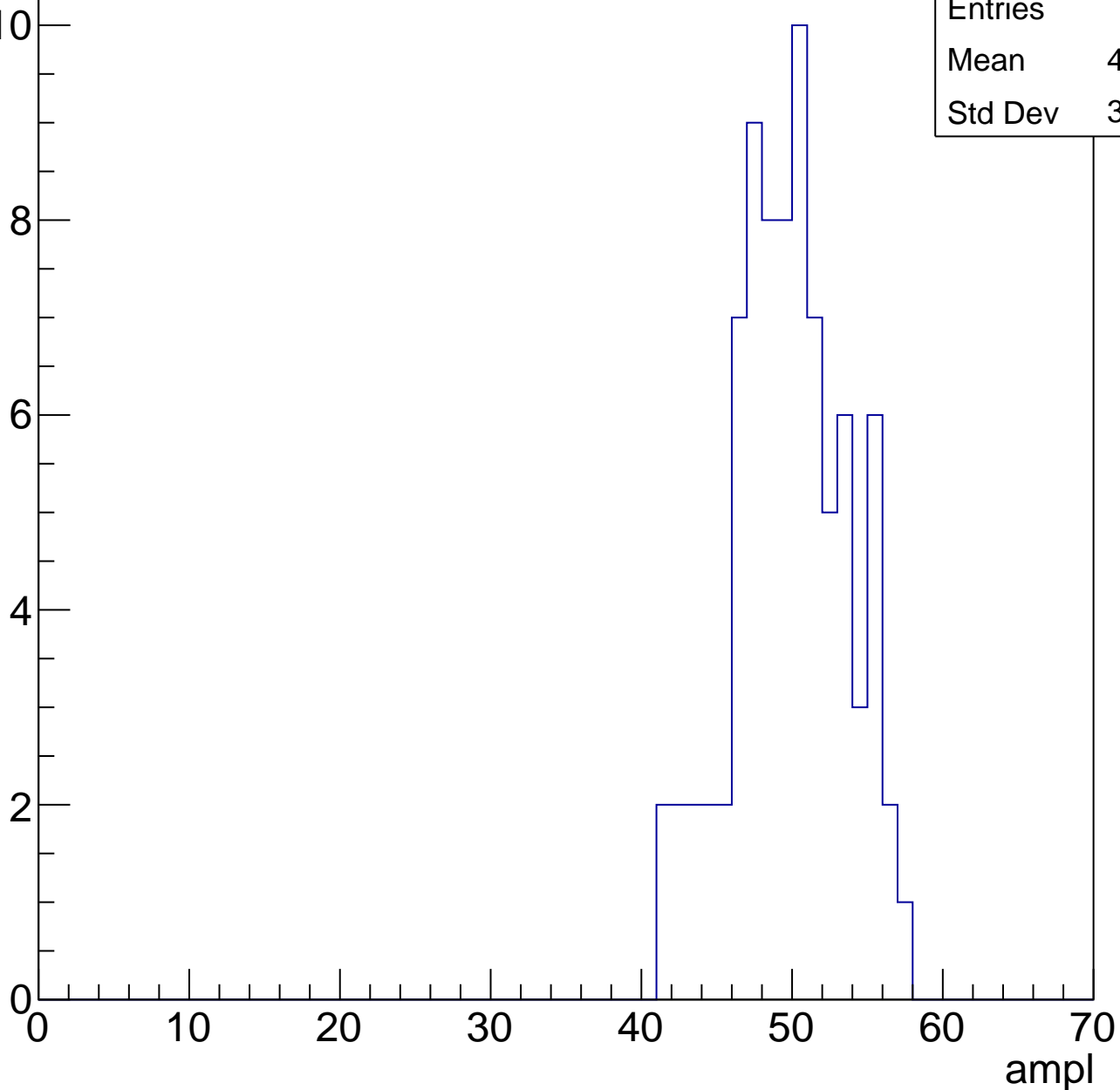


B0L102S, U12-ch22, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	49.35
Std Dev	3.677

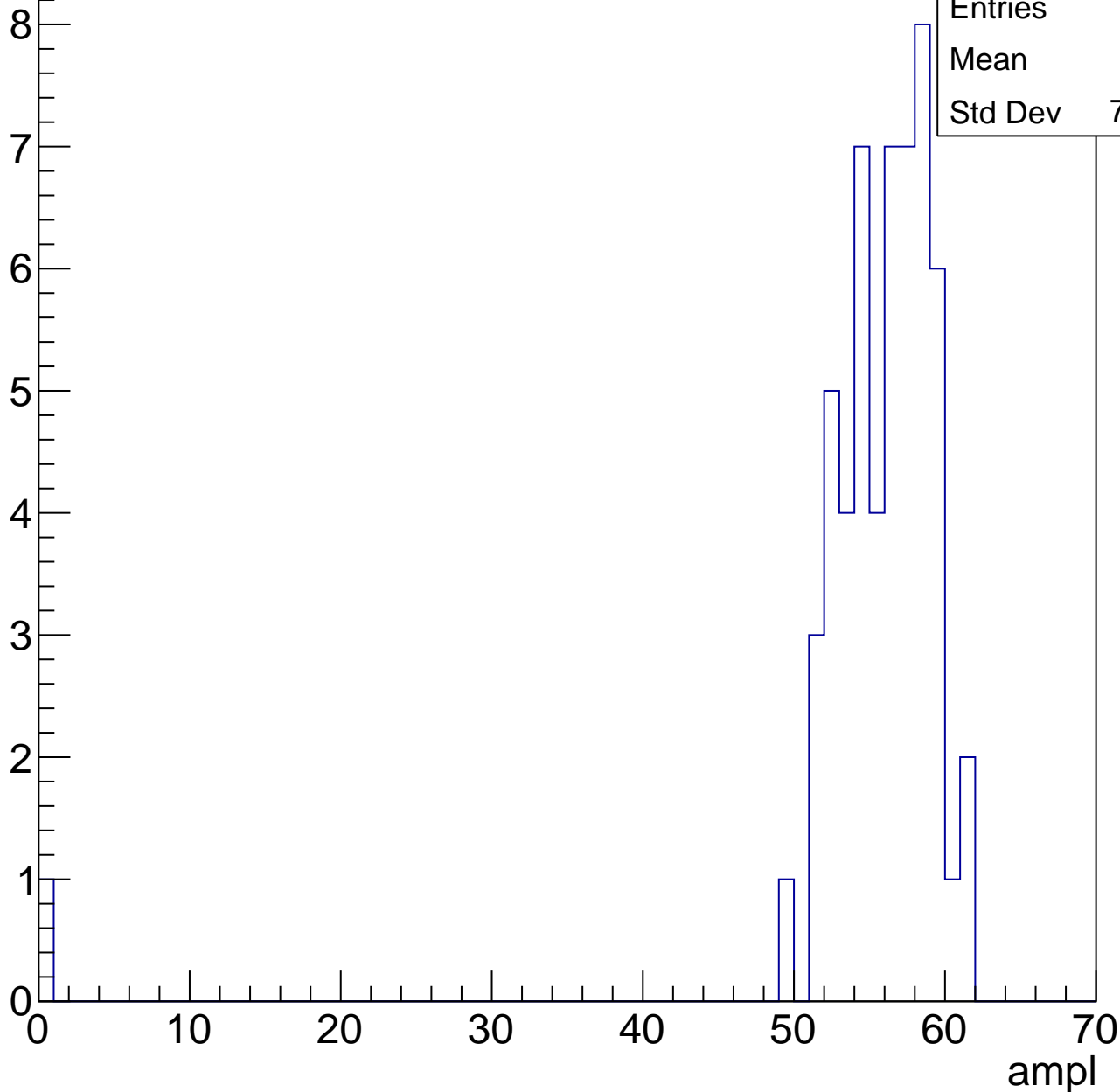


B0L102S, U12-ch22, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

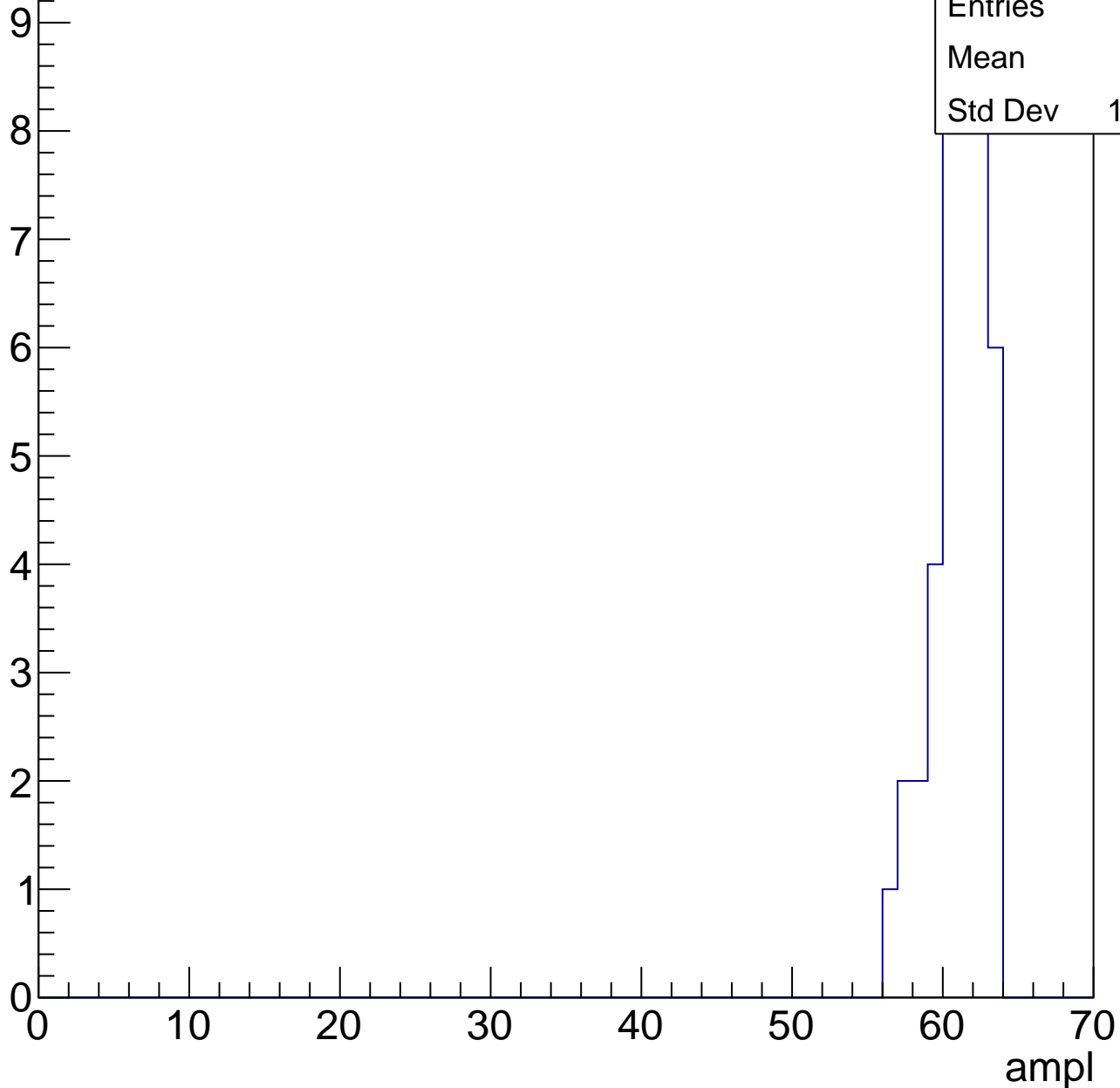
Entries	56
Mean	54.7
Std Dev	7.874



B0L102S, U12-ch22, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	40
Mean	60.6
Std Dev	1.772

B0L102S, U12-ch22, adc6

calib_packv5_042523_0143.root, FC#12, port B1

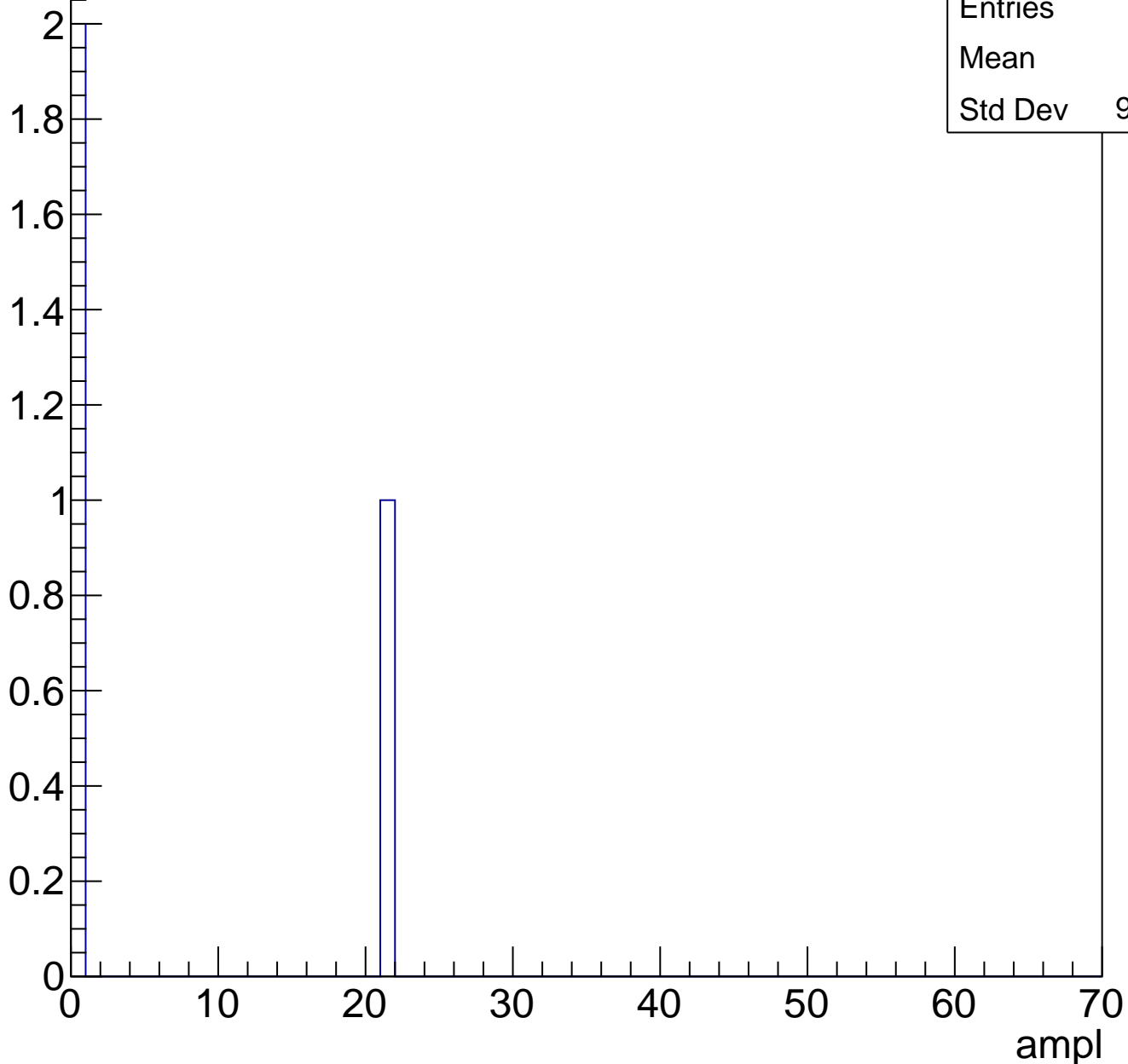
Entry



B0L102S, U12-ch22, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch23, adc0

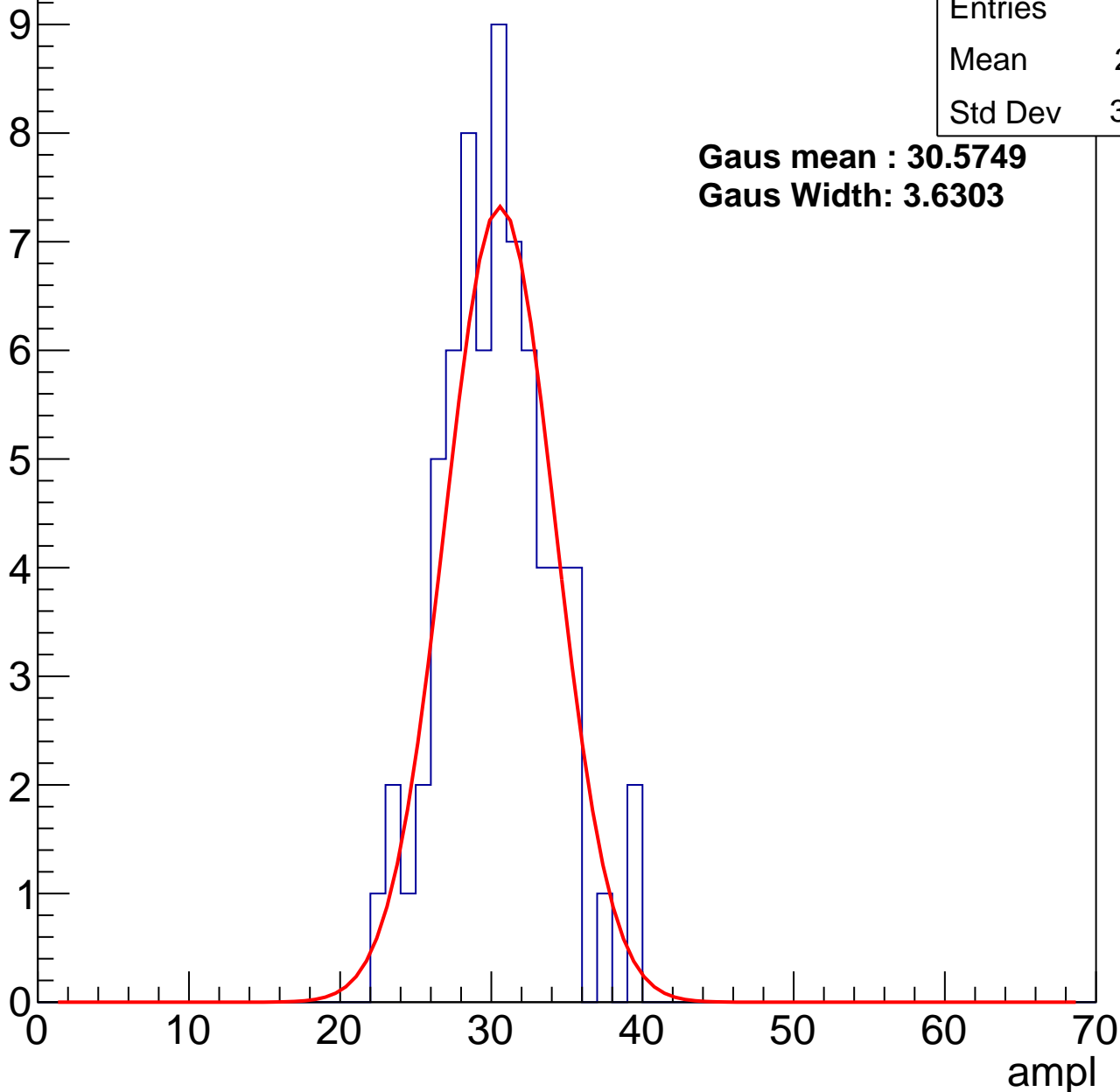
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.91
Std Dev	3.559

Gaus mean : 30.5749

Gaus Width: 3.6303



B0L102S, U12-ch23, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	82
Mean	37.2
Std Dev	3.409

Gaus mean : 37.3748

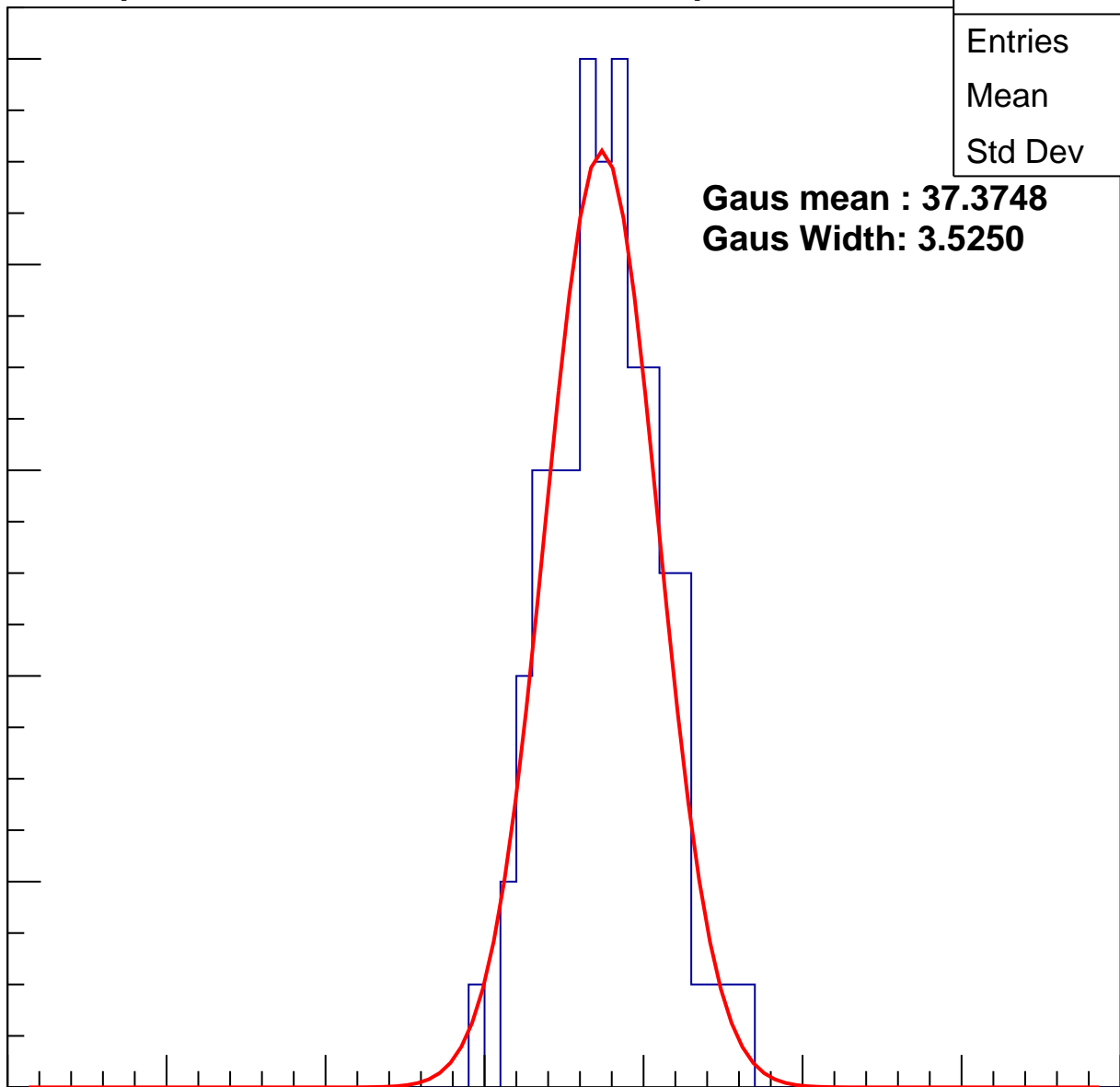
Gaus Width: 3.5250

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch23, adc2

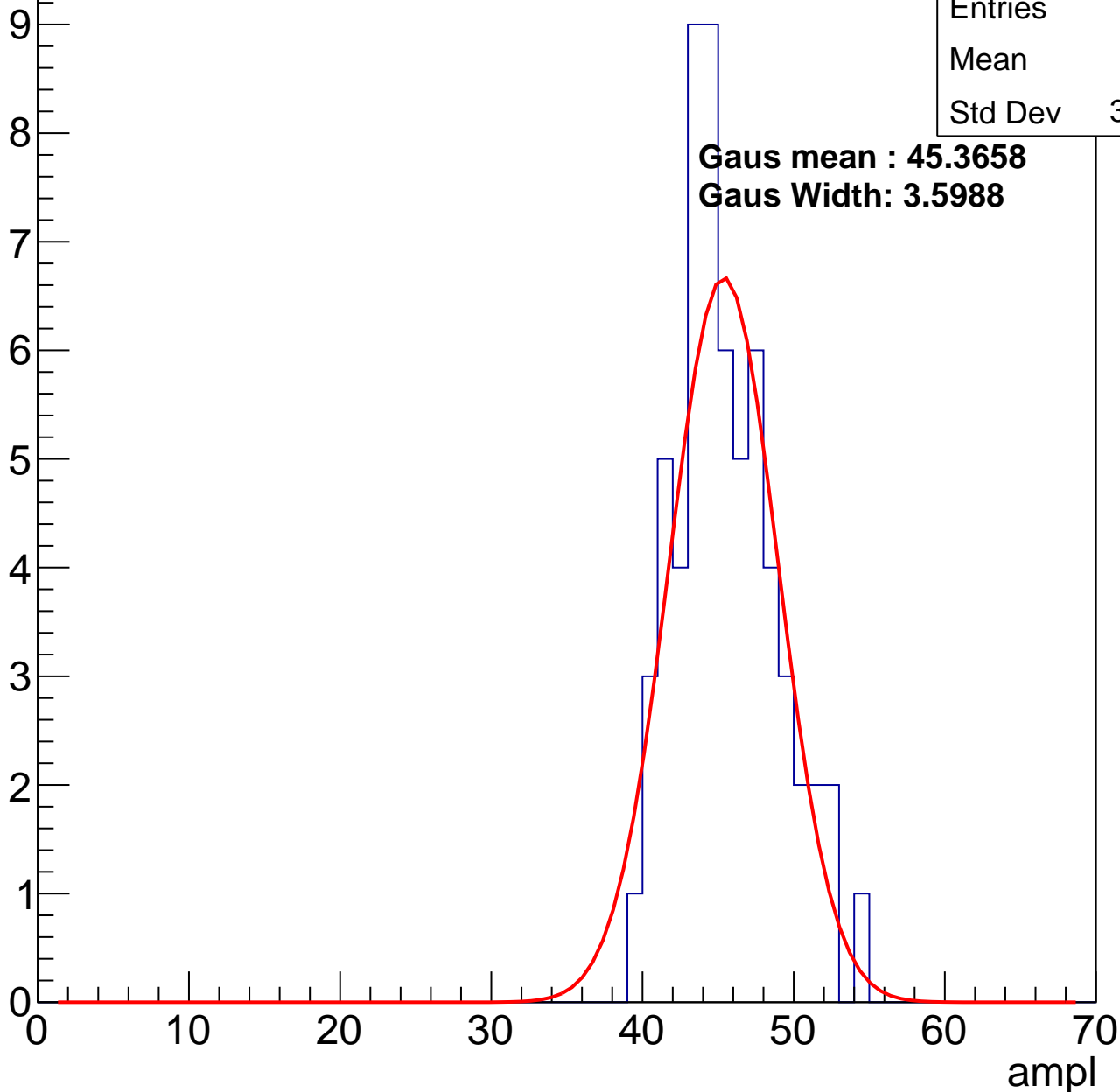
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	45.1
Std Dev	3.325

Gaus mean : 45.3658

Gaus Width: 3.5988

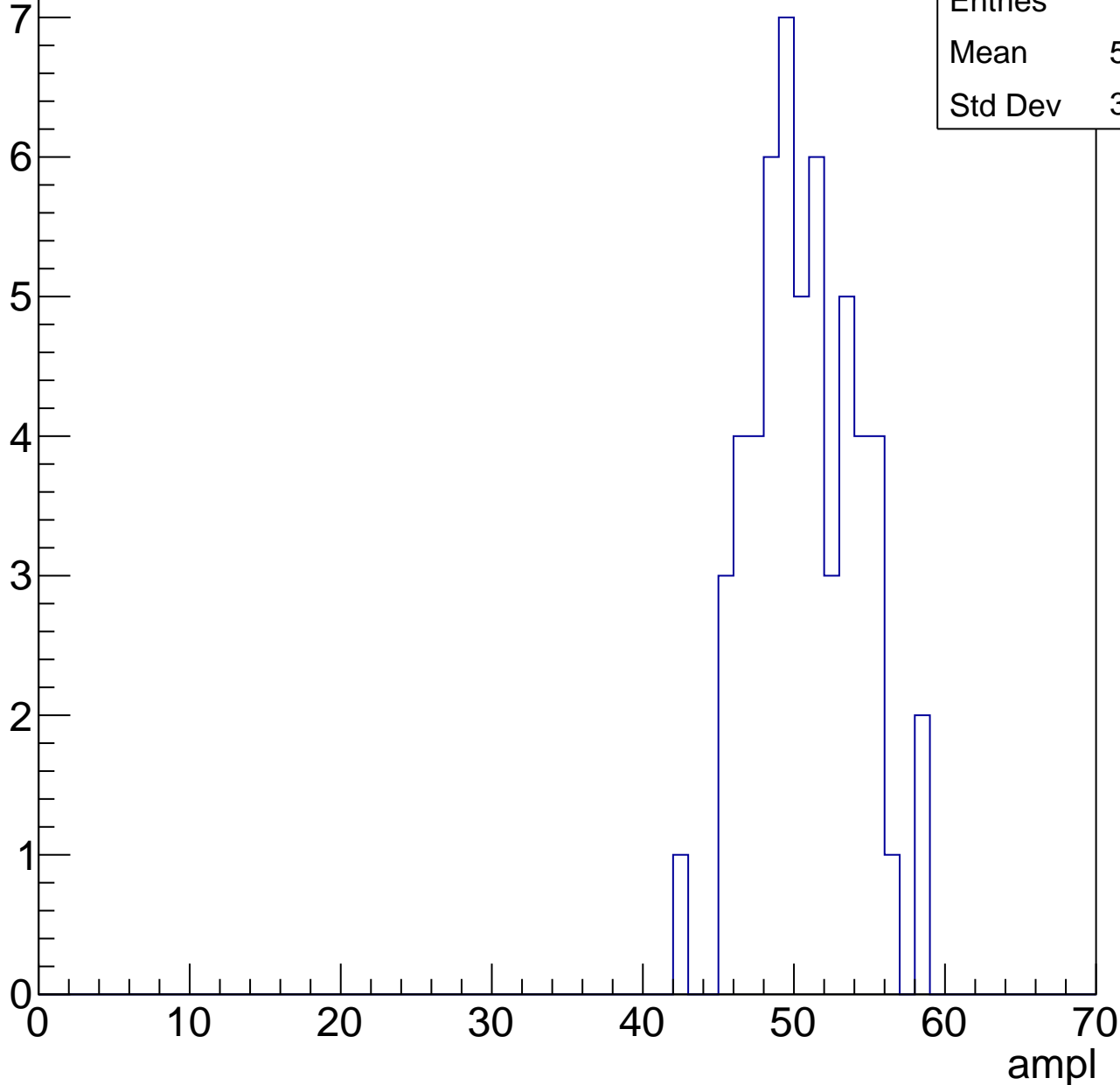


B0L102S, U12-ch23, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	50.27
Std Dev	3.456



B0L102S, U12-ch23, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

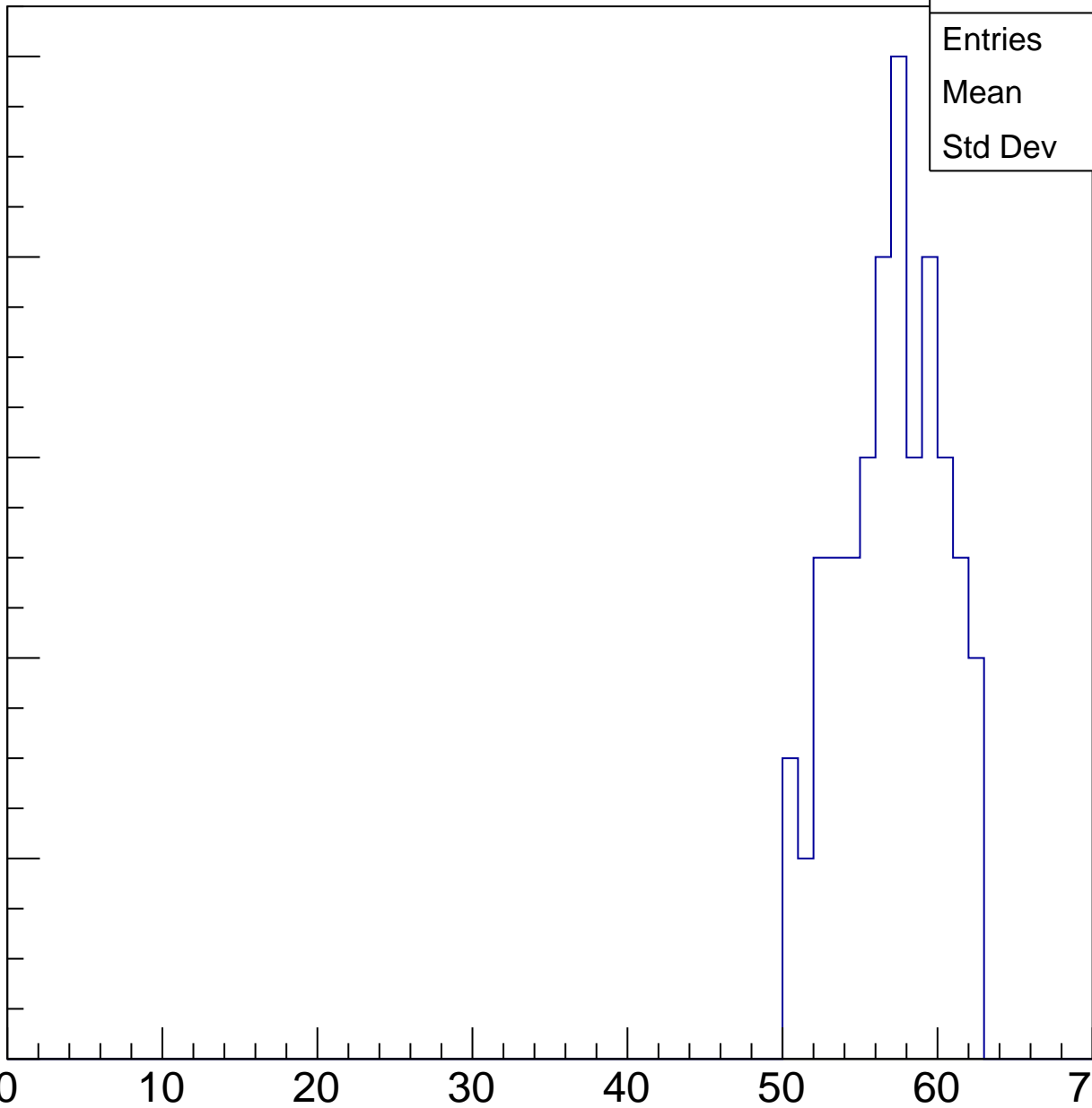
40

50

60

ampl

Entries	73
Mean	56.55
Std Dev	3.223

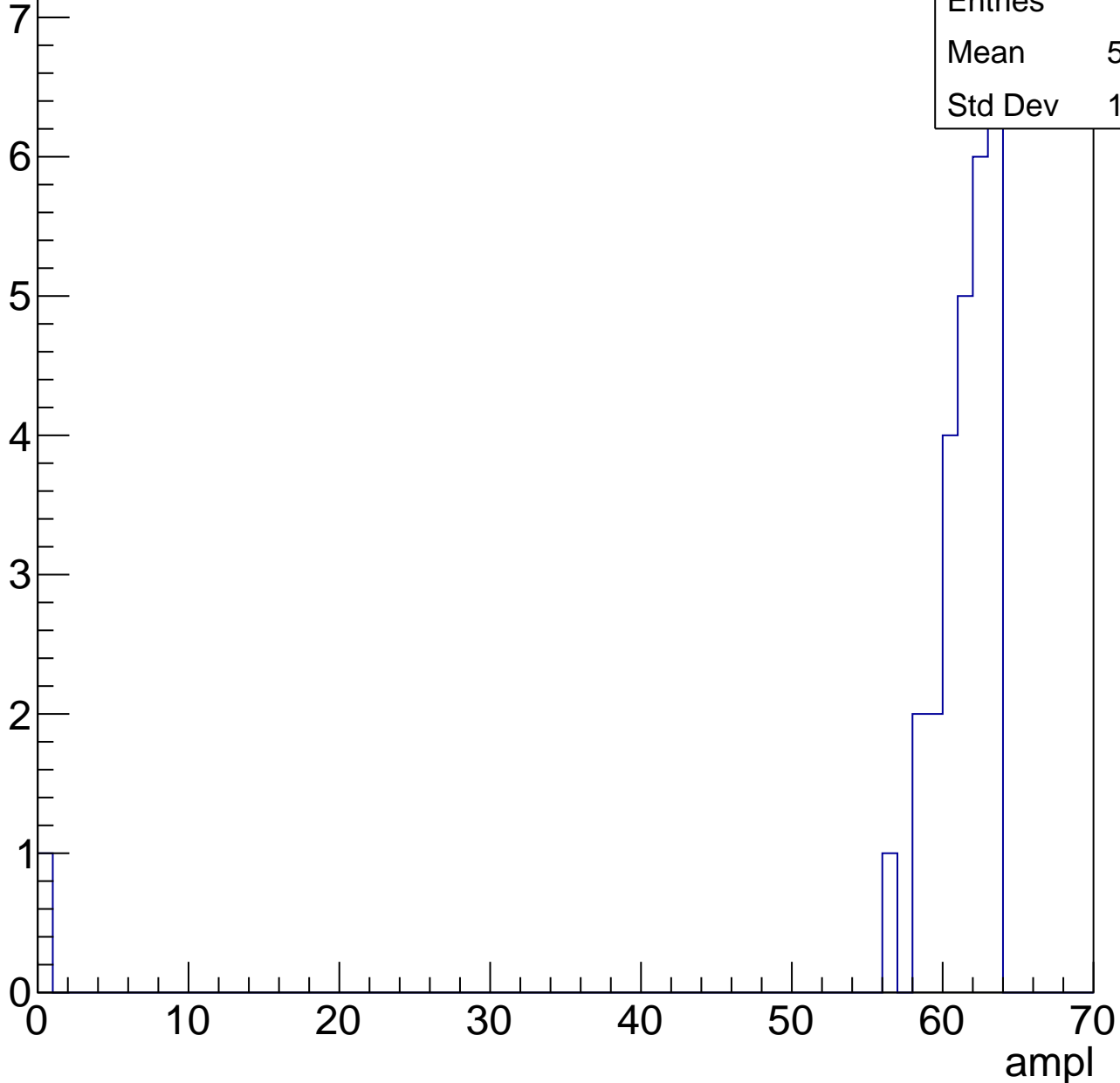


B0L102S, U12-ch23, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	28
Mean	58.86
Std Dev	11.47



B0L102S, U12-ch23, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch23, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch24, adc0

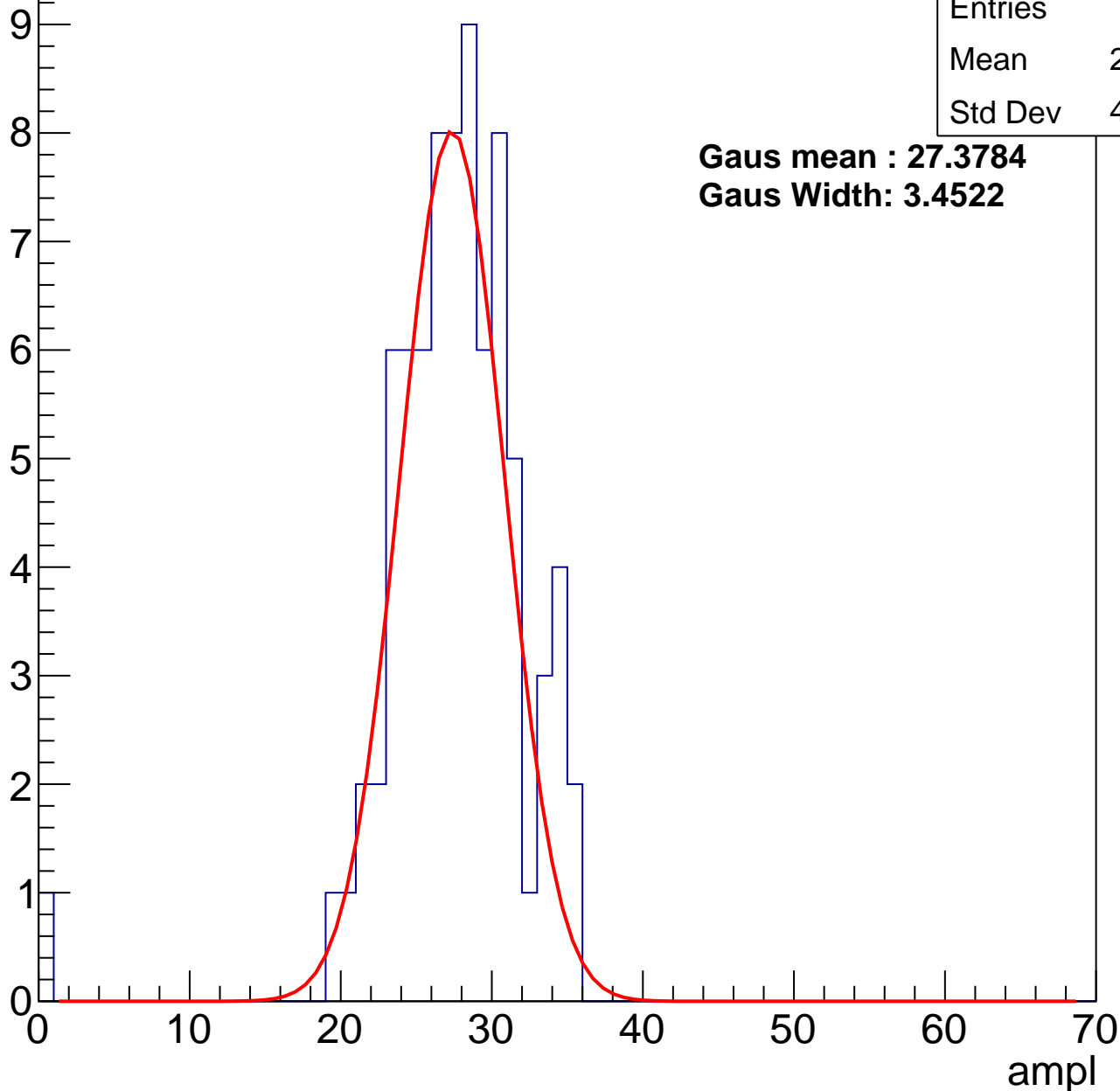
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	27.08
Std Dev	4.757

Gaus mean : 27.3784

Gaus Width: 3.4522



B0L102S, U12-ch24, adc1

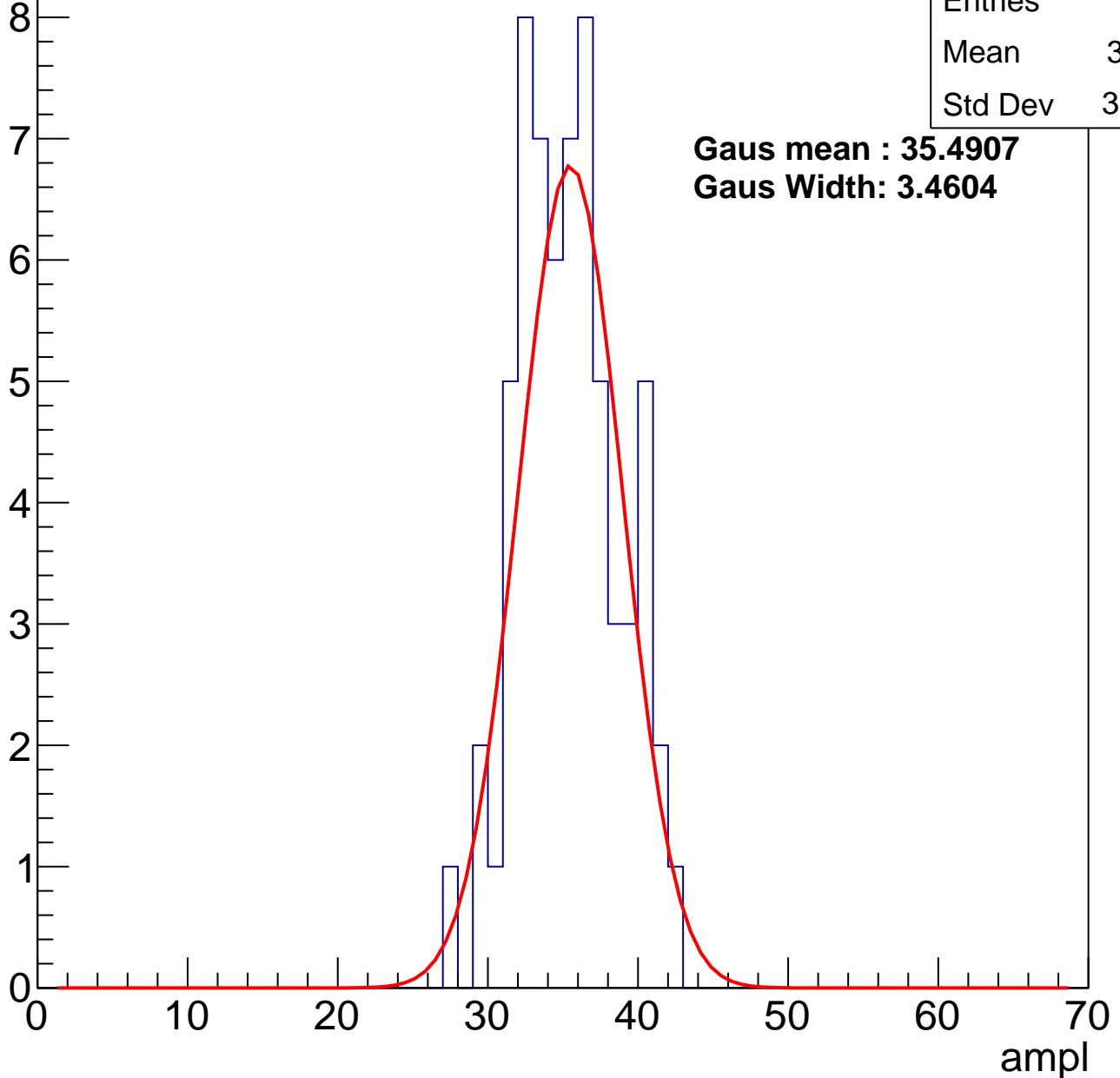
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	34.91
Std Dev	3.296

Gaus mean : 35.4907

Gaus Width: 3.4604



B0L102S, U12-ch24, adc2

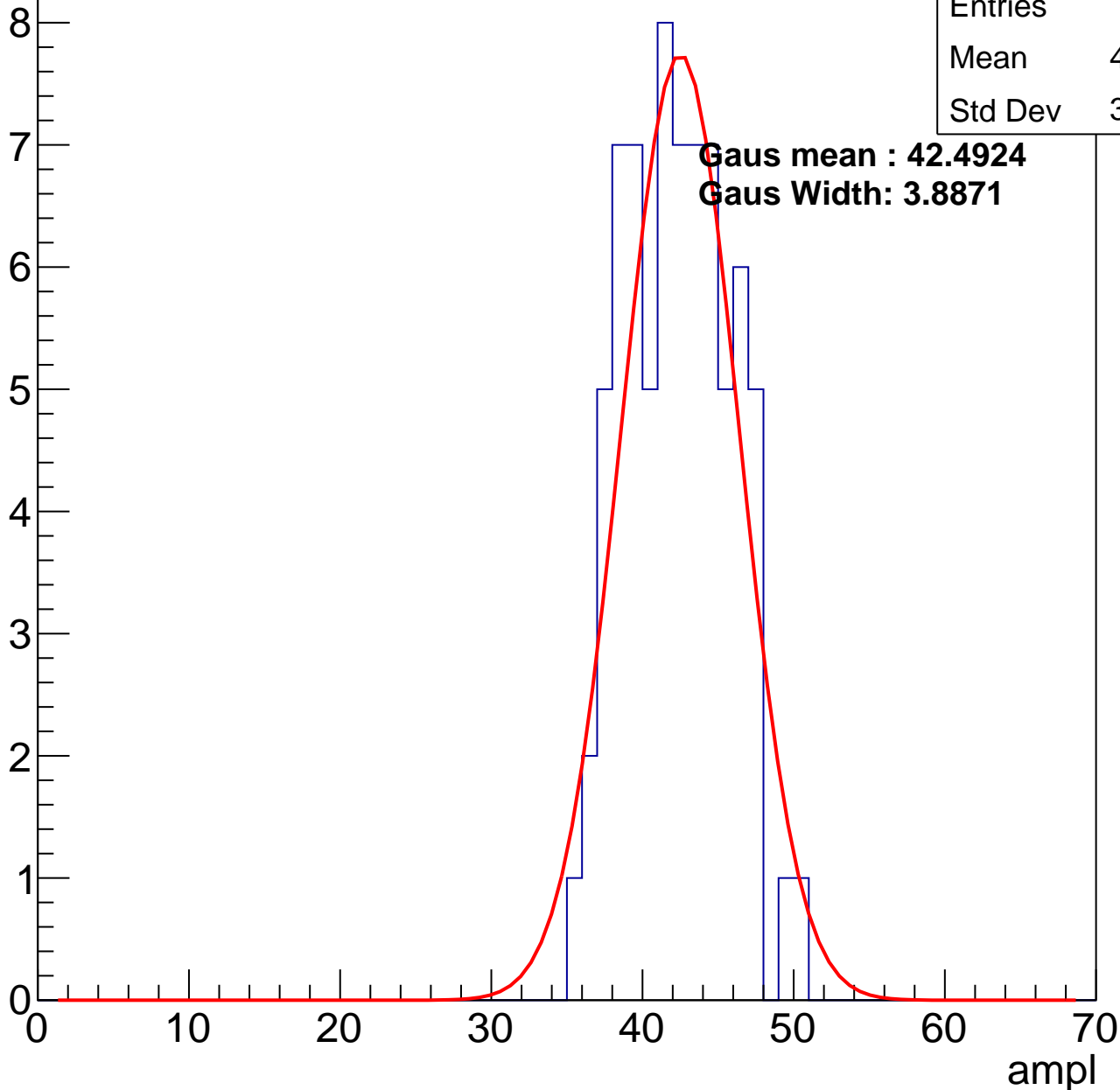
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	41.85
Std Dev	3.412

Gaus mean : 42.4924

Gaus Width: 3.8871

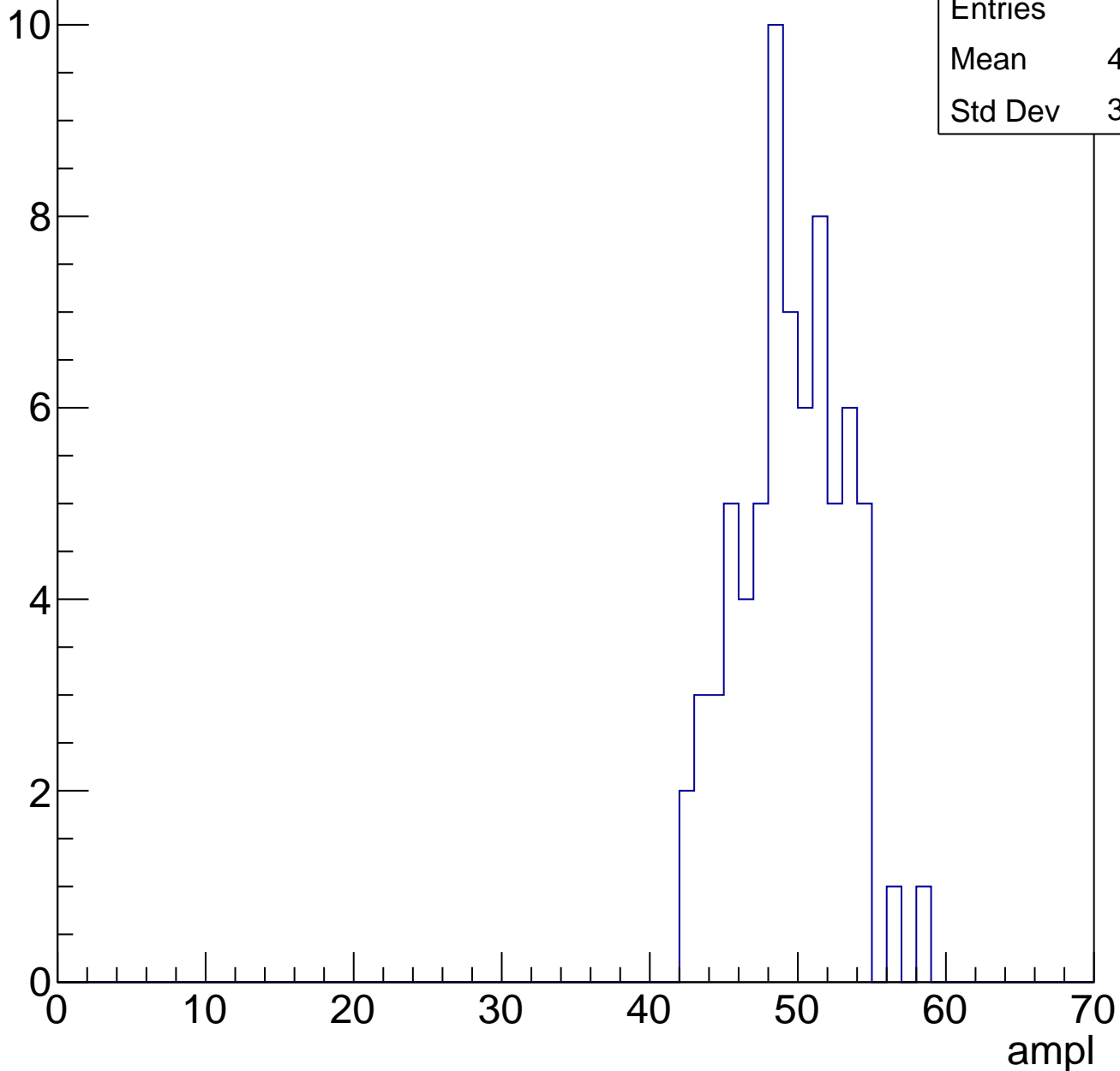


B0L102S, U12-ch24, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	49.04
Std Dev	3.482

Entry

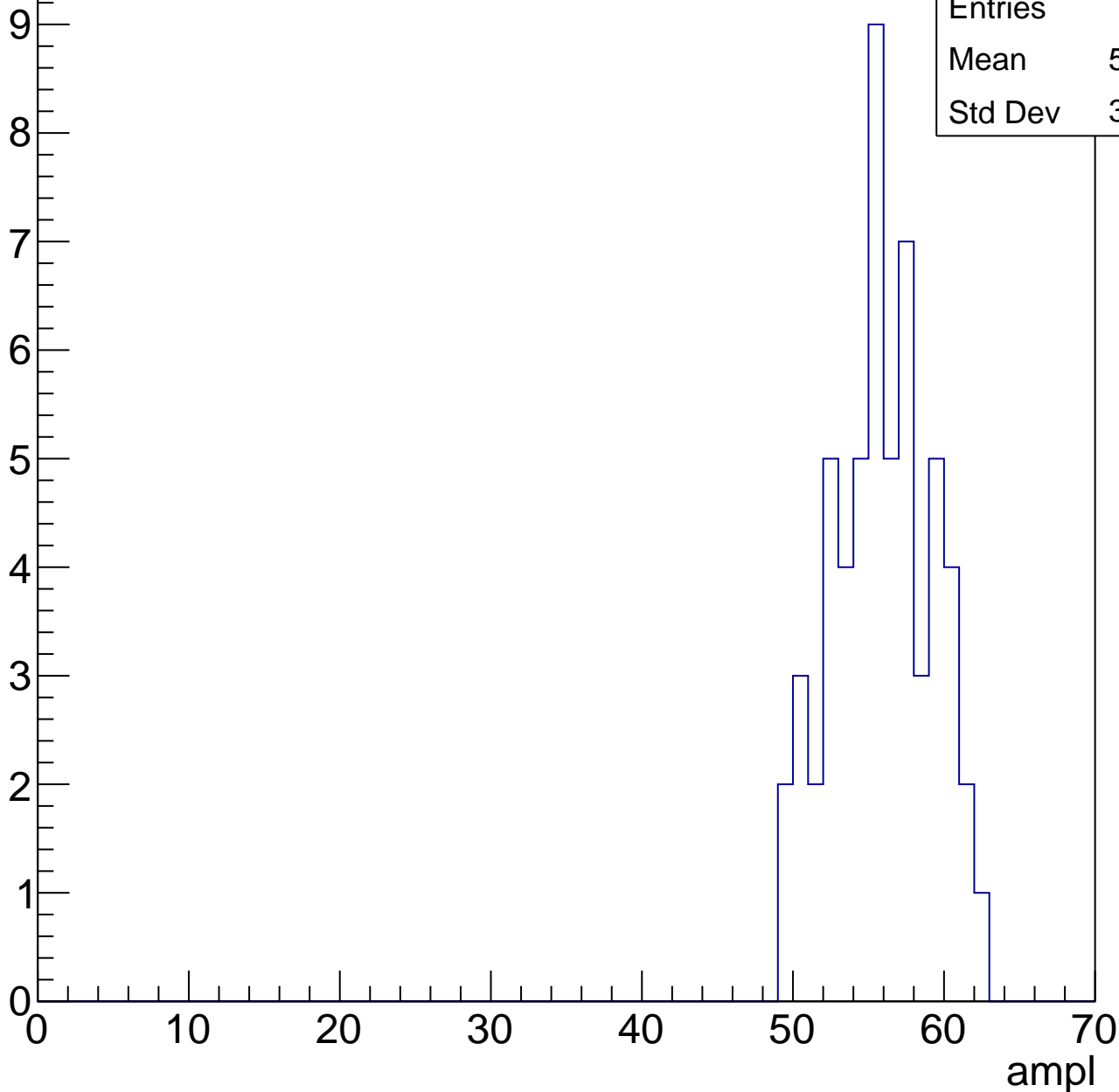


B0L102S, U12-ch24, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55.42
Std Dev	3.233

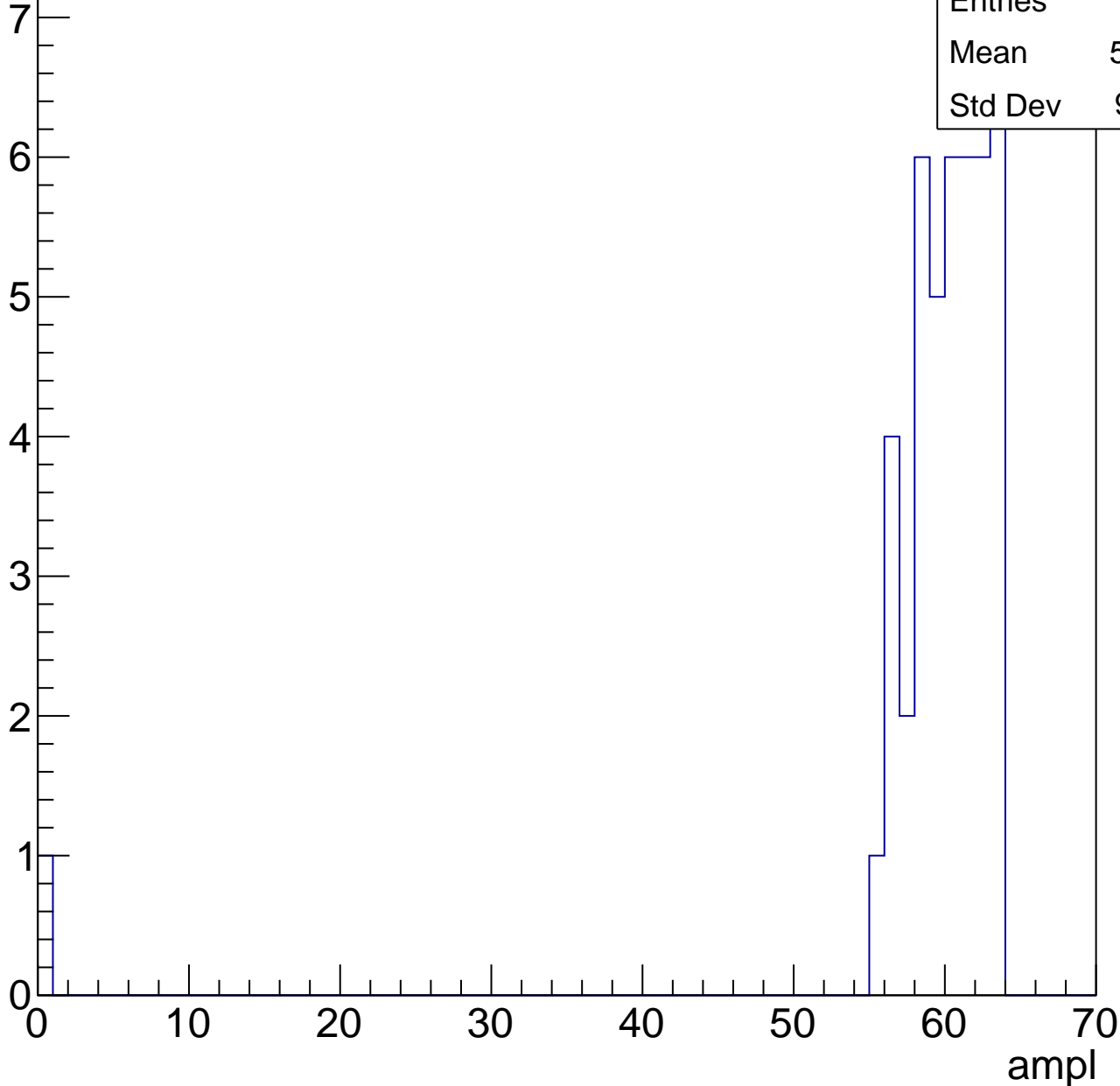


B0L102S, U12-ch24, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

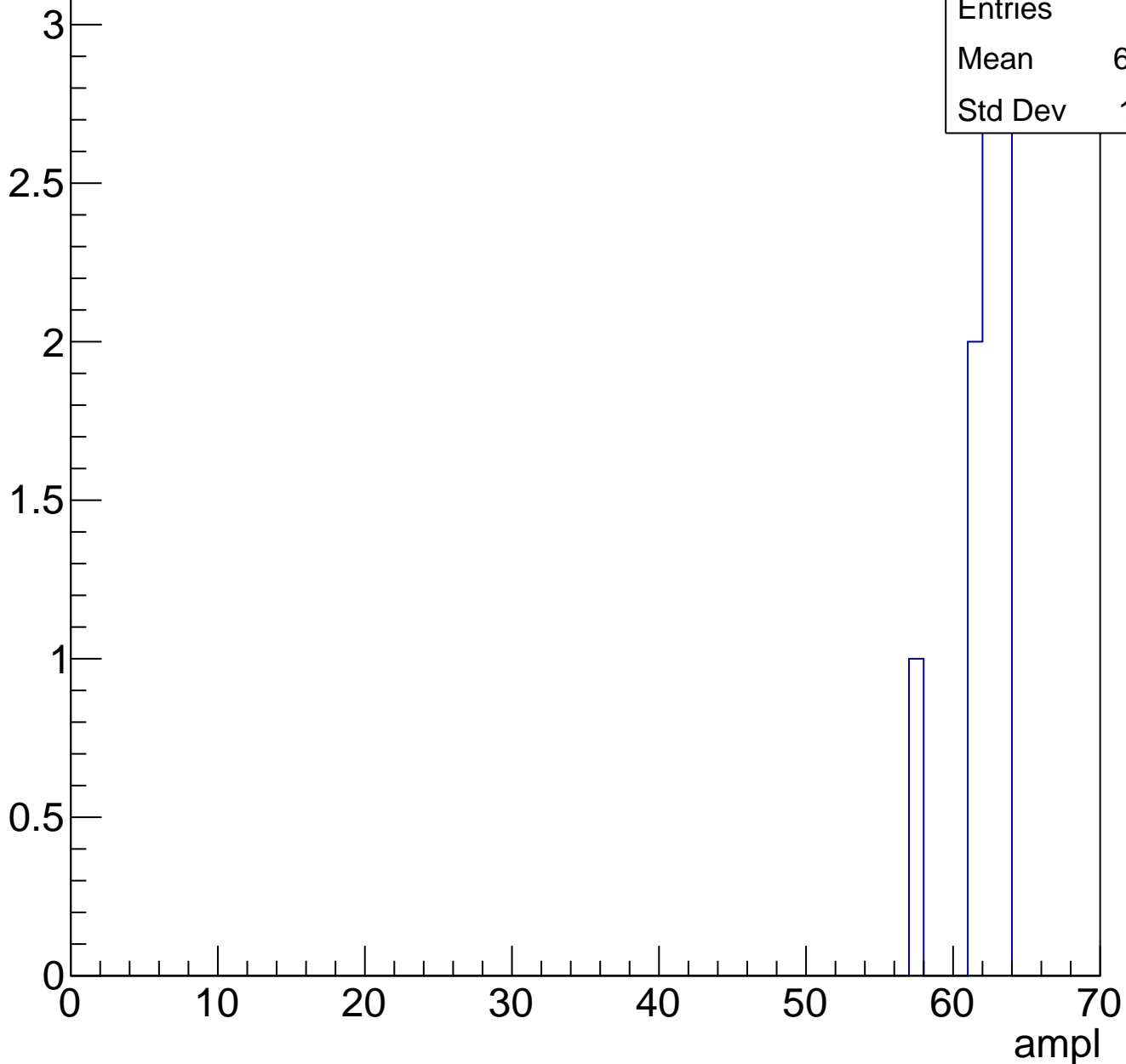
Entries	44
Mean	58.52
Std Dev	9.211



B0L102S, U12-ch24, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch24, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch25, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	94
Mean	28.79
Std Dev	4.029

Gaus mean : 29.0238

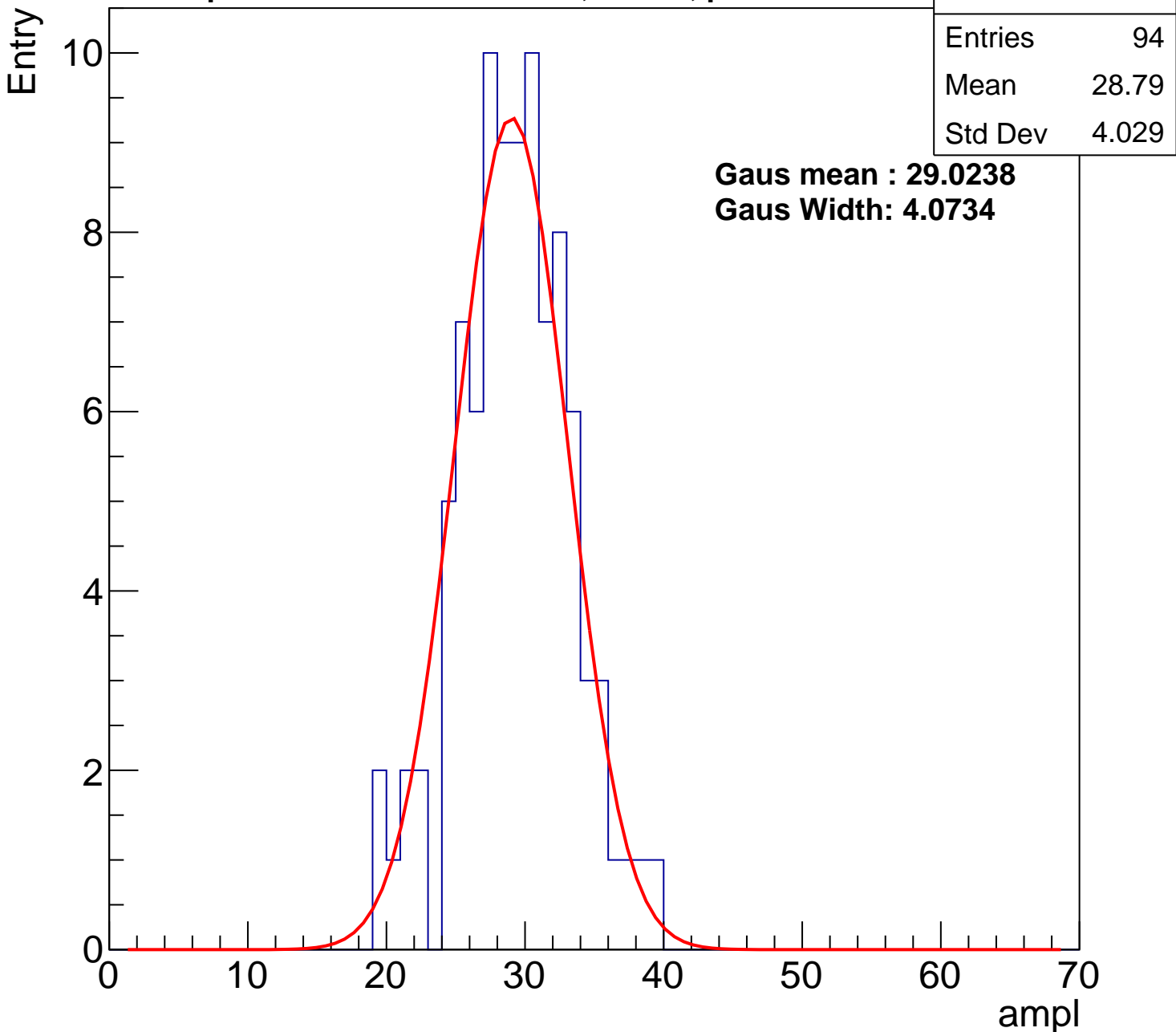
Gaus Width: 4.0734

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch25, adc1

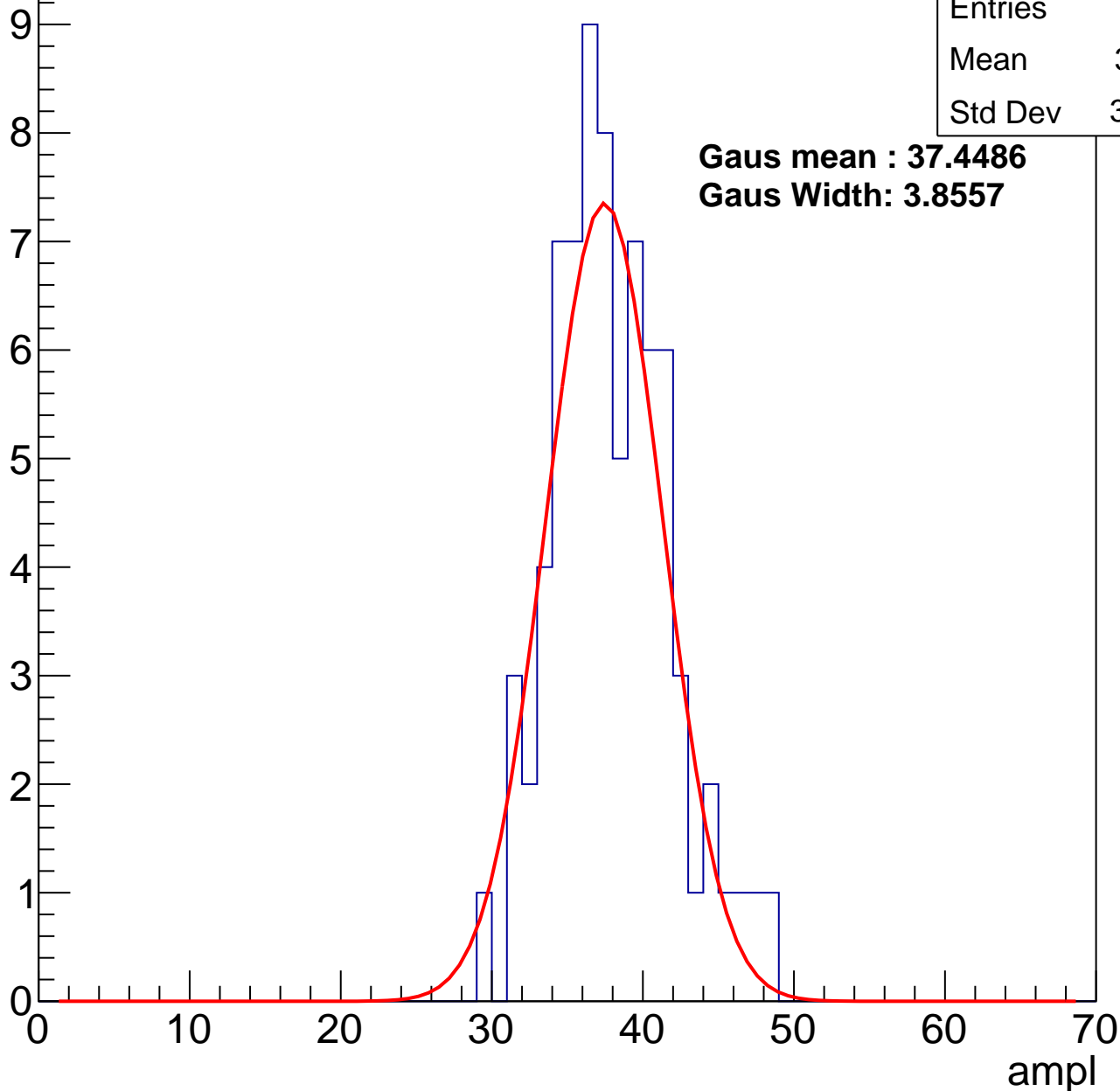
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	37.51
Std Dev	3.873

Gaus mean : 37.4486

Gaus Width: 3.8557



B0L102S, U12-ch25, adc2

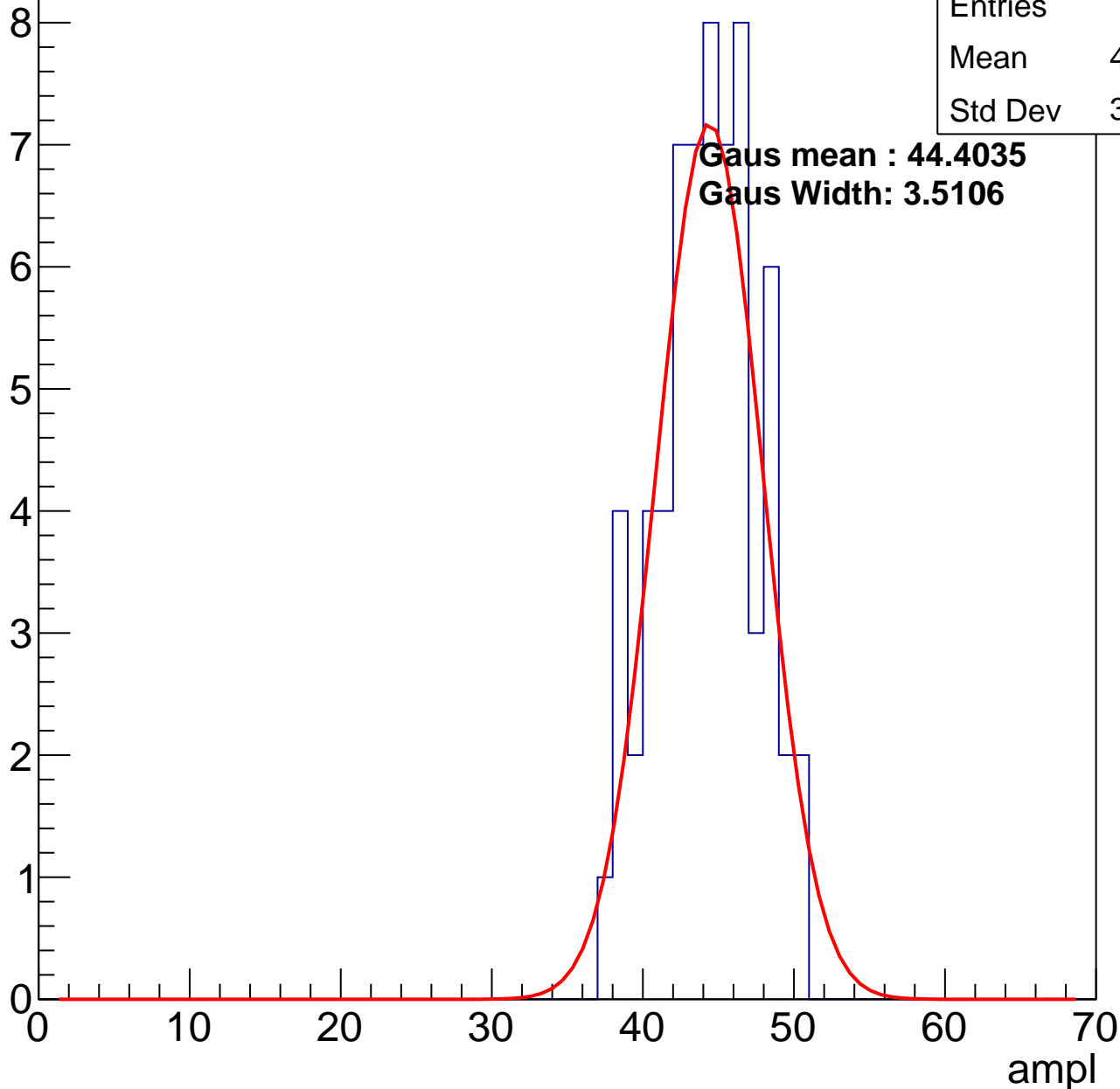
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.82
Std Dev	3.186

Gaus mean : 44.4035

Gaus Width: 3.5106

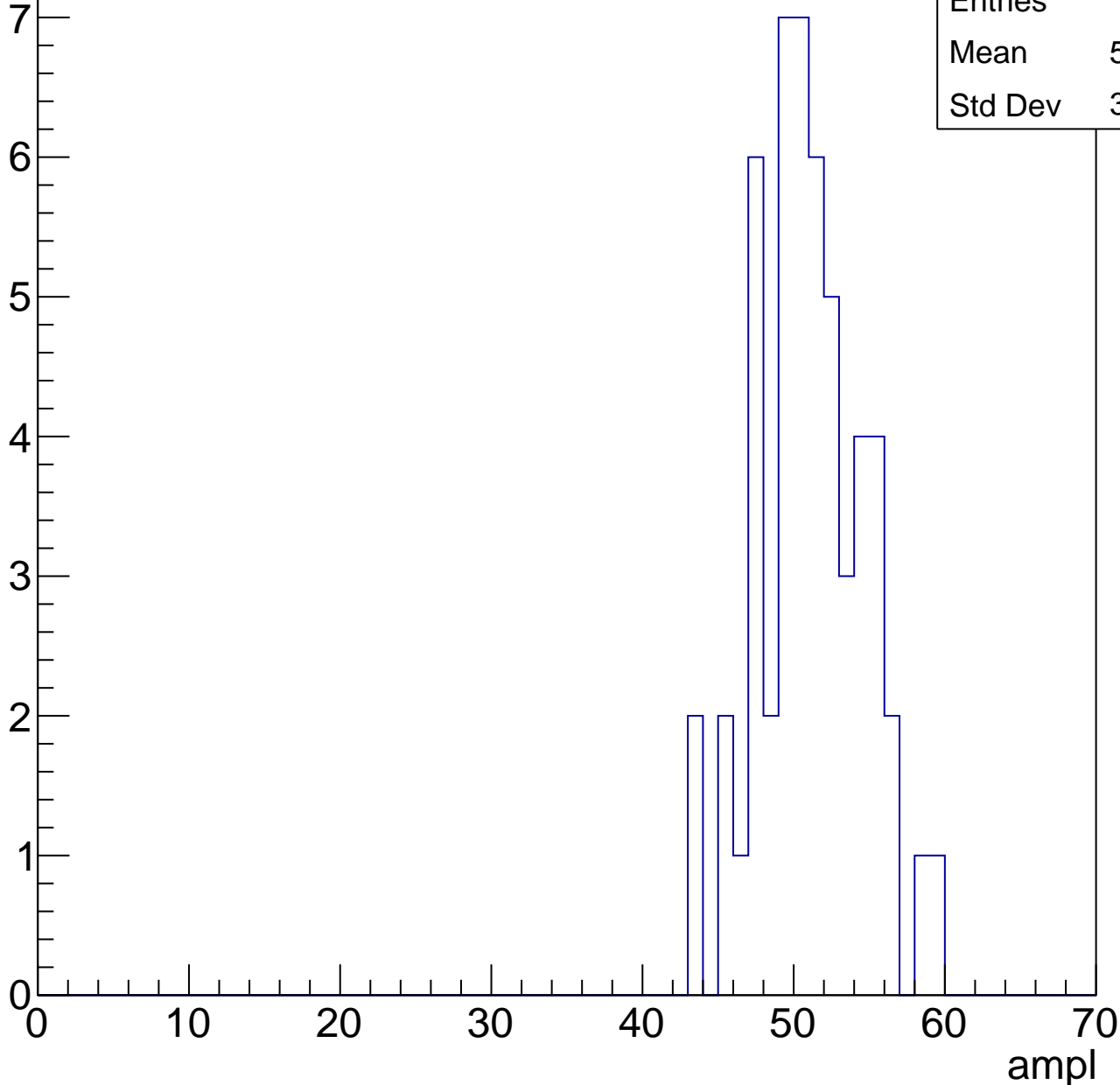


B0L102S, U12-ch25, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	50.62
Std Dev	3.492

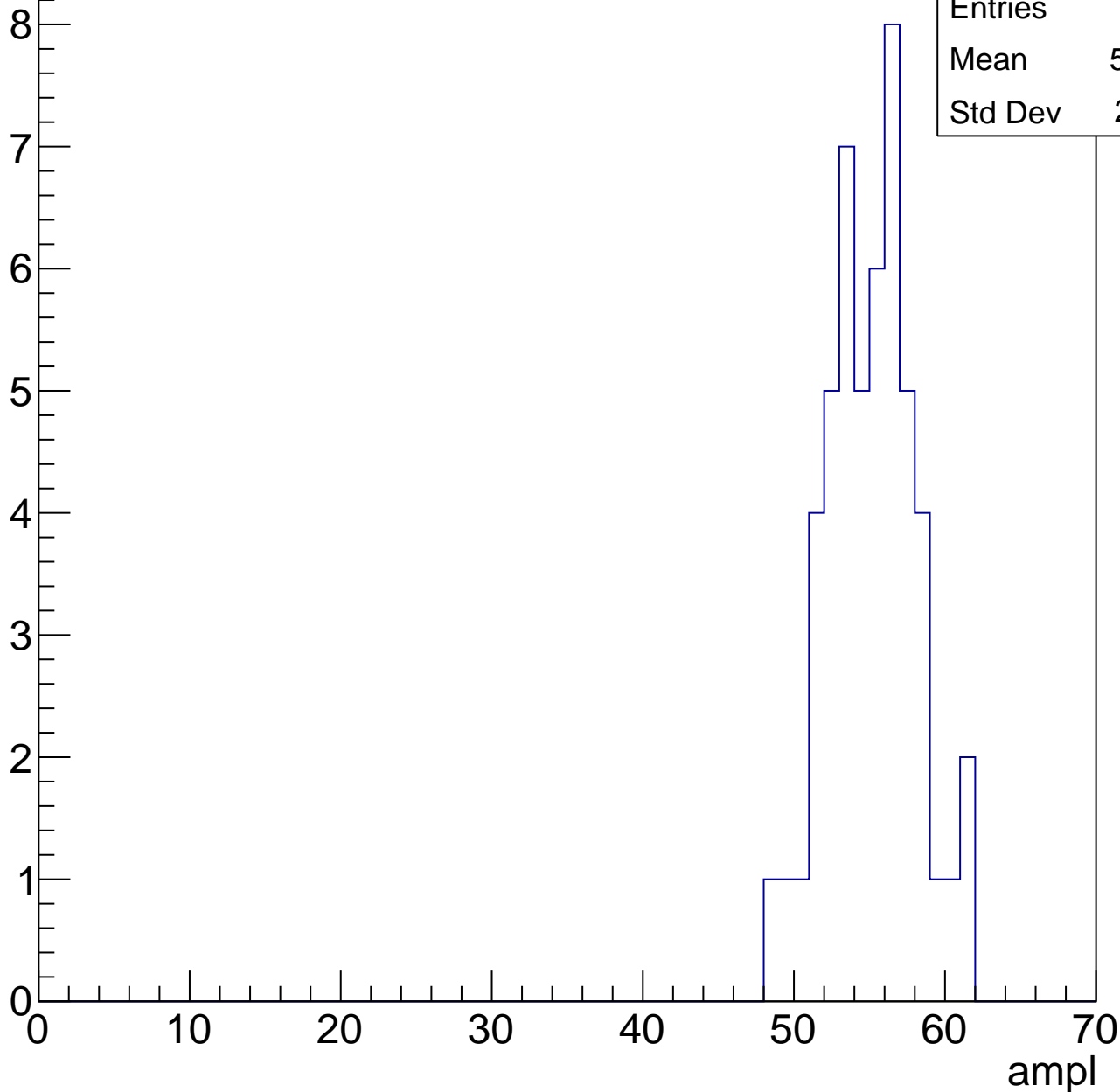


B0L102S, U12-ch25, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	54.67
Std Dev	2.881



B0L102S, U12-ch25, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

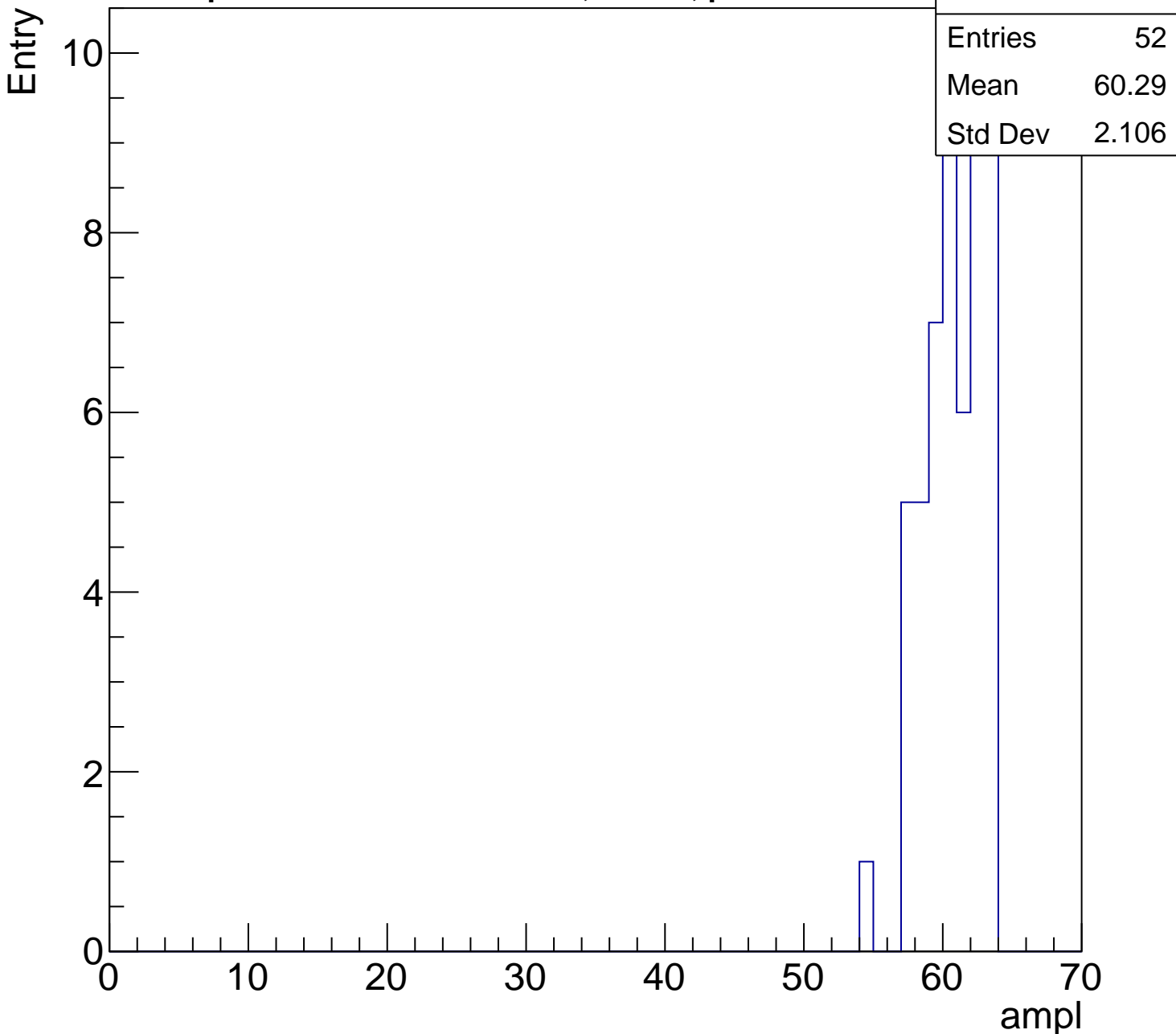
40

50

60

ampl

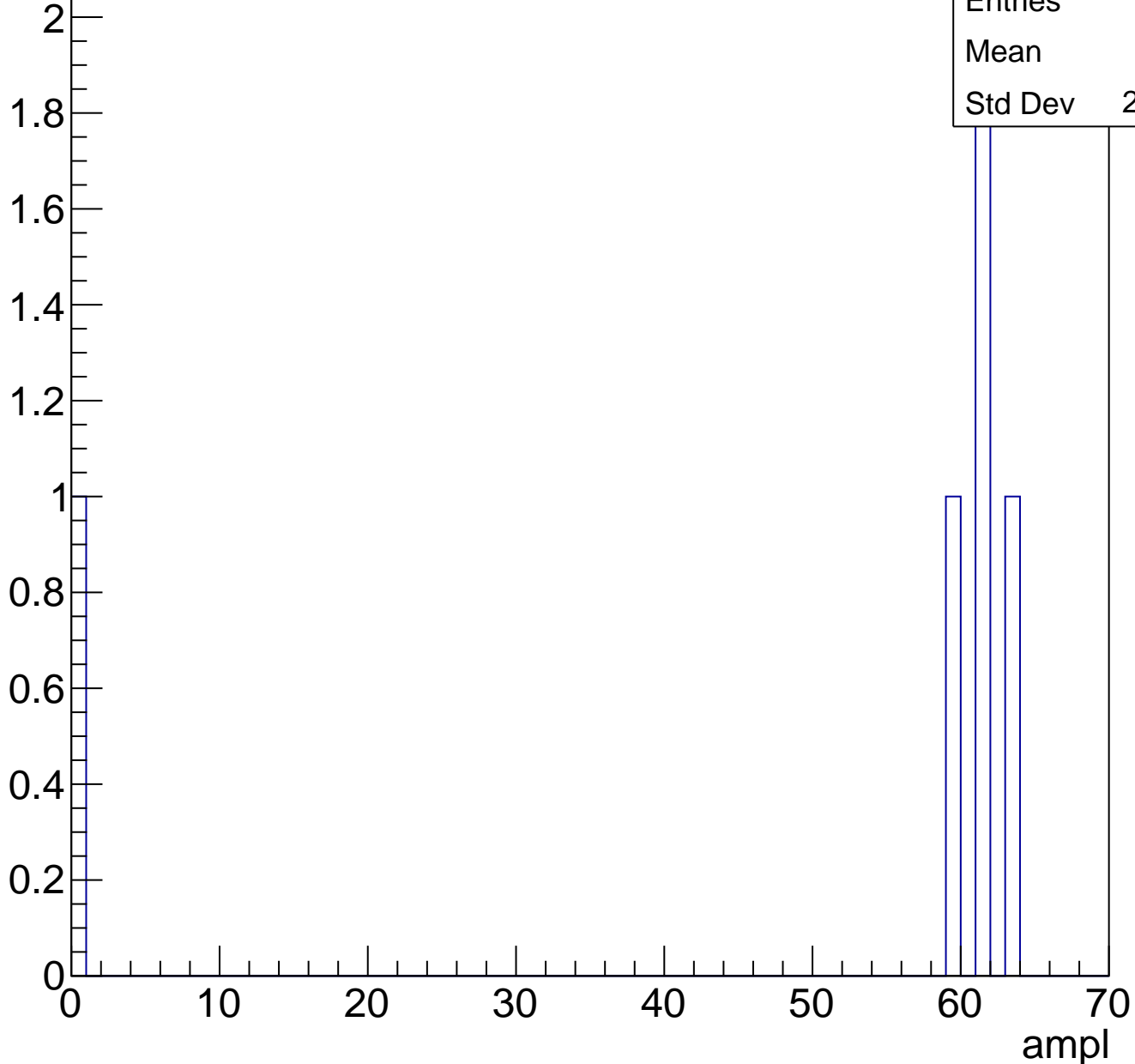
Entries	52
Mean	60.29
Std Dev	2.106



B0L102S, U12-ch25, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	48.8
Std Dev	24.43

B0L102S, U12-ch25, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch26, adc0

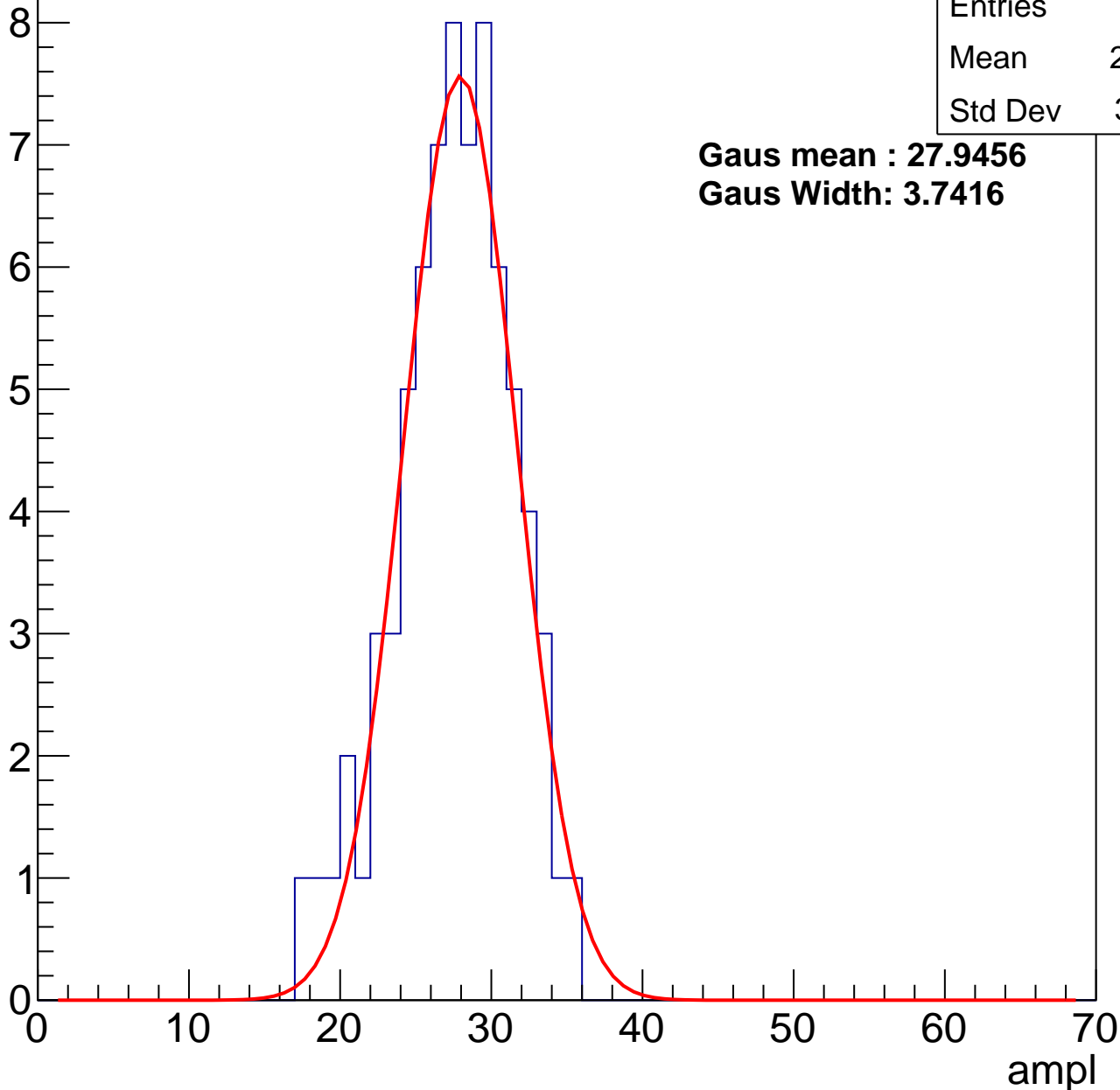
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	27.08
Std Dev	3.831

Gaus mean : 27.9456

Gaus Width: 3.7416



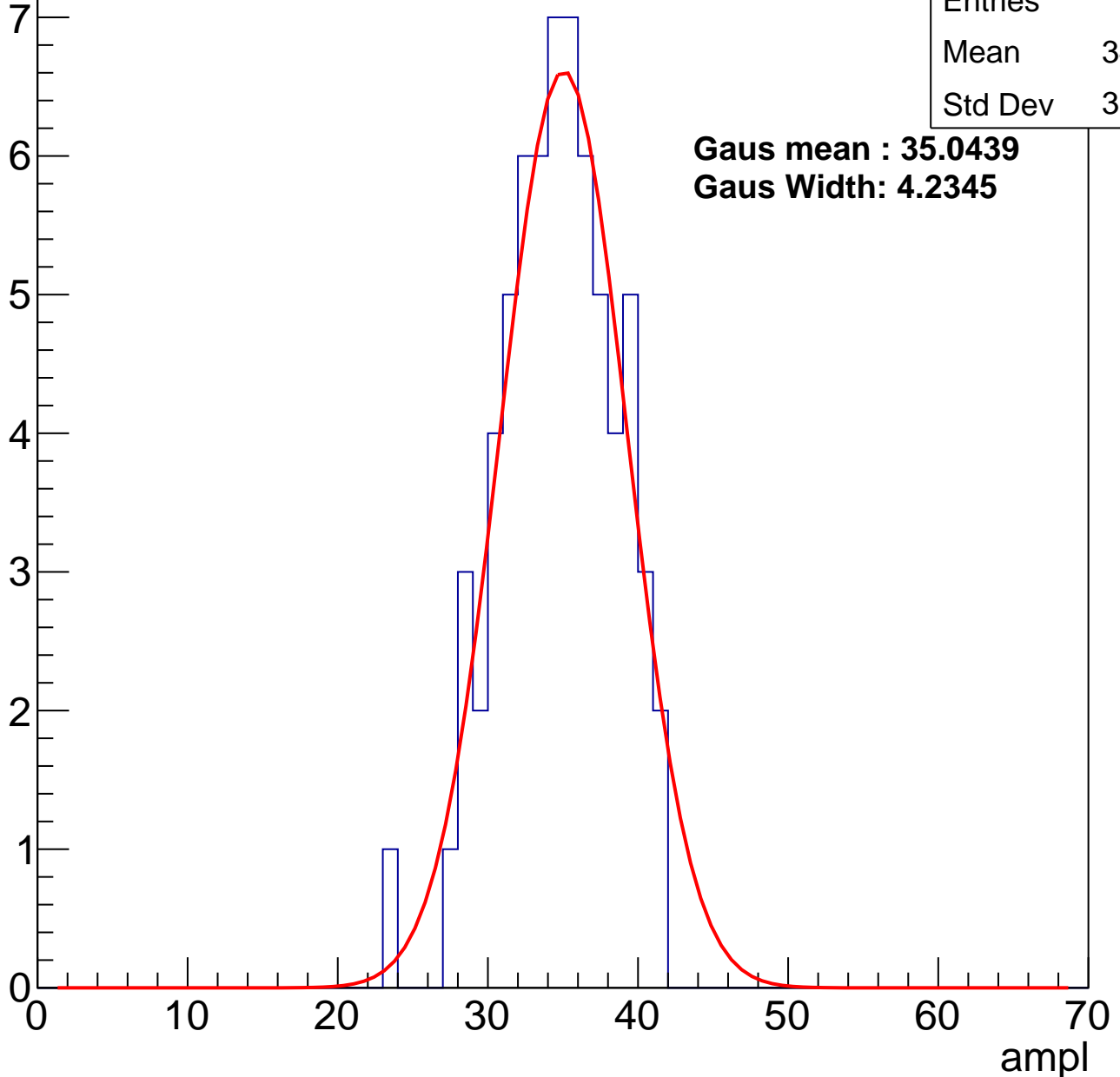
B0L102S, U12-ch26, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	34.18
Std Dev	3.737

Gaus mean : 35.0439
Gaus Width: 4.2345



B0L102S, U12-ch26, adc2

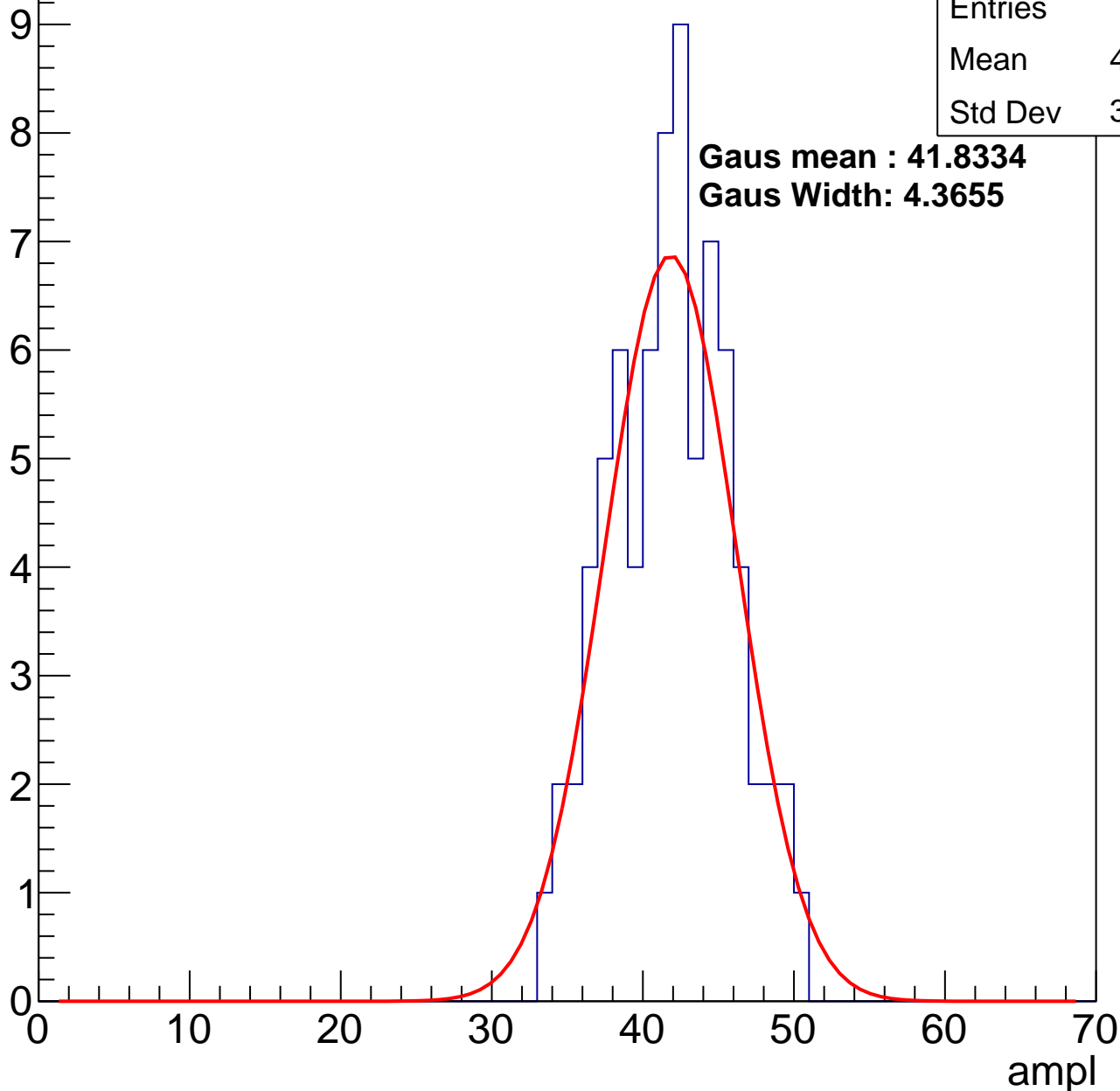
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	41.38
Std Dev	3.883

Gaus mean : 41.8334

Gaus Width: 4.3655

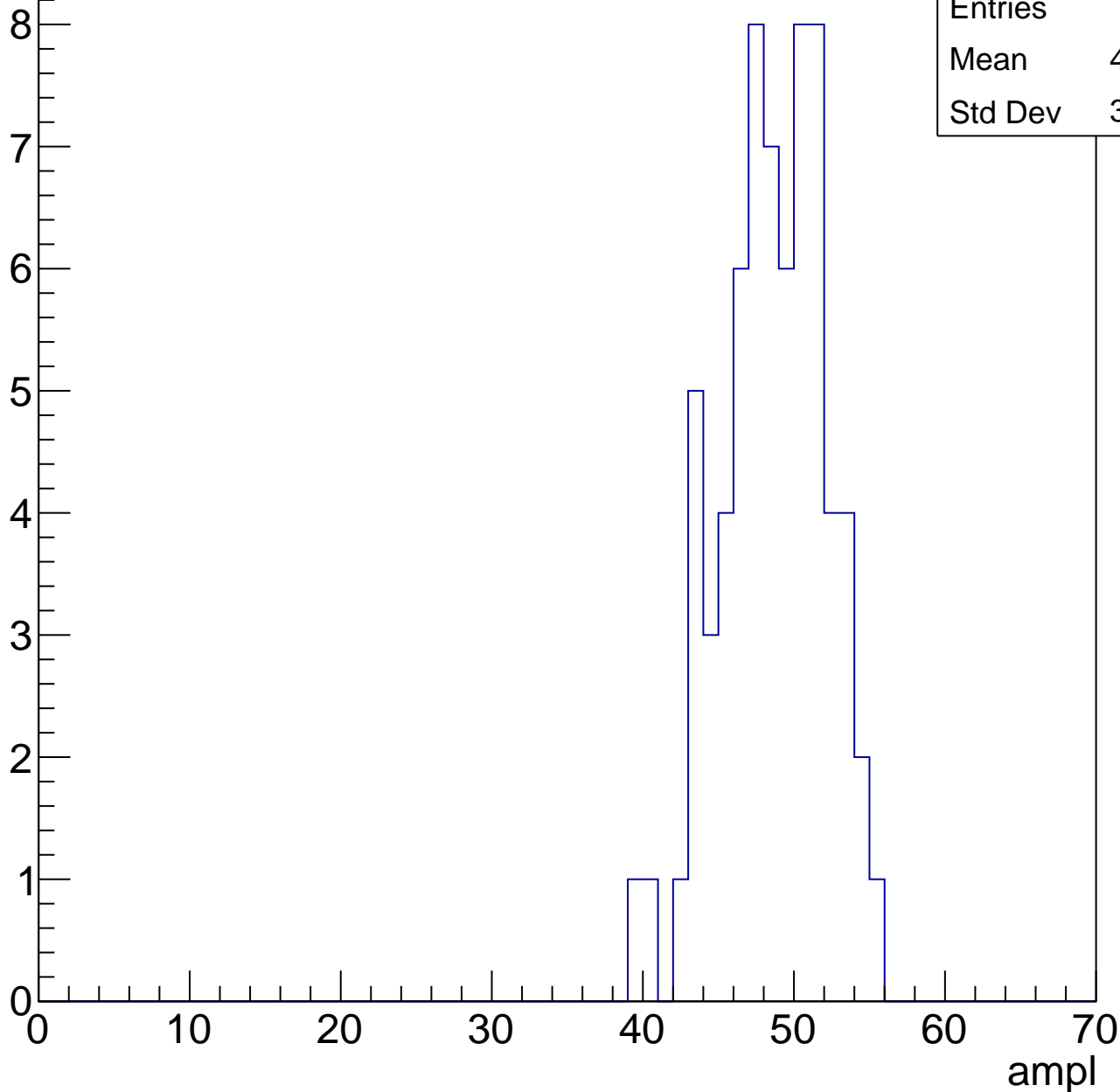


B0L102S, U12-ch26, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	48.13
Std Dev	3.439

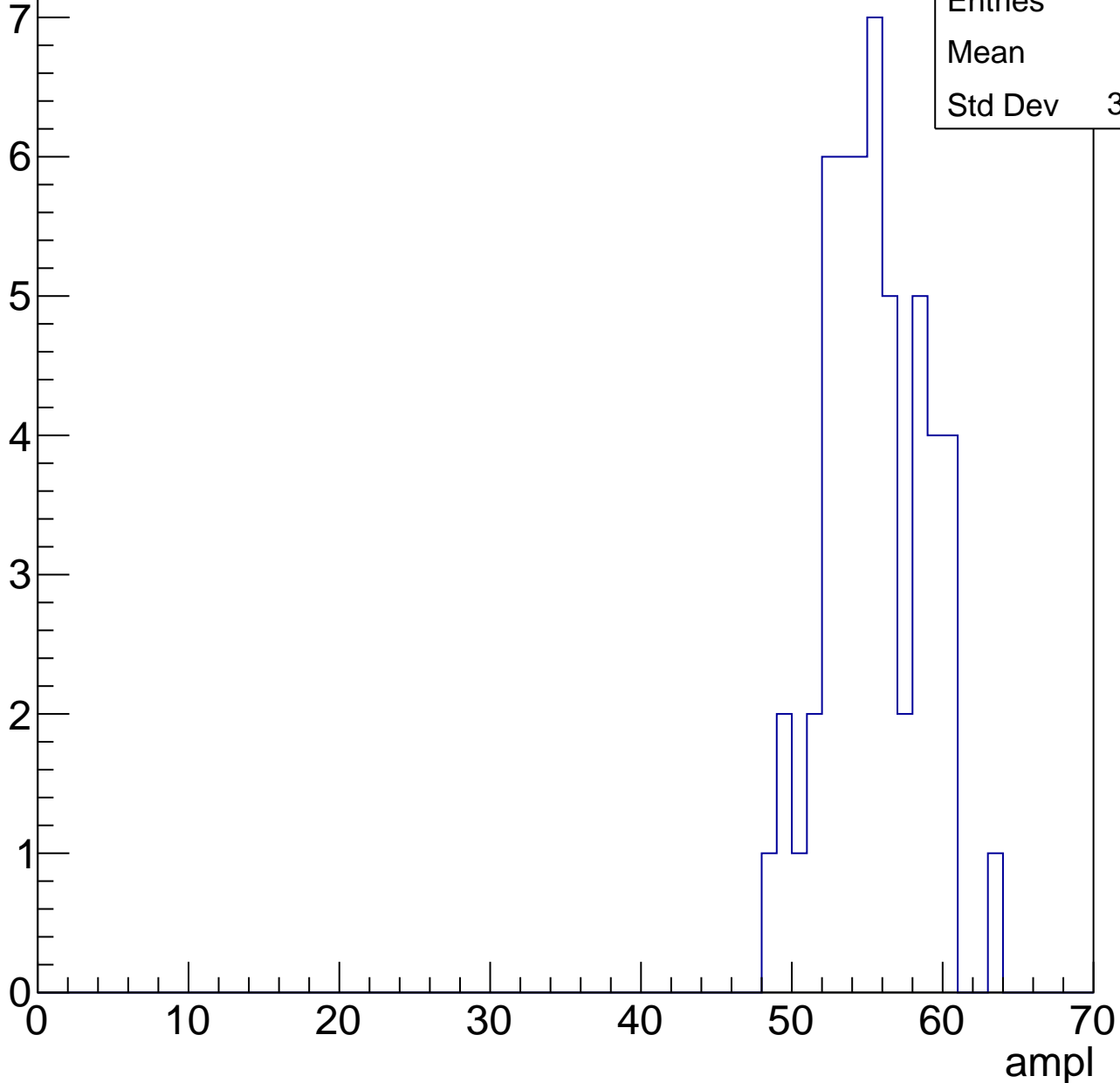


B0L102S, U12-ch26, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55
Std Dev	3.246



B0L102S, U12-ch26, adc5

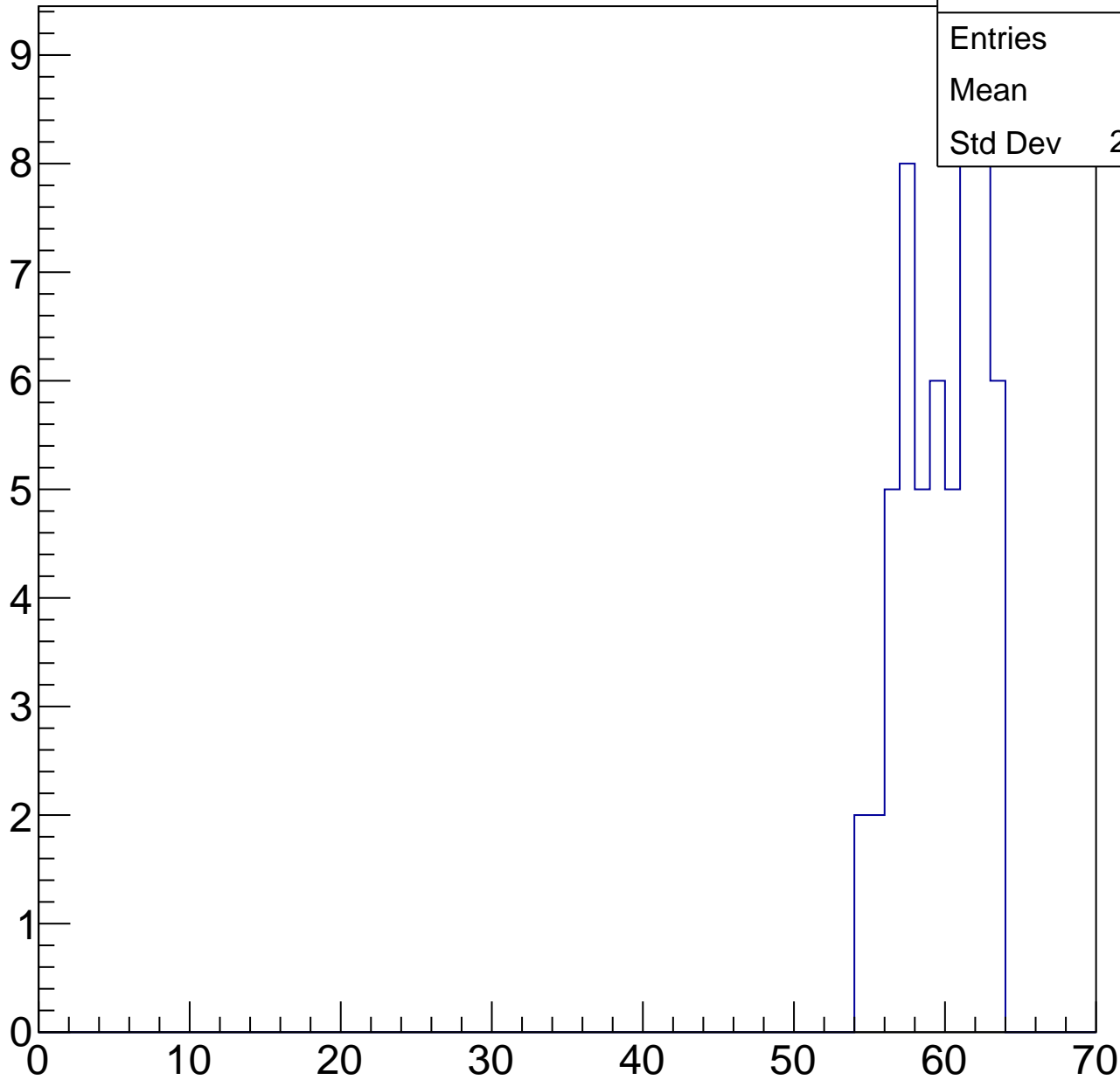
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.3
Std Dev	2.563

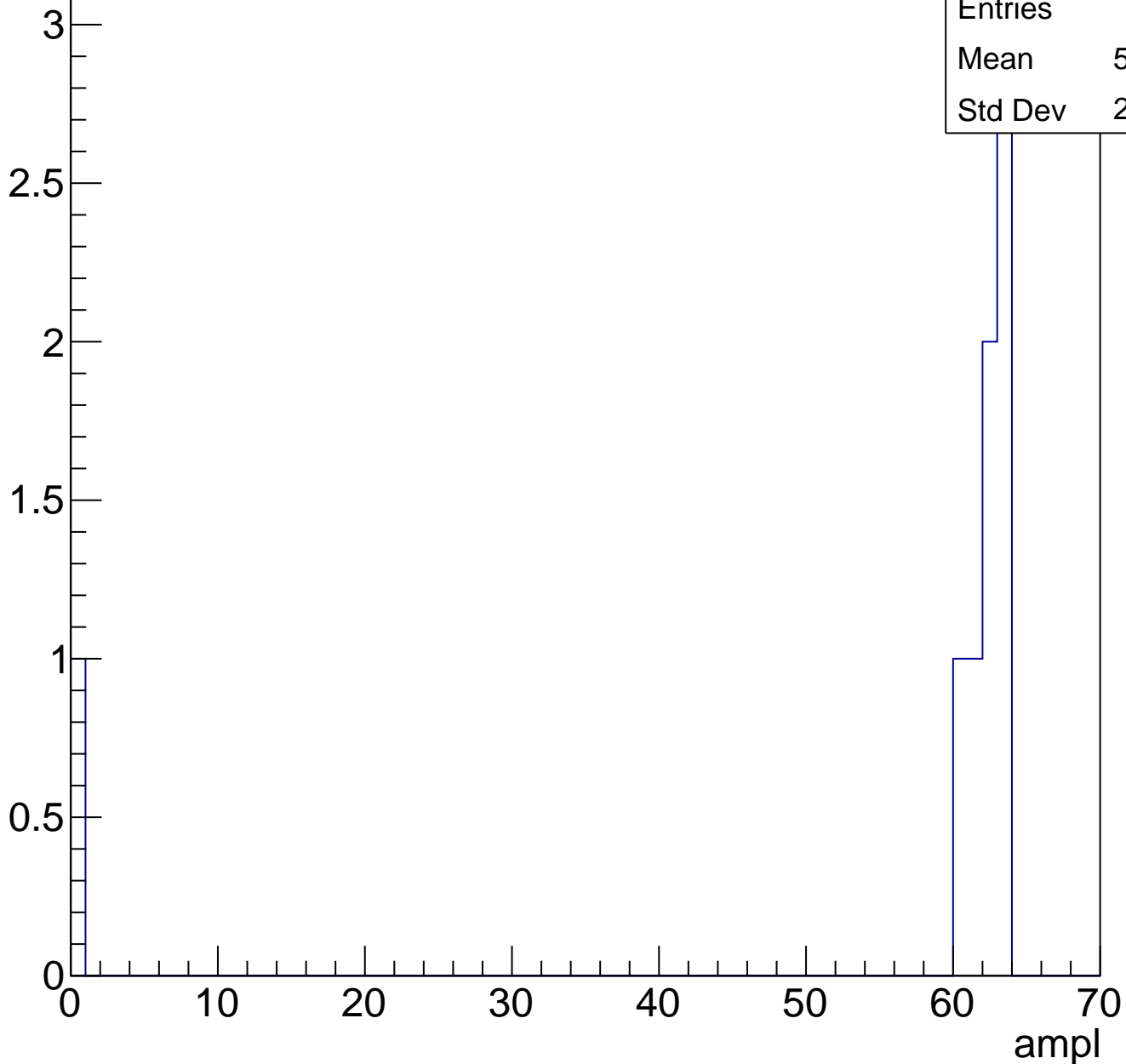
ampl



B0L102S, U12-ch26, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch26, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch27, adc0

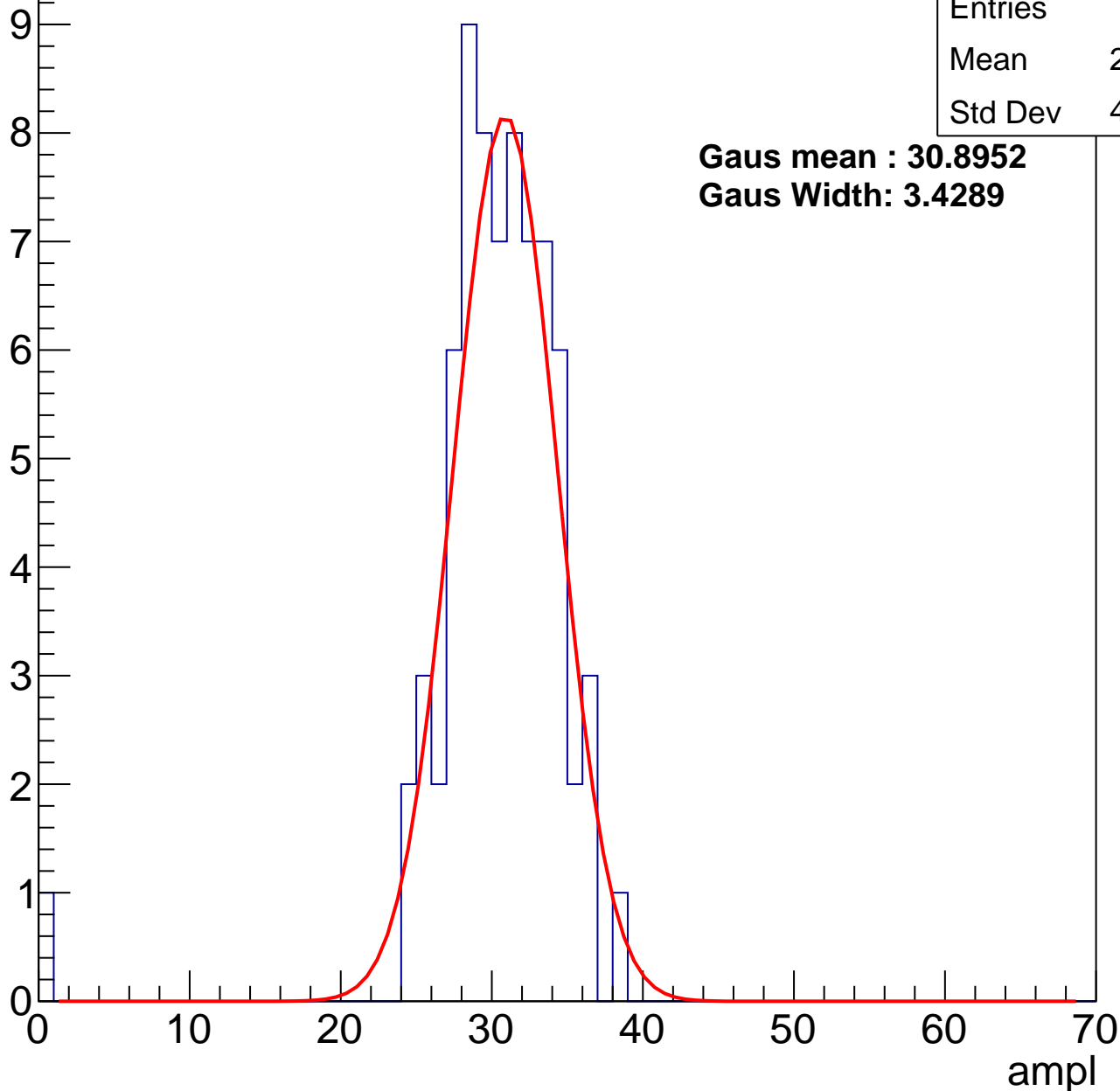
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	29.92
Std Dev	4.704

Gaus mean : 30.8952

Gaus Width: 3.4289



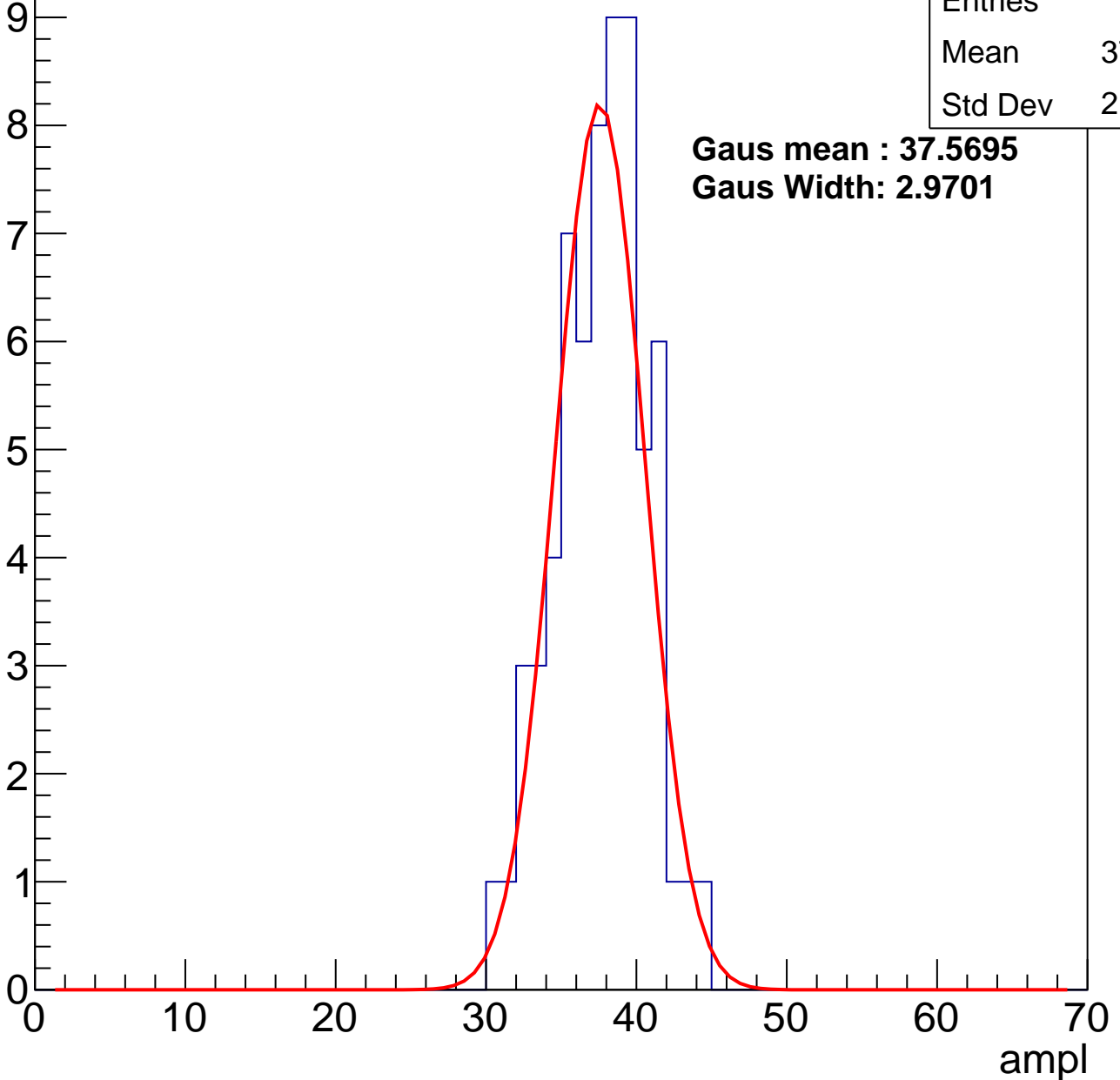
B0L102S, U12-ch27, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.18
Std Dev	2.966

Gaus mean : 37.5695
Gaus Width: 2.9701



B0L102S, U12-ch27, adc2

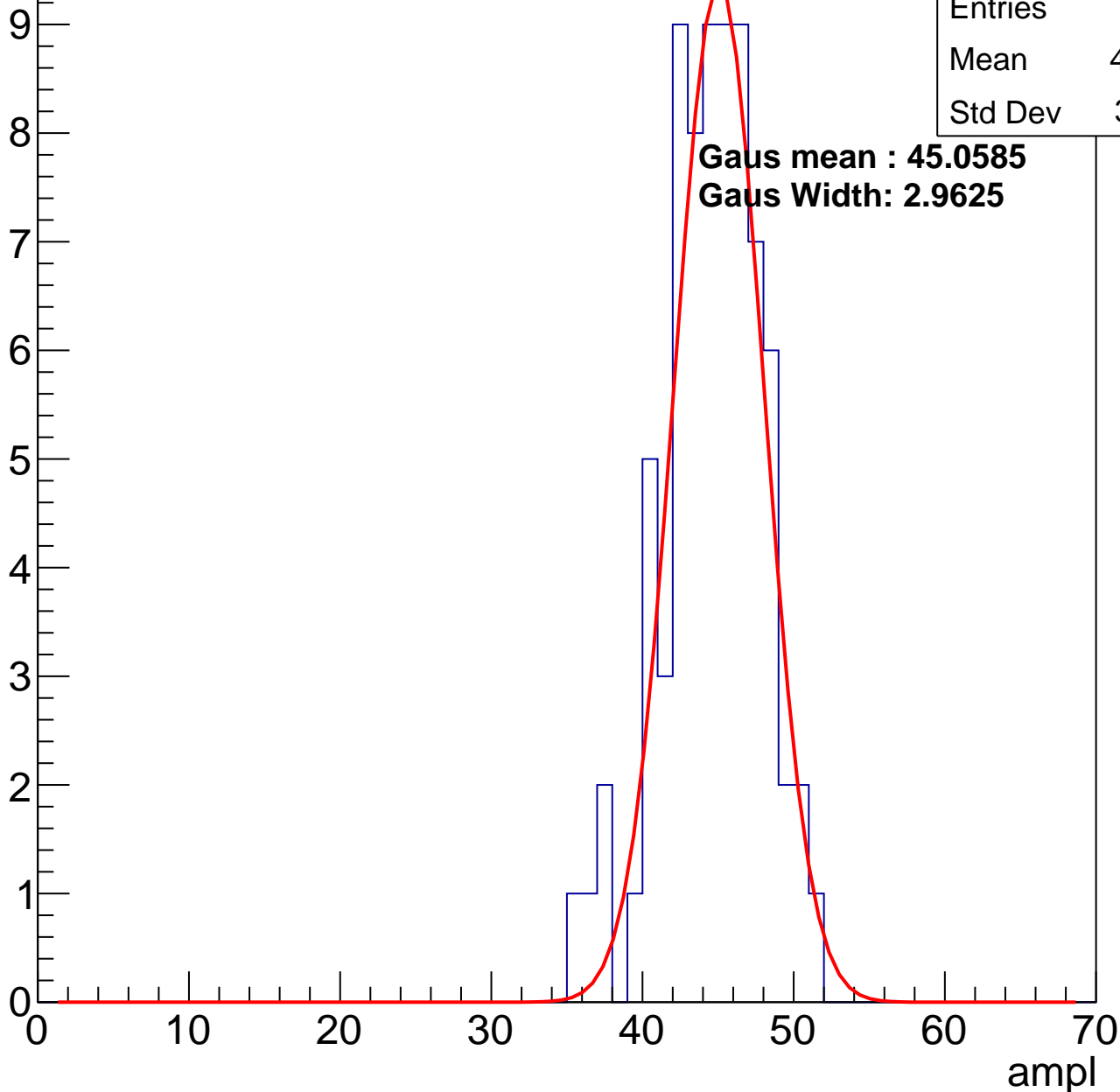
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	44.13
Std Dev	3.251

Gaus mean : 45.0585

Gaus Width: 2.9625

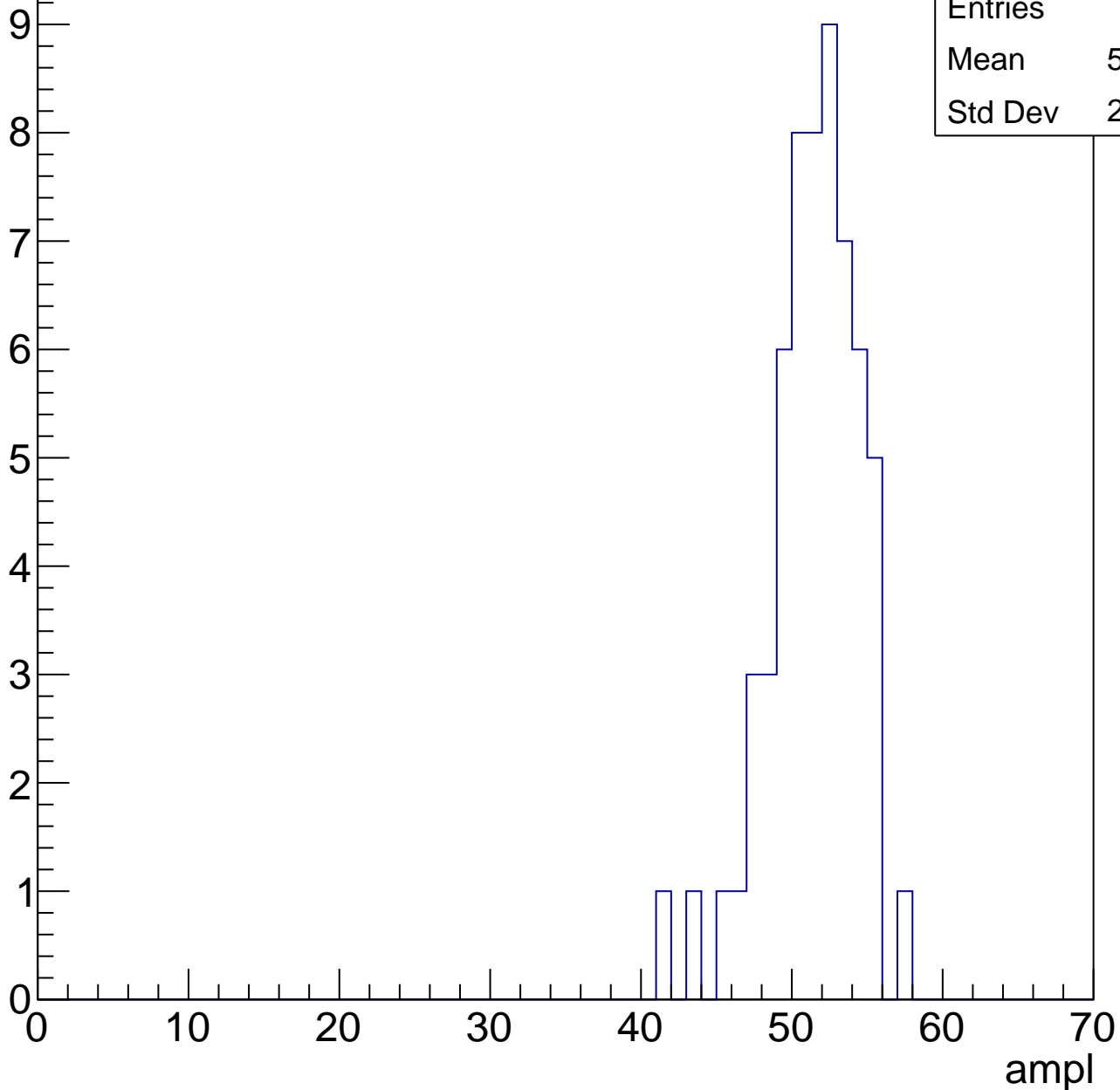


B0L102S, U12-ch27, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	50.95
Std Dev	2.997

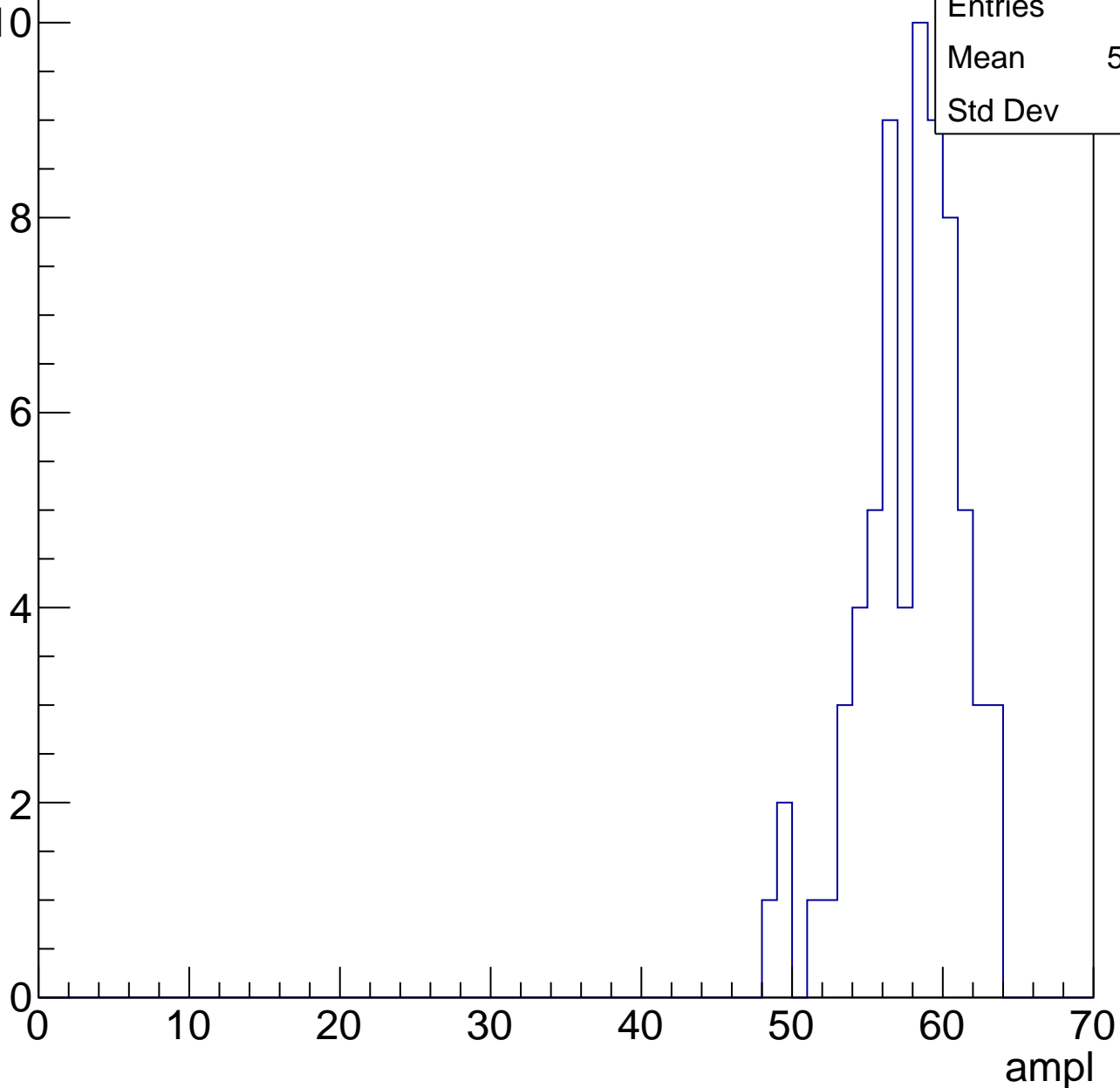


B0L102S, U12-ch27, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	57.38
Std Dev	3.33

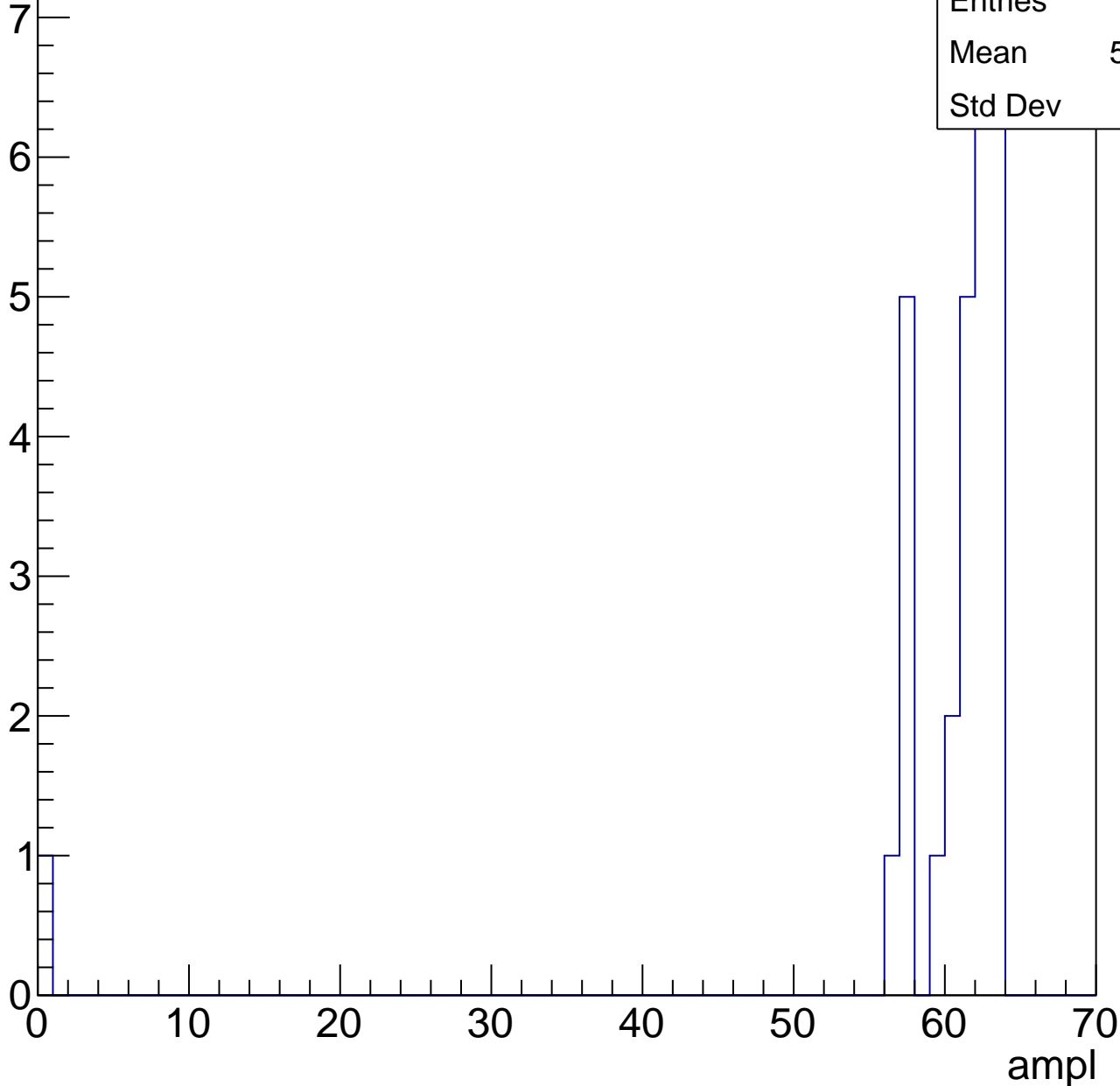


B0L102S, U12-ch27, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	29
Mean	58.62
Std Dev	11.3



B0L102S, U12-ch27, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch27, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch28, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	29.16
Std Dev	3.905

Gaus mean : 29.9562

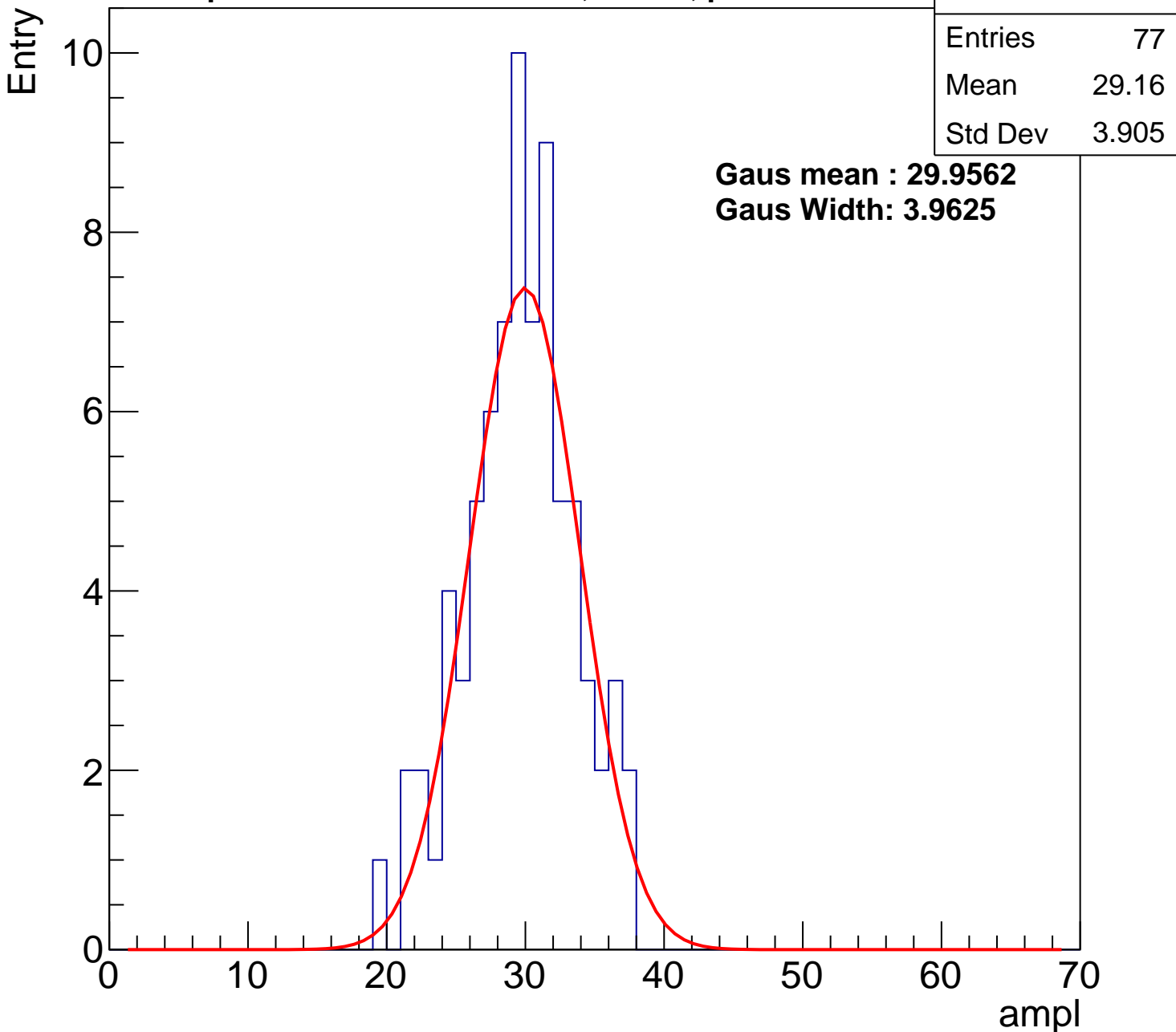
Gaus Width: 3.9625

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch28, adc1

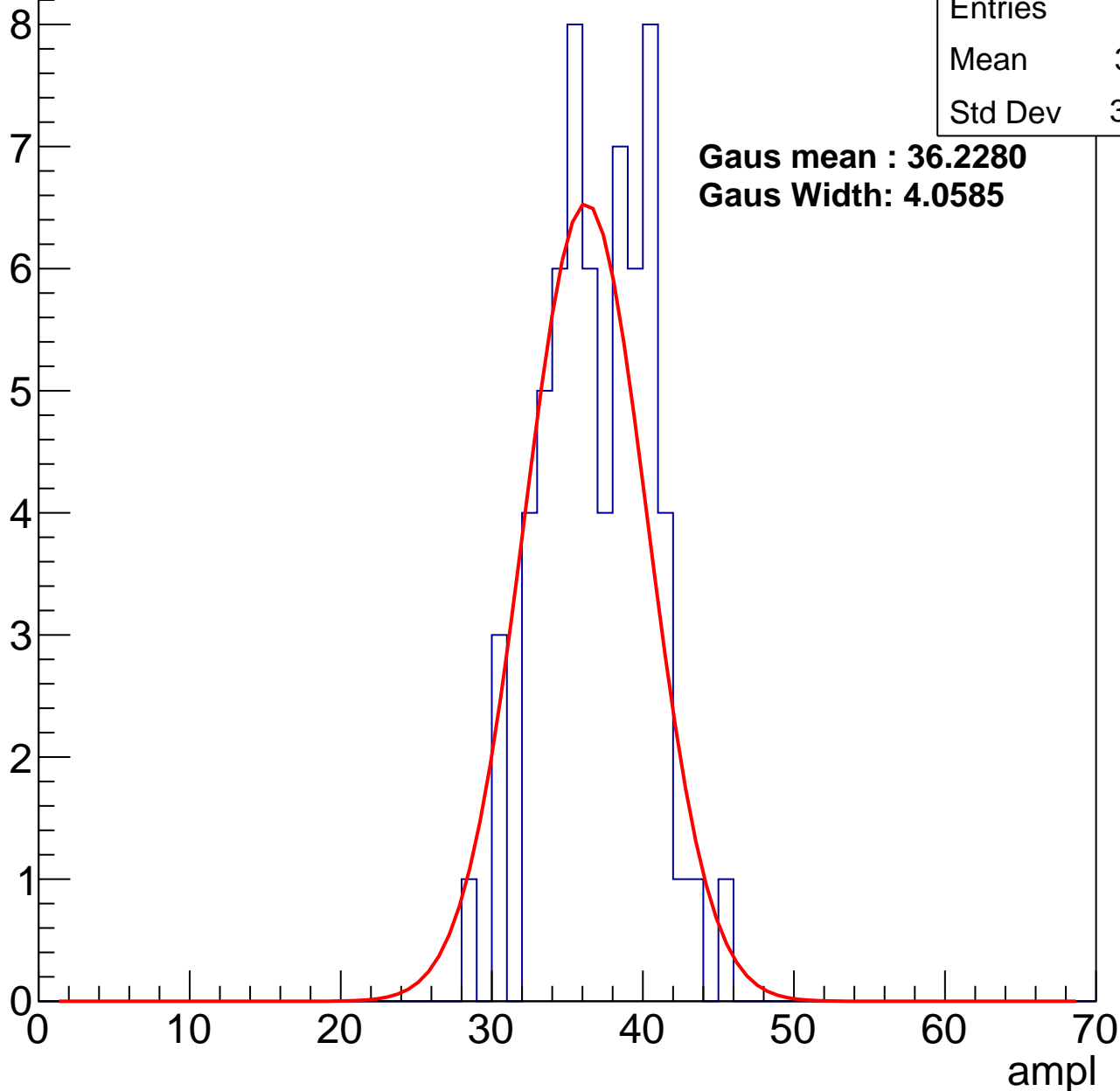
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.51
Std Dev	3.456

Gaus mean : 36.2280

Gaus Width: 4.0585



B0L102S, U12-ch28, adc2

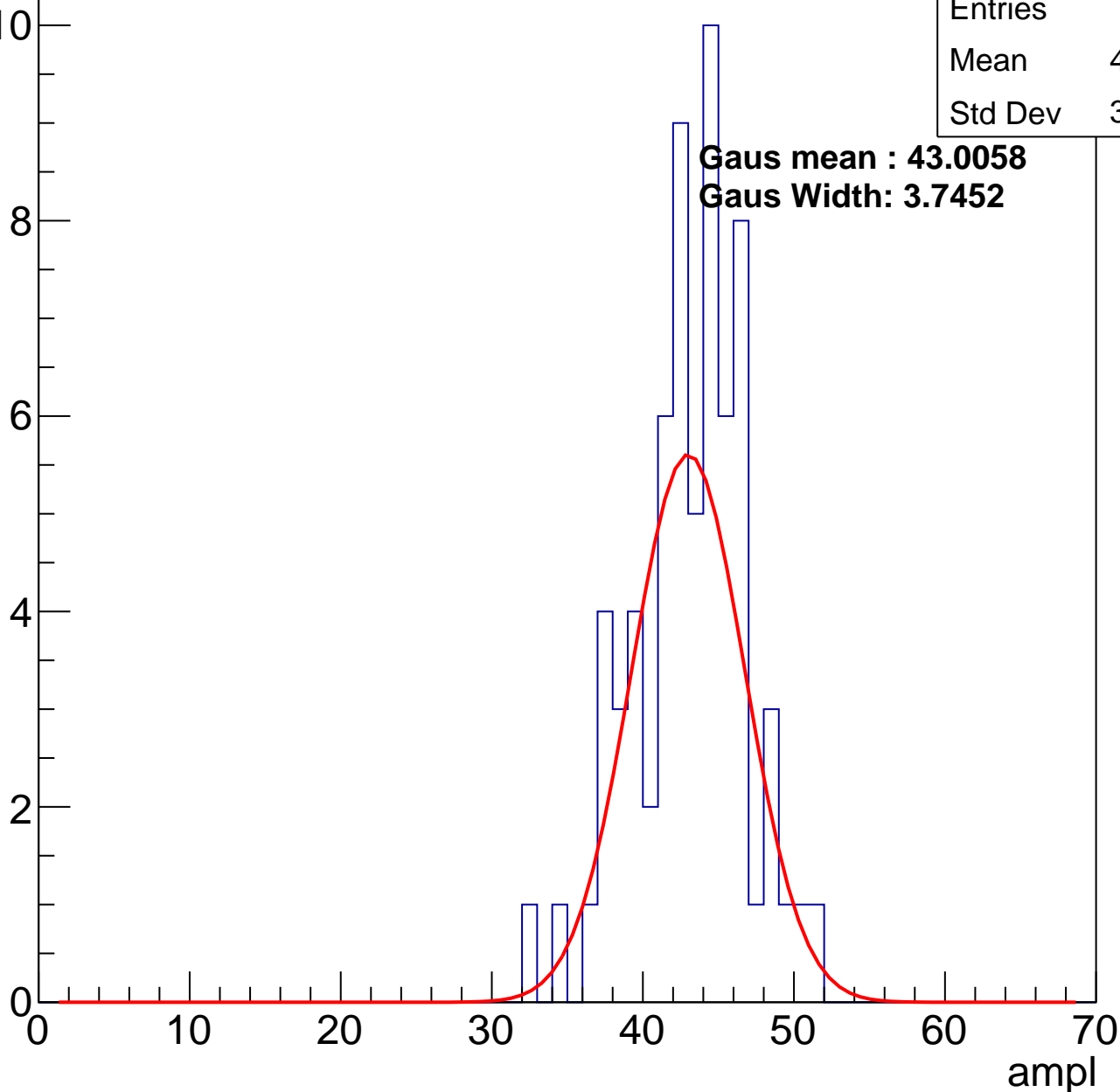
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	42.66
Std Dev	3.712

Gaus mean : 43.0058

Gaus Width: 3.7452

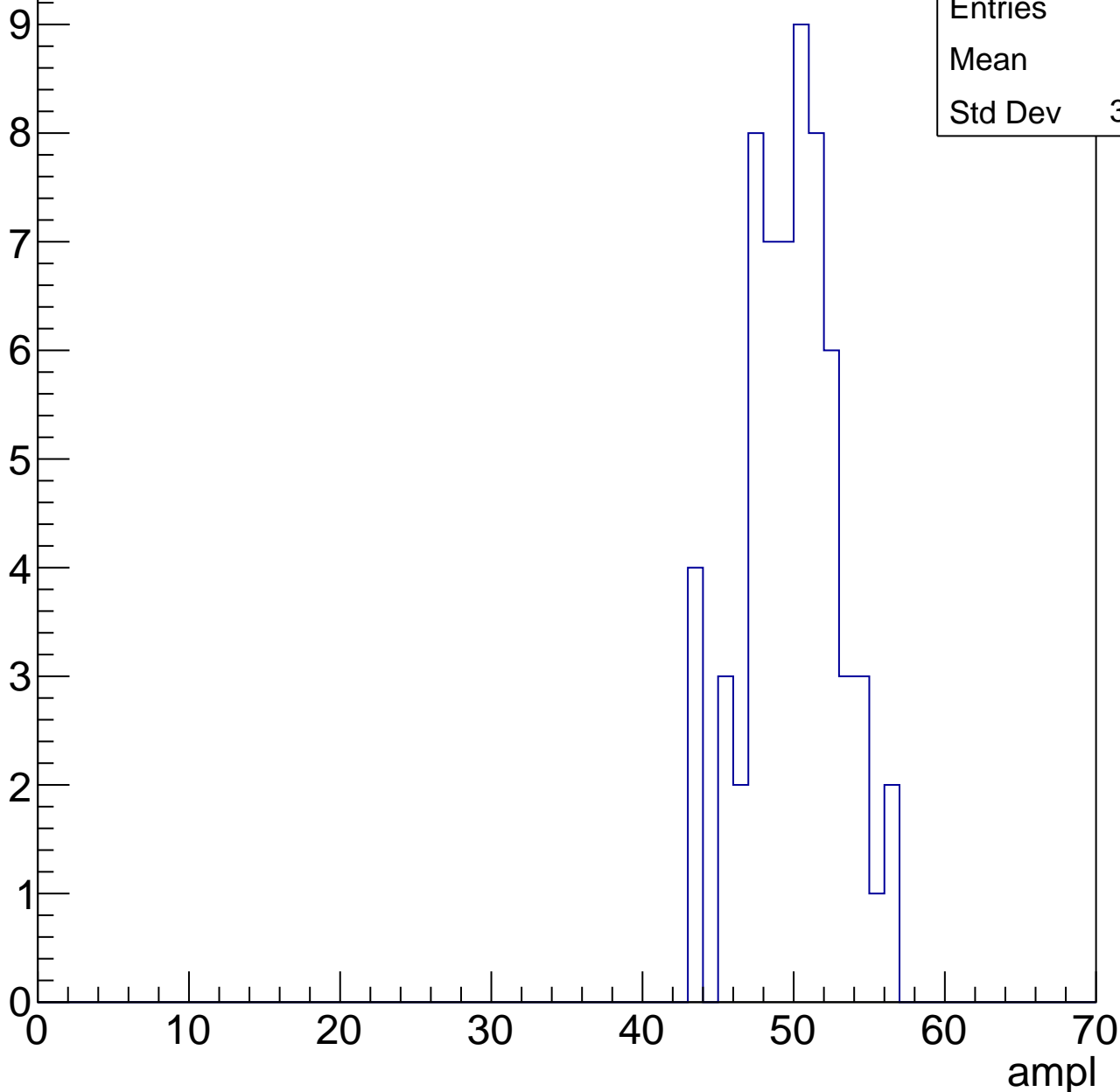


B0L102S, U12-ch28, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.4
Std Dev	3.063



B0L102S, U12-ch28, adc4

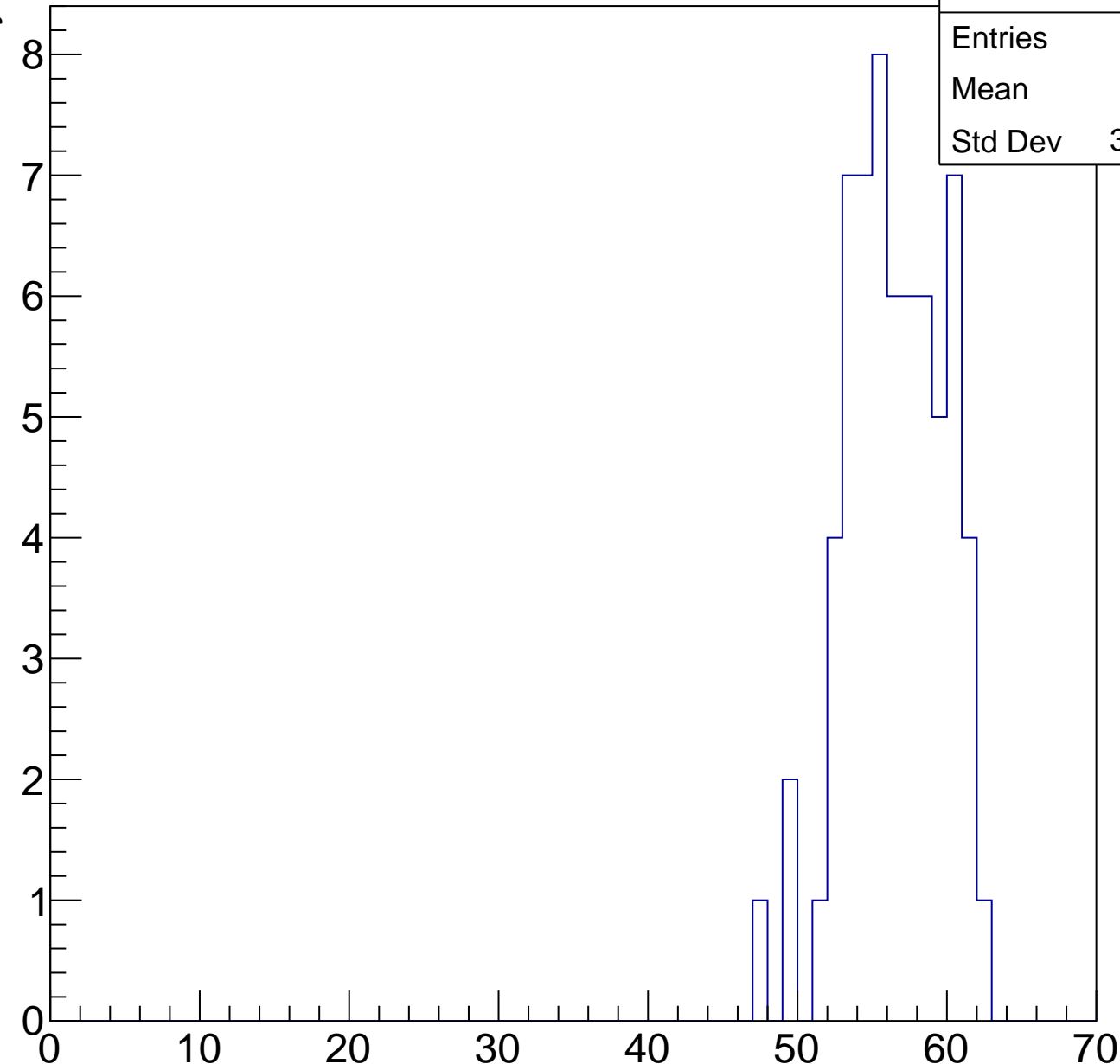
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	65
Mean	56
Std Dev	3.258

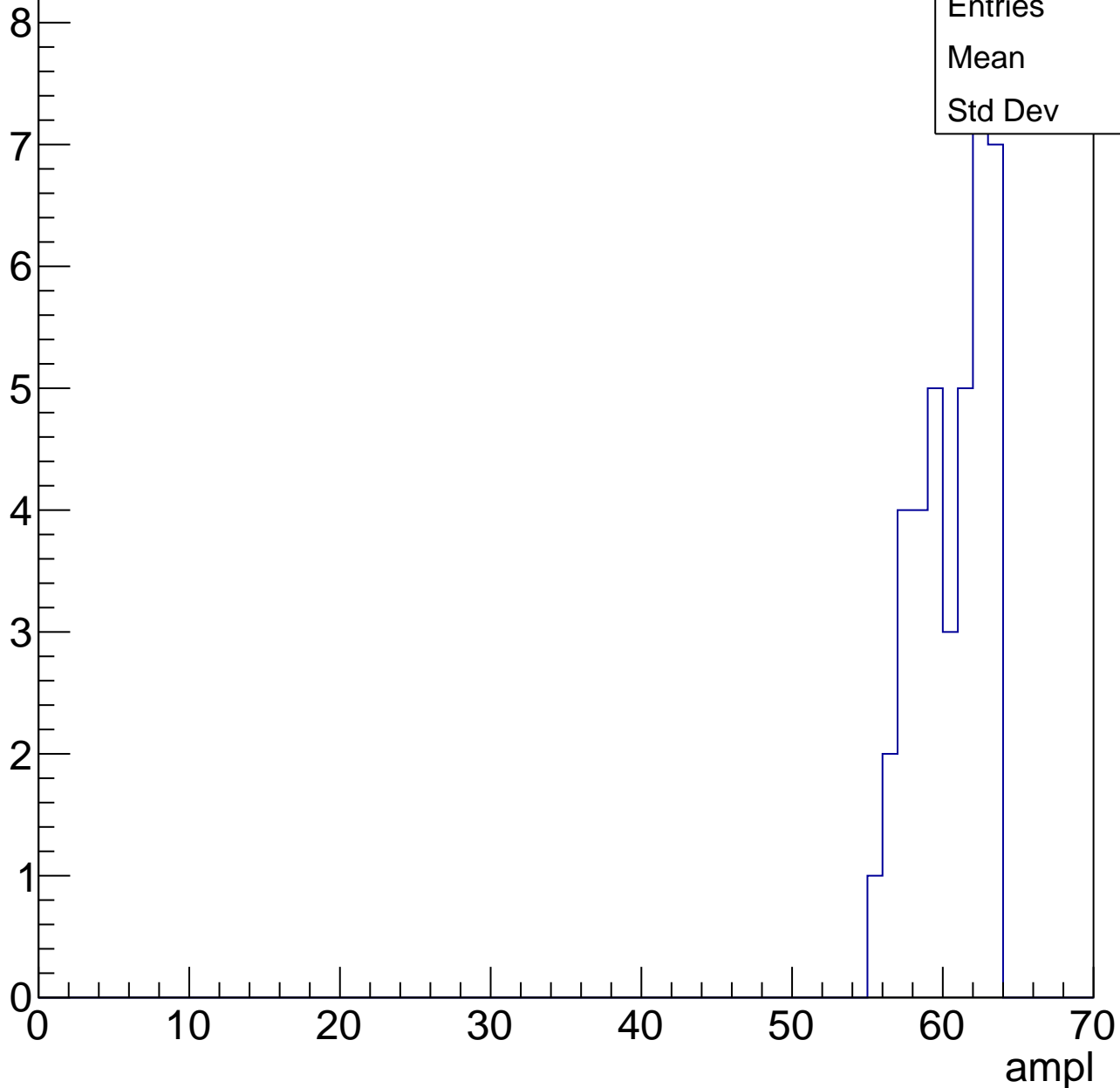
ampl



B0L102S, U12-ch28, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	39
Mean	60.1
Std Dev	2.34

B0L102S, U12-ch28, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch28, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch29, adc0

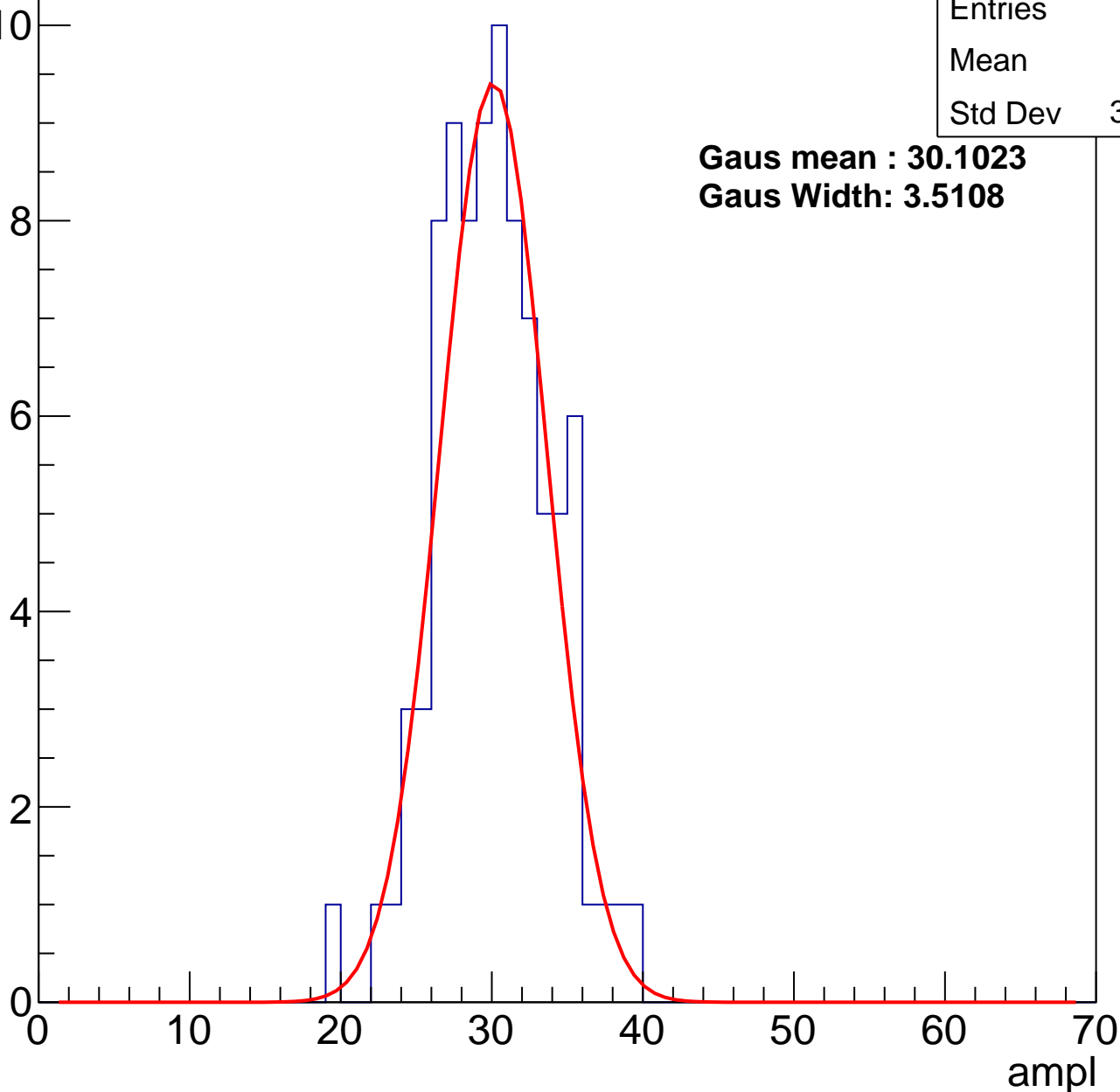
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	88
Mean	29.7
Std Dev	3.693

Gaus mean : 30.1023

Gaus Width: 3.5108



B0L102S, U12-ch29, adc1

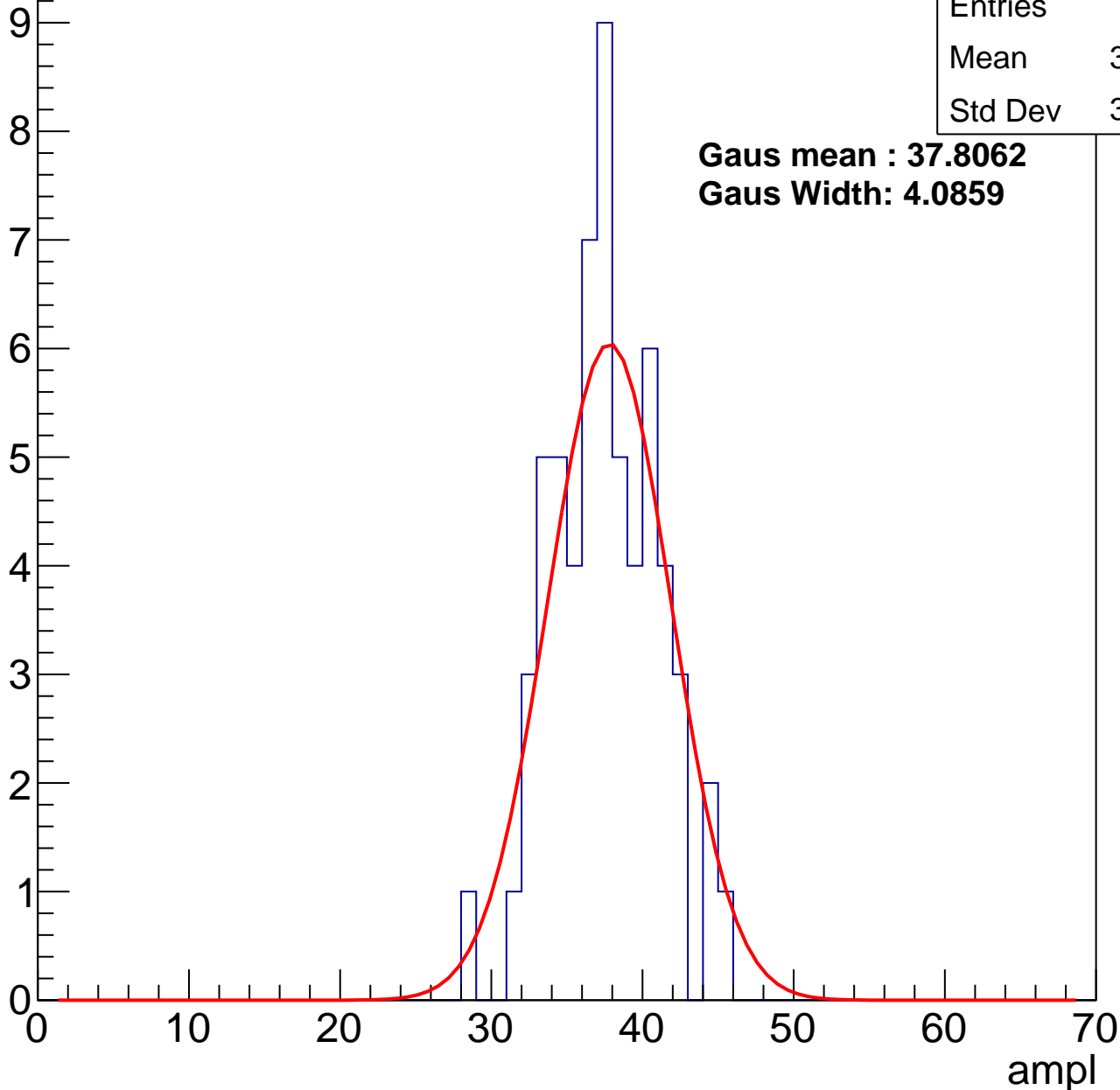
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	37.07
Std Dev	3.454

Gaus mean : 37.8062

Gaus Width: 4.0859



B0L102S, U12-ch29, adc2

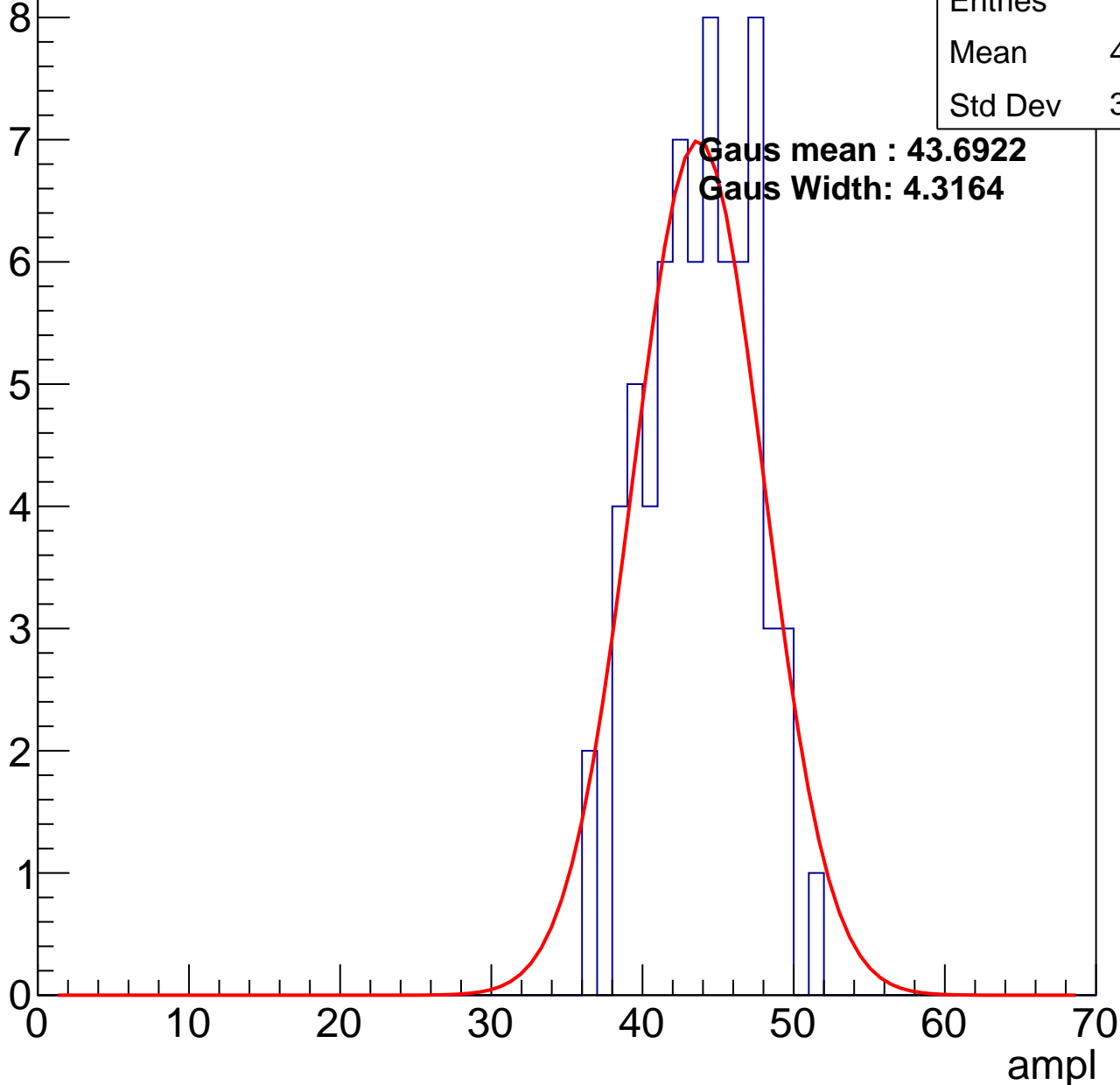
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	43.38
Std Dev	3.397

Gaus mean : 43.6922

Gaus Width: 4.3164

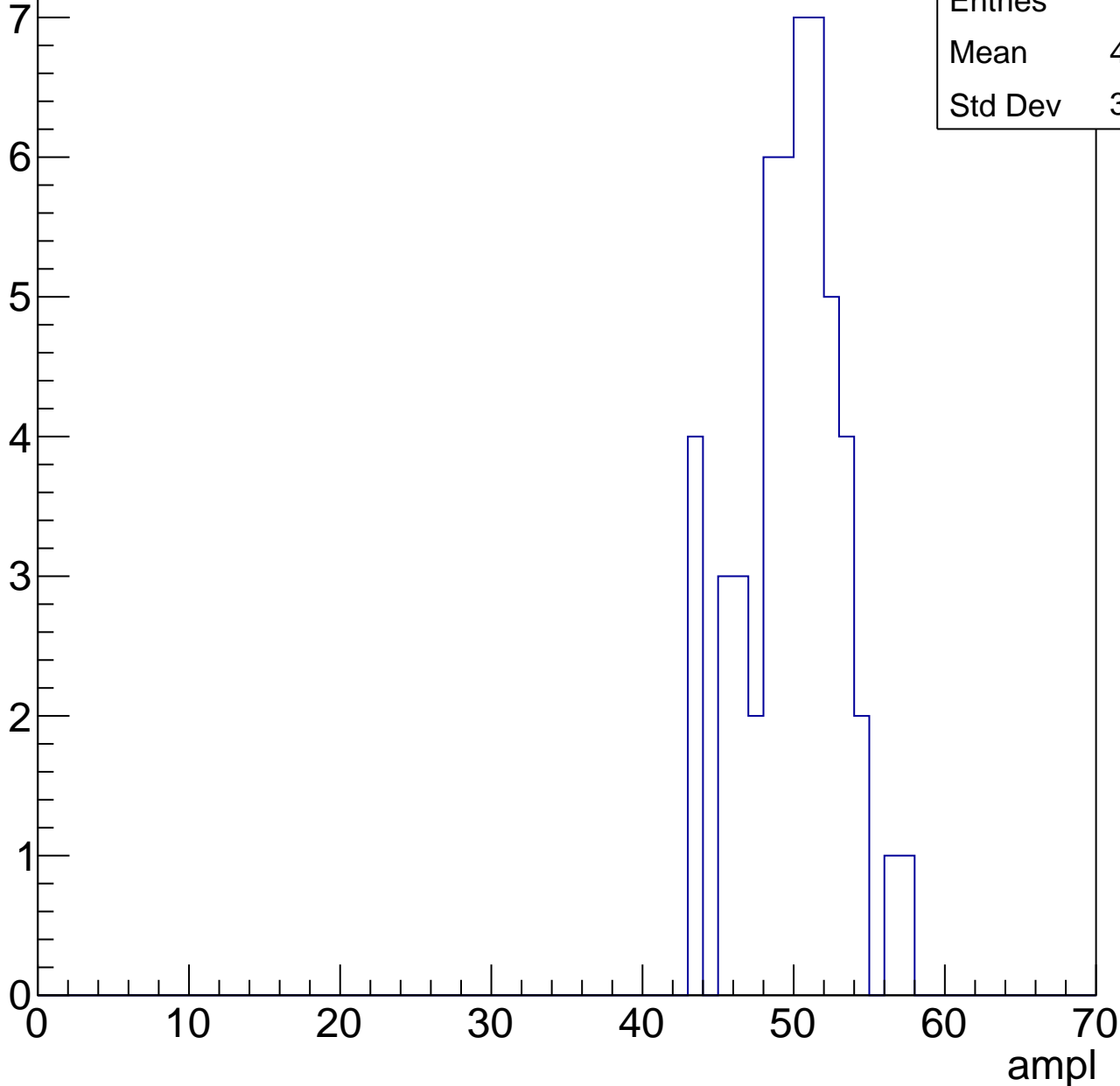


B0L102S, U12-ch29, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	49.43
Std Dev	3.225

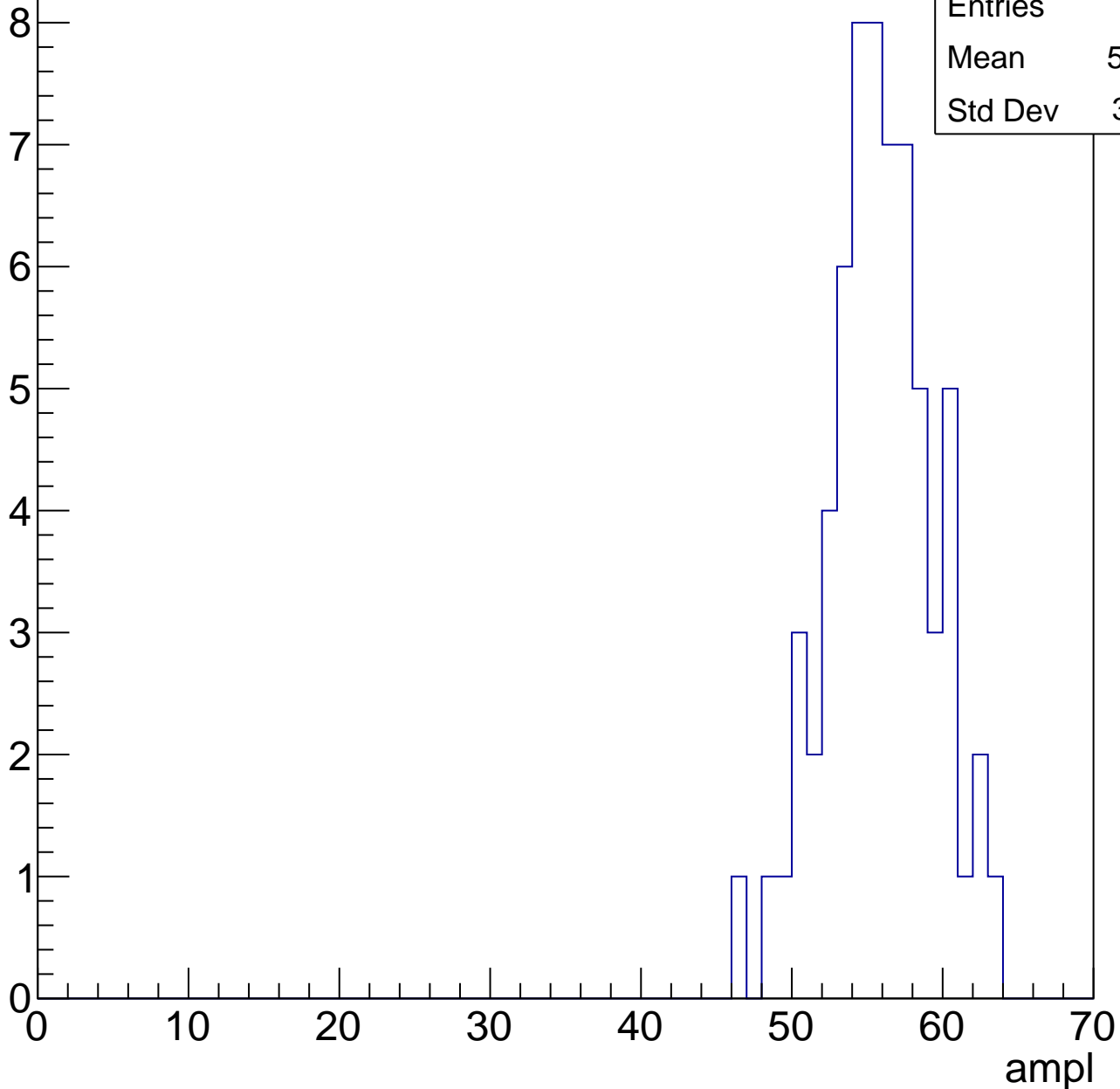


B0L102S, U12-ch29, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	55.37
Std Dev	3.471



B0L102S, U12-ch29, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.91
Std Dev	2.544

ampl

0

10

20

30

40

50

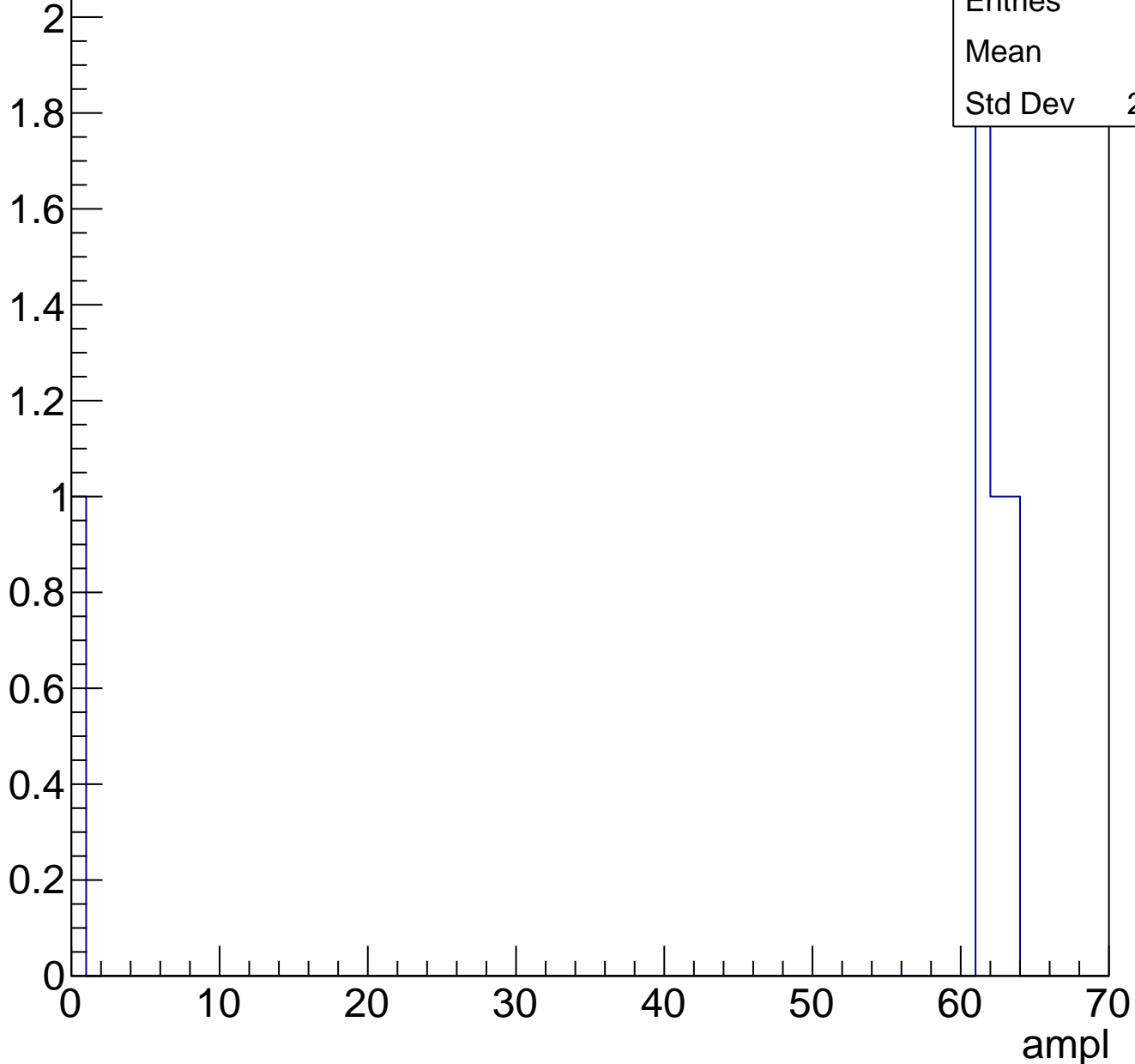
60

70

B0L102S, U12-ch29, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

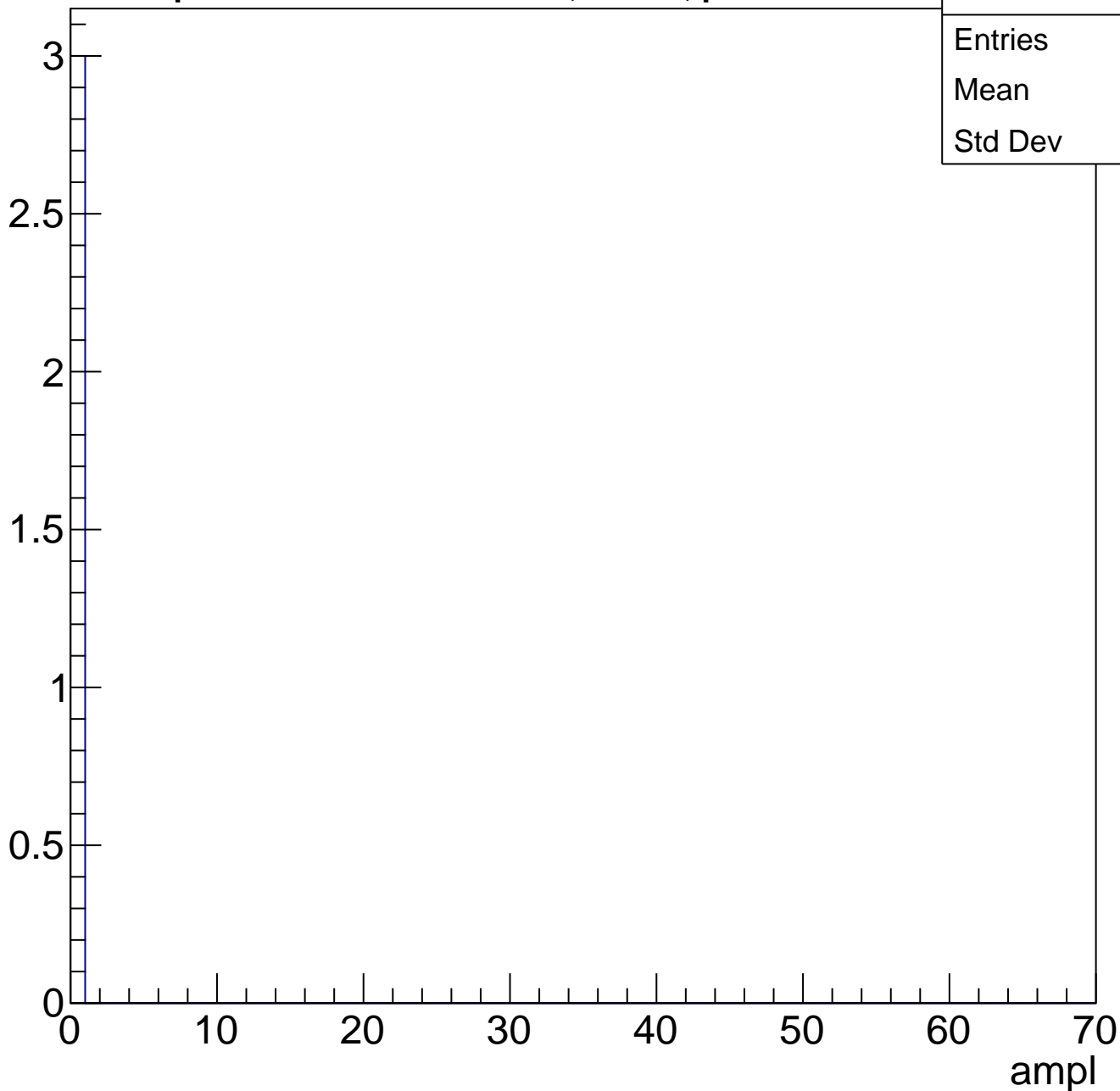


Entries	5
Mean	49.4
Std Dev	24.71

B0L102S, U12-ch29, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U12-ch30, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	29.51
Std Dev	6.96

Gaus mean : 31.2129

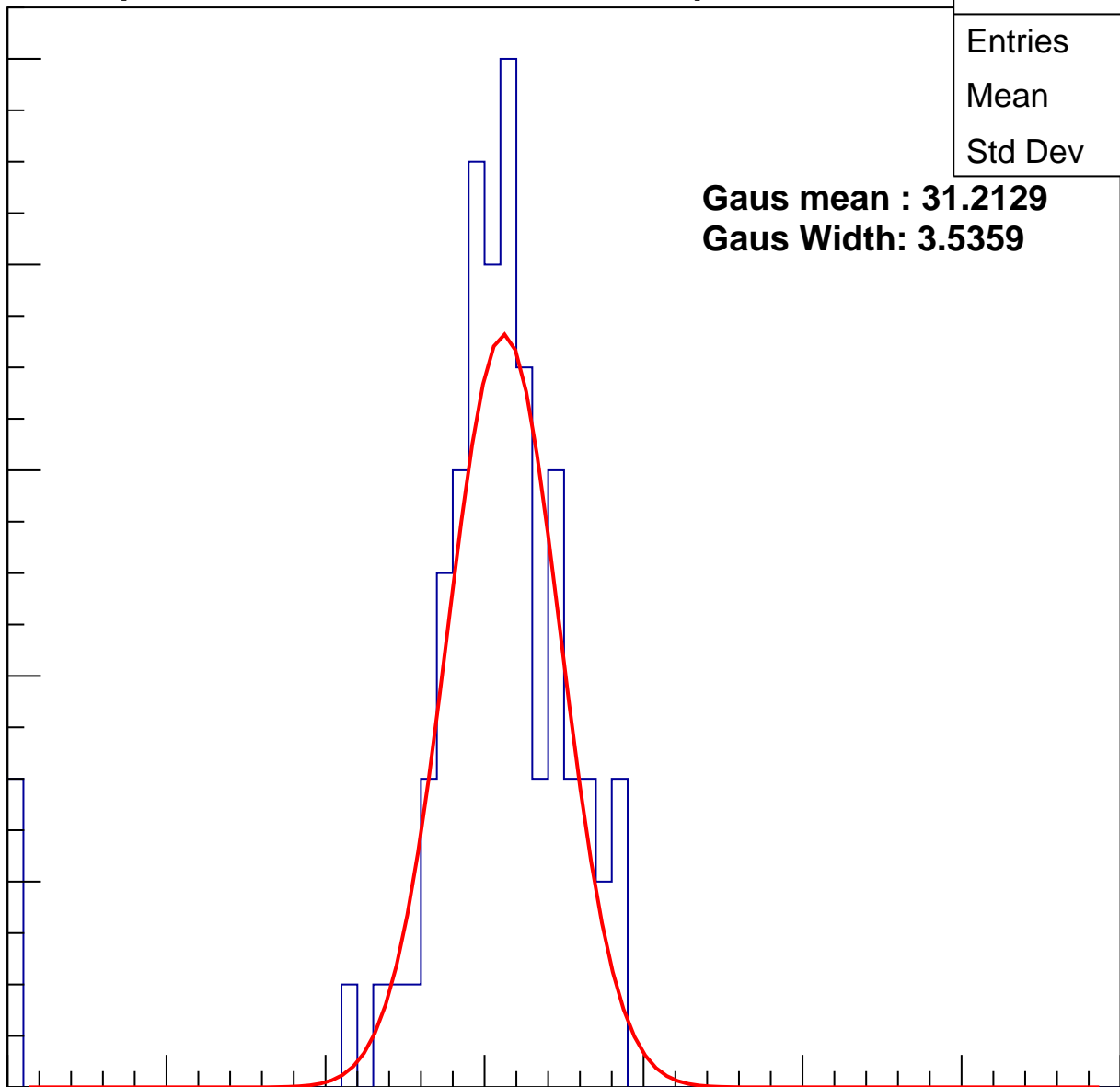
Gaus Width: 3.5359

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch30, adc1

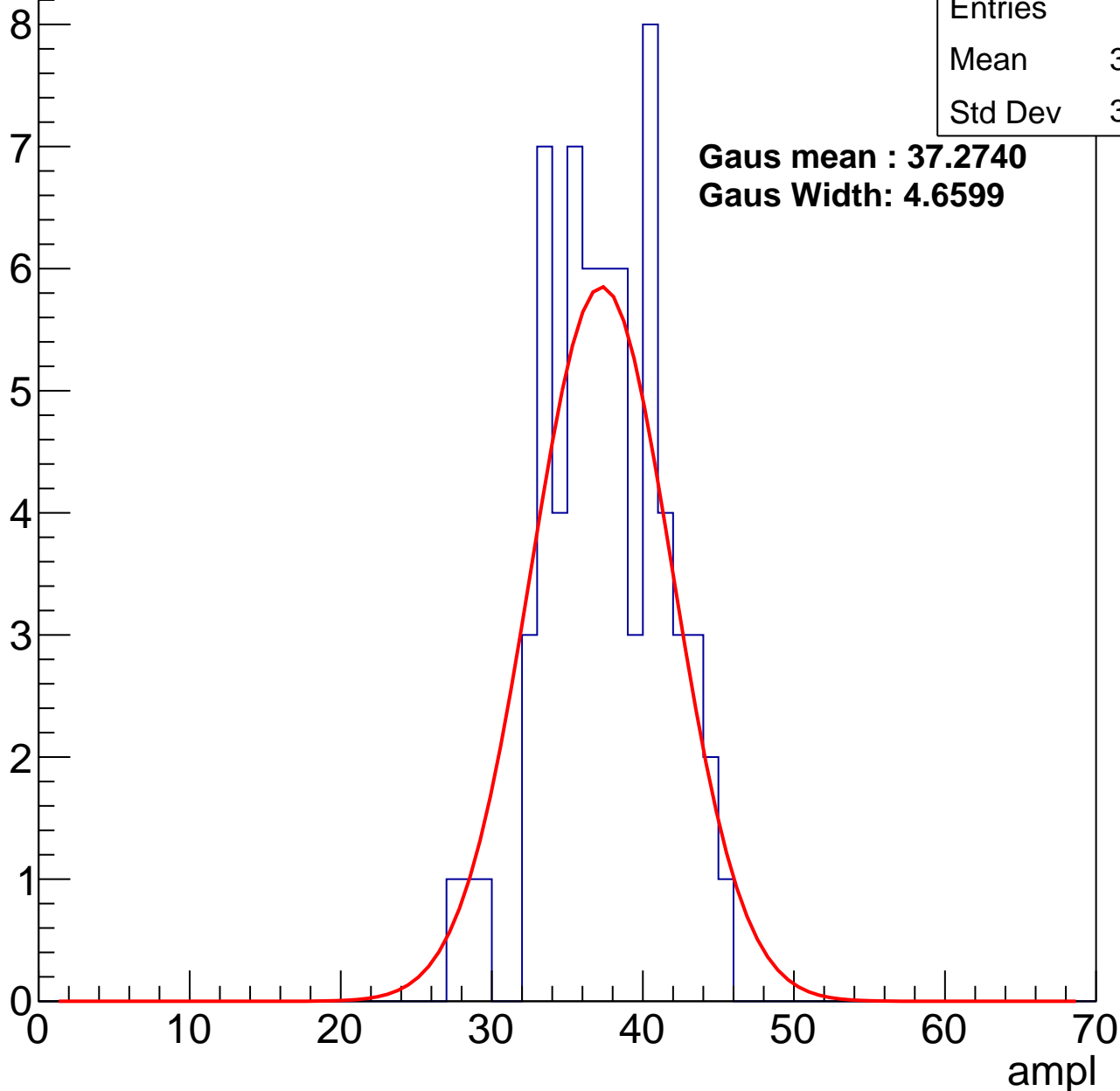
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	37.08
Std Dev	3.894

Gaus mean : 37.2740

Gaus Width: 4.6599



B0L102S, U12-ch30, adc2

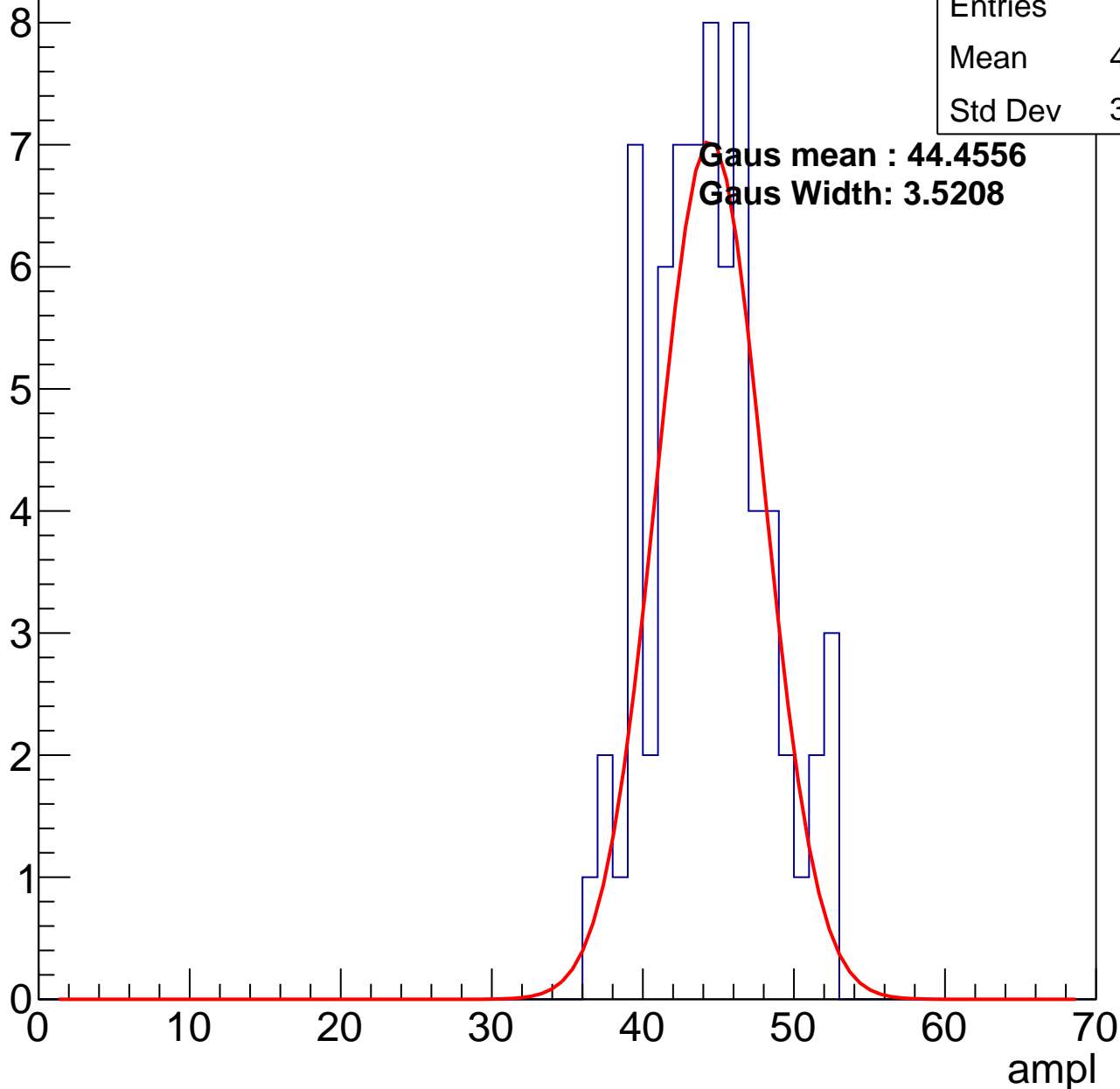
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	43.92
Std Dev	3.767

Gaus mean : 44.4556

Gaus Width: 3.5208

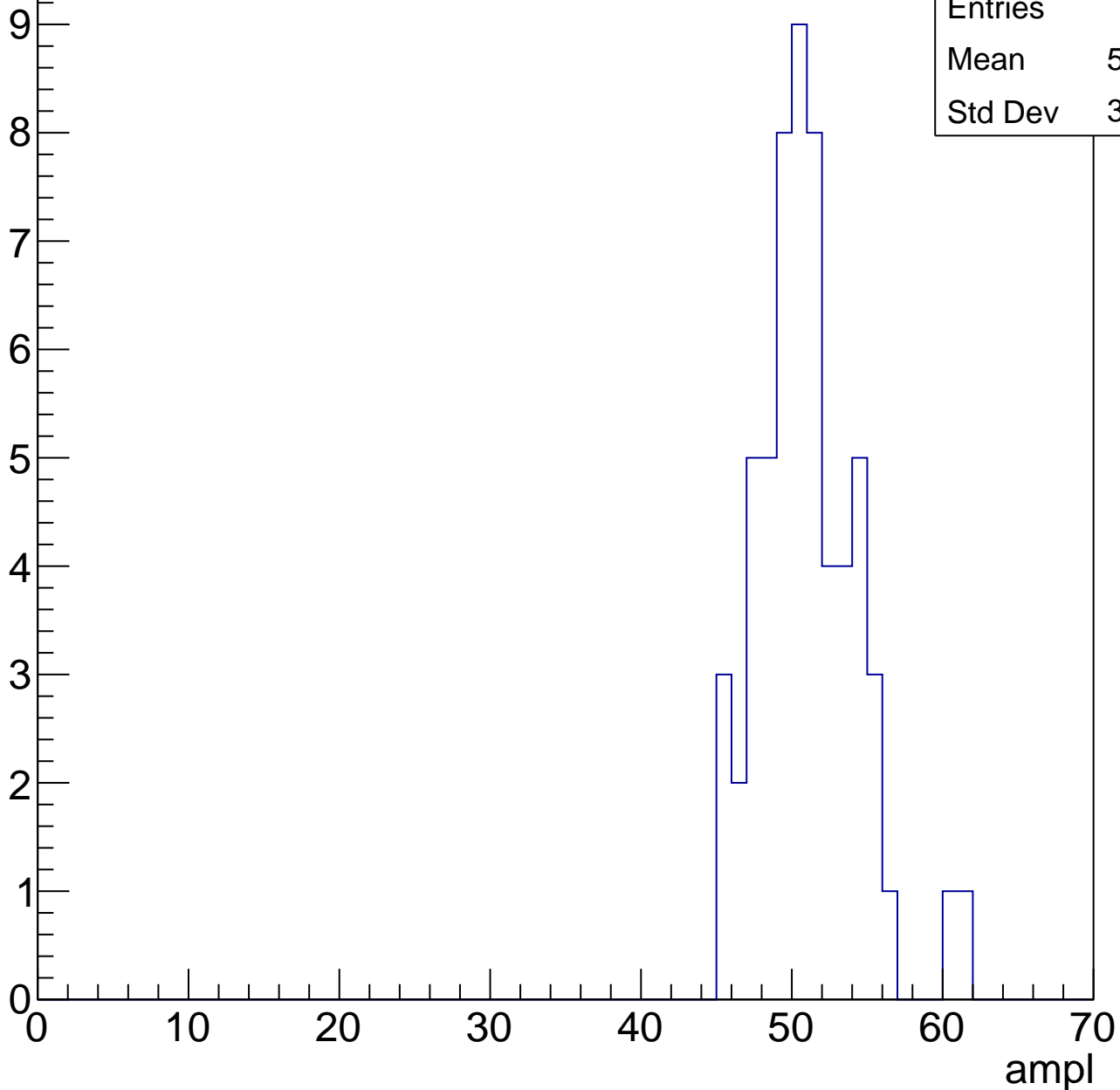


B0L102S, U12-ch30, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	50.58
Std Dev	3.274



B0L102S, U12-ch30, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	54
Mean	56
Std Dev	3.025

Entry

10

8

6

4

2

0

0

10

20

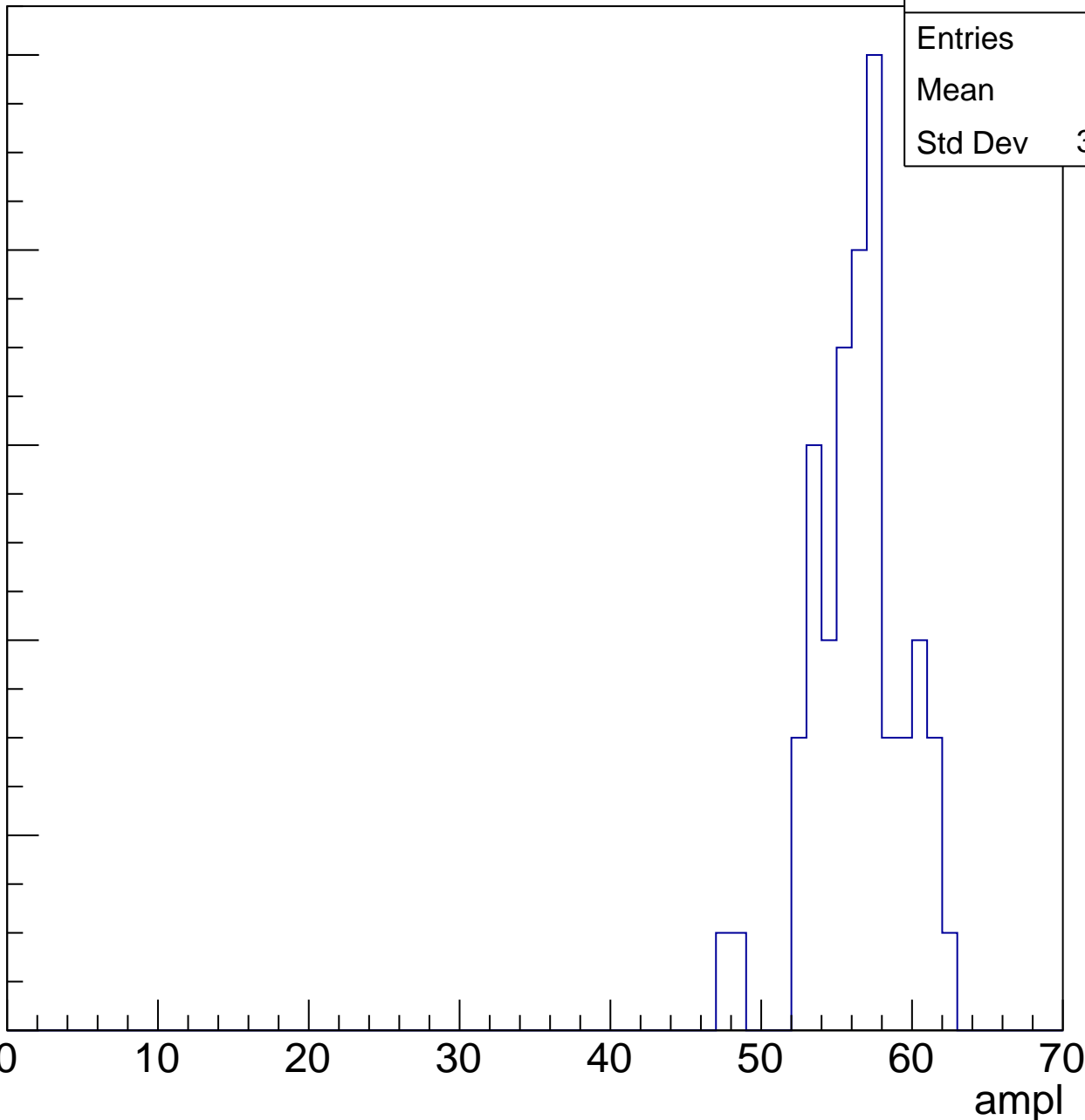
30

40

50

60

ampl

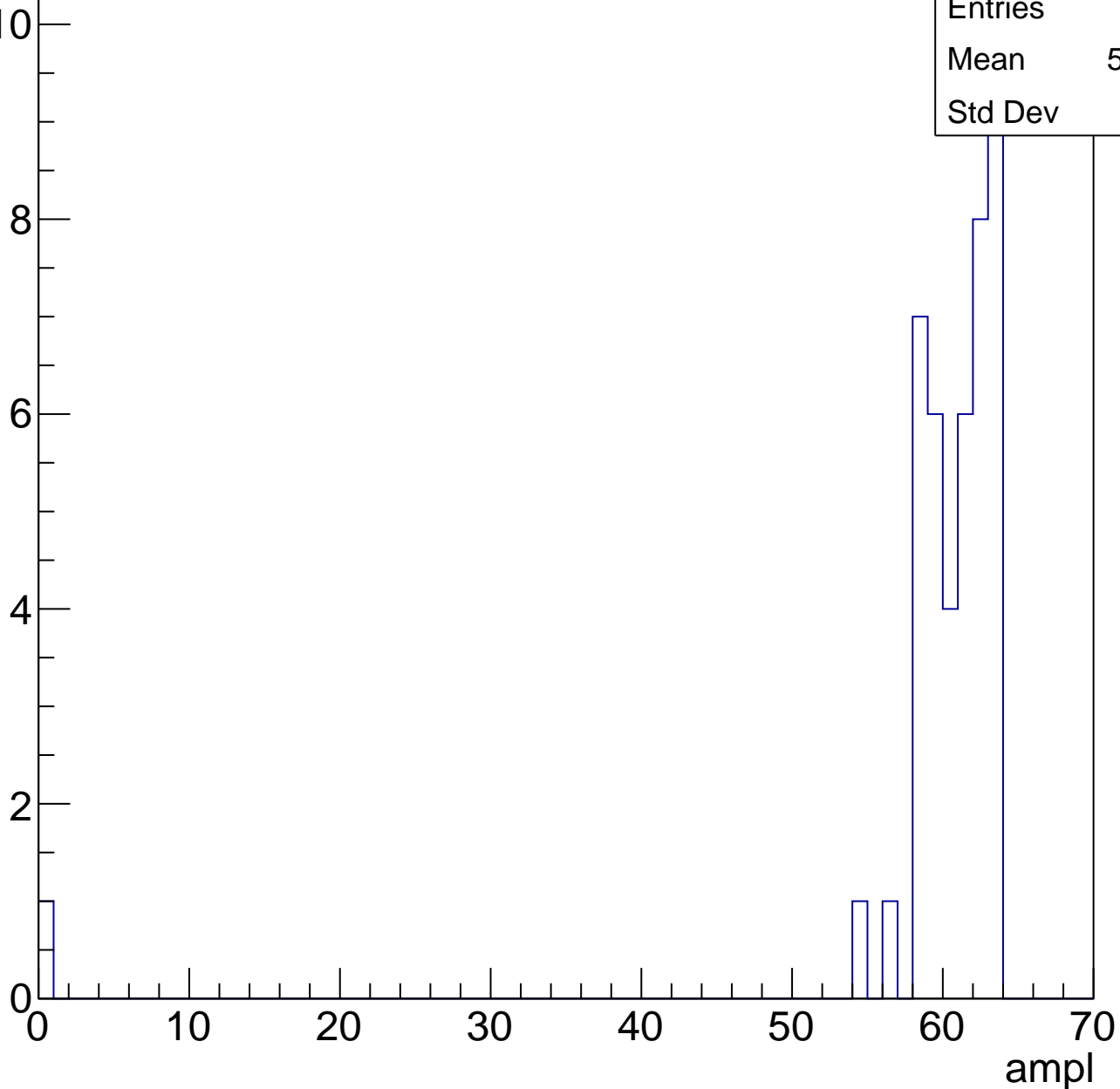


B0L102S, U12-ch30, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

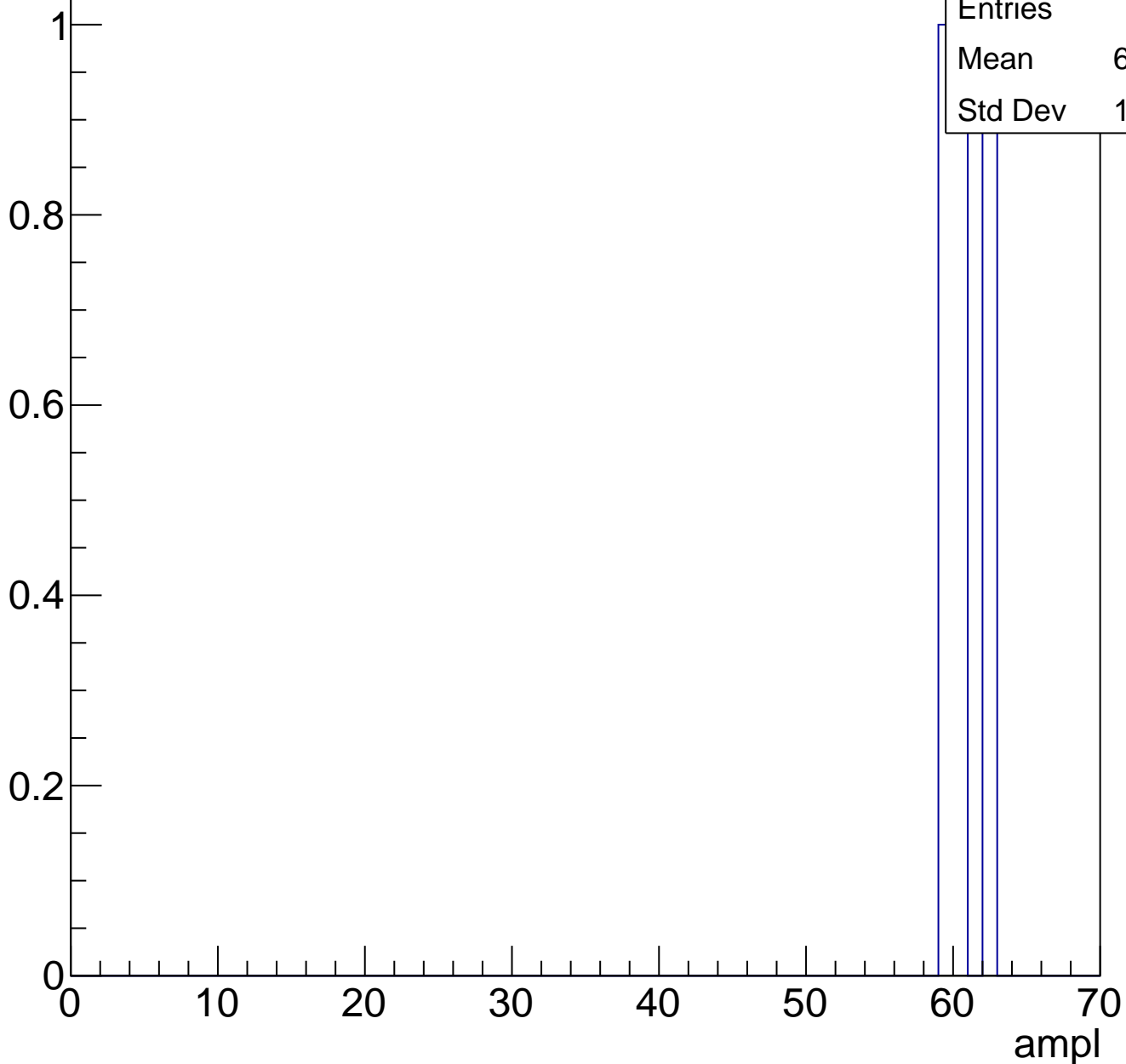
Entries	44
Mean	59.14
Std Dev	9.27



B0L102S, U12-ch30, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch30, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch31, adc0

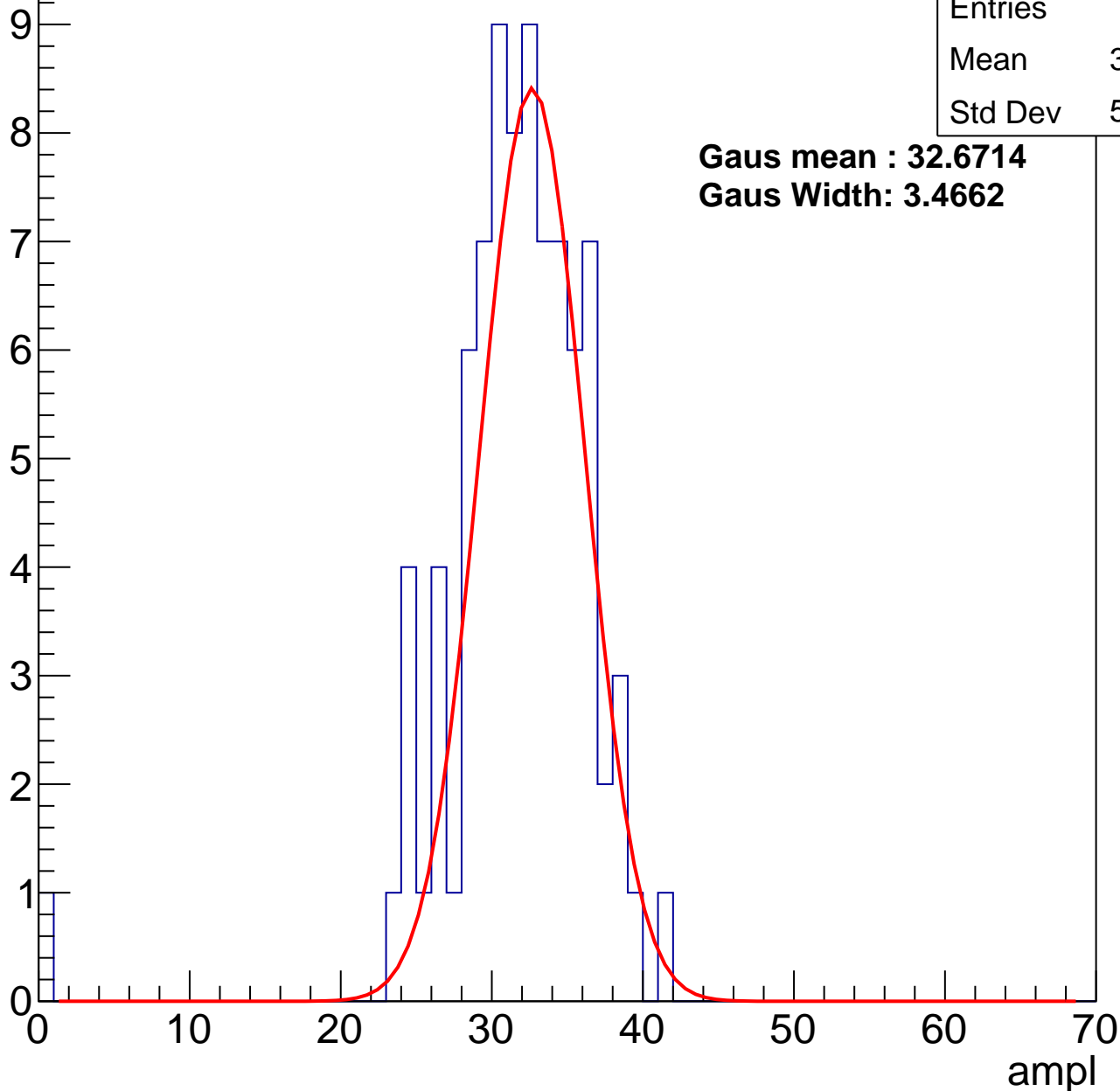
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	31.19
Std Dev	5.105

Gaus mean : 32.6714

Gaus Width: 3.4662



B0L102S, U12-ch31, adc1

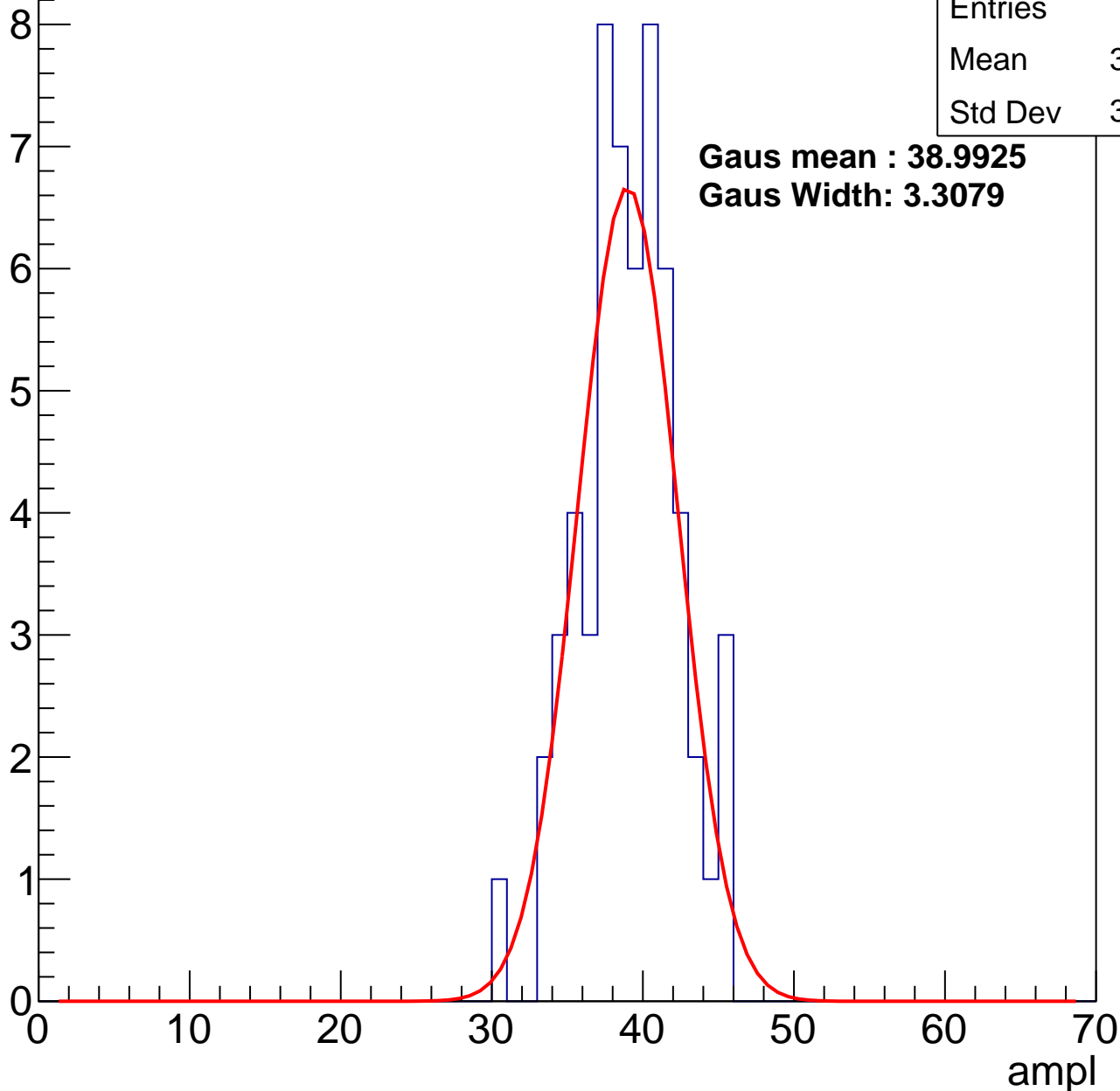
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	38.64
Std Dev	3.172

Gaus mean : 38.9925

Gaus Width: 3.3079



B0L102S, U12-ch31, adc2

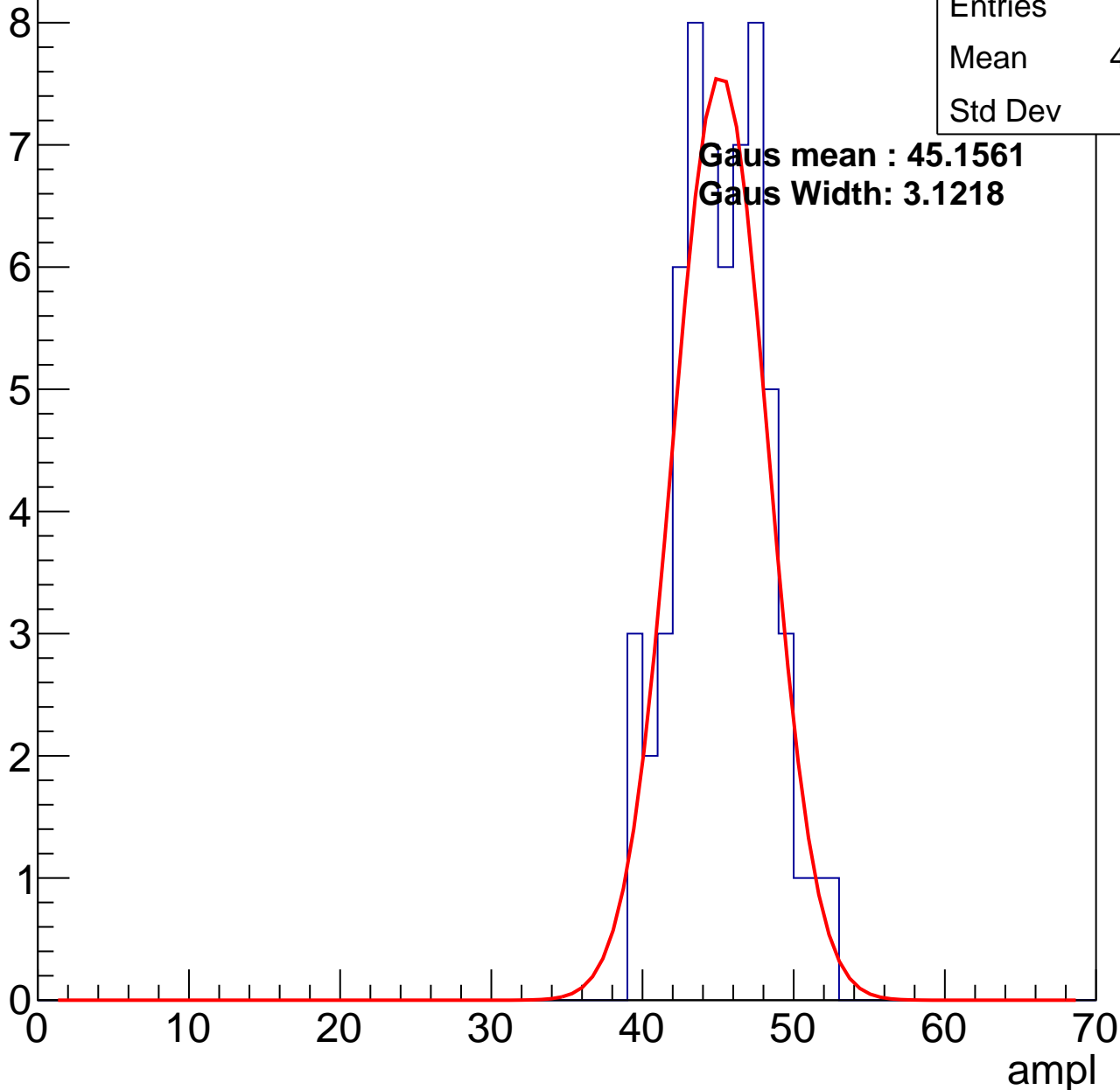
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	44.79
Std Dev	2.97

Gaus mean : 45.1561

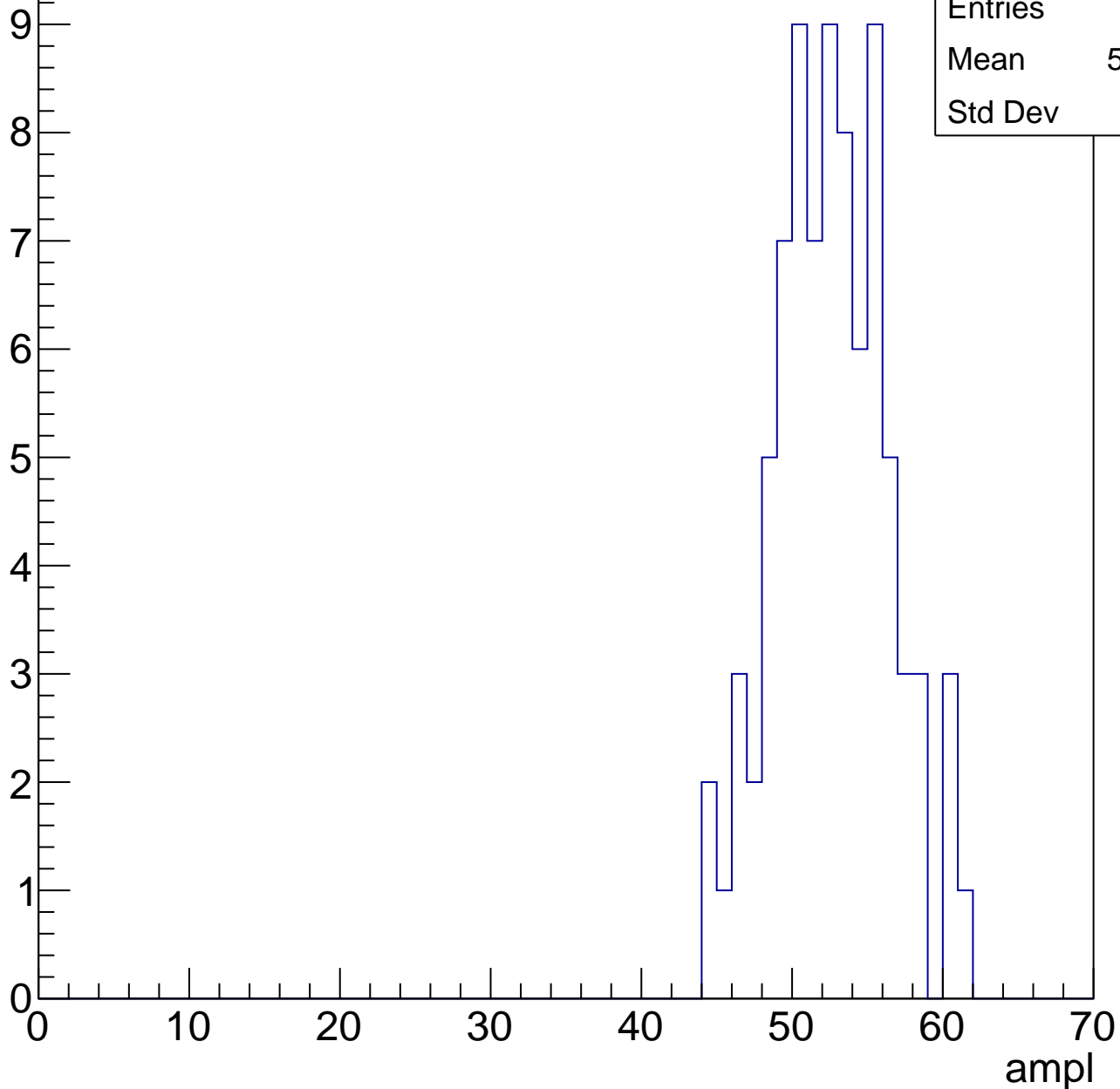
Gaus Width: 3.1218



B0L102S, U12-ch31, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



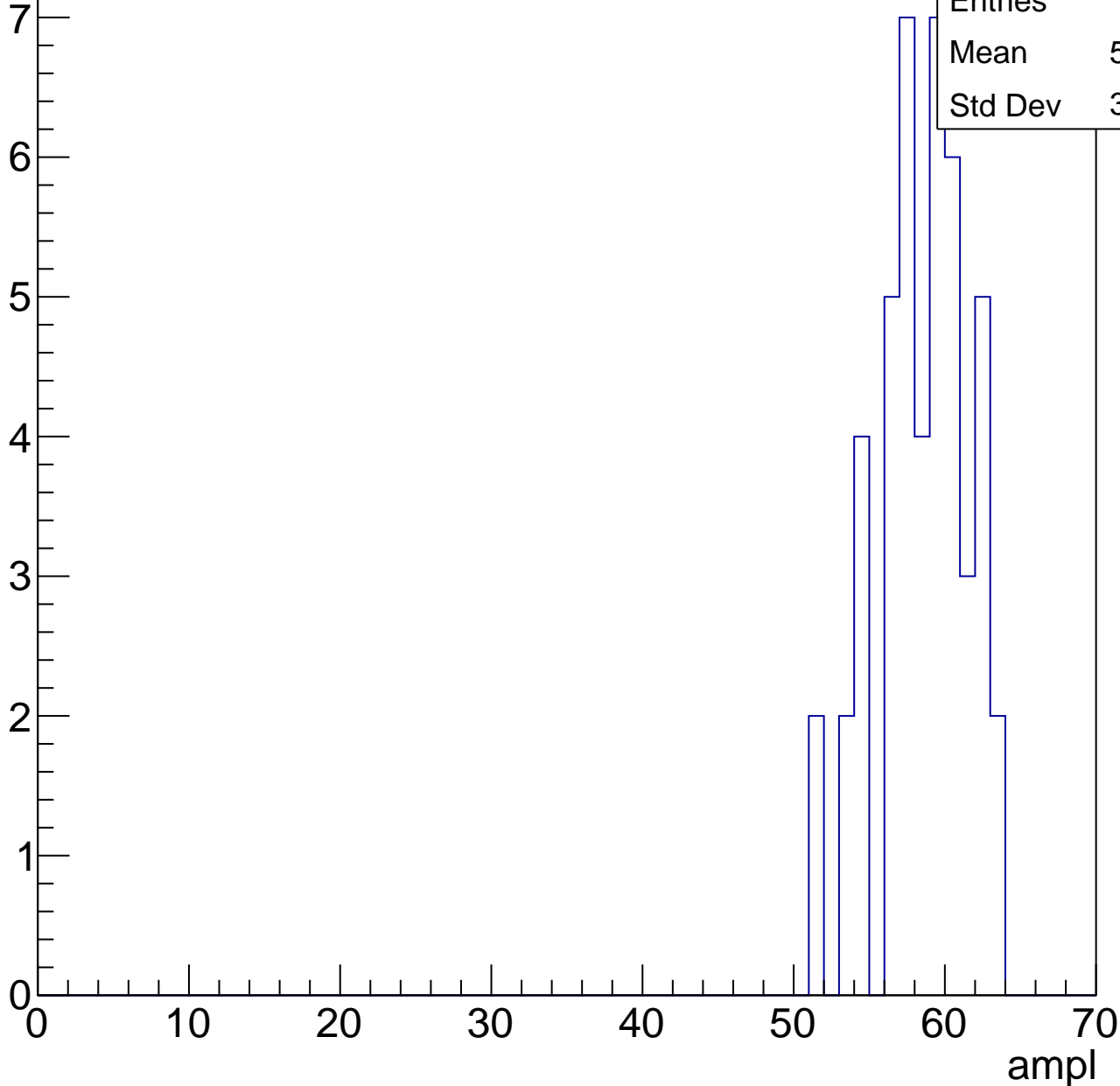
Entries	83
Mean	52.19
Std Dev	3.75

B0L102S, U12-ch31, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	58.02
Std Dev	3.014

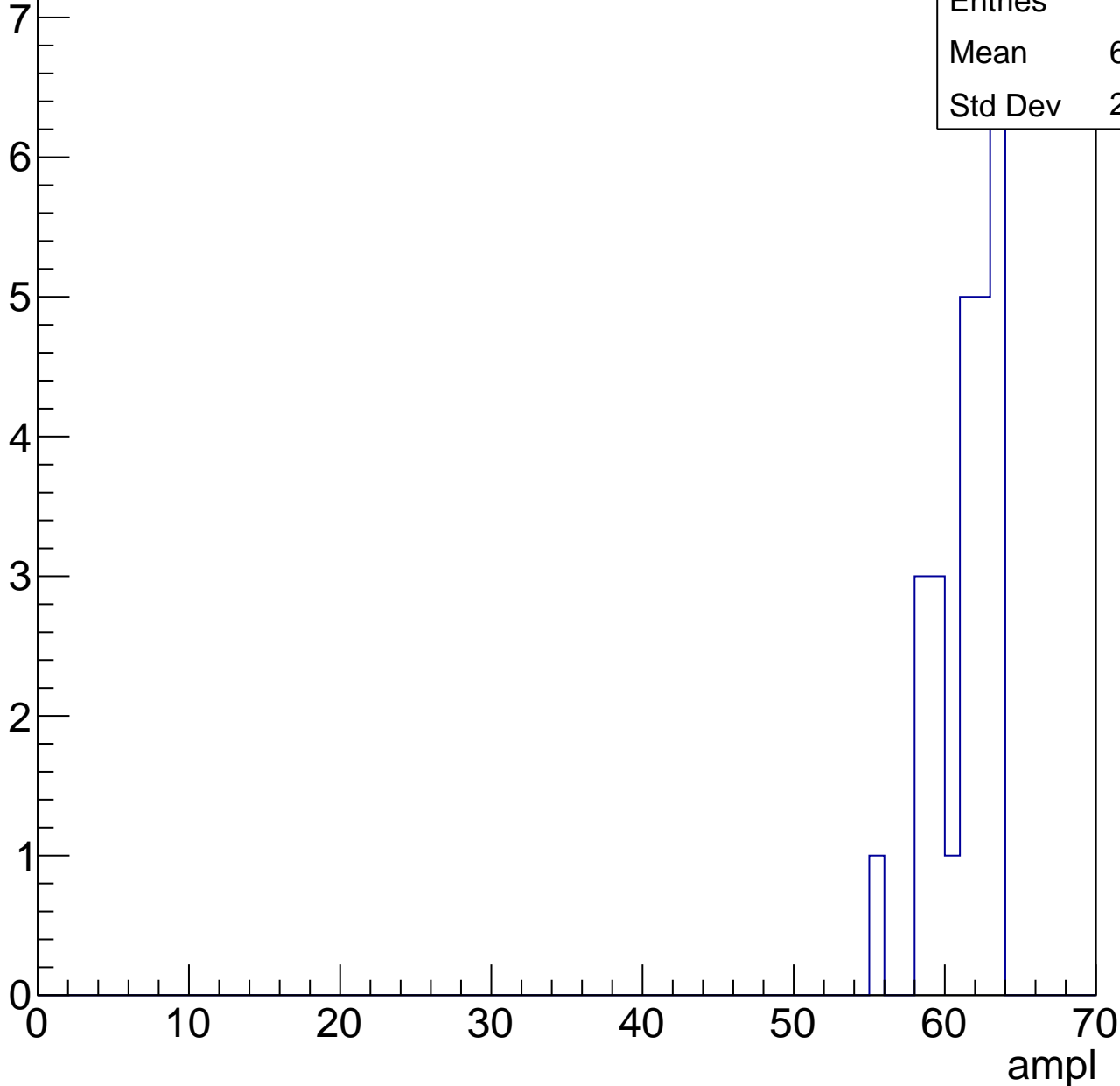


B0L102S, U12-ch31, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

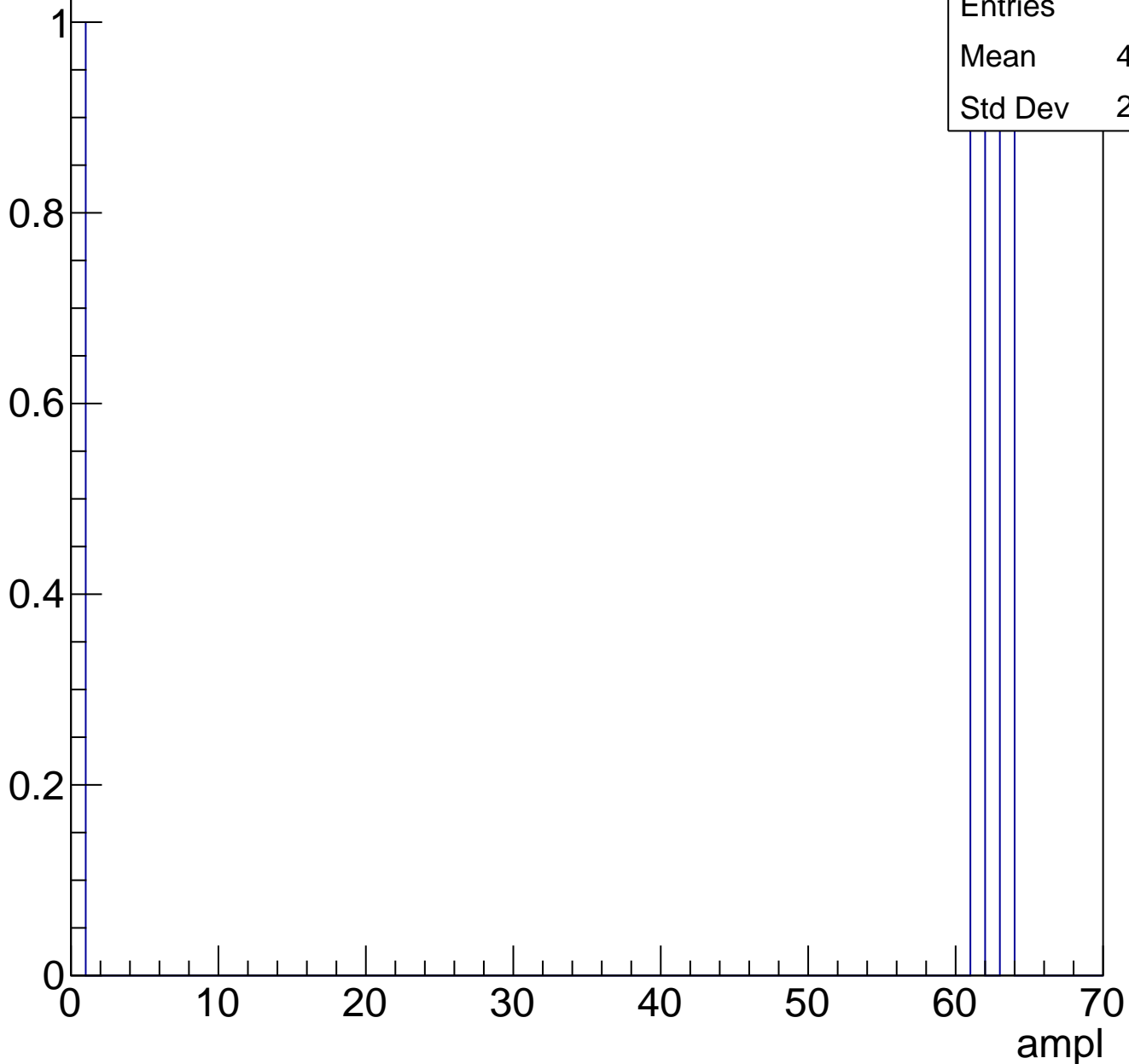
Entries	25
Mean	60.88
Std Dev	2.085



B0L102S, U12-ch31, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch31, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch32, adc0

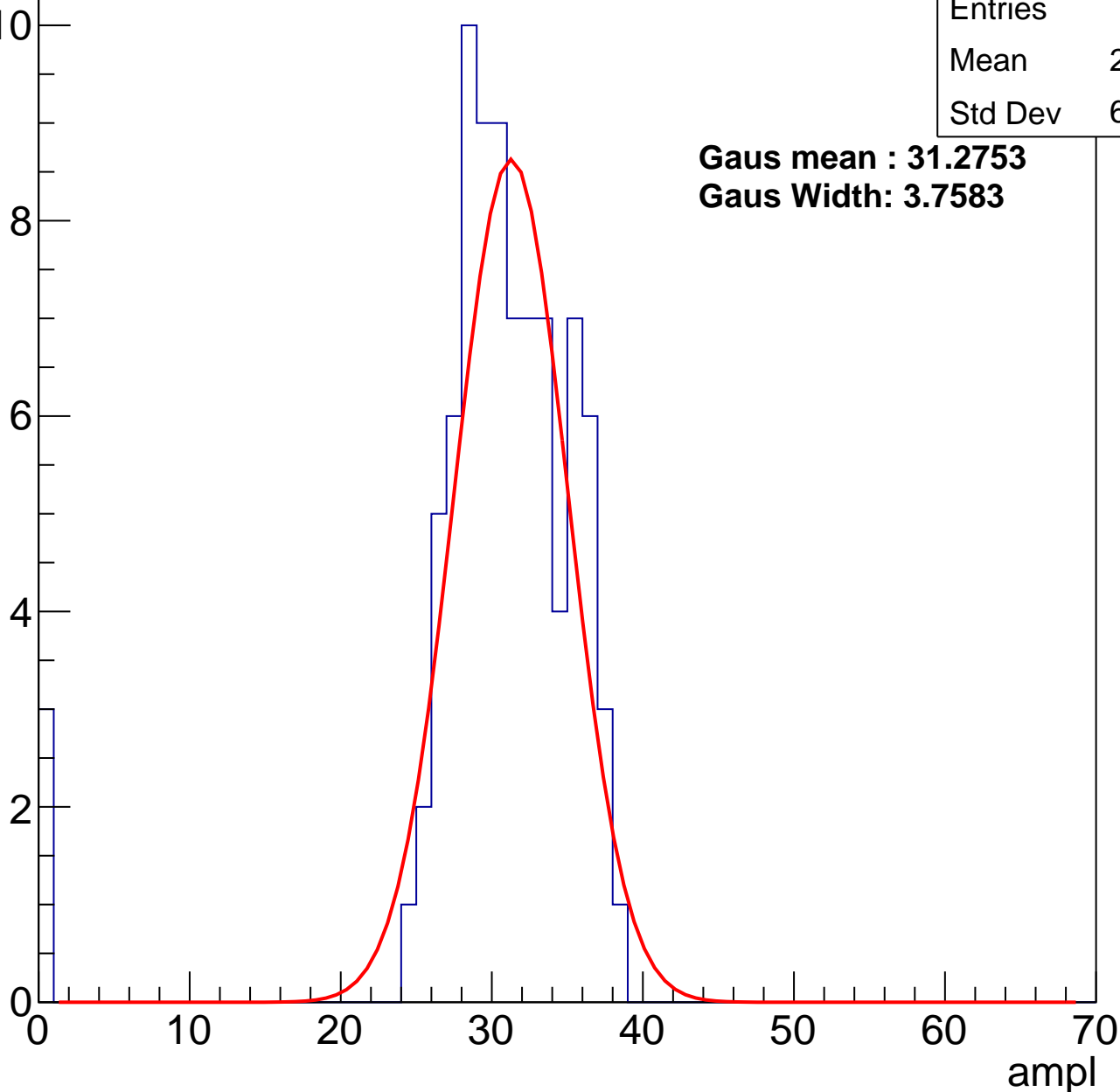
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	29.83
Std Dev	6.548

Gaus mean : 31.2753

Gaus Width: 3.7583



B0L102S, U12-ch32, adc1

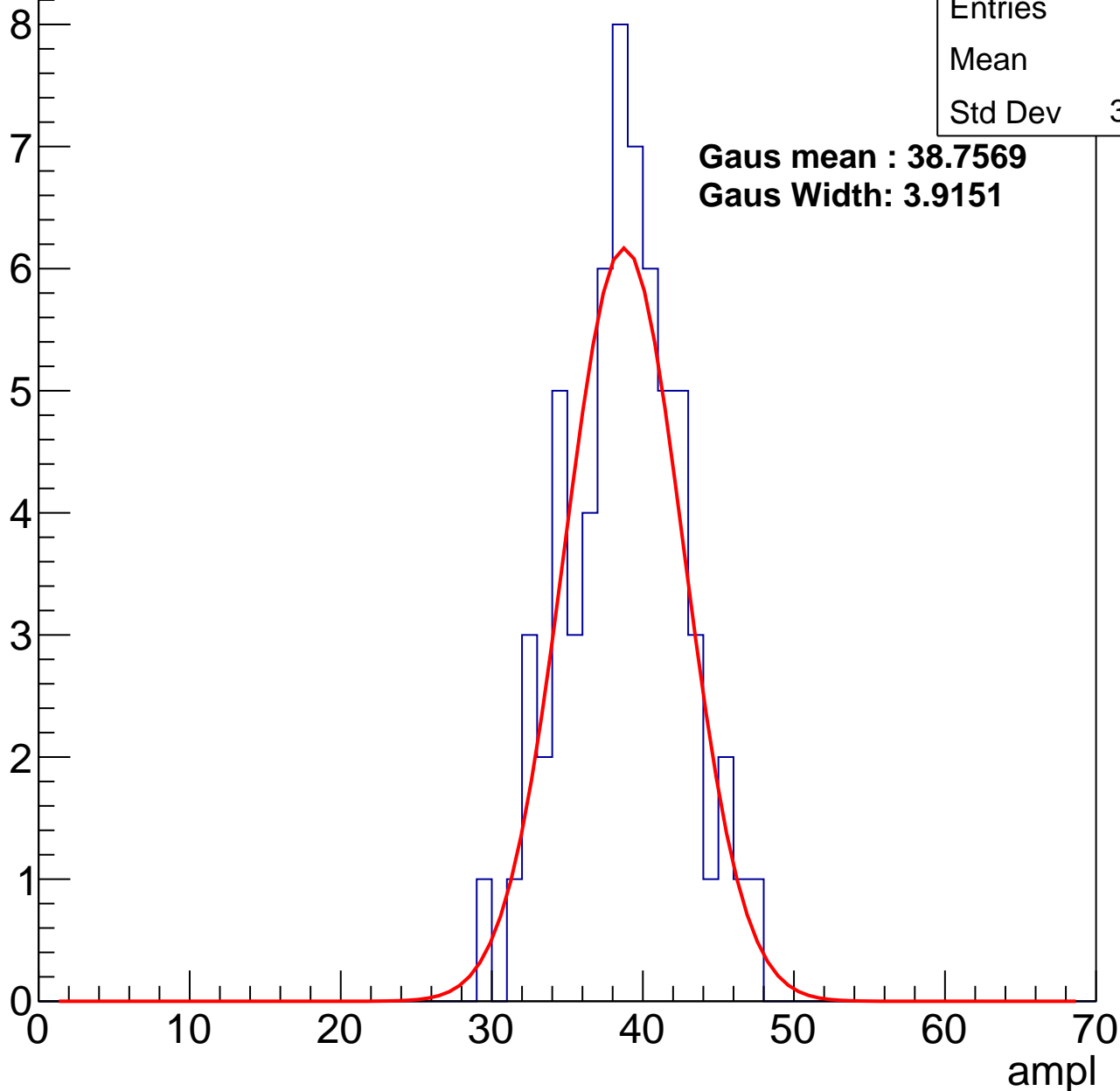
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	38.3
Std Dev	3.794

Gaus mean : 38.7569

Gaus Width: 3.9151



B0L102S, U12-ch32, adc2

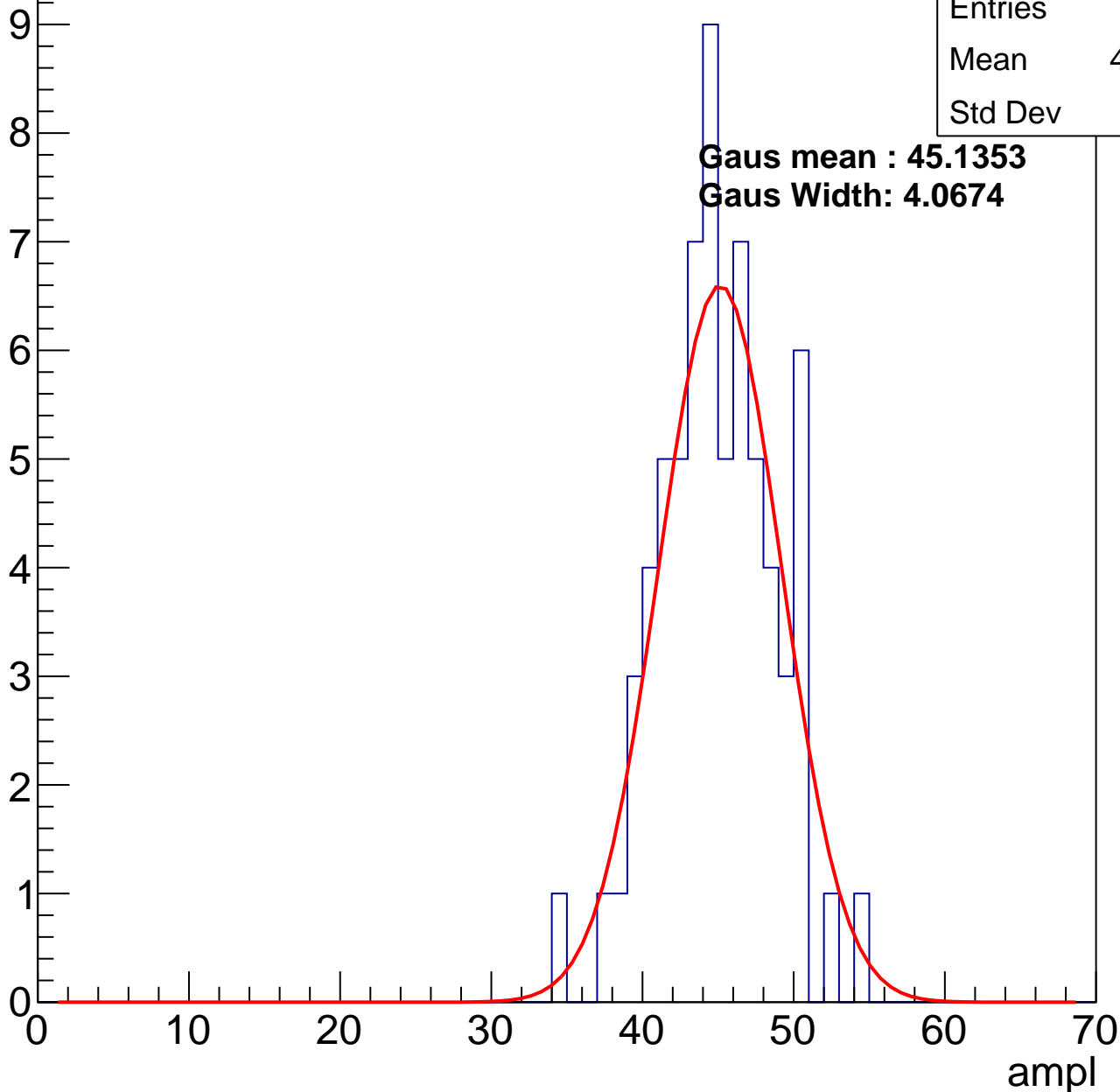
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	44.49
Std Dev	3.79

Gaus mean : 45.1353

Gaus Width: 4.0674

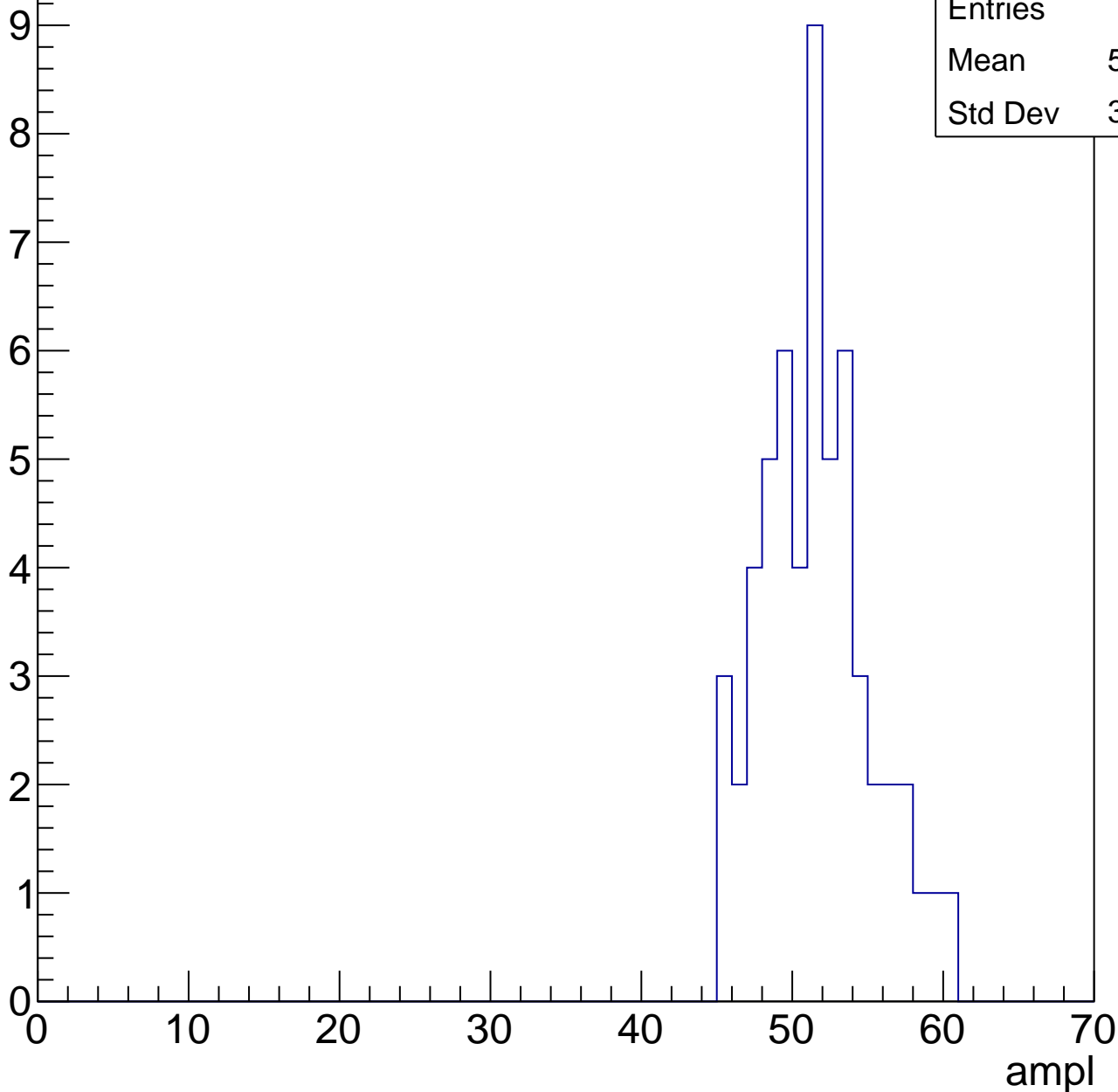


B0L102S, U12-ch32, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	51.09
Std Dev	3.532



B0L102S, U12-ch32, adc4

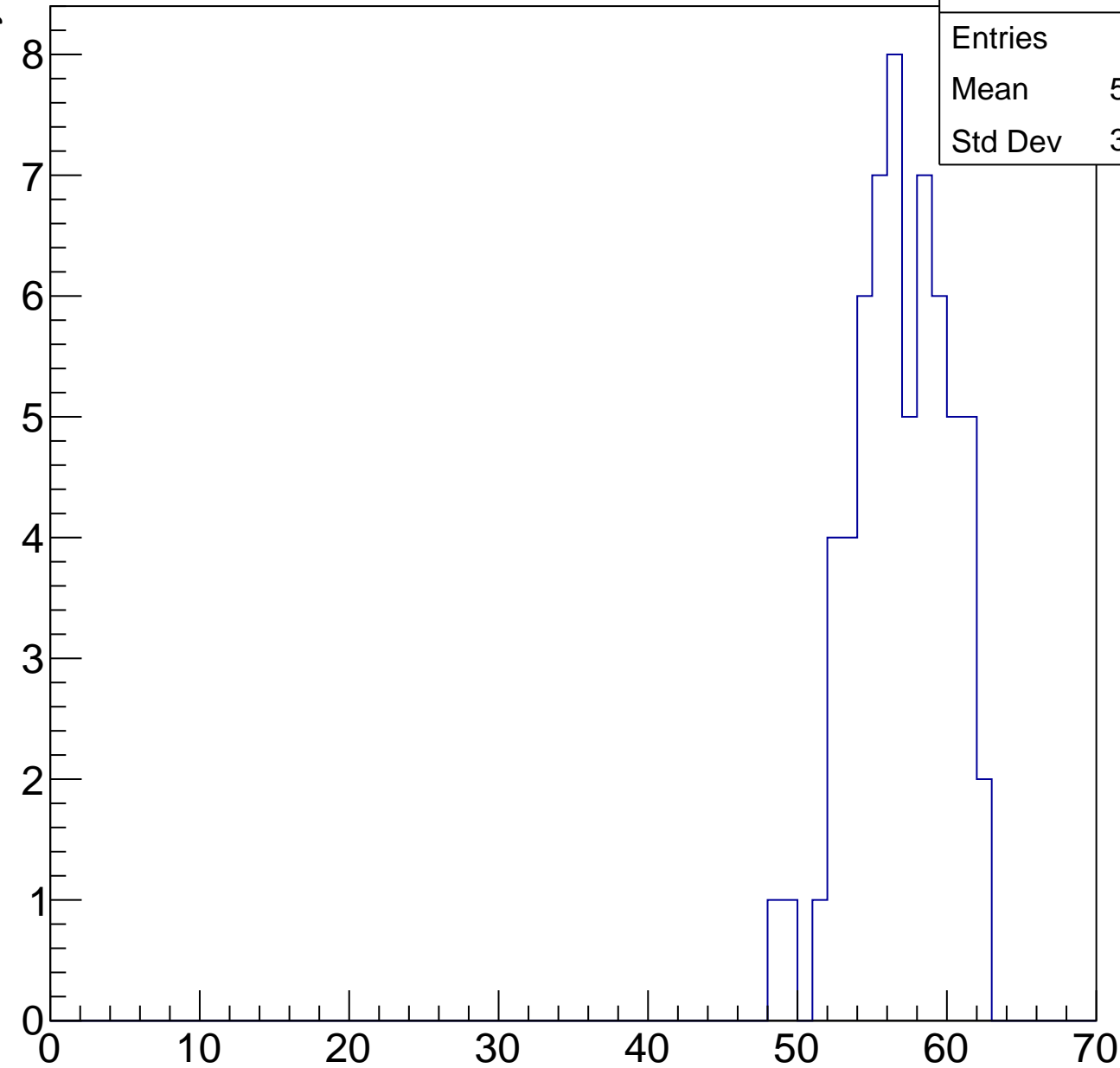
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	62
Mean	56.44
Std Dev	3.165

ampl

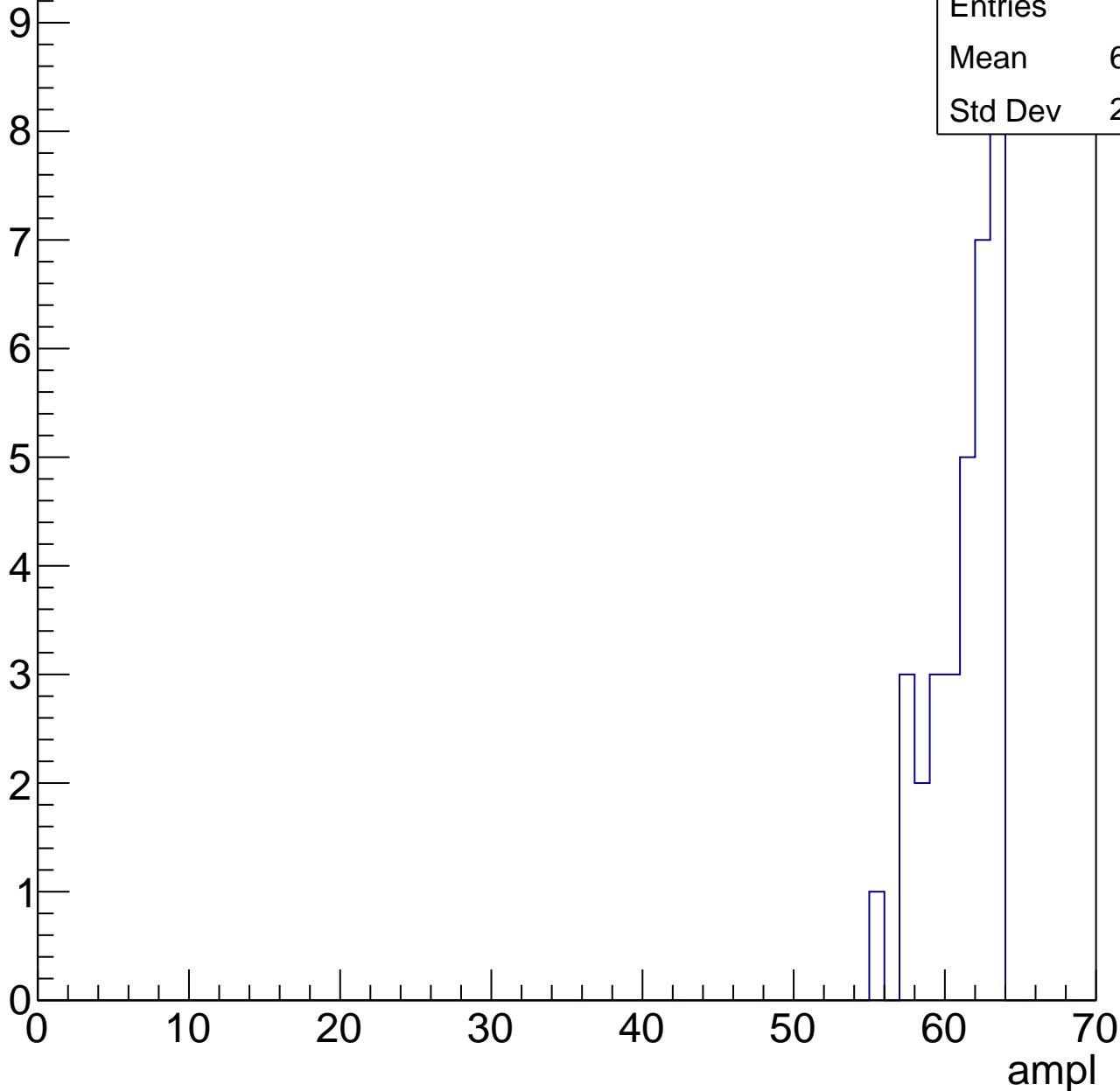


B0L102S, U12-ch32, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

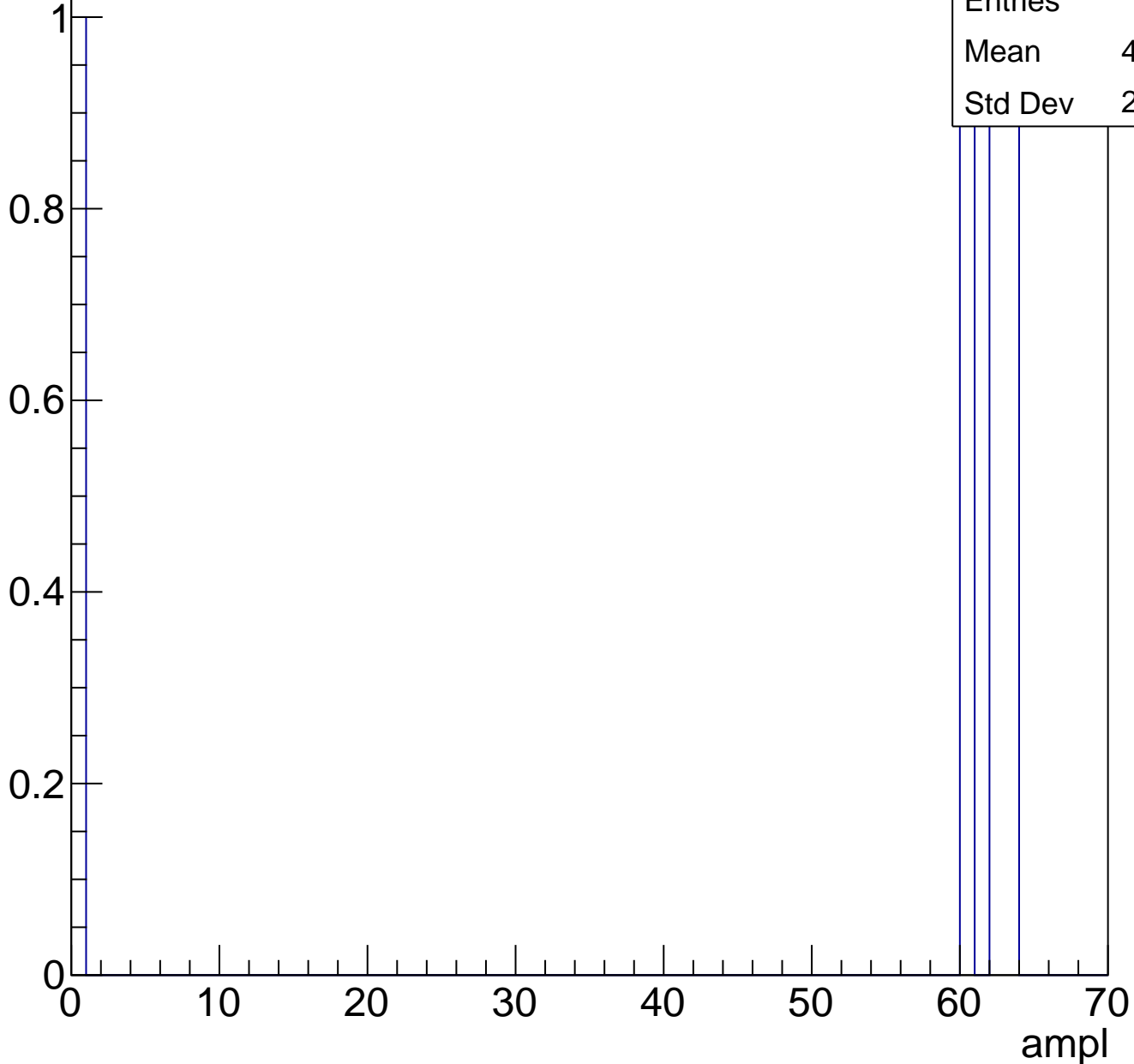
Entries	33
Mean	60.76
Std Dev	2.189



B0L102S, U12-ch32, adc6

calib_packv5_042523_0143.root, FC#12, port B1

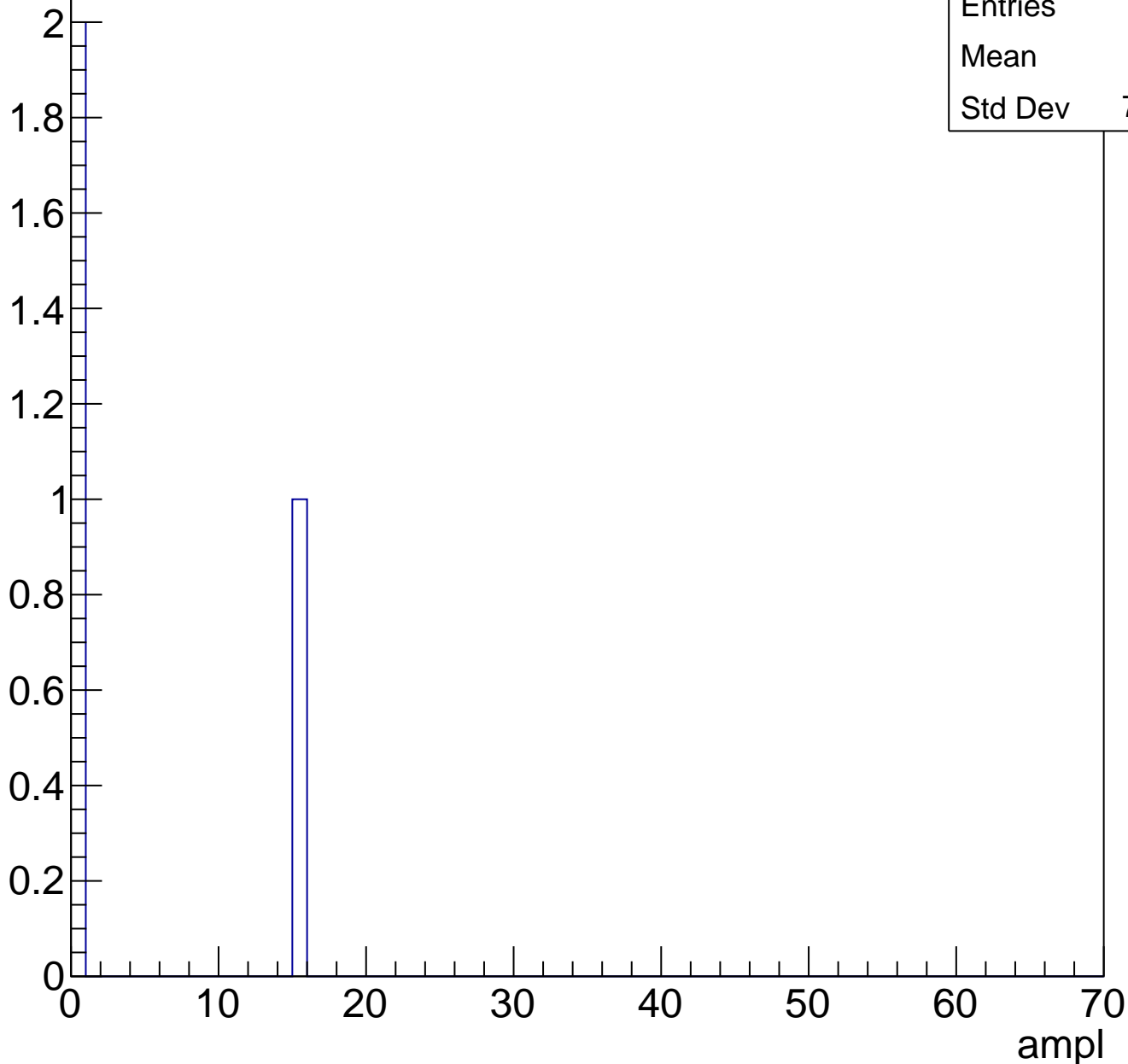
Entry



B0L102S, U12-ch32, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch33, adc0

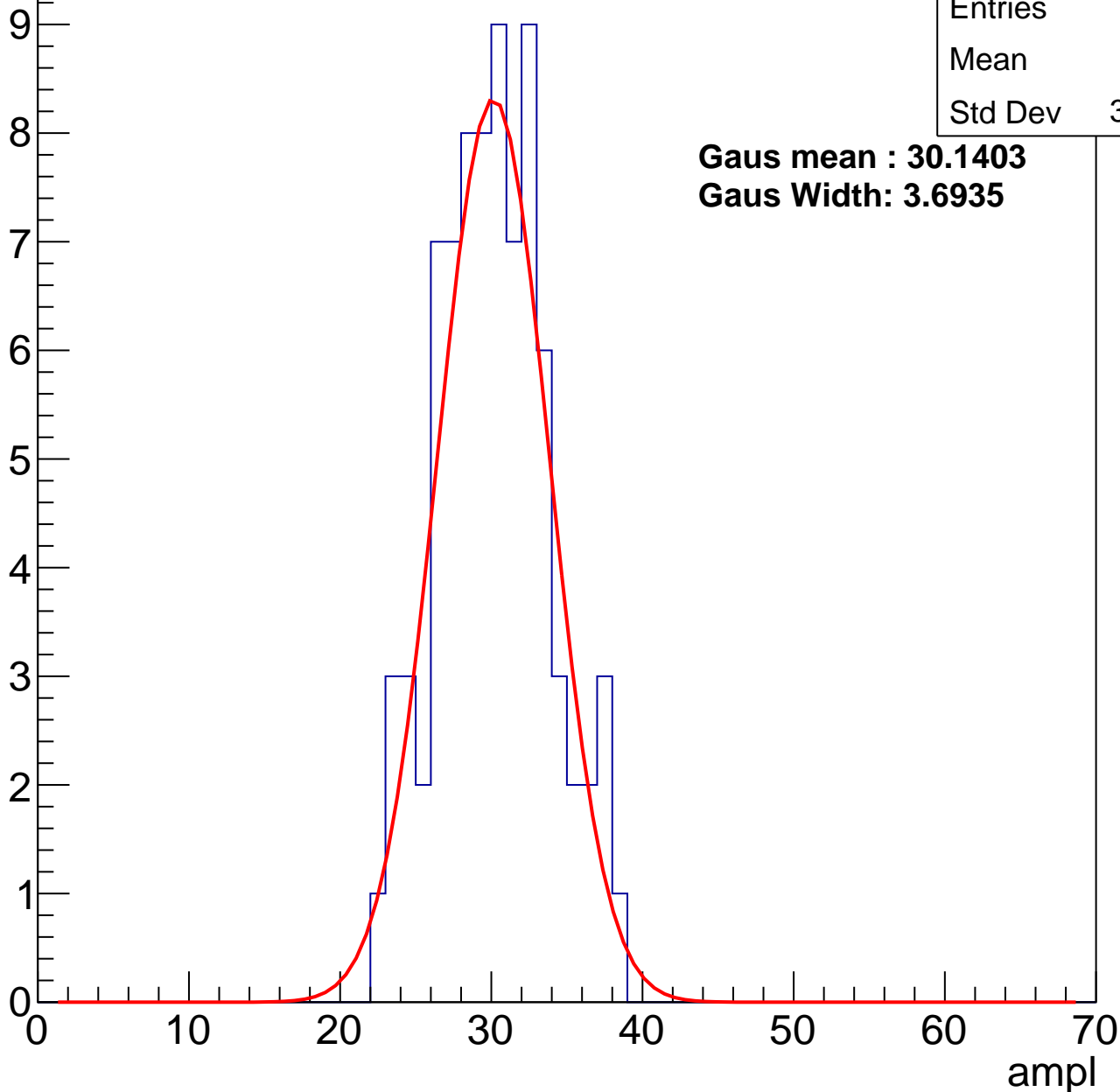
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.7
Std Dev	3.609

Gaus mean : 30.1403

Gaus Width: 3.6935



B0L102S, U12-ch33, adc1

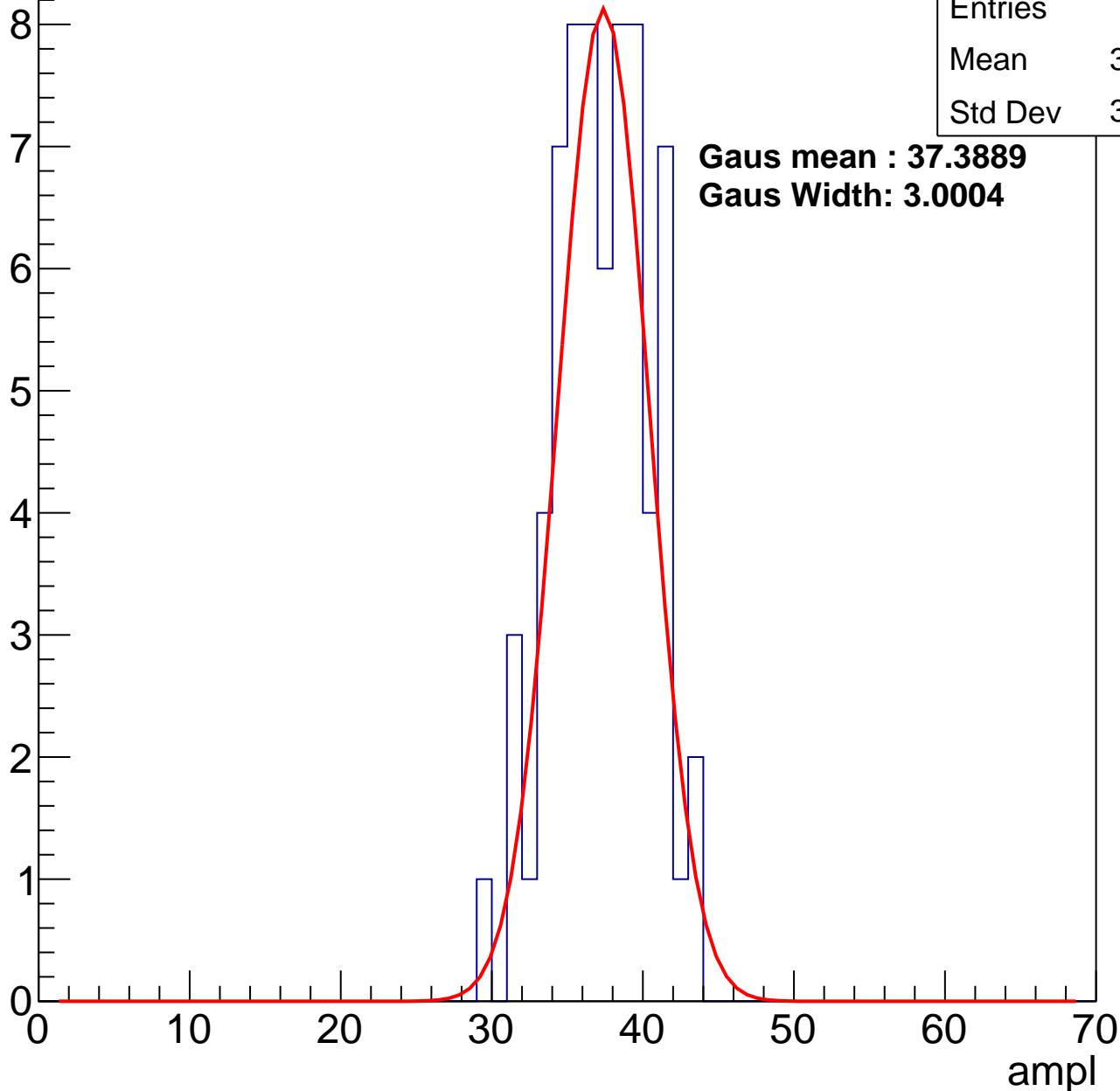
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.84
Std Dev	3.085

Gaus mean : 37.3889

Gaus Width: 3.0004



B0L102S, U12-ch33, adc2

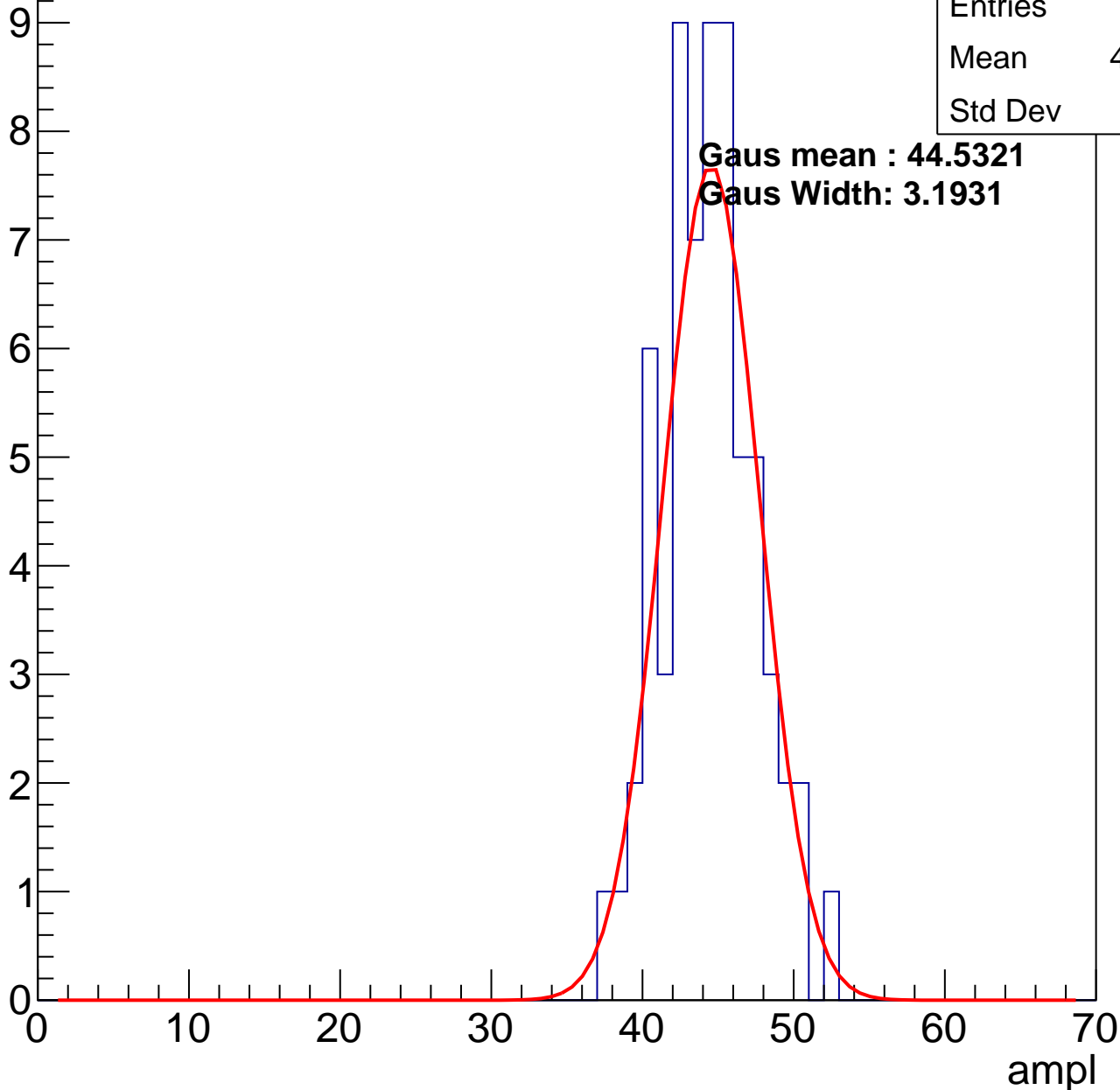
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.92
Std Dev	3.06

Gaus mean : 44.5321

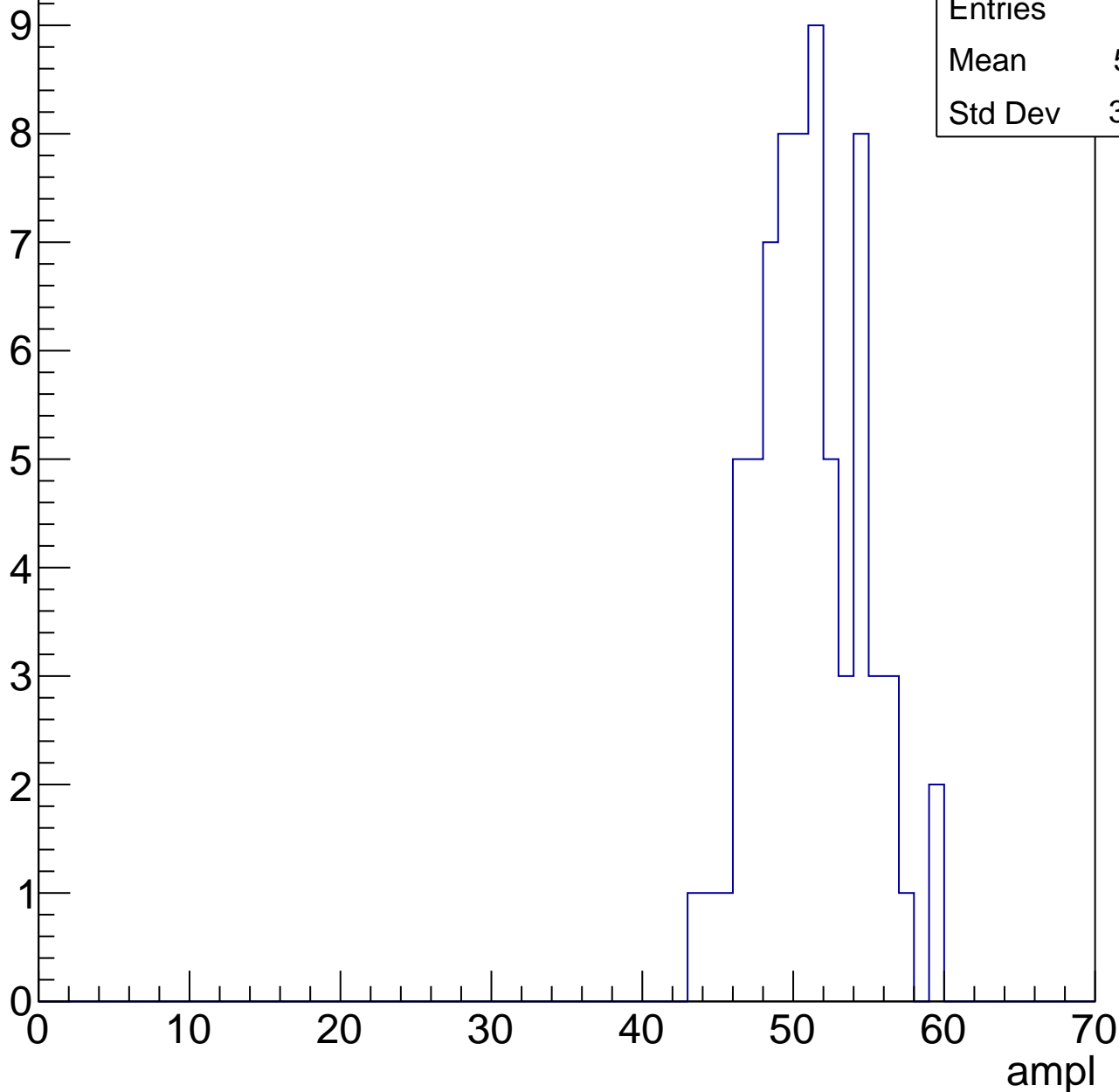
Gaus Width: 3.1931



B0L102S, U12-ch33, adc3

calib_packv5_042523_0143.root, FC#12, port B1

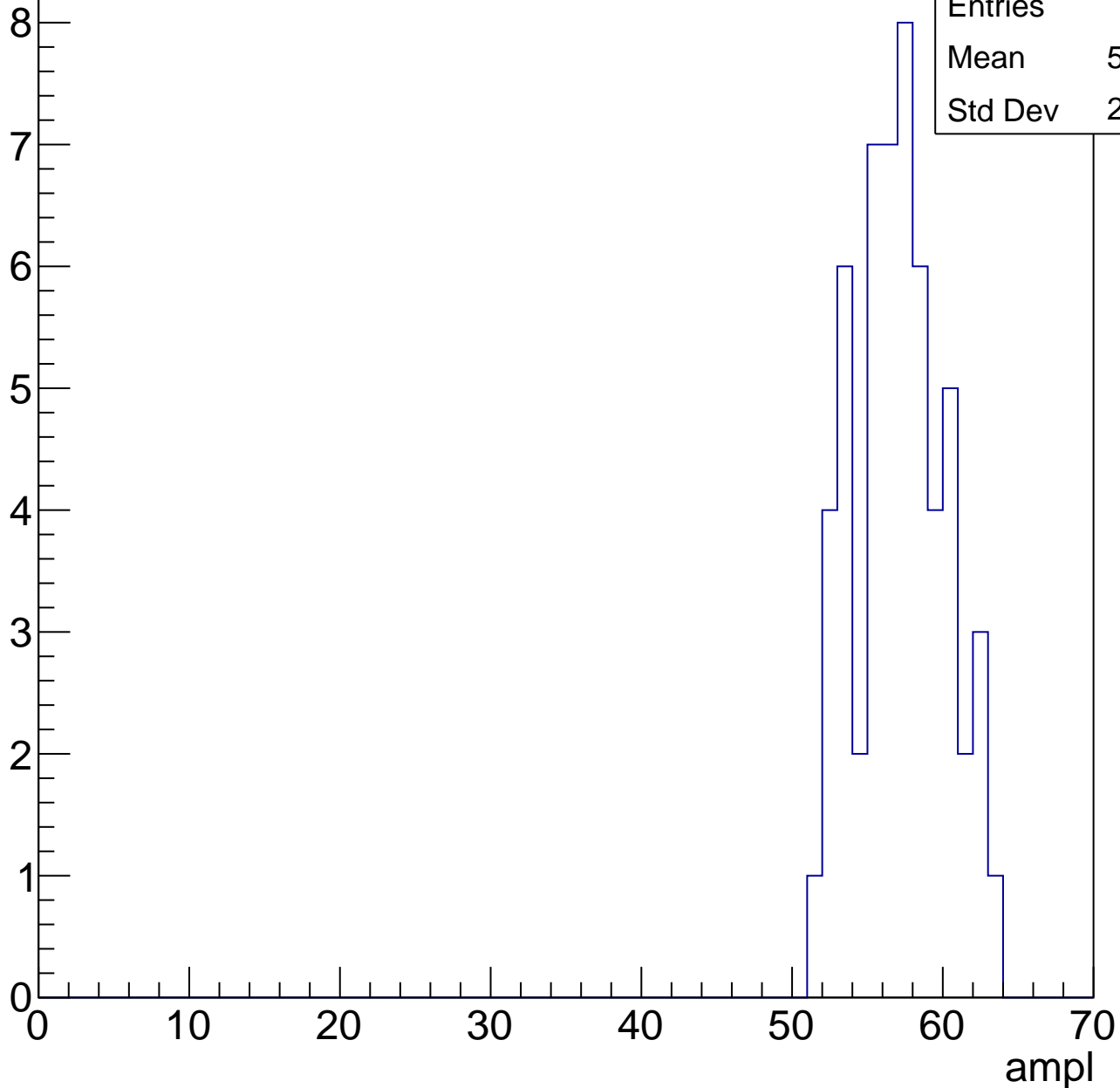
Entry



B0L102S, U12-ch33, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

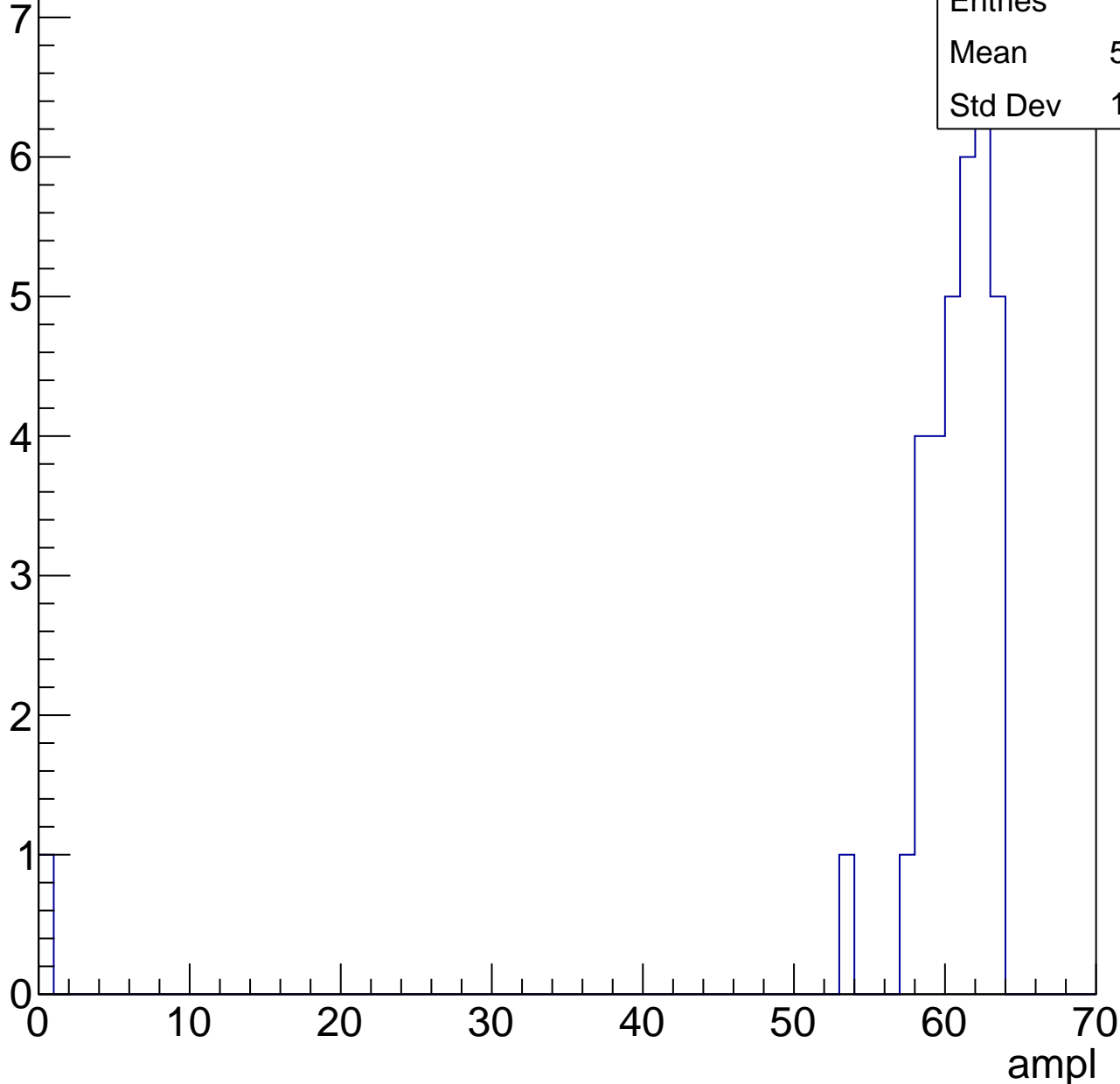


B0L102S, U12-ch33, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

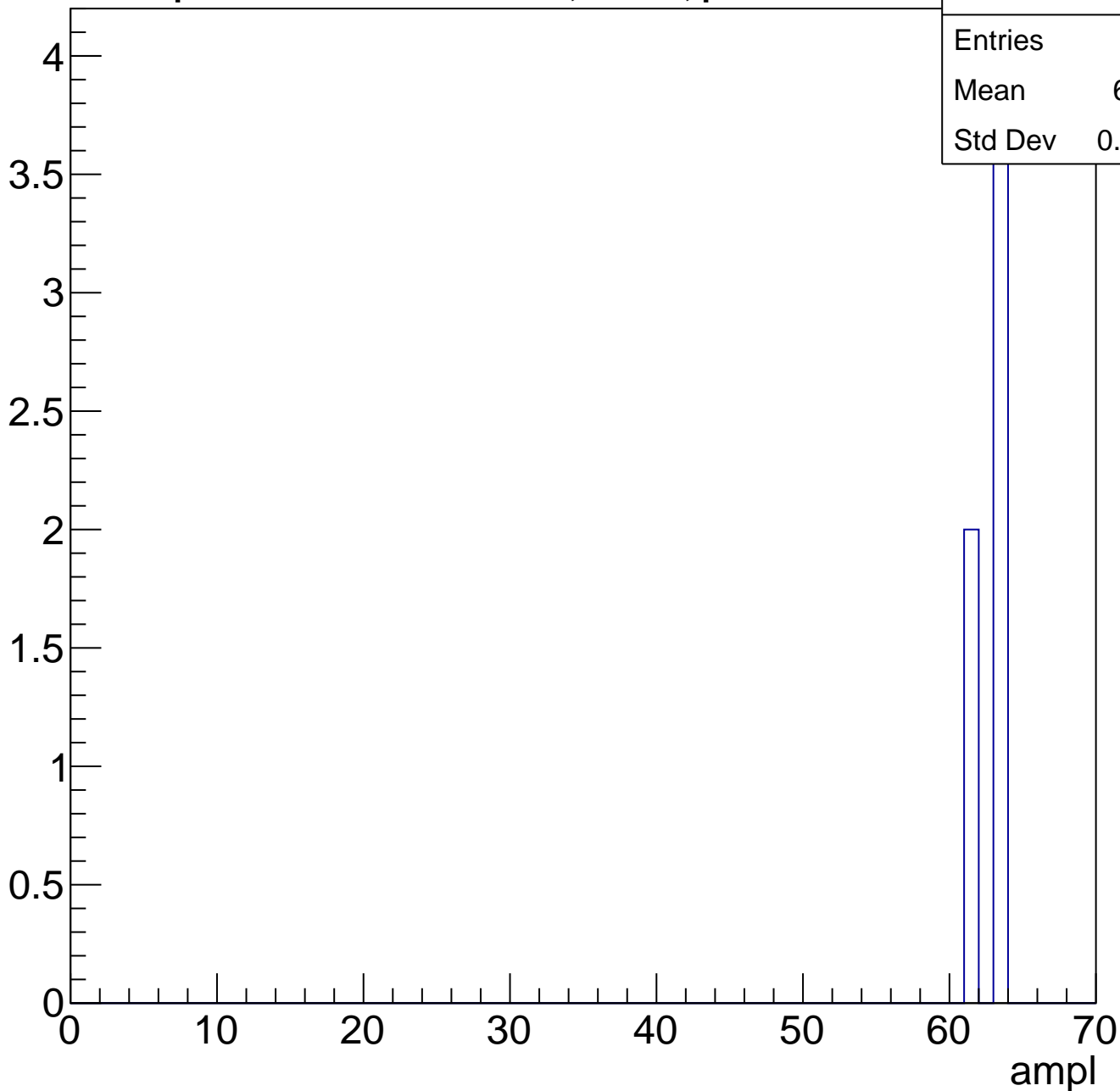
Entries	34
Mean	58.62
Std Dev	10.42



B0L102S, U12-ch33, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch33, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch34, adc0

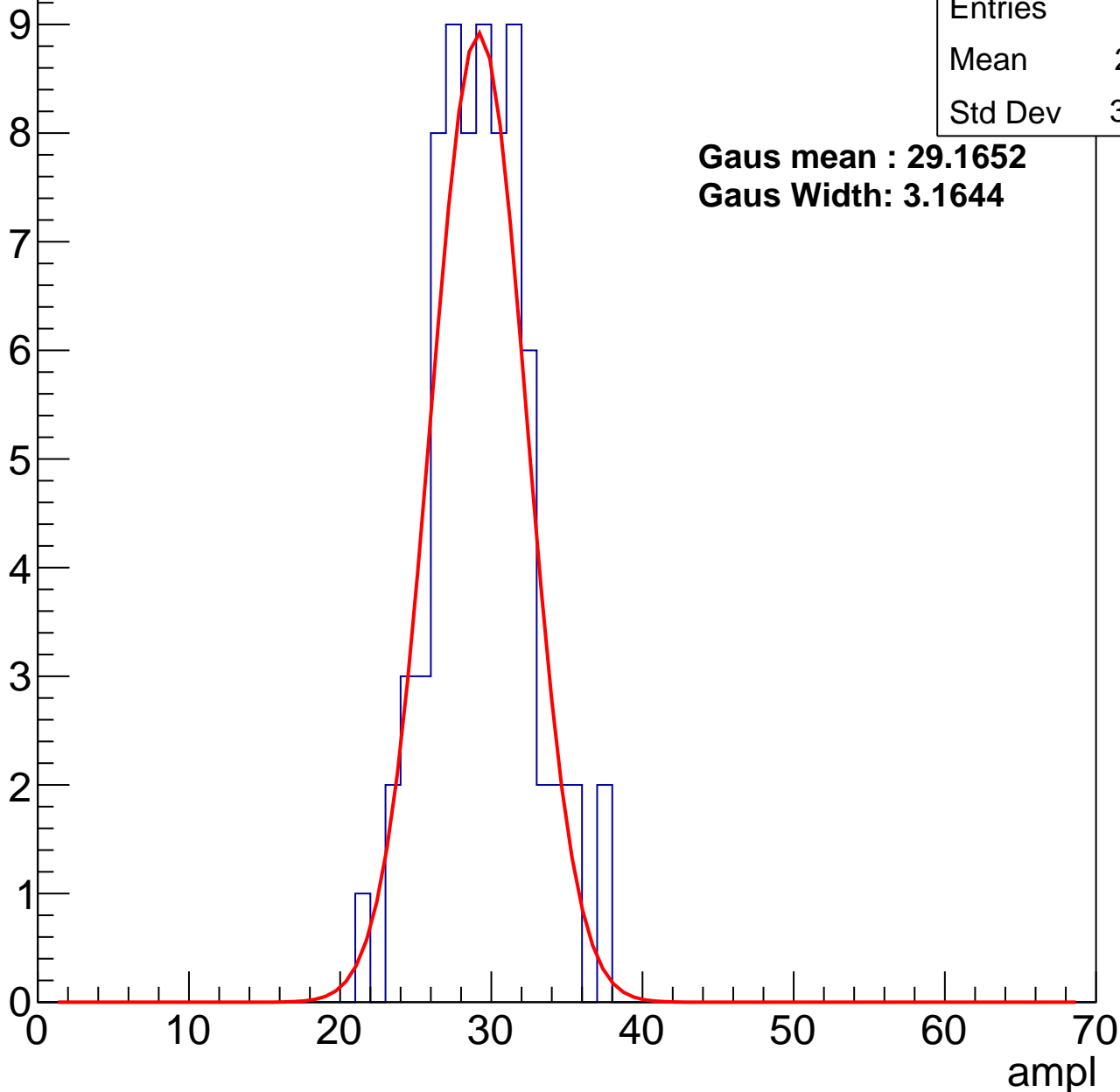
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	28.91
Std Dev	3.193

Gaus mean : 29.1652

Gaus Width: 3.1644



B0L102S, U12-ch34, adc1

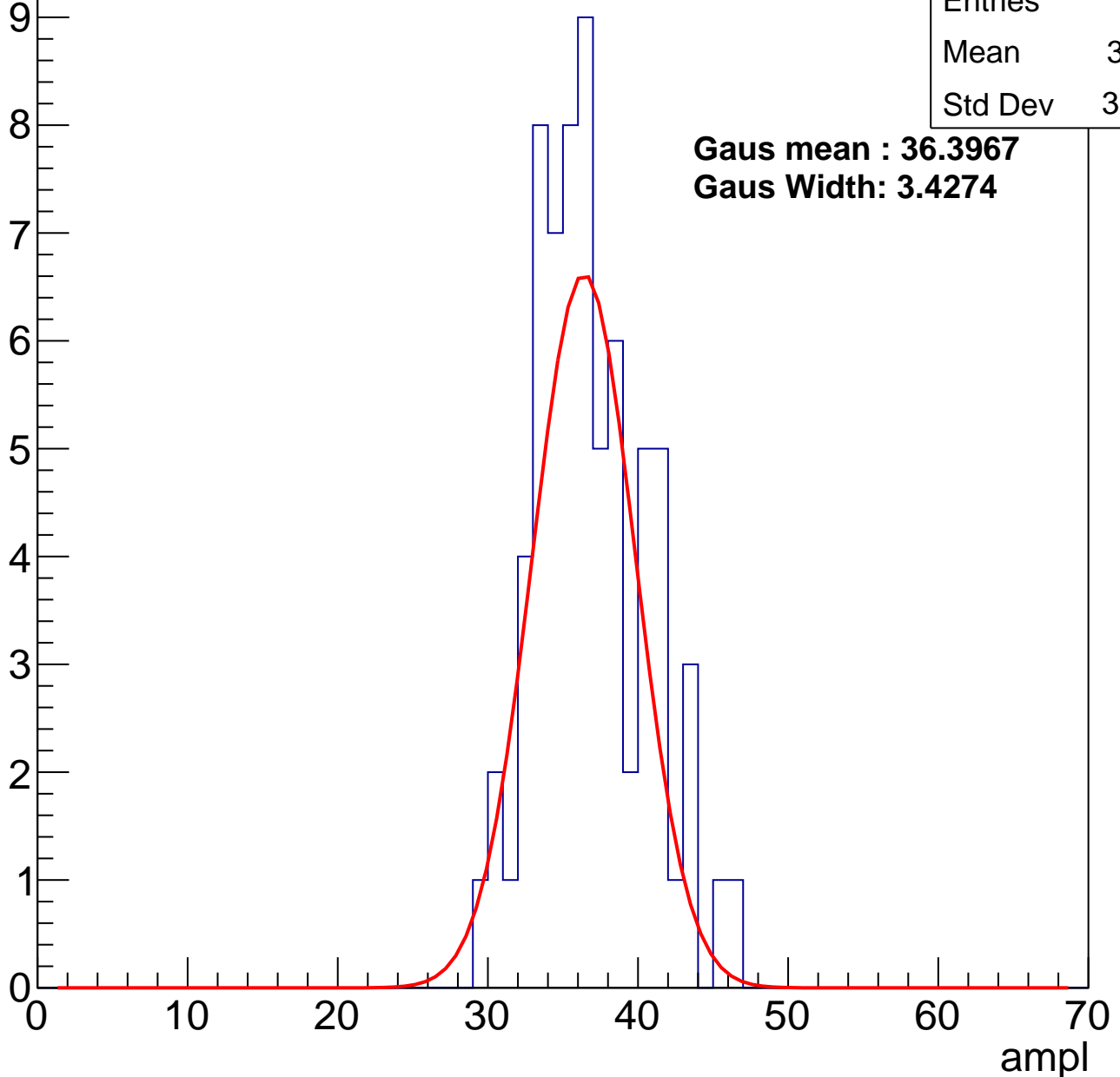
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.41
Std Dev	3.657

Gaus mean : 36.3967

Gaus Width: 3.4274



B0L102S, U12-ch34, adc2

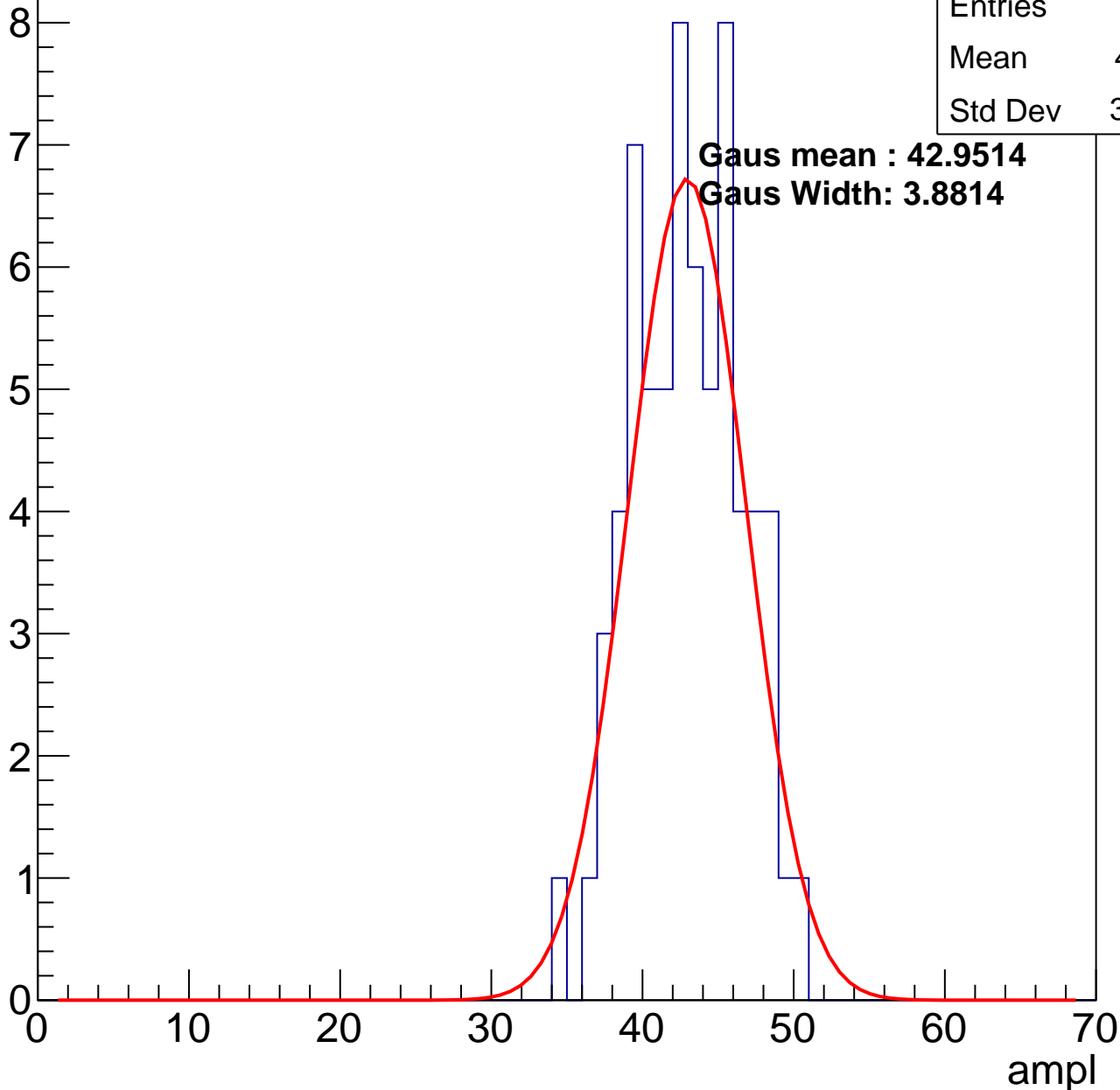
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	42.51
Std Dev	3.517

Gaus mean : 42.9514

Gaus Width: 3.8814

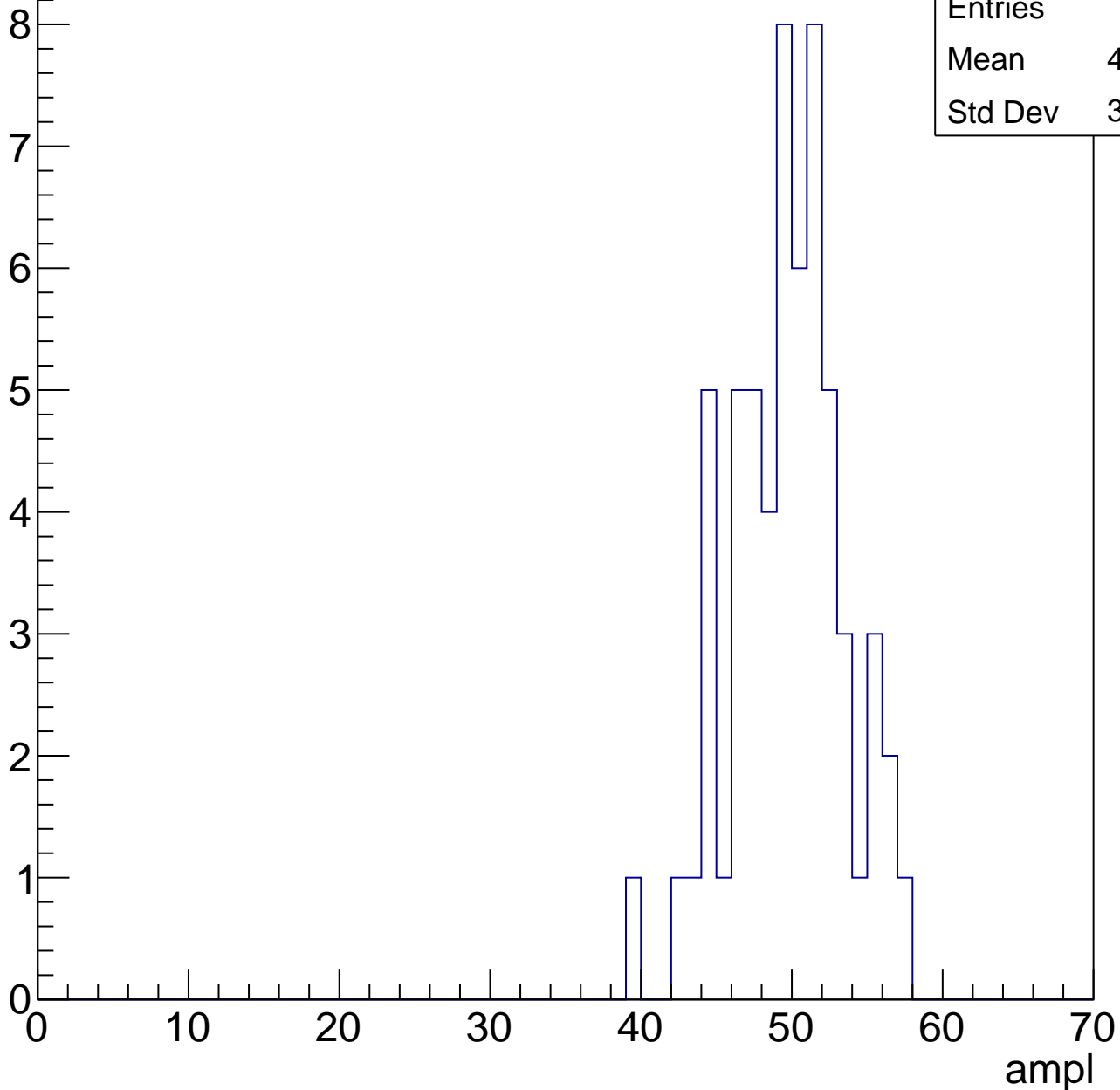


B0L102S, U12-ch34, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	49.22
Std Dev	3.693

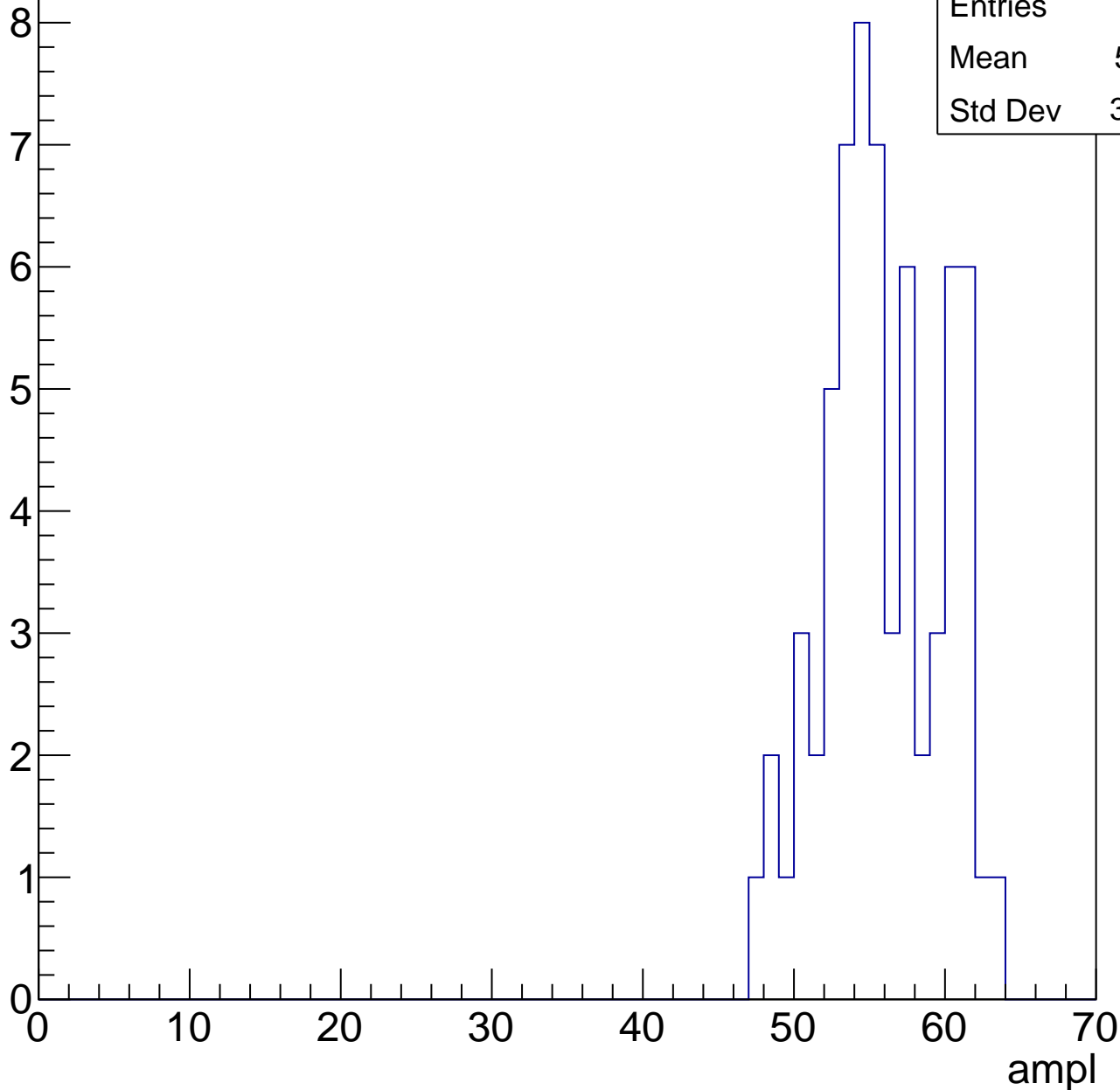


B0L102S, U12-ch34, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	55.41
Std Dev	3.848

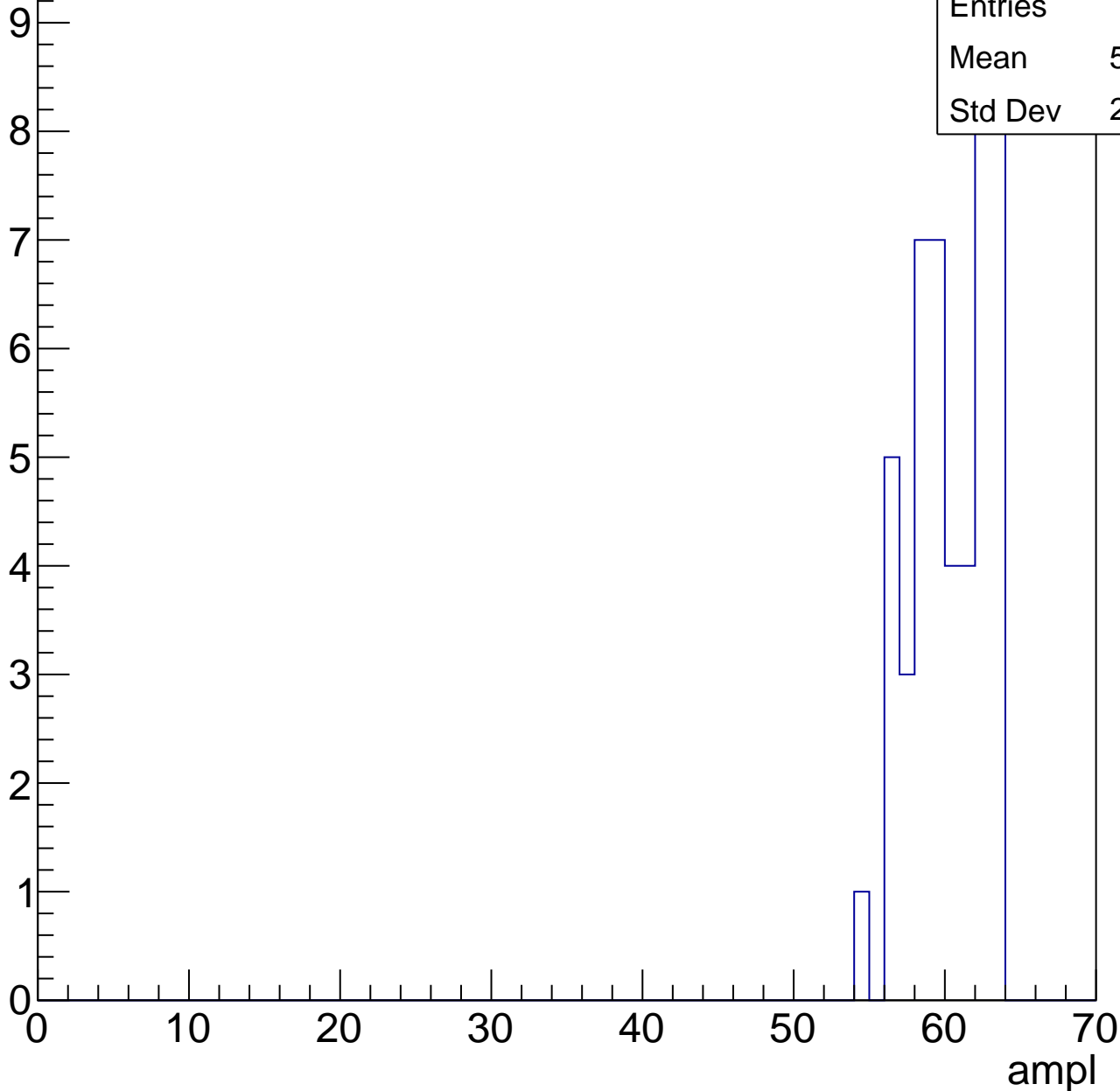


B0L102S, U12-ch34, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

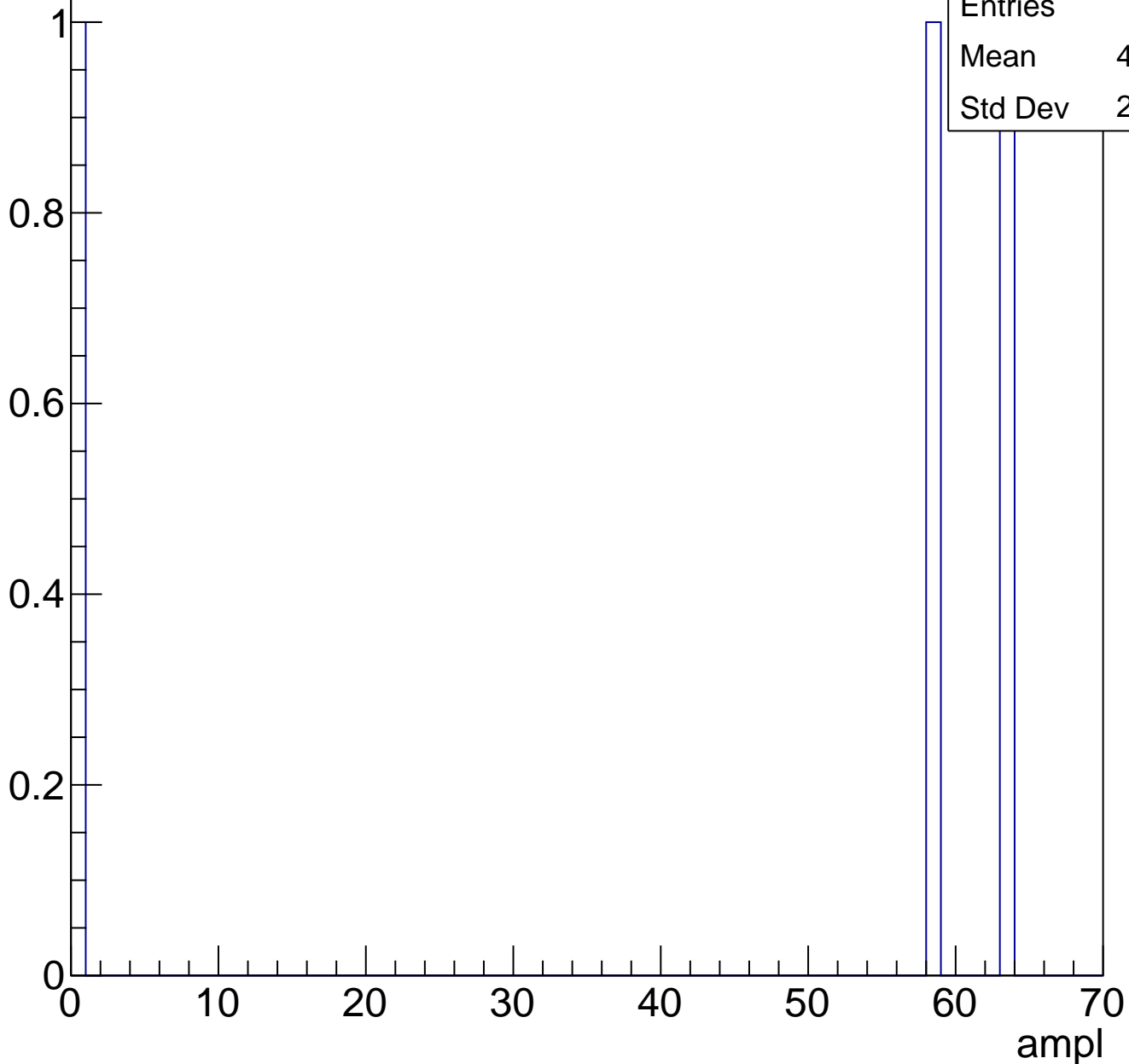
Entries	48
Mean	59.79
Std Dev	2.449



B0L102S, U12-ch34, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch34, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch35, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	64
Mean	30.84
Std Dev	3.289

Gaus mean : 31.2180

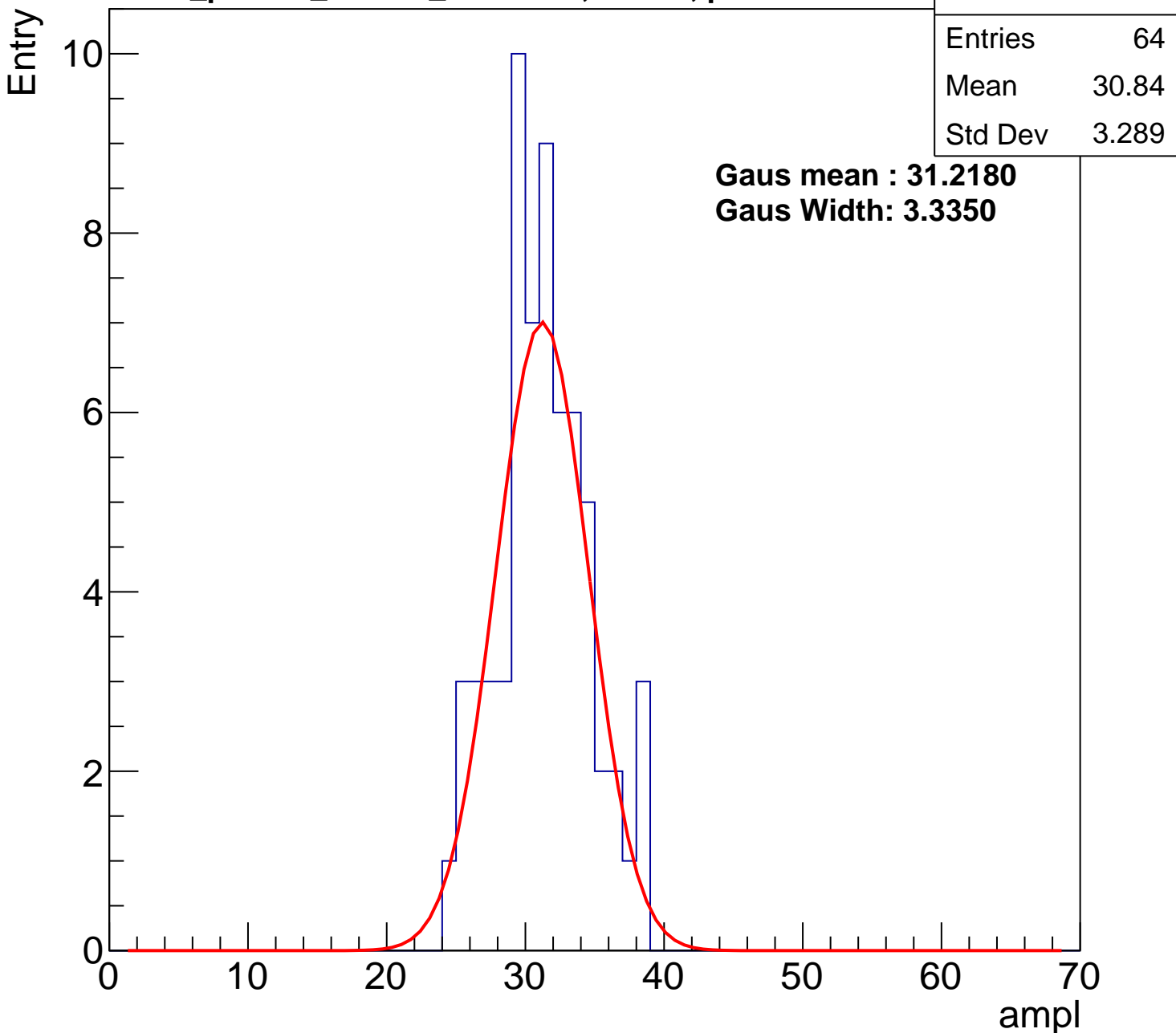
Gaus Width: 3.3350

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



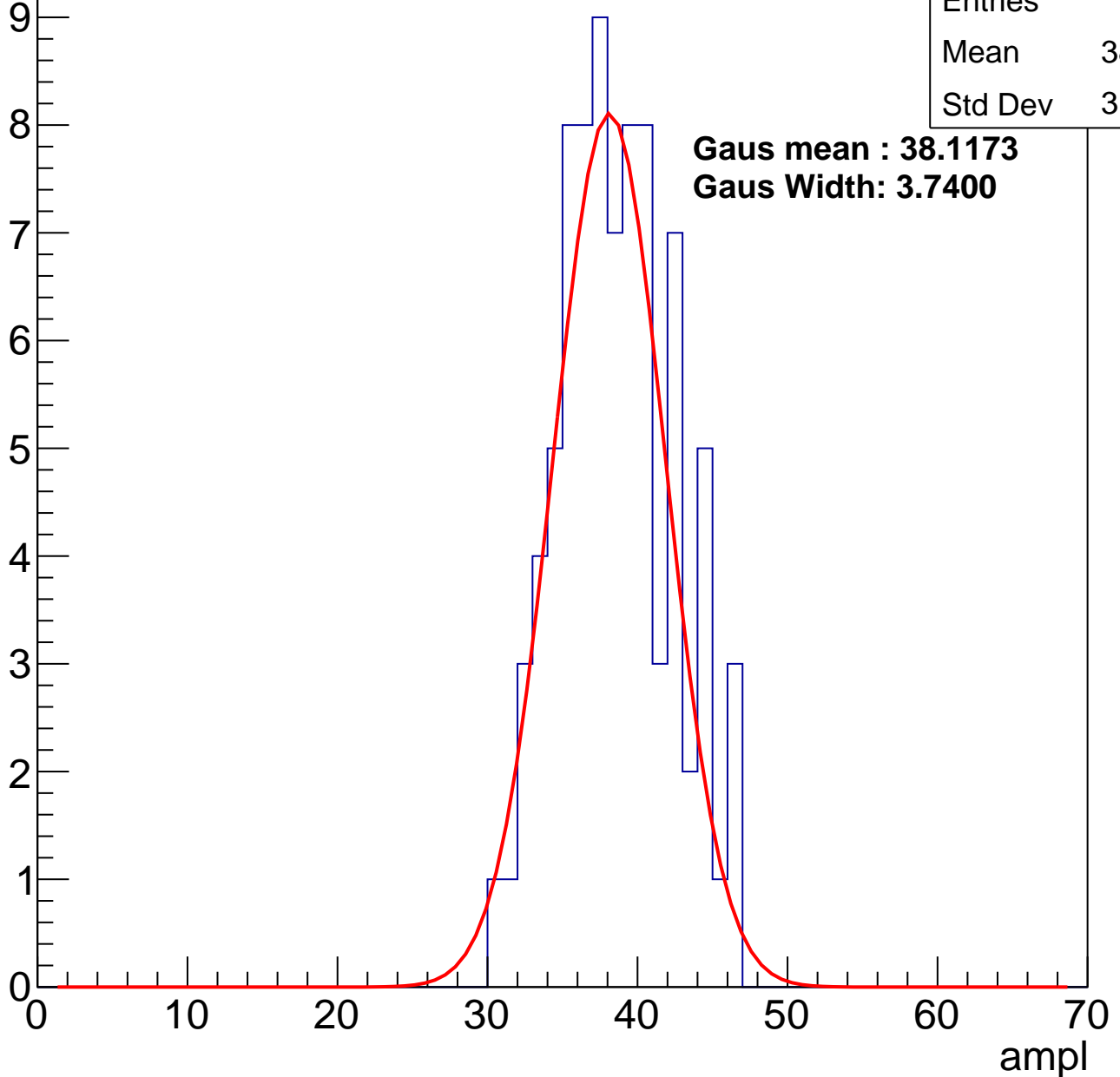
B0L102S, U12-ch35, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	38.12
Std Dev	3.743

Gaus mean : 38.1173
Gaus Width: 3.7400



B0L102S, U12-ch35, adc2

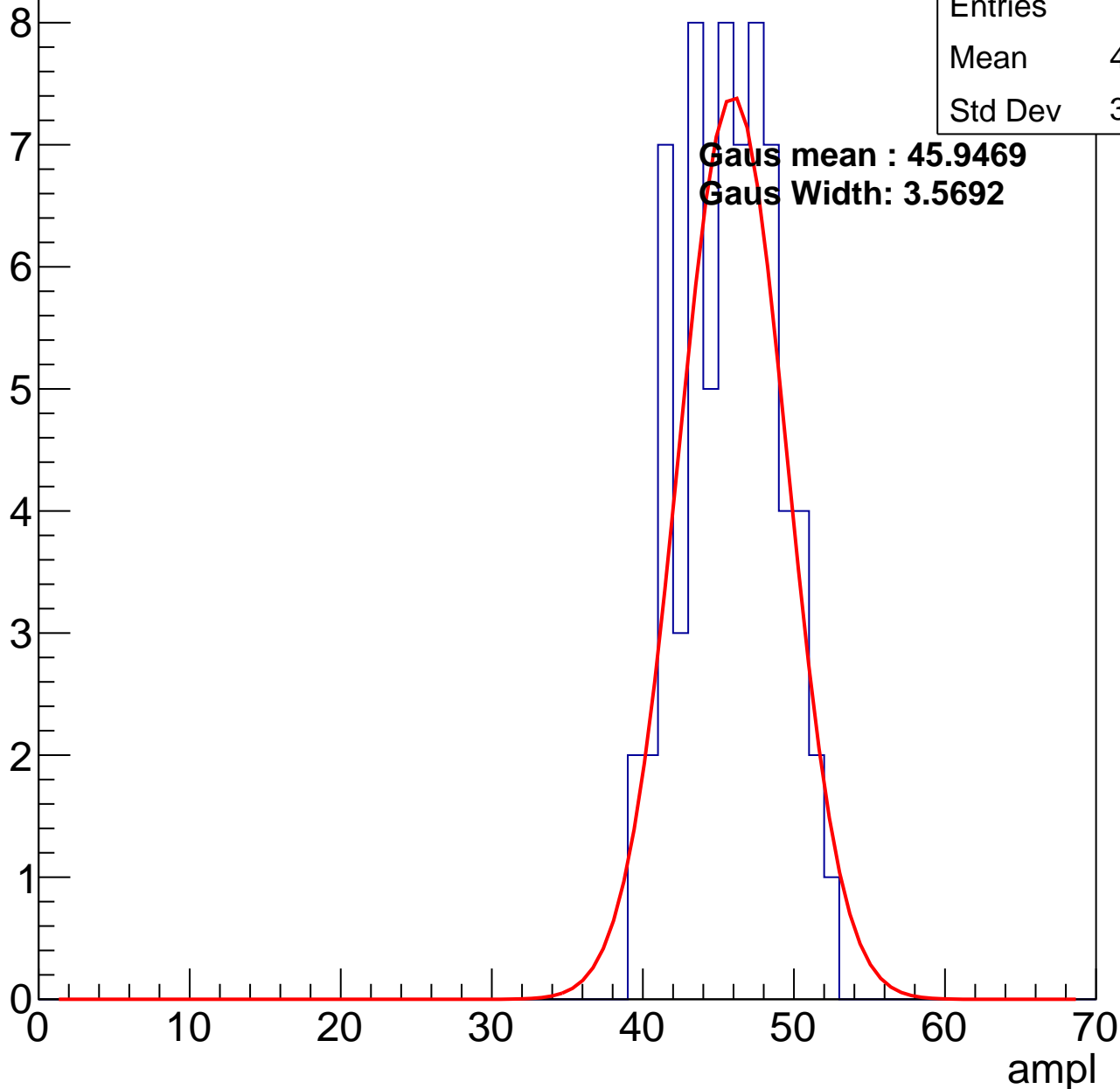
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	45.28
Std Dev	3.162

Gaus mean : 45.9469

Gaus Width: 3.5692

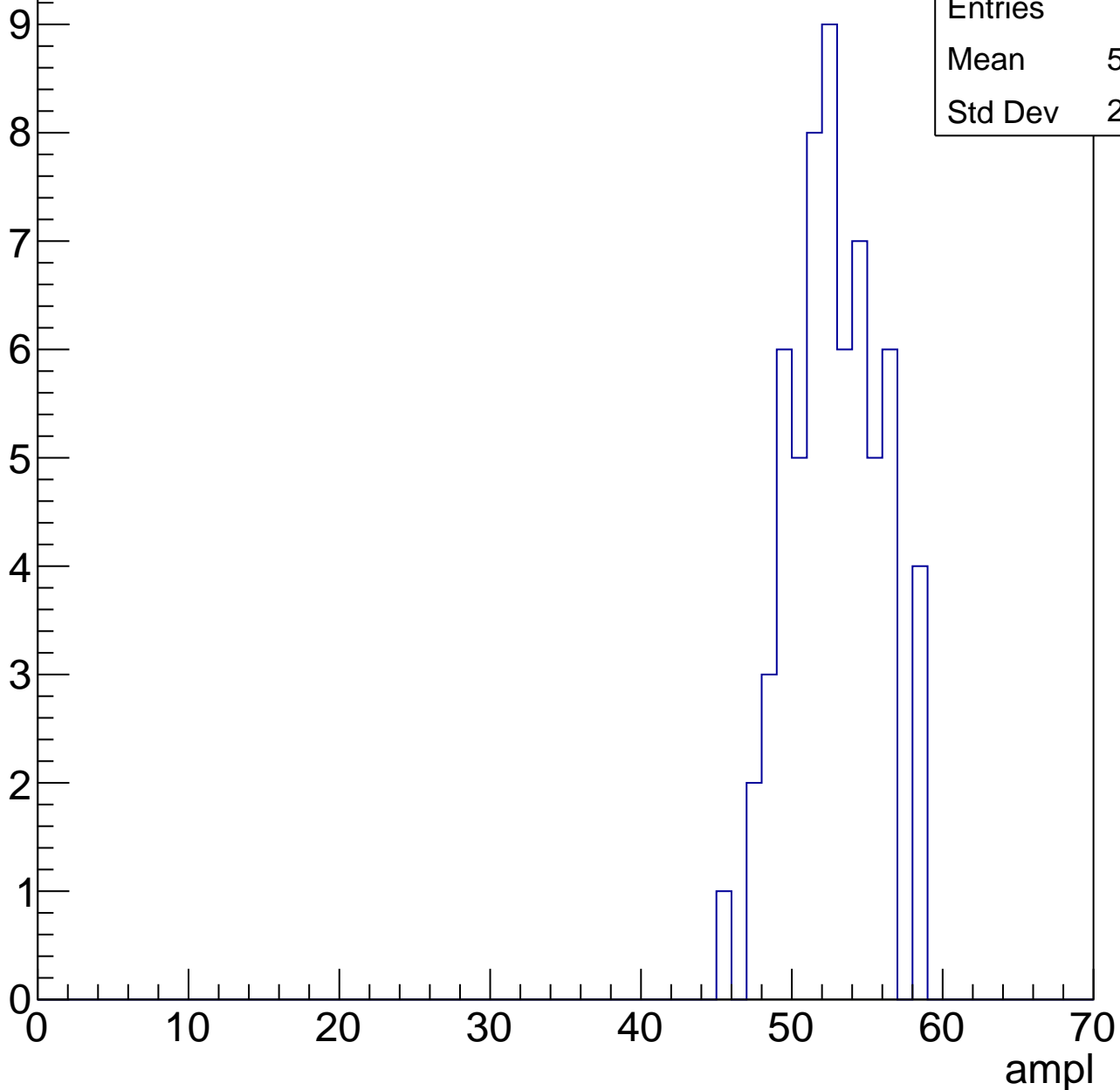


B0L102S, U12-ch35, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	52.29
Std Dev	2.959

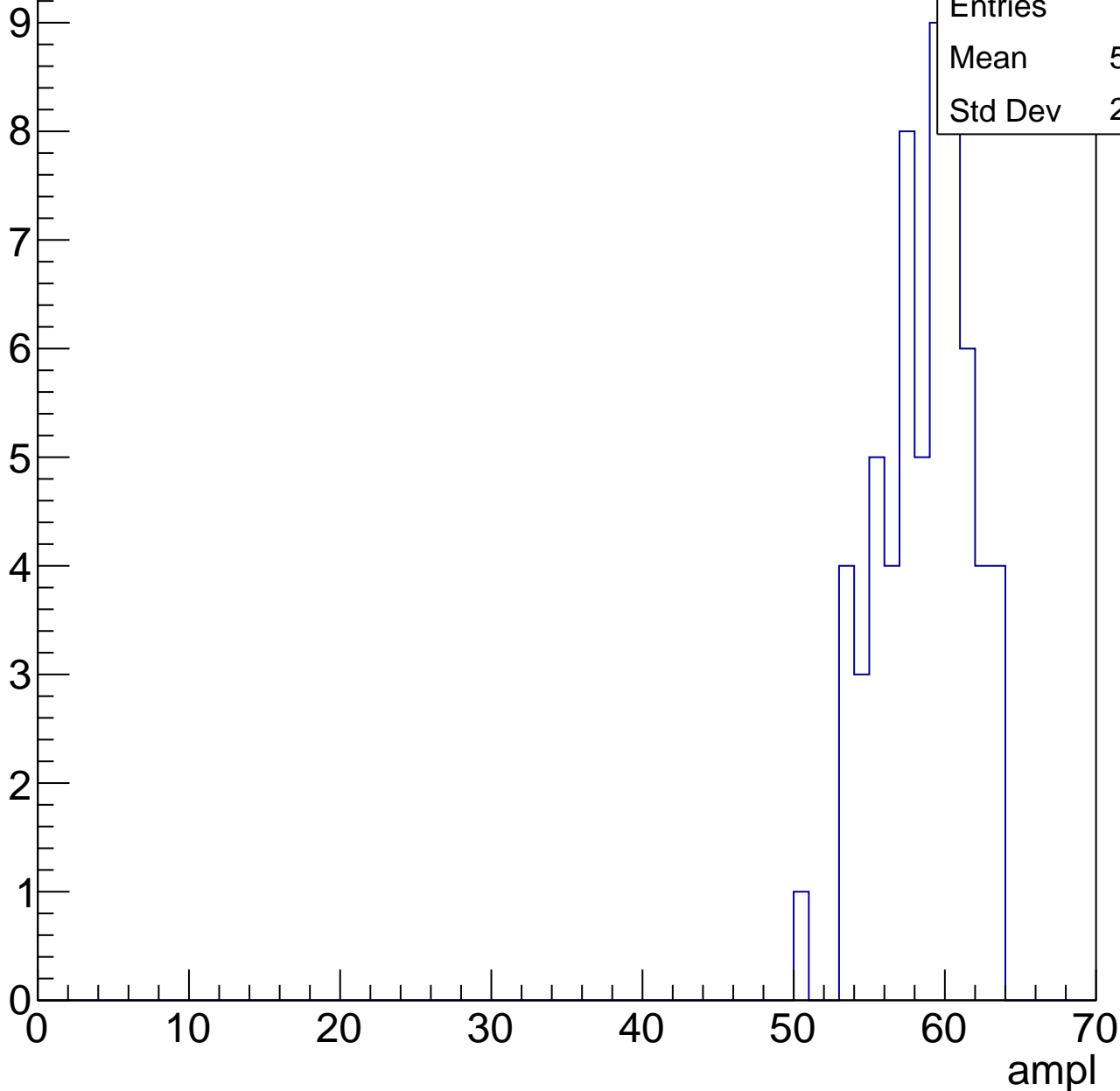


B0L102S, U12-ch35, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	58.13
Std Dev	2.972

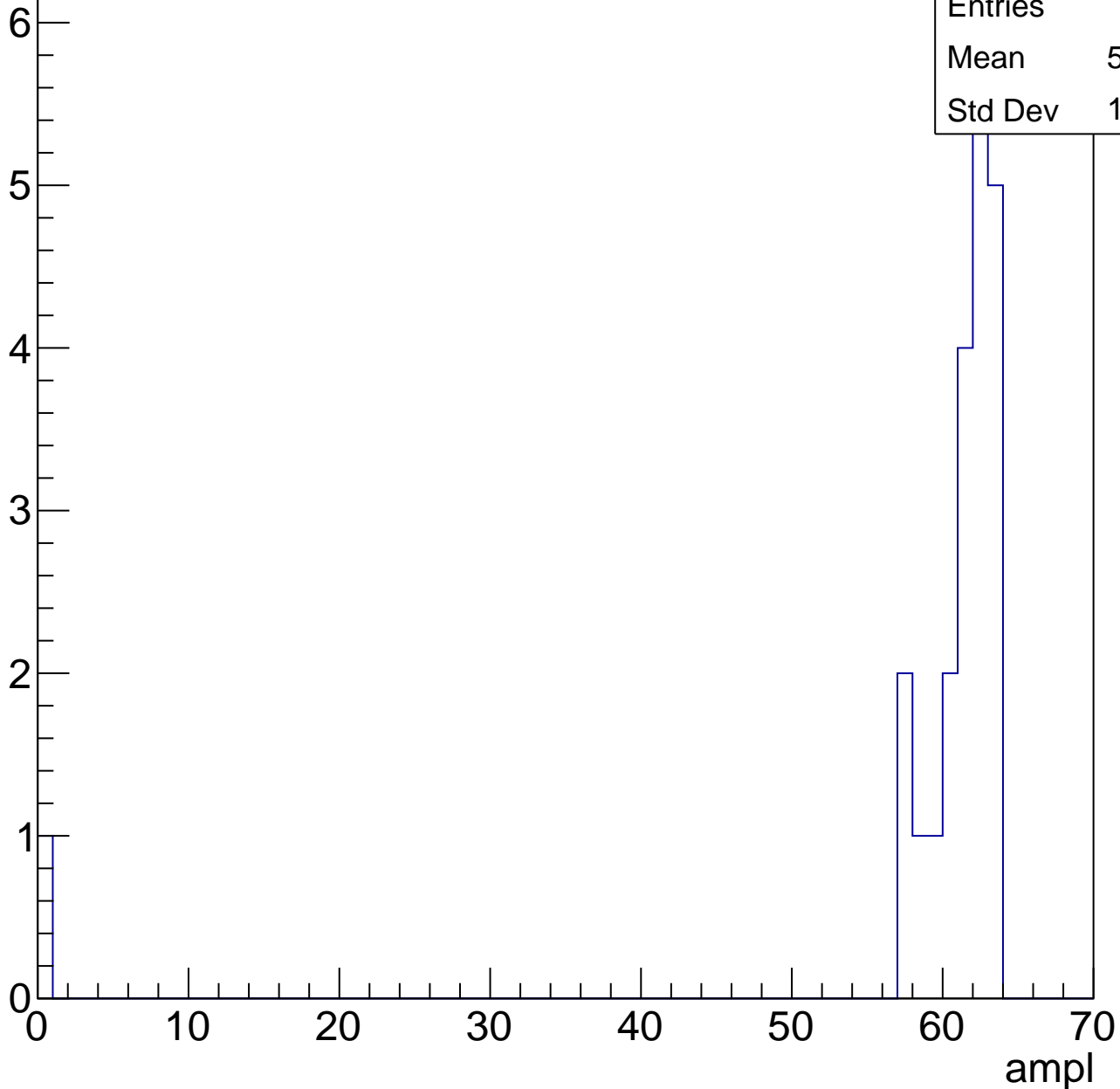


B0L102S, U12-ch35, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	22
Mean	58.27
Std Dev	12.85



B0L102S, U12-ch35, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L102S, U12-ch35, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch36, adc0

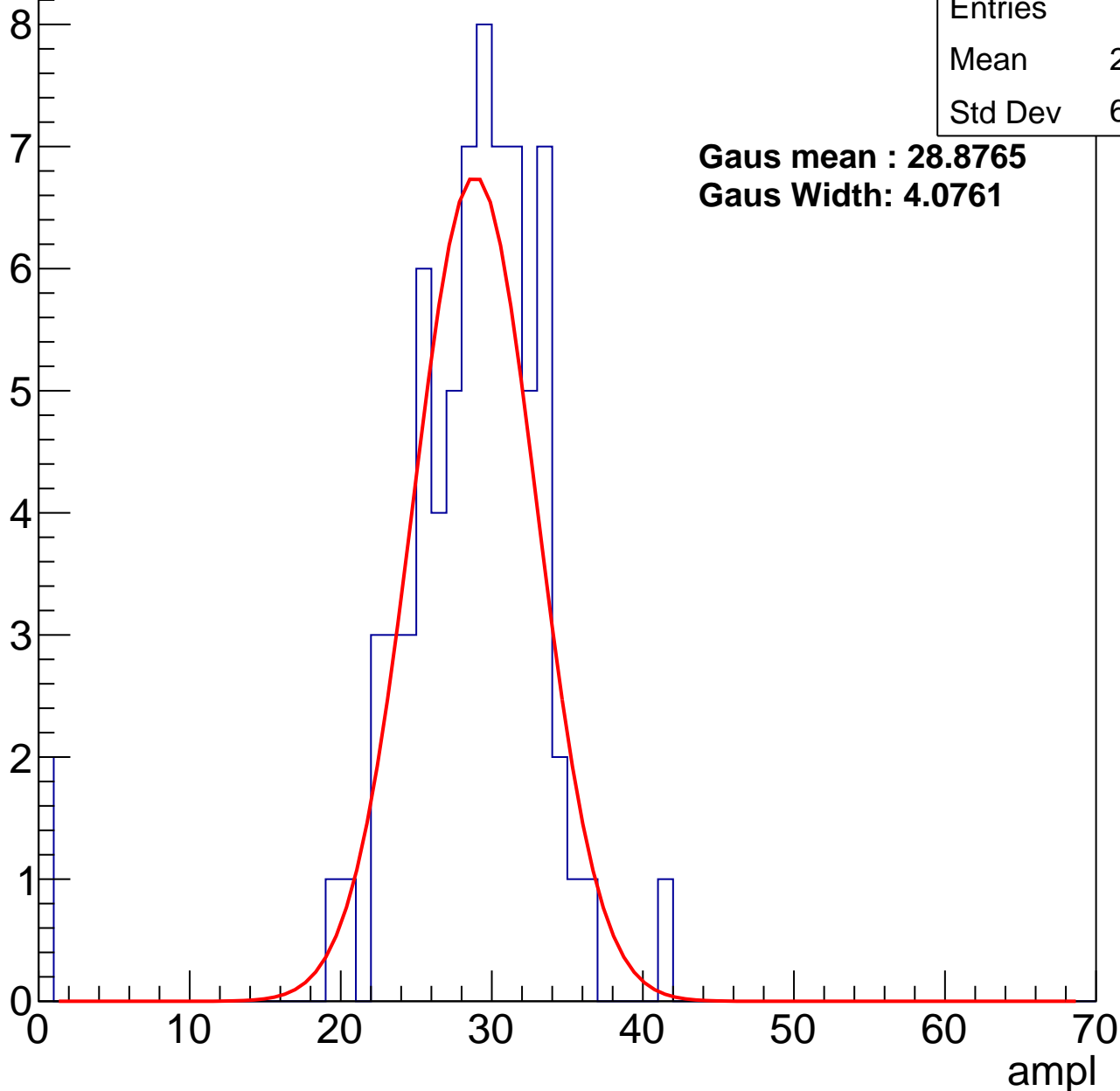
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	27.85
Std Dev	6.069

Gaus mean : 28.8765

Gaus Width: 4.0761



B0L102S, U12-ch36, adc1

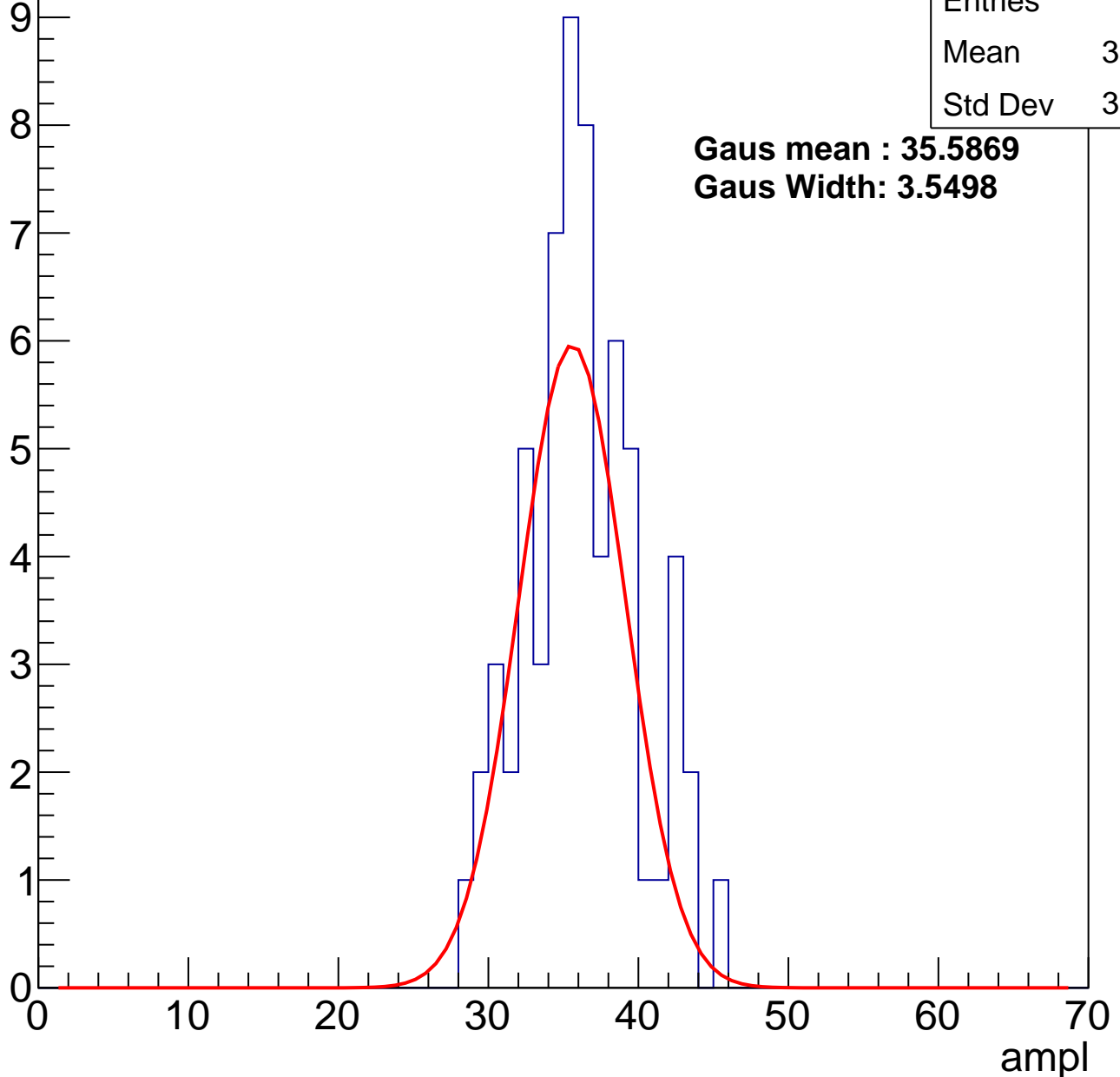
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.77
Std Dev	3.745

Gaus mean : 35.5869

Gaus Width: 3.5498



B0L102S, U12-ch36, adc2

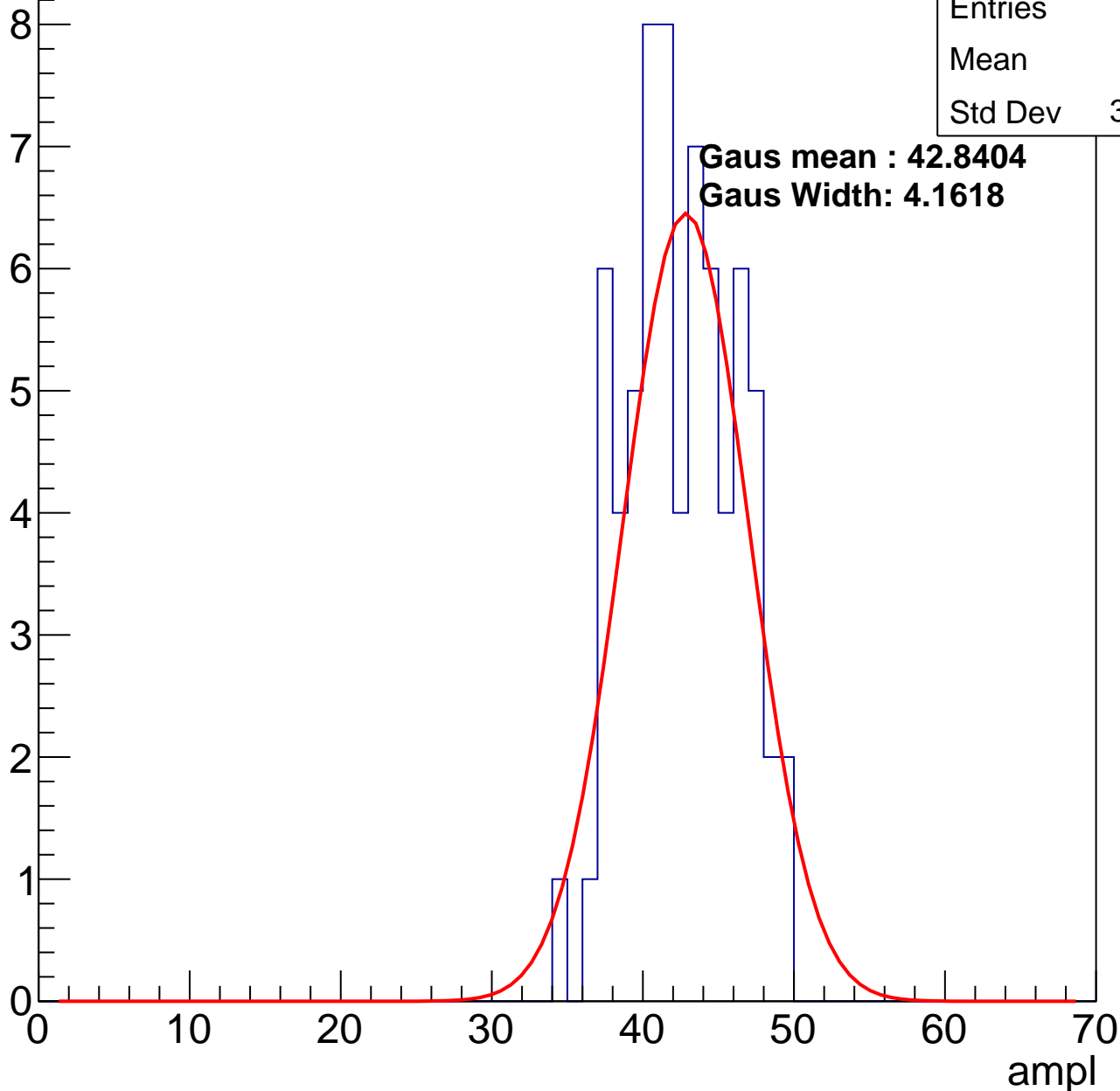
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	42.1
Std Dev	3.523

Gaus mean : 42.8404

Gaus Width: 4.1618

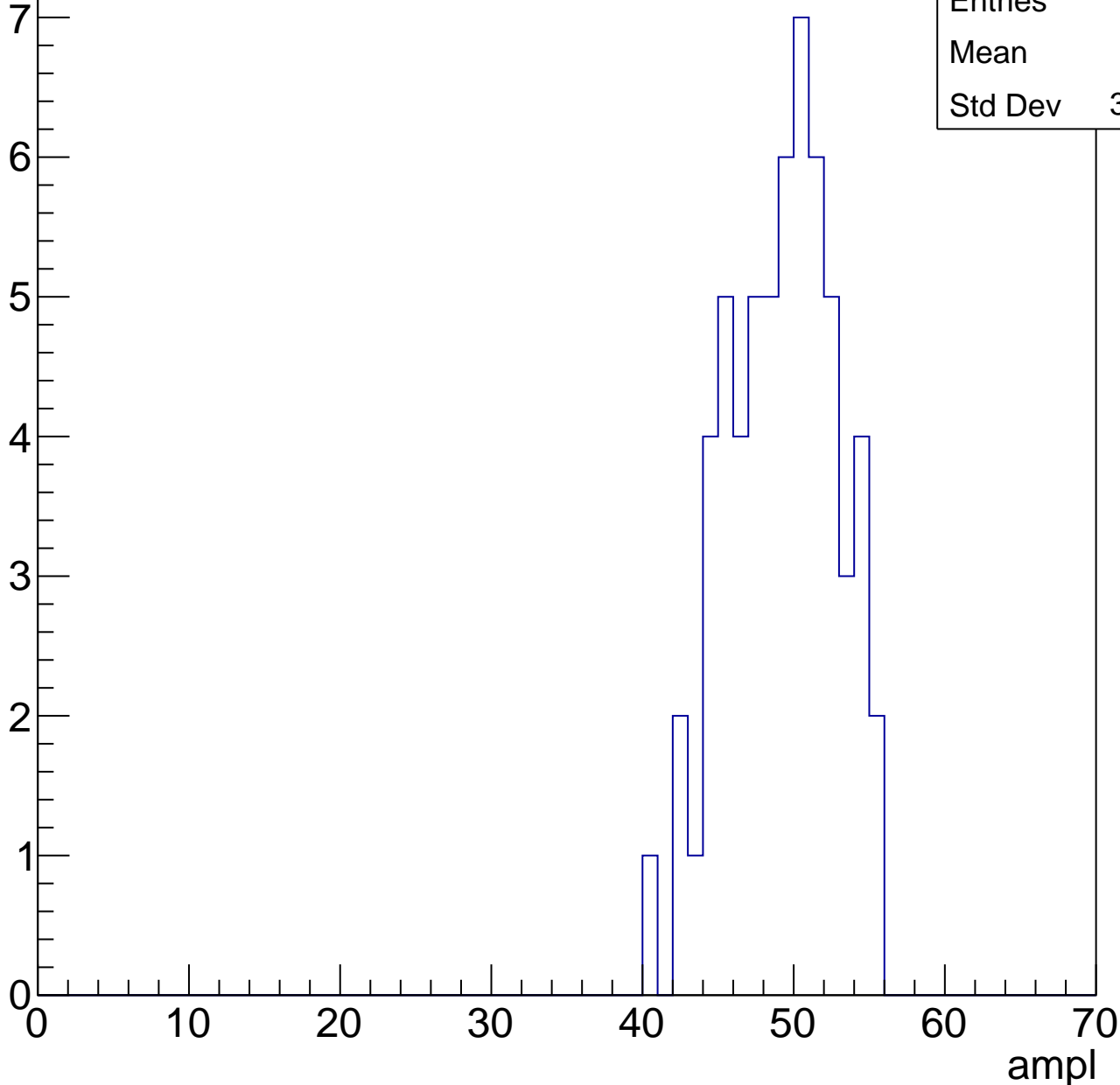


B0L102S, U12-ch36, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	48.7
Std Dev	3.528

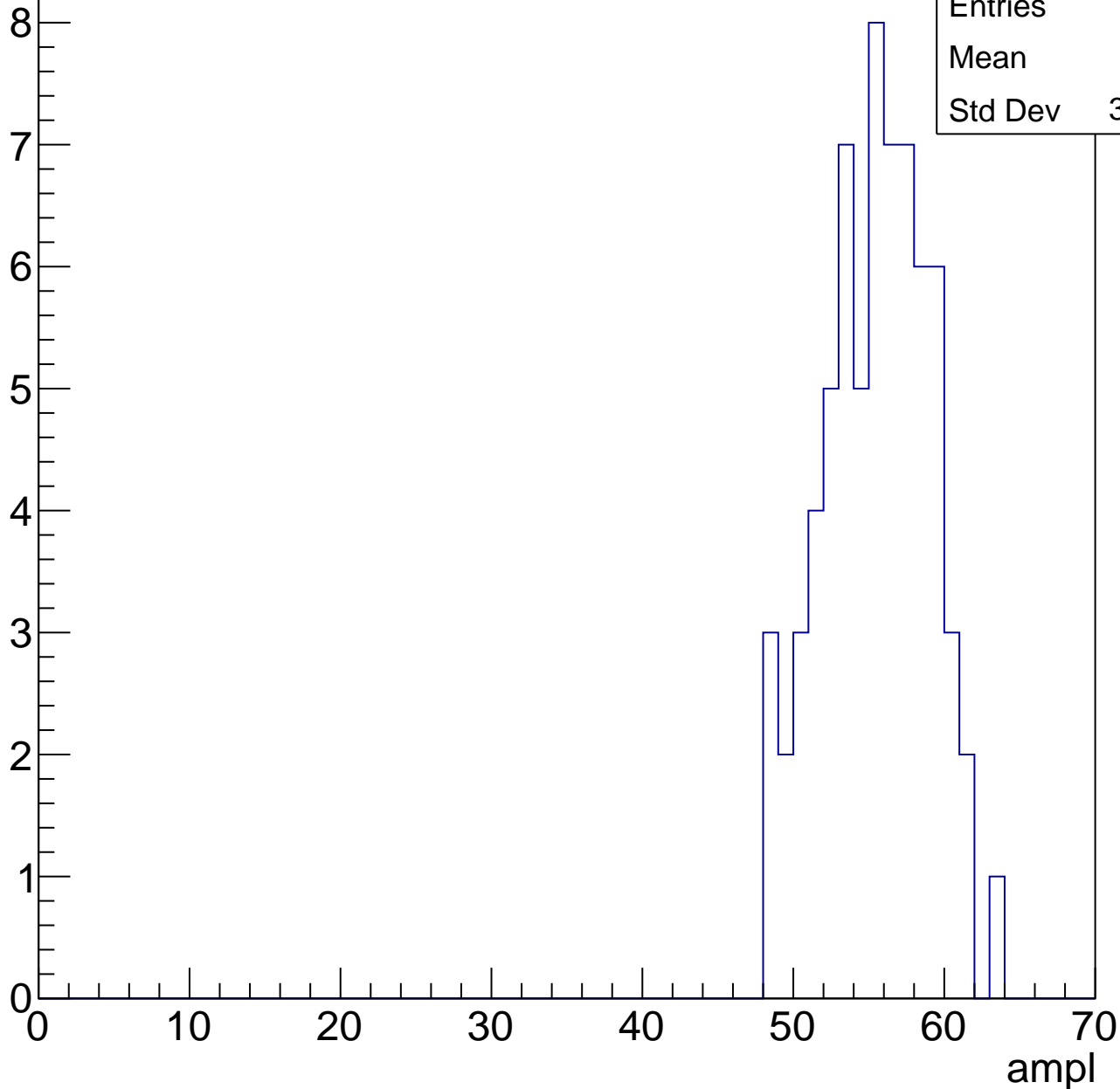


B0L102S, U12-ch36, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	55
Std Dev	3.472

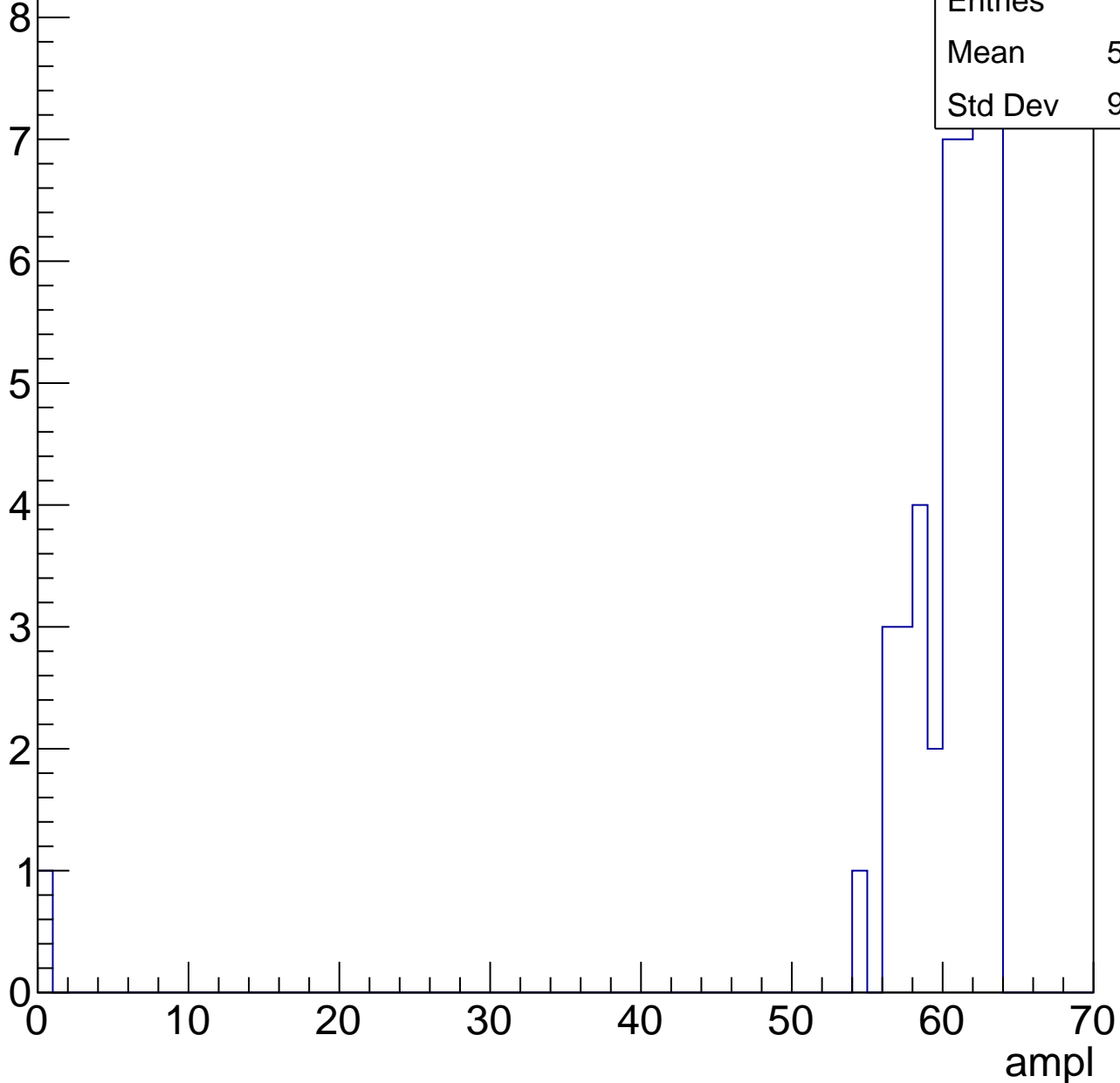


B0L102S, U12-ch36, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

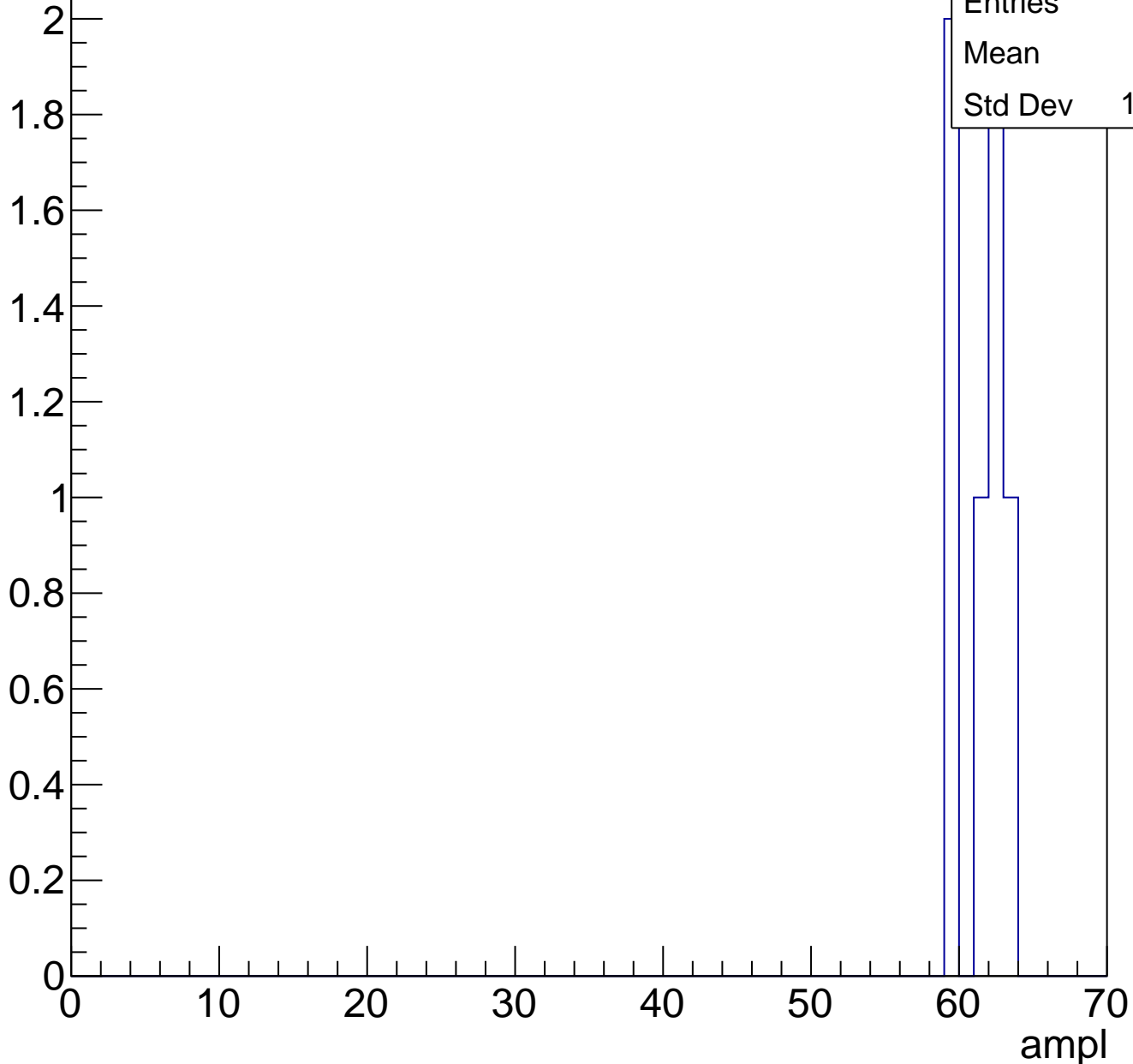
Entries	44
Mean	58.86
Std Dev	9.273



B0L102S, U12-ch36, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch36, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch37, adc0

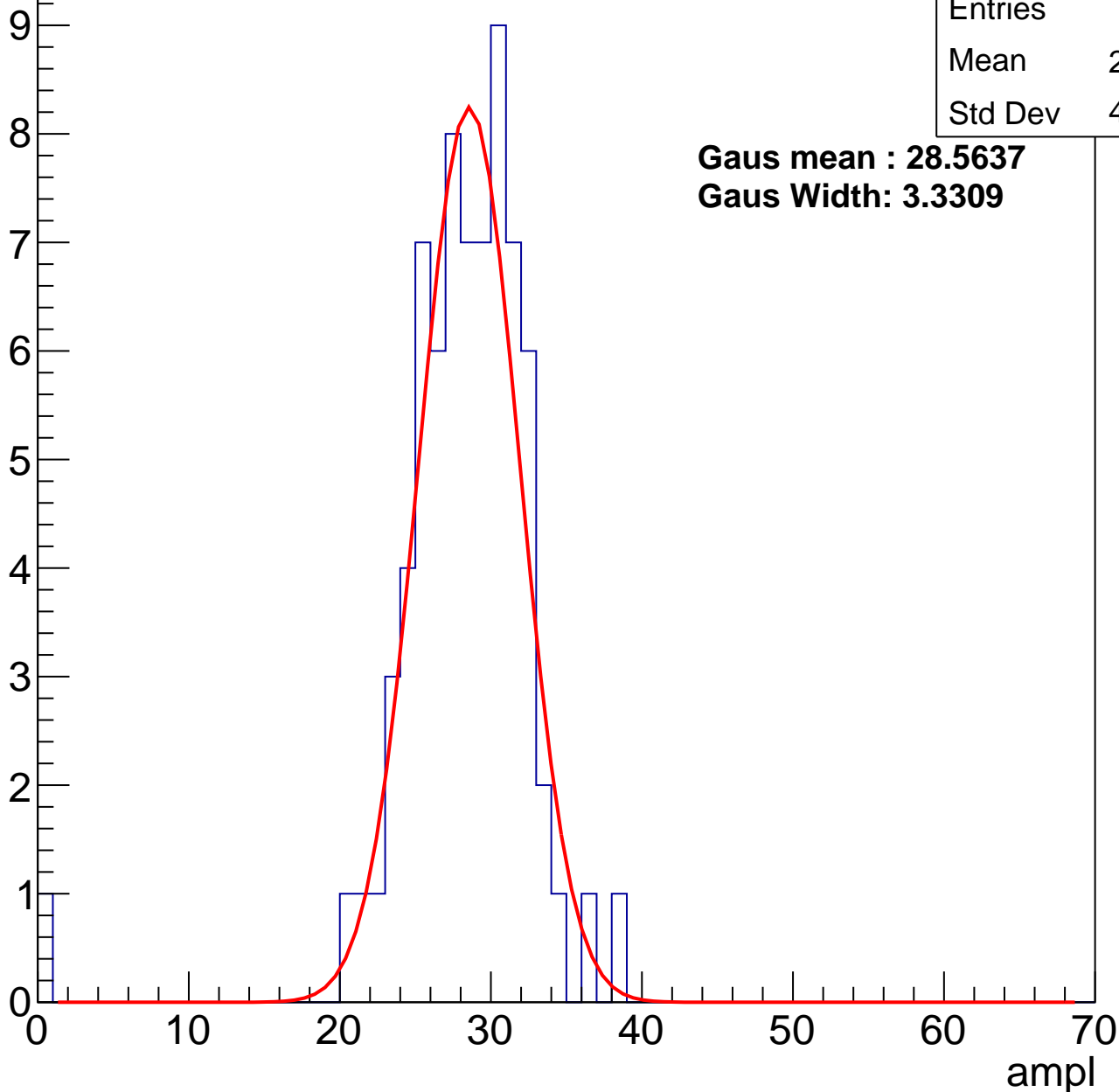
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	27.77
Std Dev	4.704

Gaus mean : 28.5637

Gaus Width: 3.3309



B0L102S, U12-ch37, adc1

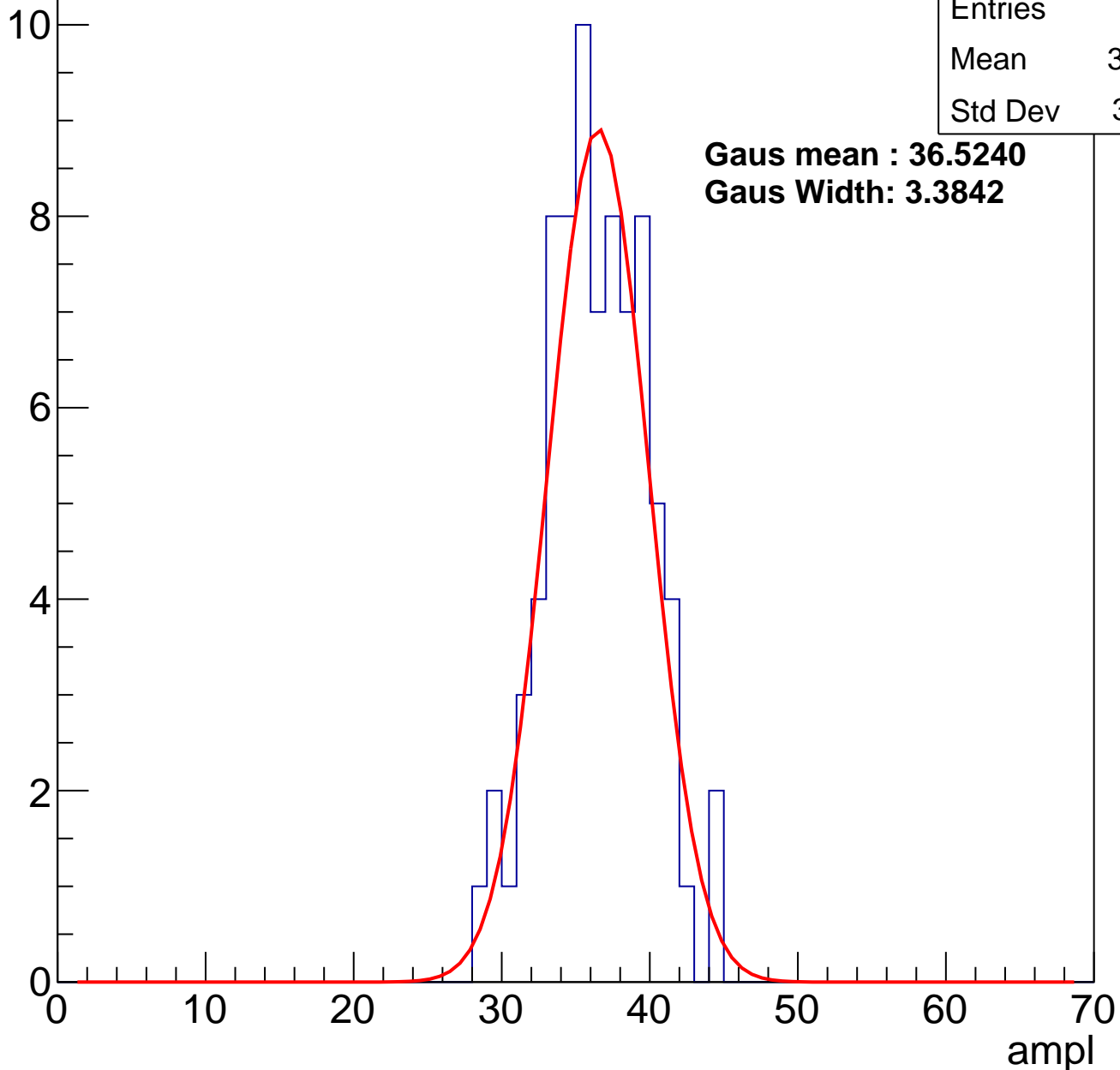
calib_packv5_042523_0143.root, FC#12, port B1

Entries	79
Mean	35.99
Std Dev	3.381

Gaus mean : 36.5240

Gaus Width: 3.3842

Entry



B0L102S, U12-ch37, adc2

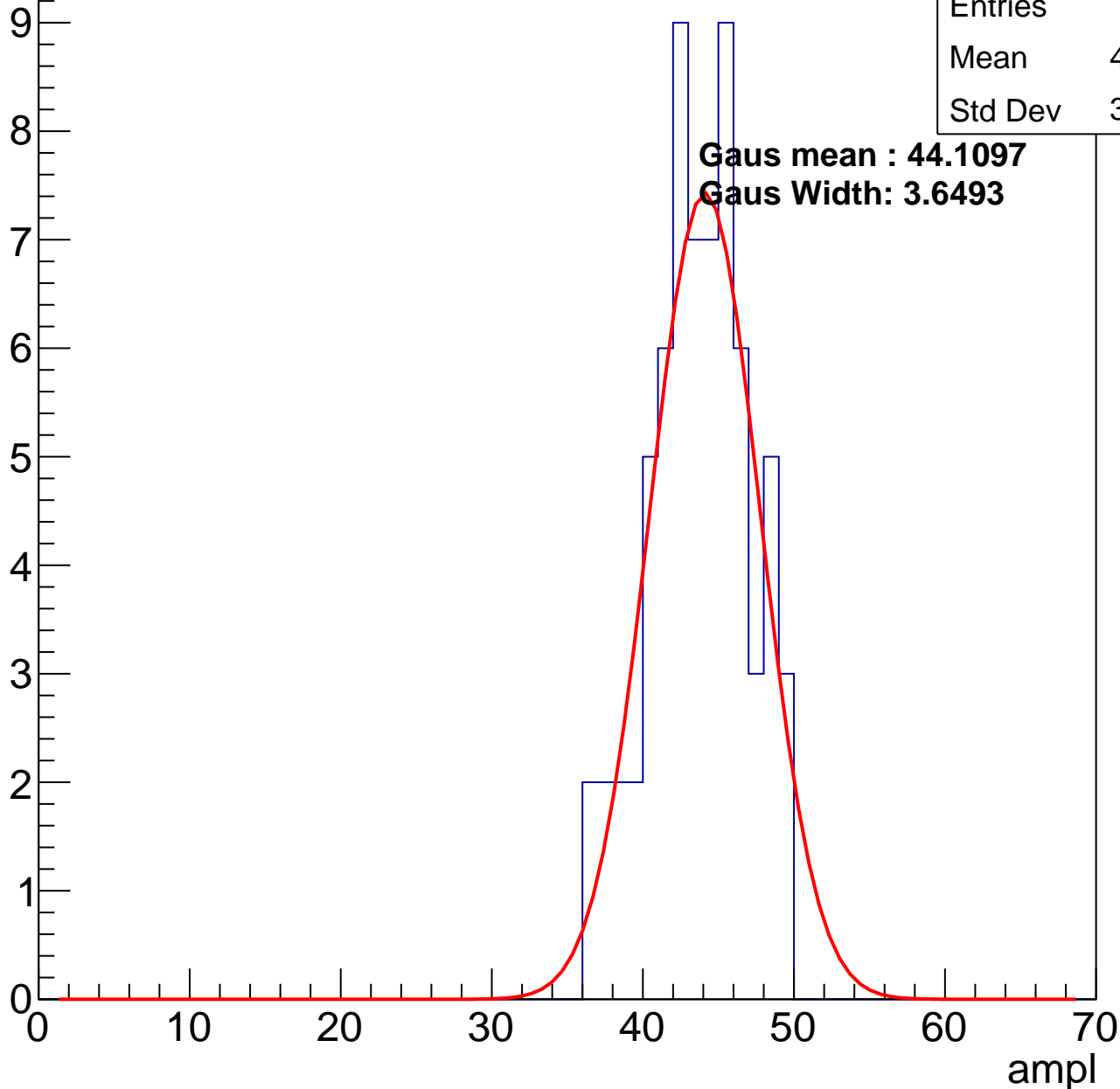
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	43.26
Std Dev	3.216

Gaus mean : 44.1097

Gaus Width: 3.6493

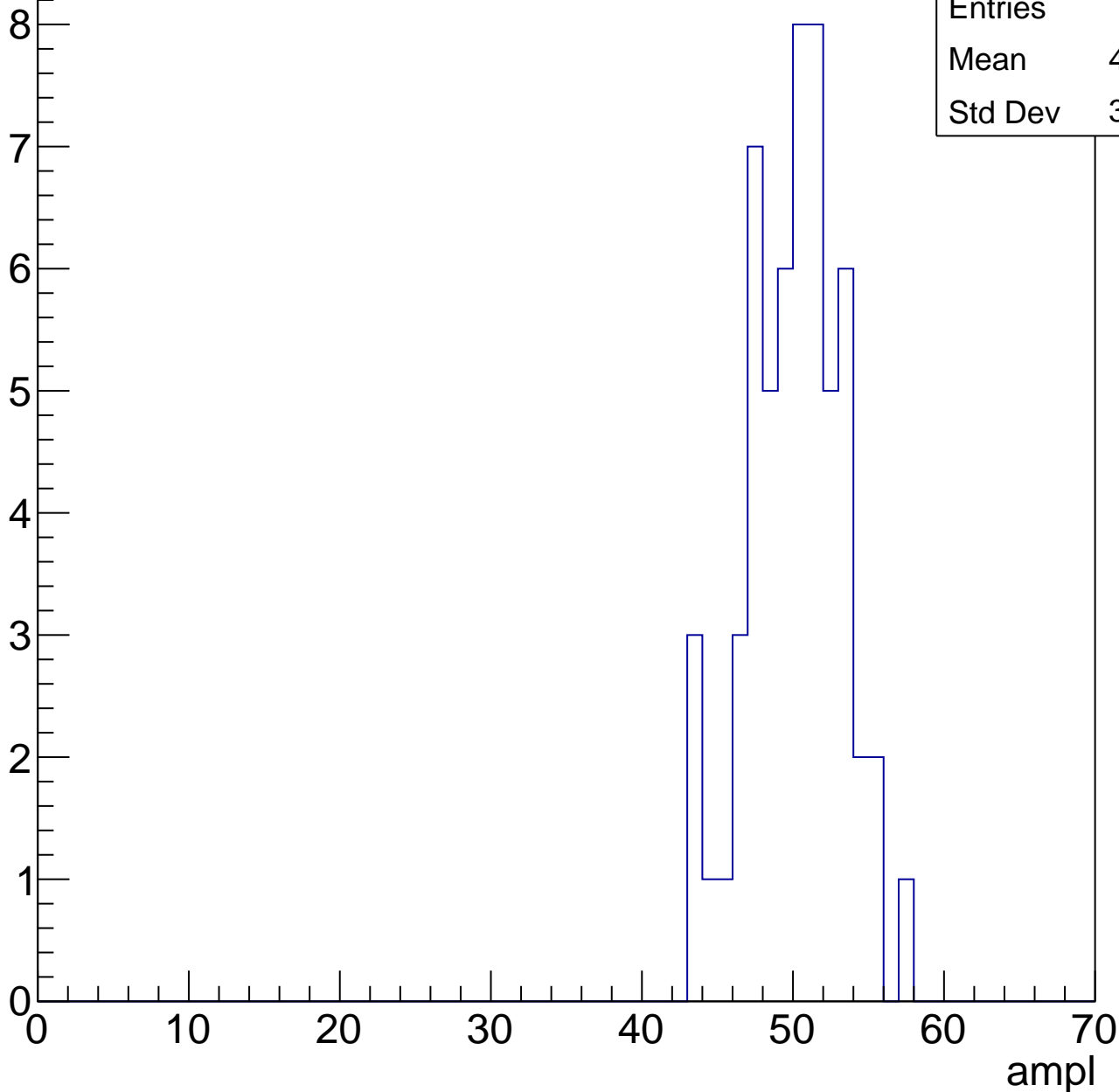


B0L102S, U12-ch37, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	49.66
Std Dev	3.082



B0L102S, U12-ch37, adc4

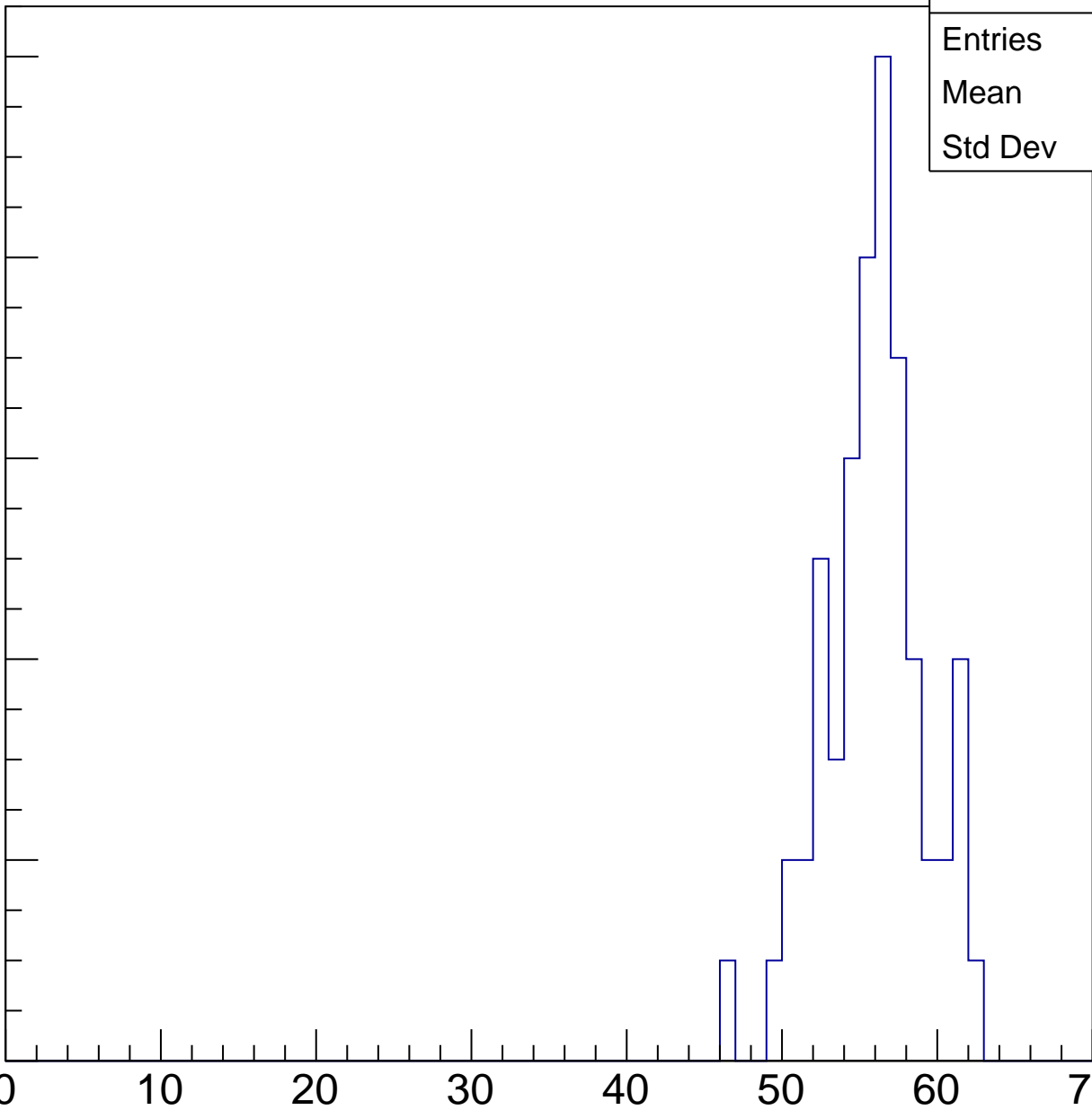
calib_packv5_042523_0143.root, FC#12, port B1

Entry

10
8
6
4
2
0

Entries	58
Mean	55.43
Std Dev	3.217

ampl

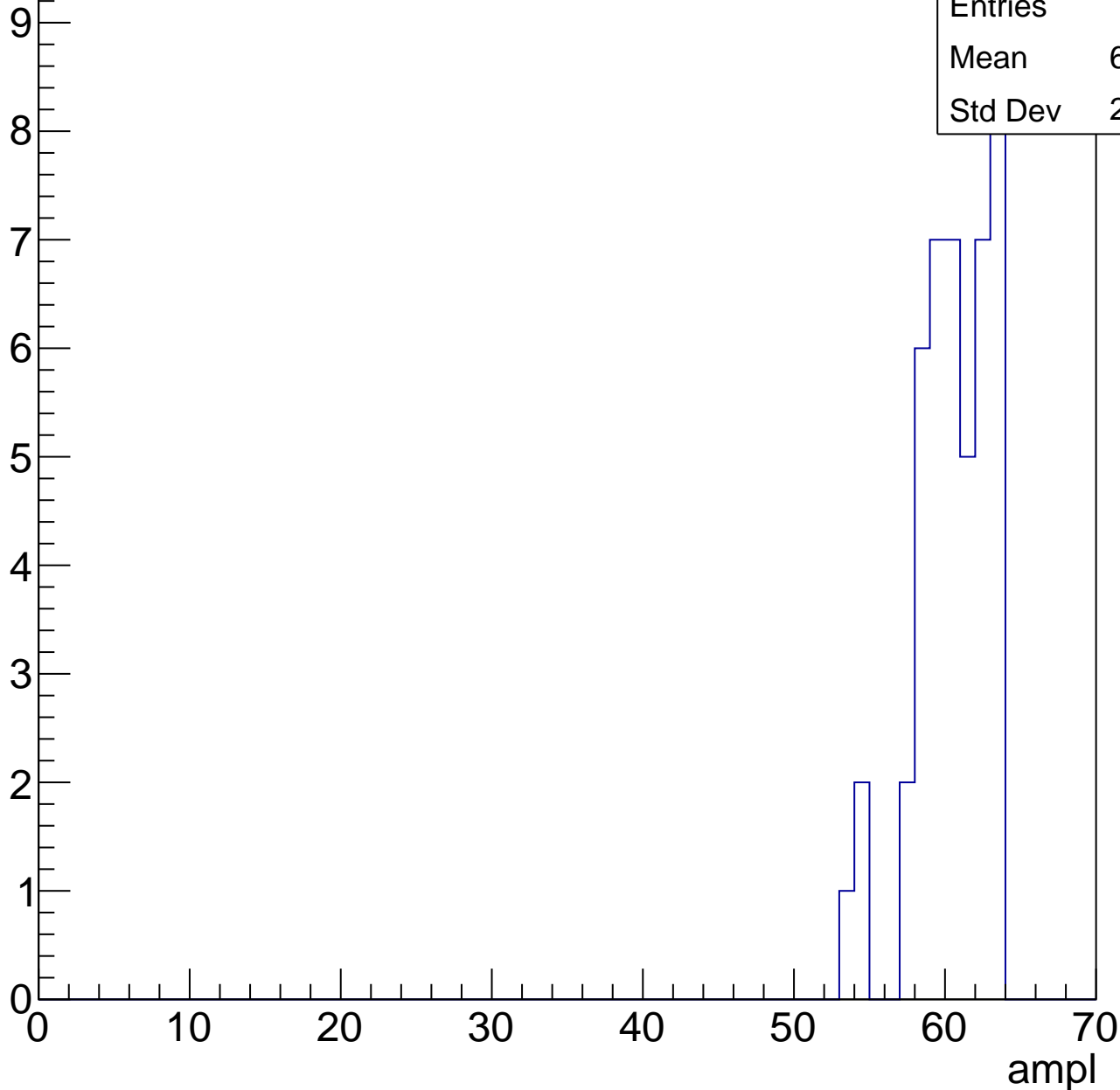


B0L102S, U12-ch37, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	60.04
Std Dev	2.484



B0L102S, U12-ch37, adc6

calib_packv5_042523_0143.root, FC#12, port B1

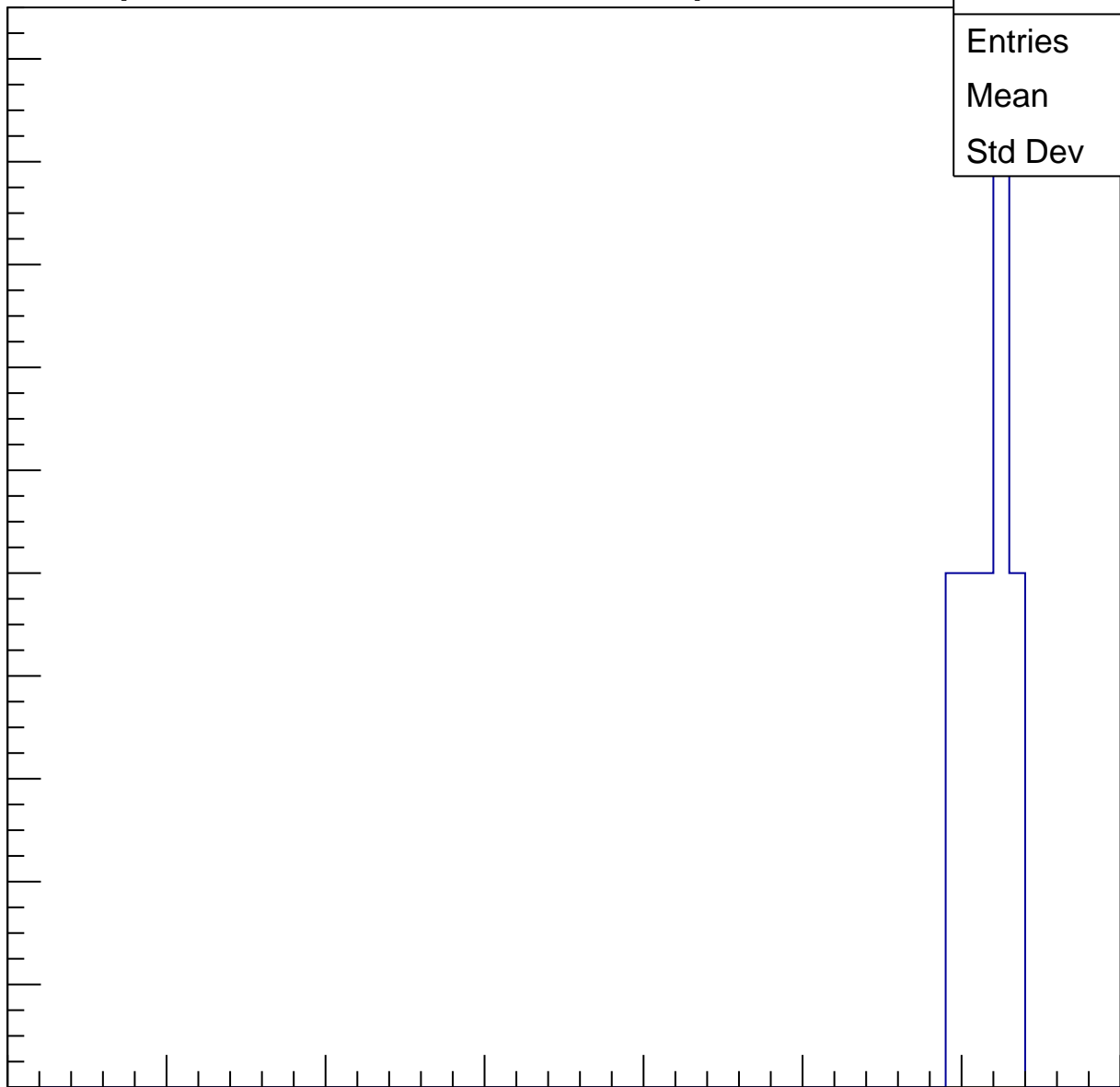
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.344

0 10 20 30 40 50 60 70

ampl



B0L102S, U12-ch37, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch38, adc0

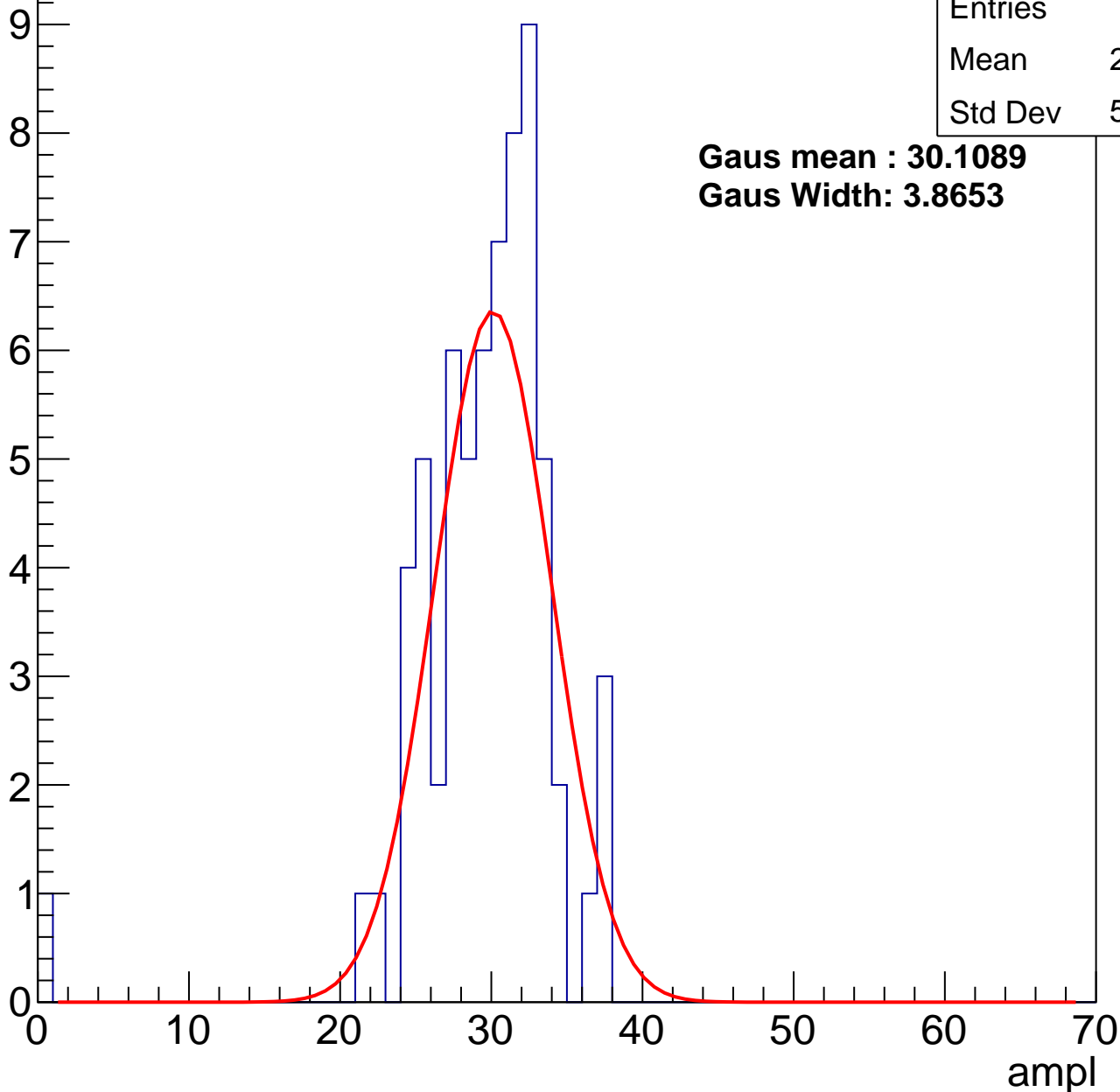
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	29.06
Std Dev	5.033

Gaus mean : 30.1089

Gaus Width: 3.8653



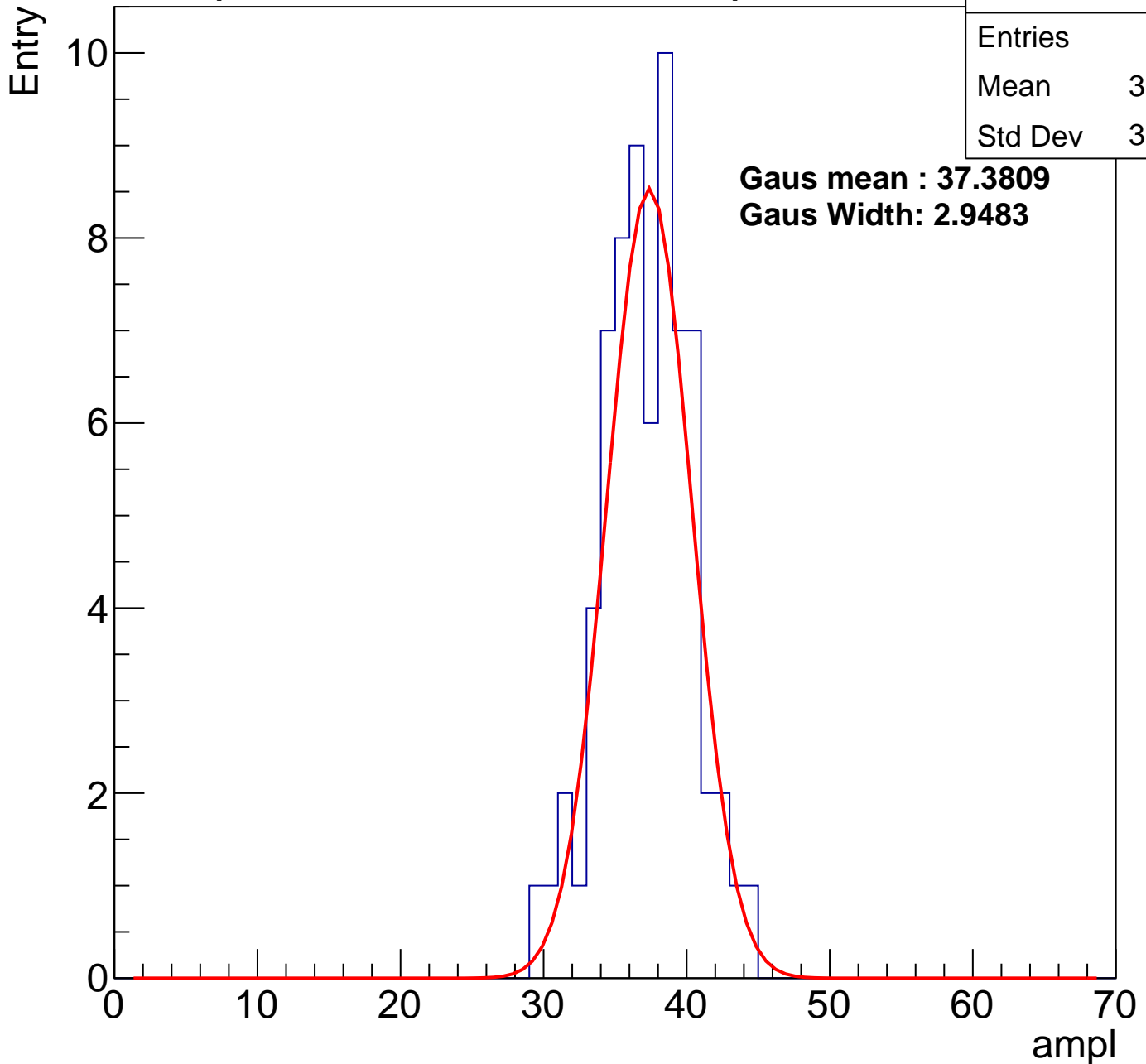
B0L102S, U12-ch38, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	36.74
Std Dev	3.049

Gaus mean : 37.3809

Gaus Width: 2.9483



B0L102S, U12-ch38, adc2

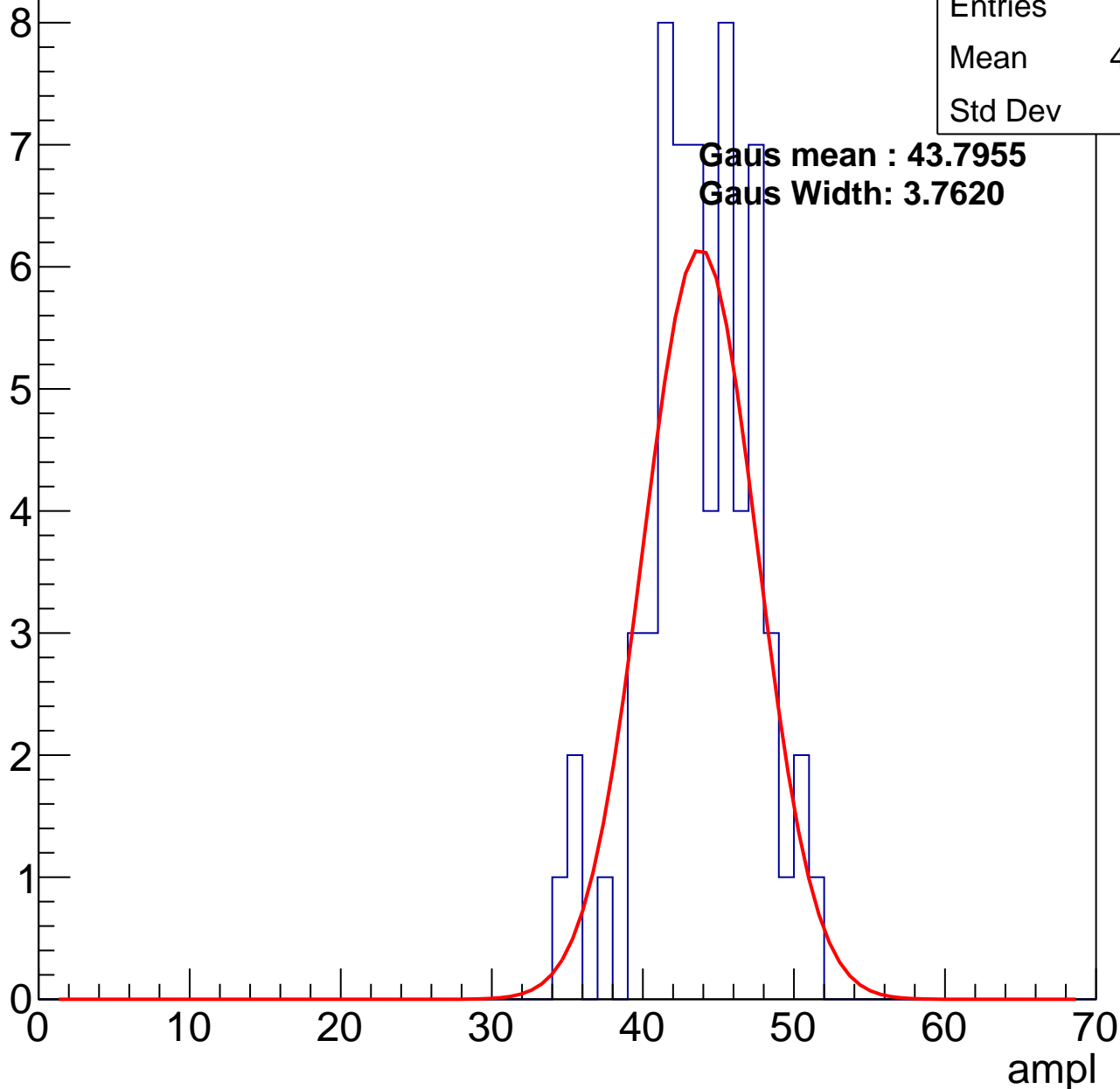
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	43.45
Std Dev	3.6

Gaus mean : 43.7955

Gaus Width: 3.7620

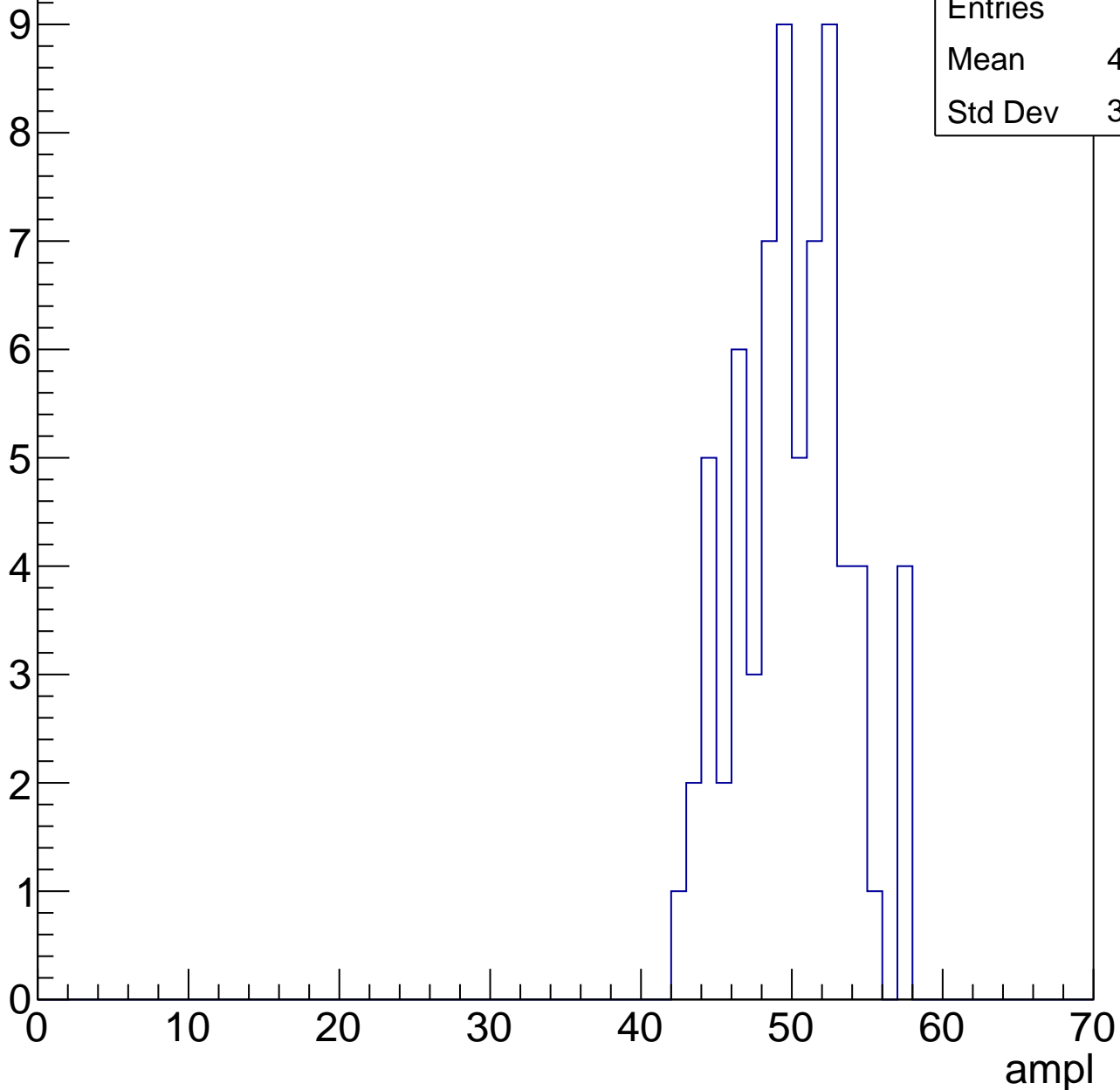


B0L102S, U12-ch38, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	49.54
Std Dev	3.614

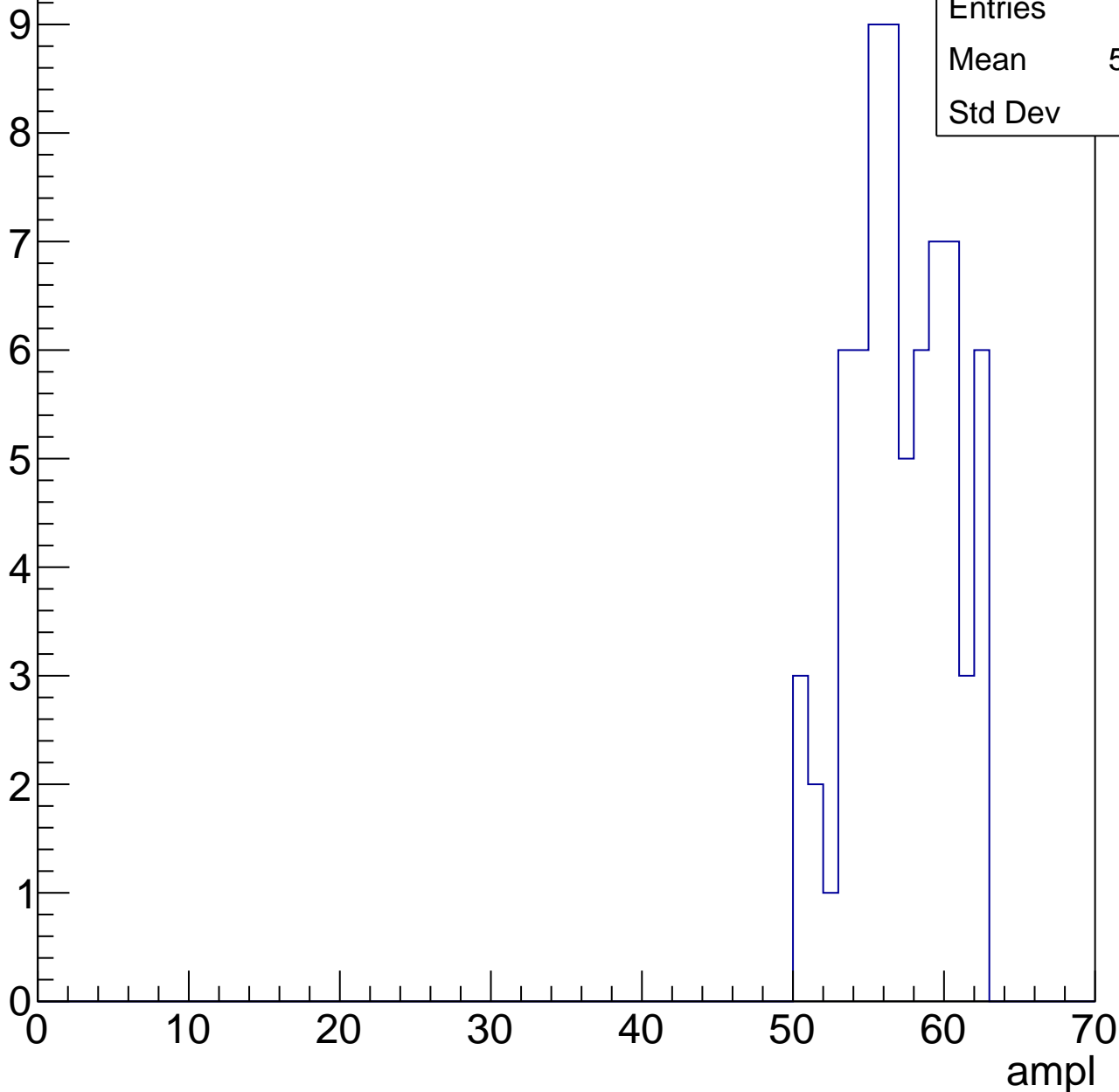


B0L102S, U12-ch38, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	56.66
Std Dev	3.22

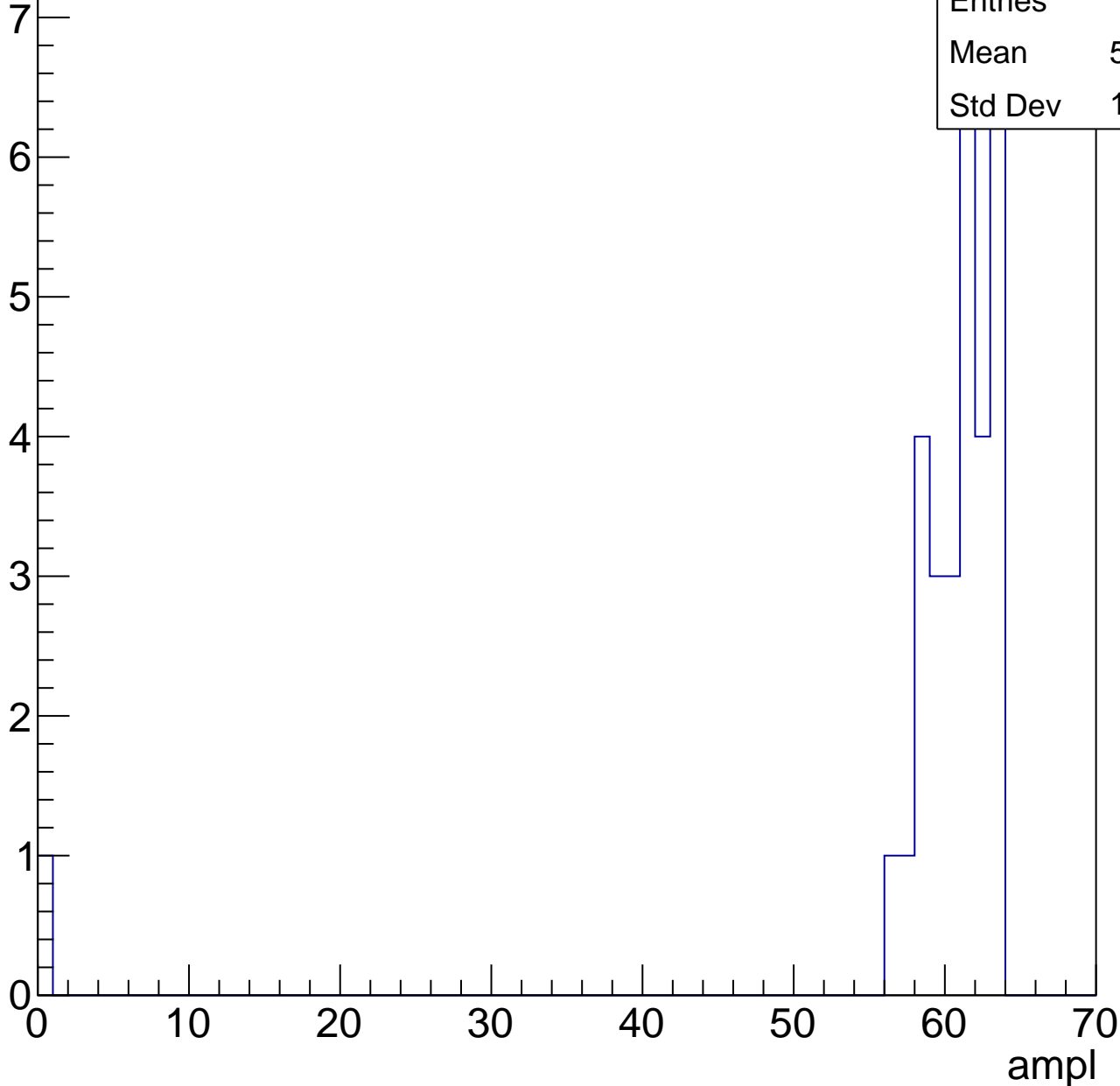


B0L102S, U12-ch38, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	31
Mean	58.65
Std Dev	10.89



B0L102S, U12-ch38, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch38, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch39, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	31.21
Std Dev	3.365

Gaus mean : 31.3204

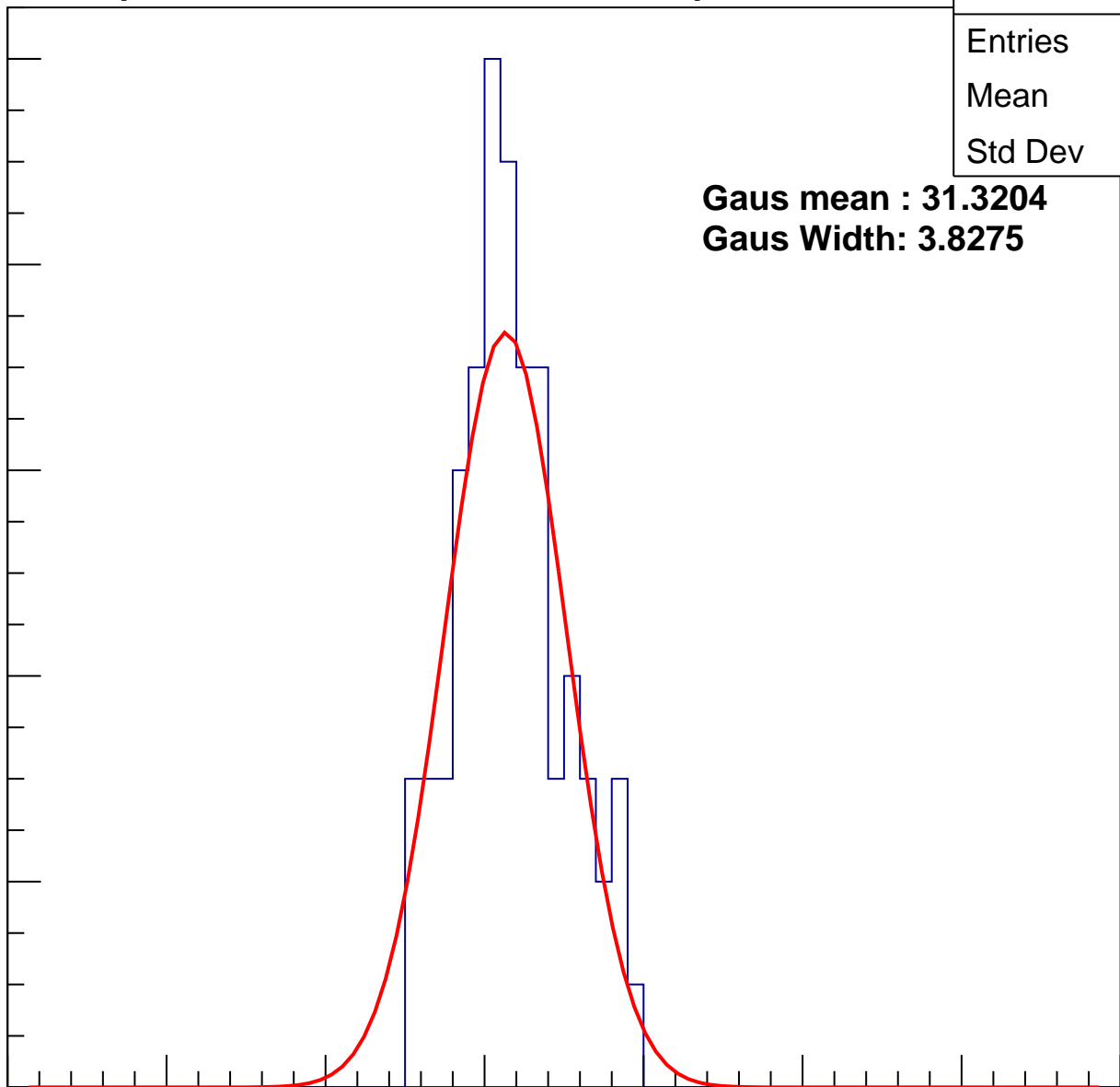
Gaus Width: 3.8275

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



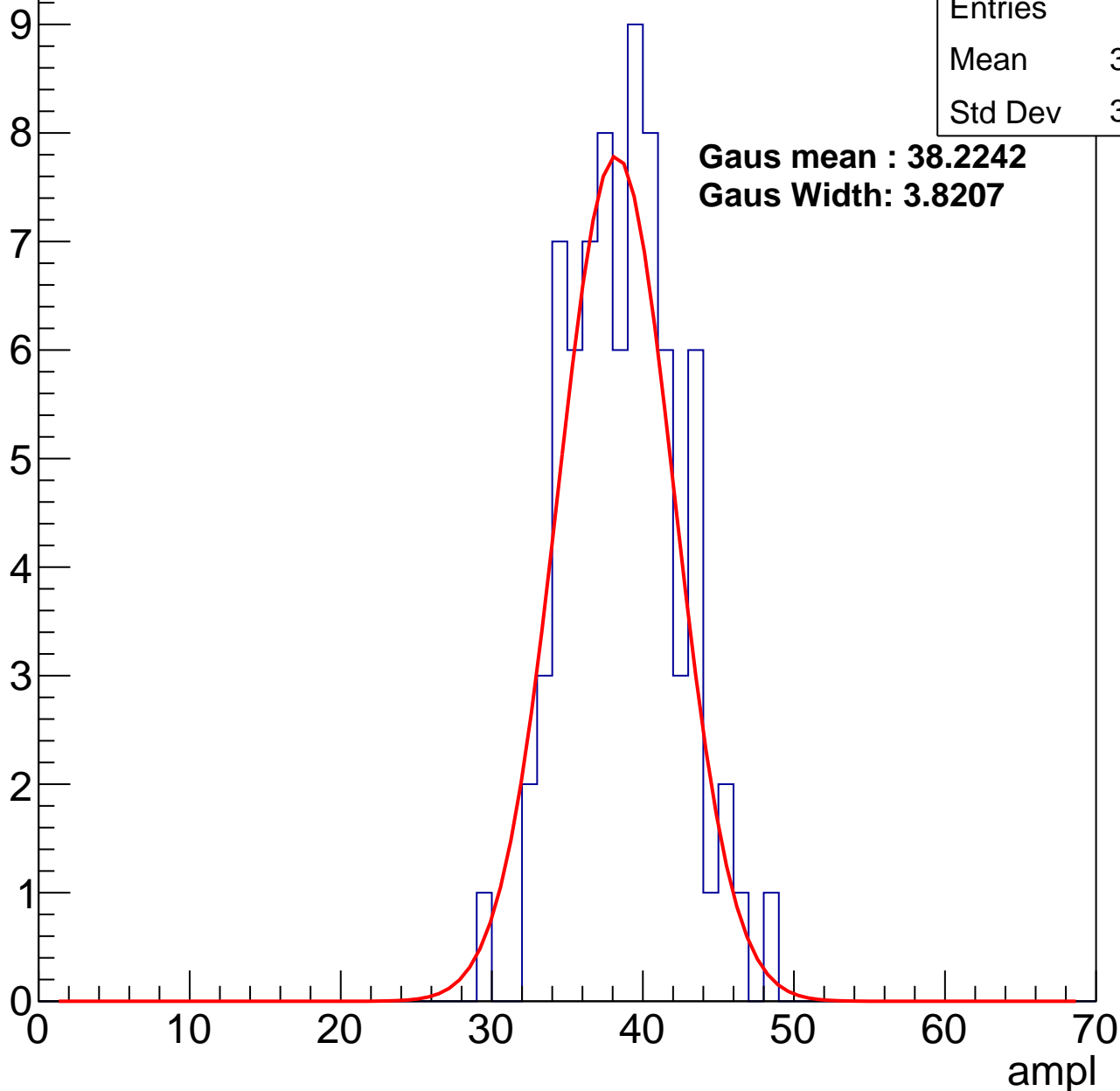
B0L102S, U12-ch39, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	38.25
Std Dev	3.633

Gaus mean : 38.2242
Gaus Width: 3.8207



B0L102S, U12-ch39, adc2

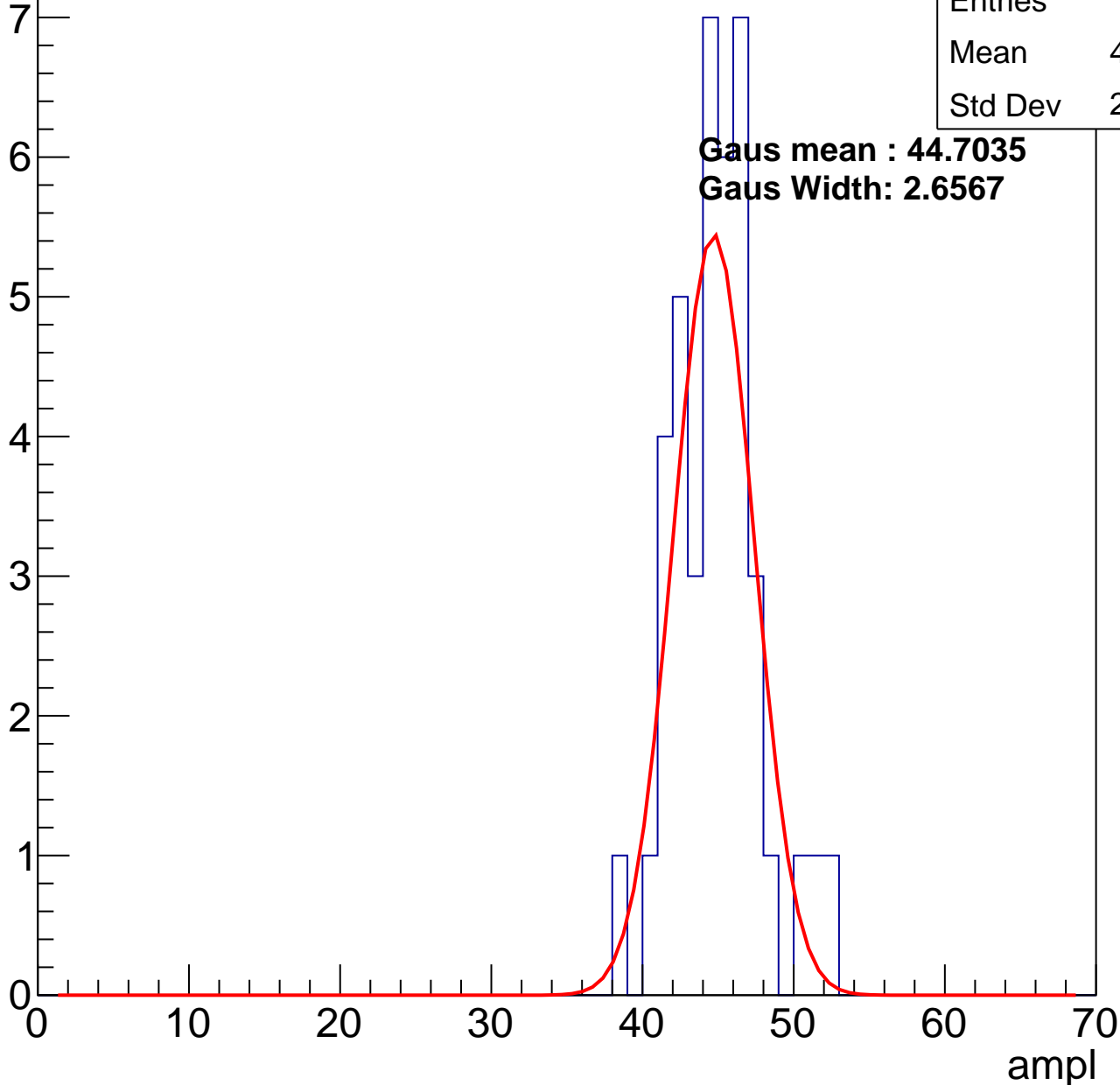
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	44.46
Std Dev	2.829

Gaus mean : 44.7035

Gaus Width: 2.6567

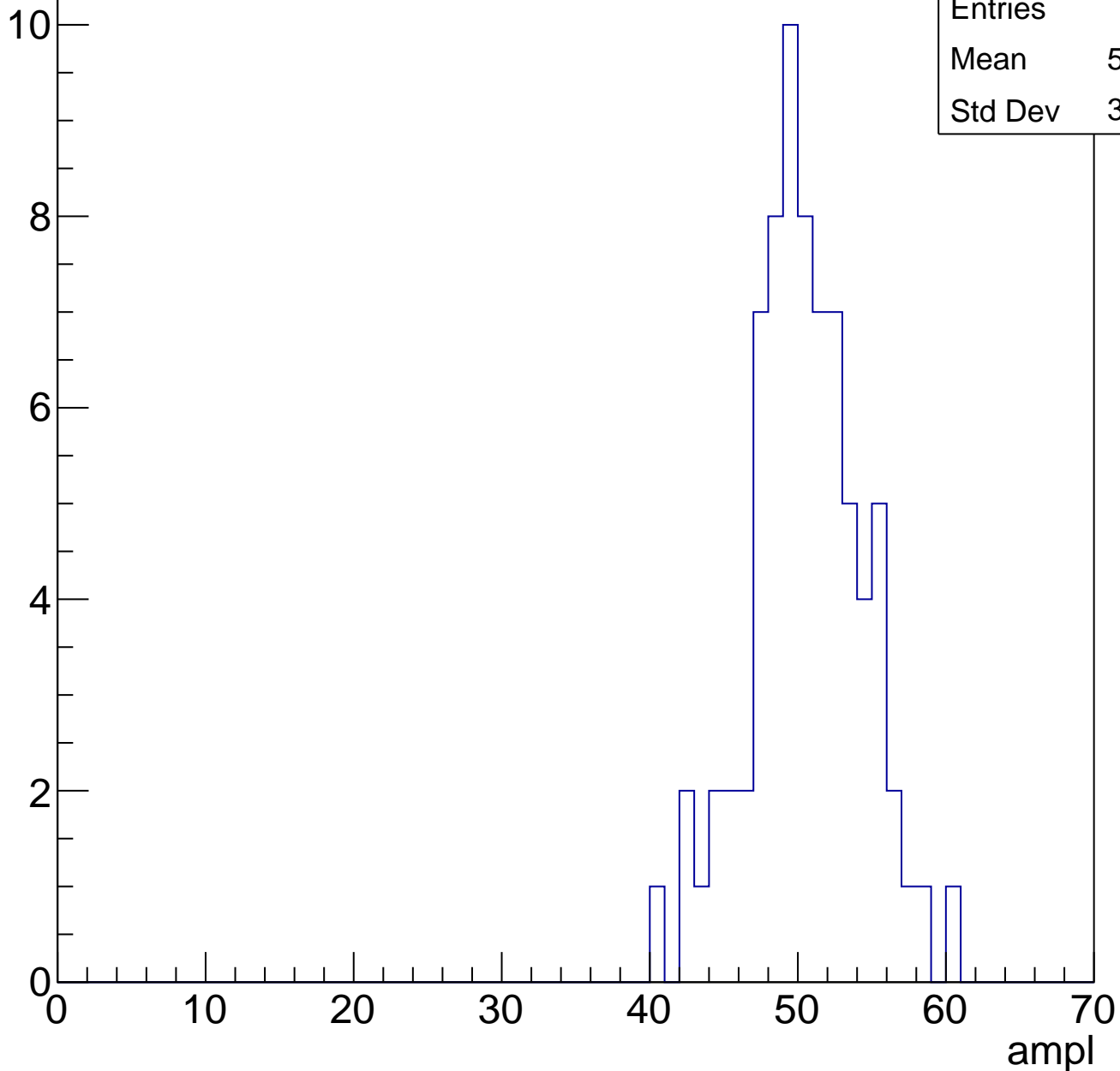


B0L102S, U12-ch39, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	50.05
Std Dev	3.787

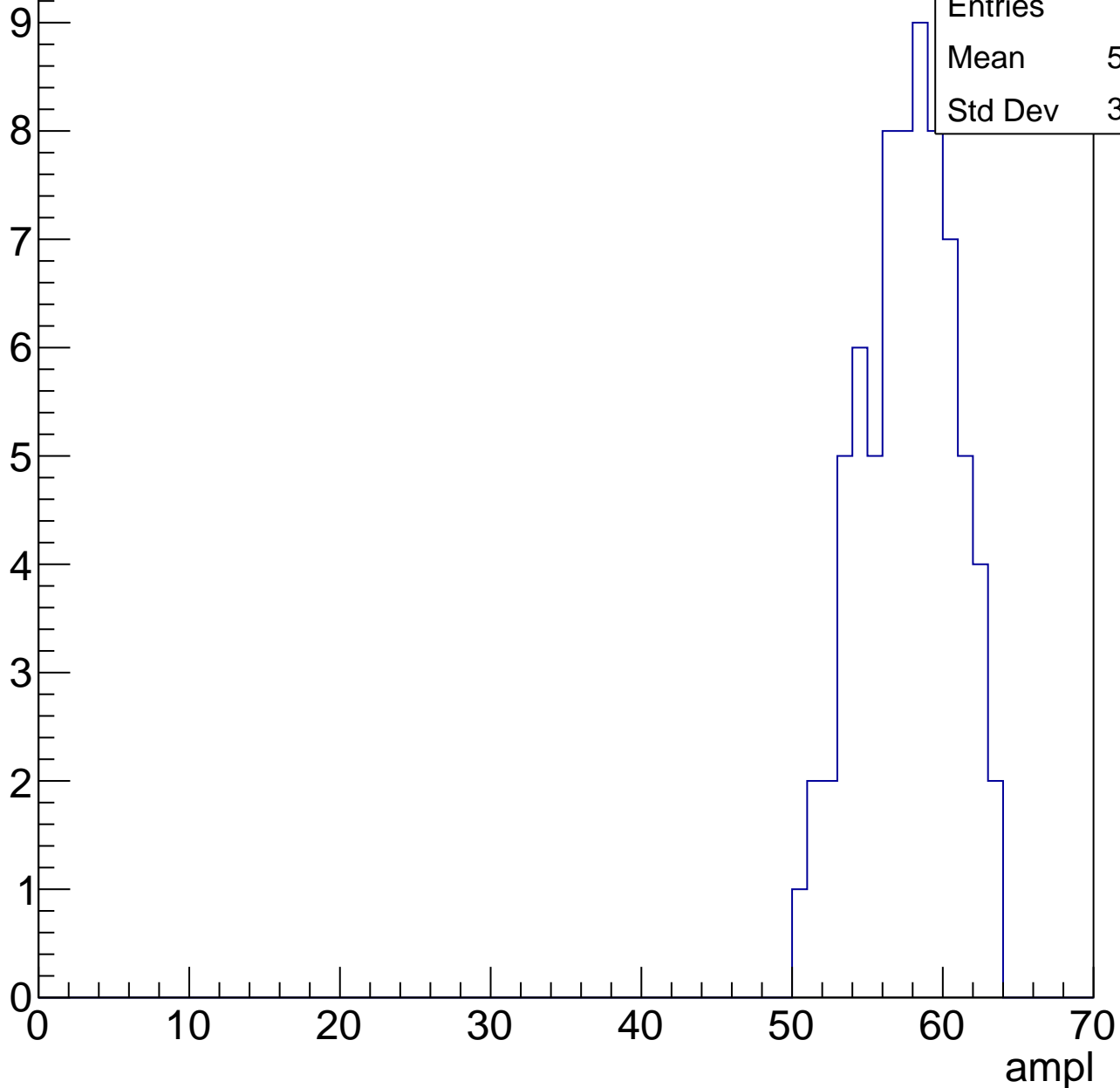
Entry



B0L102S, U12-ch39, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch39, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	23
Mean	61.48
Std Dev	1.584

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L102S, U12-ch39, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch39, adc7

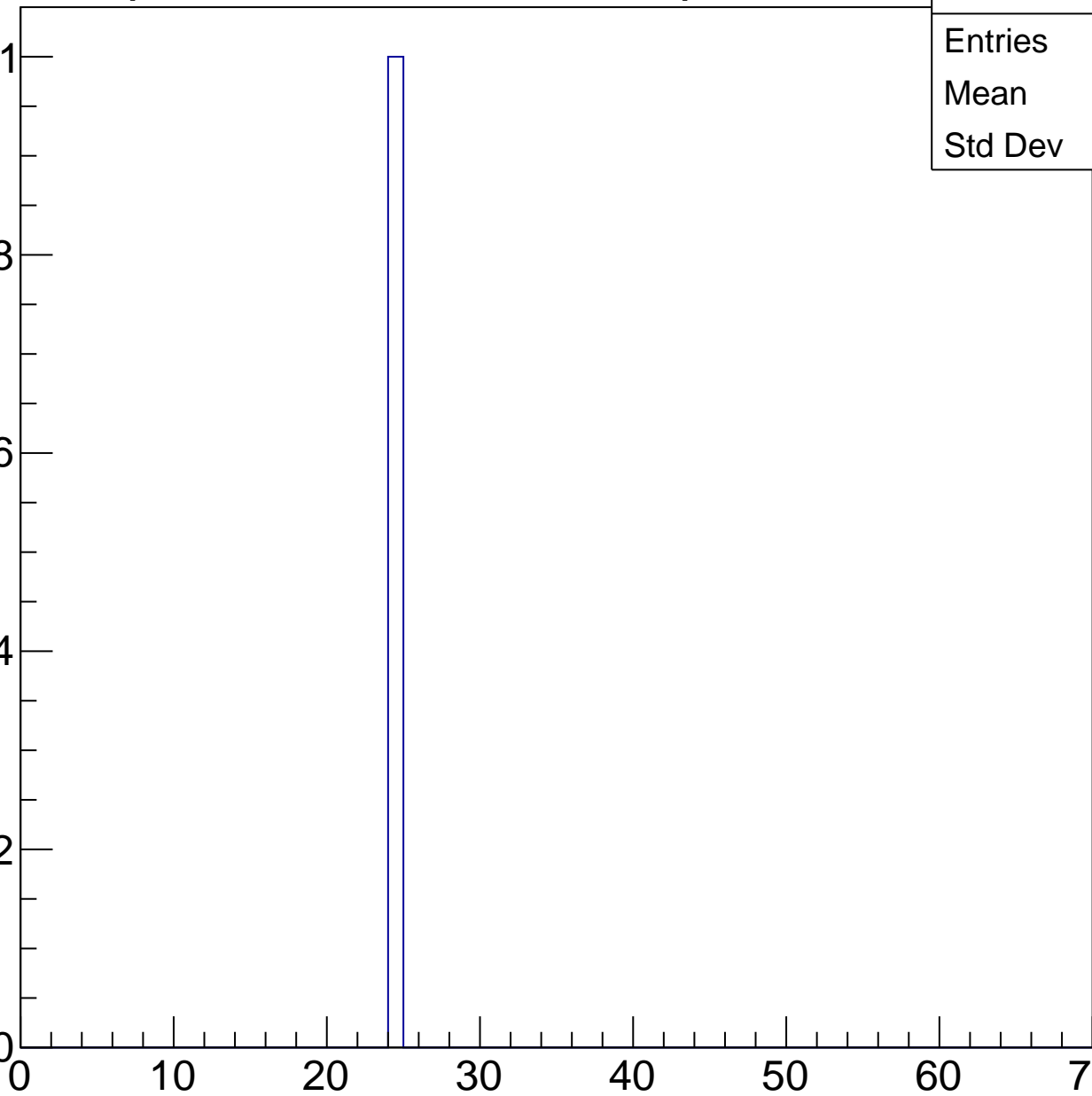
calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B0L102S, U12-ch40, adc0

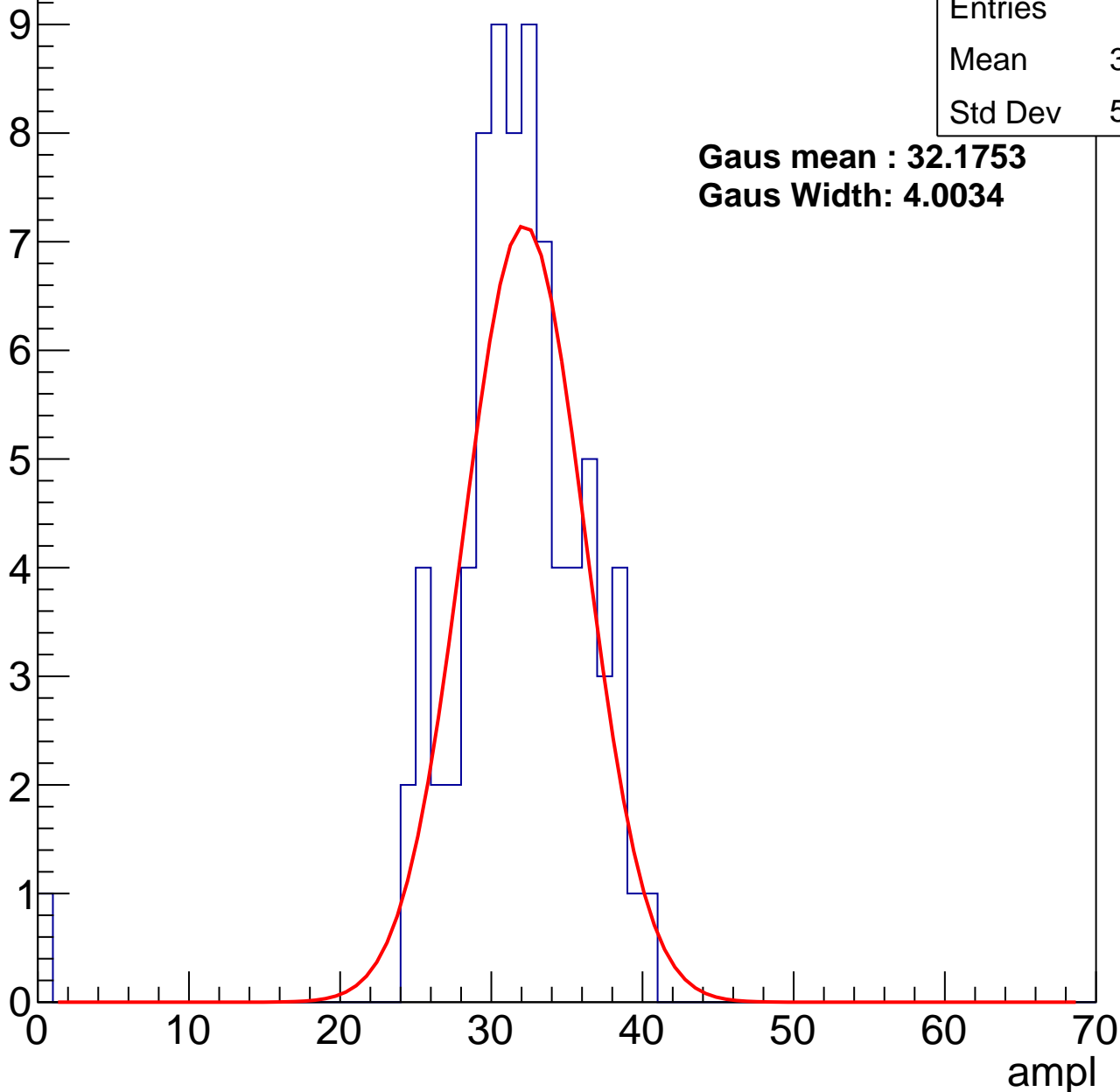
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	31.19
Std Dev	5.152

Gaus mean : 32.1753

Gaus Width: 4.0034



B0L102S, U12-ch40, adc1

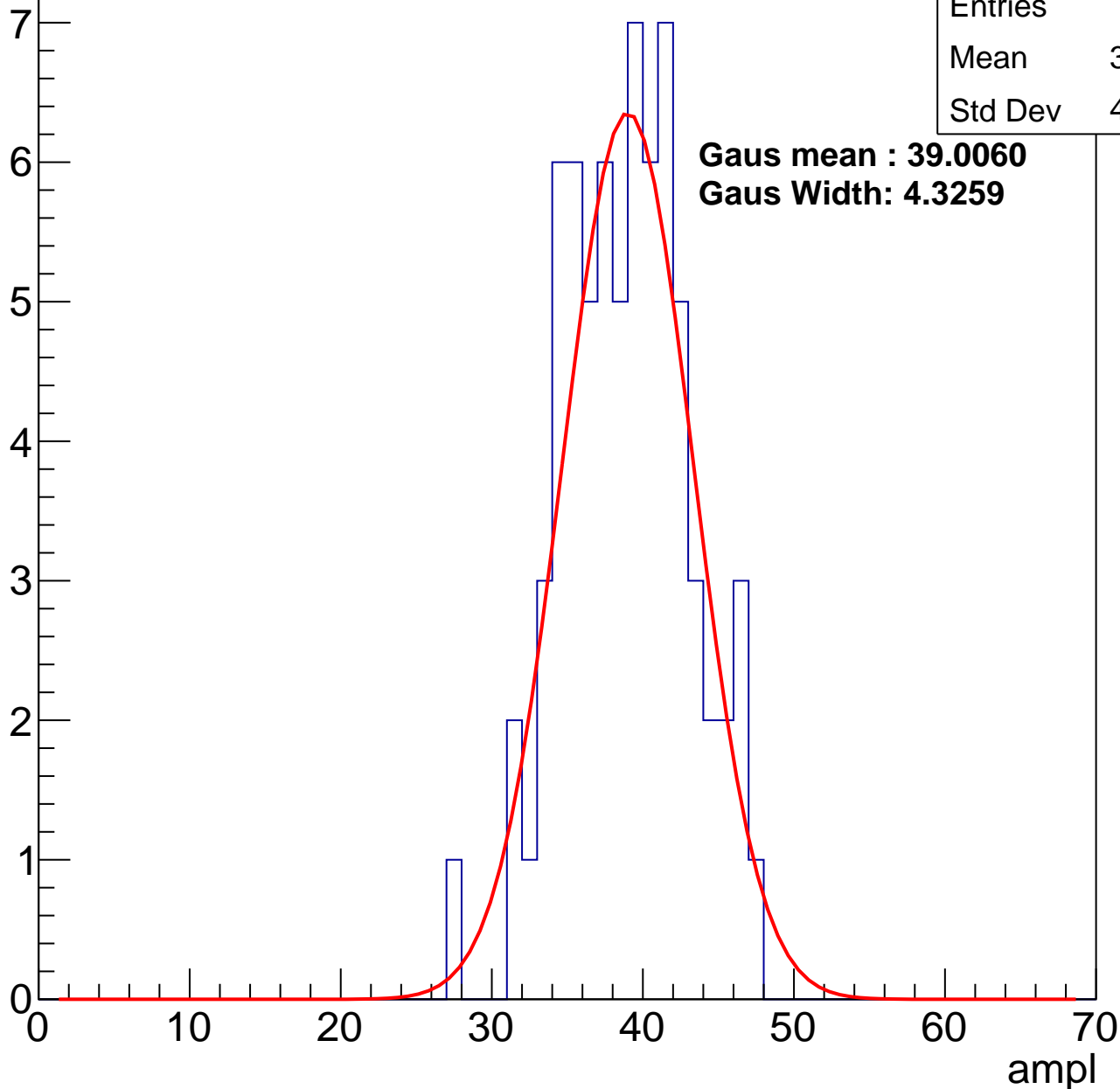
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	38.42
Std Dev	4.086

Gaus mean : 39.0060

Gaus Width: 4.3259



B0L102S, U12-ch40, adc2

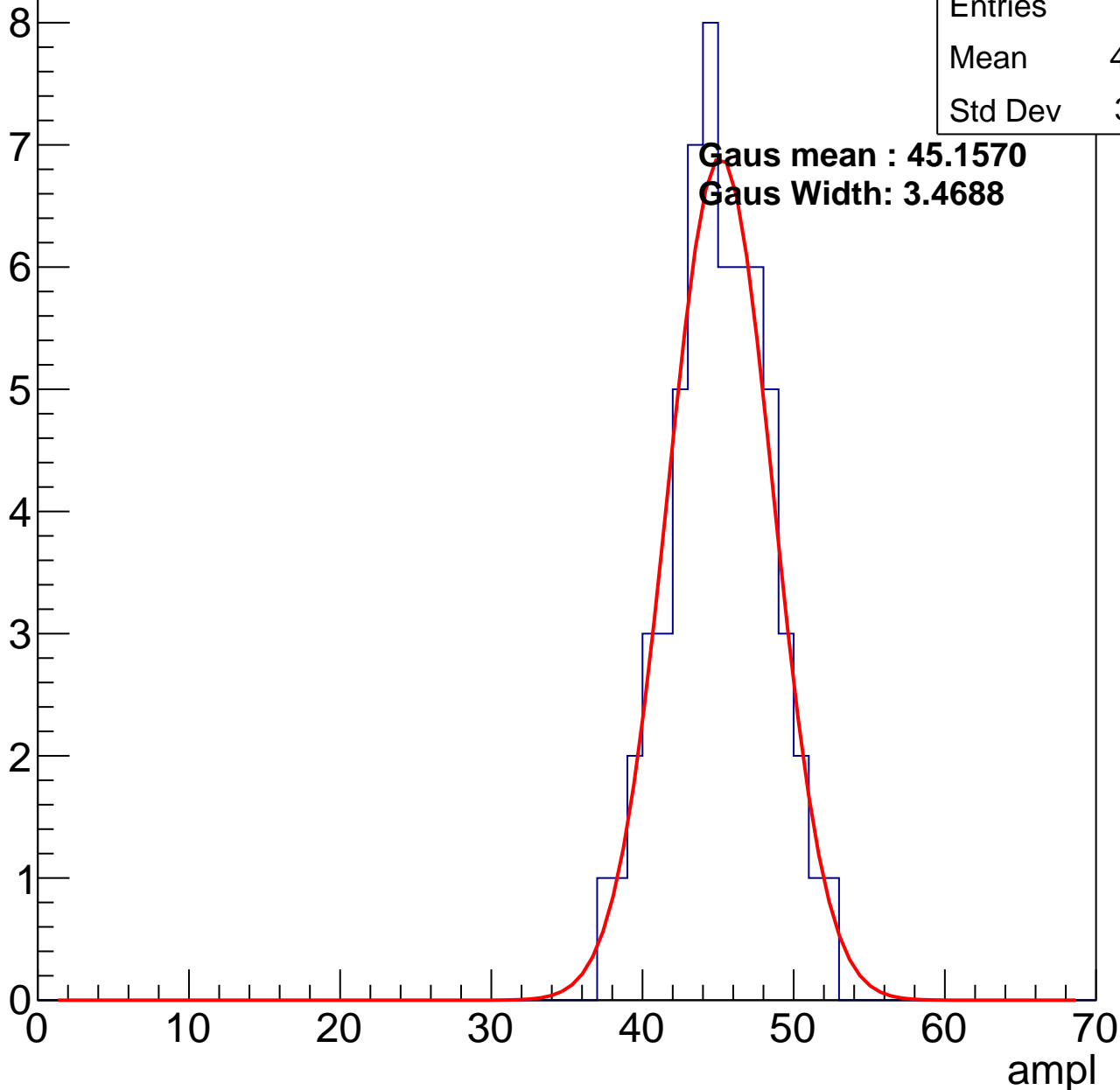
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	44.62
Std Dev	3.261

Gaus mean : 45.1570

Gaus Width: 3.4688

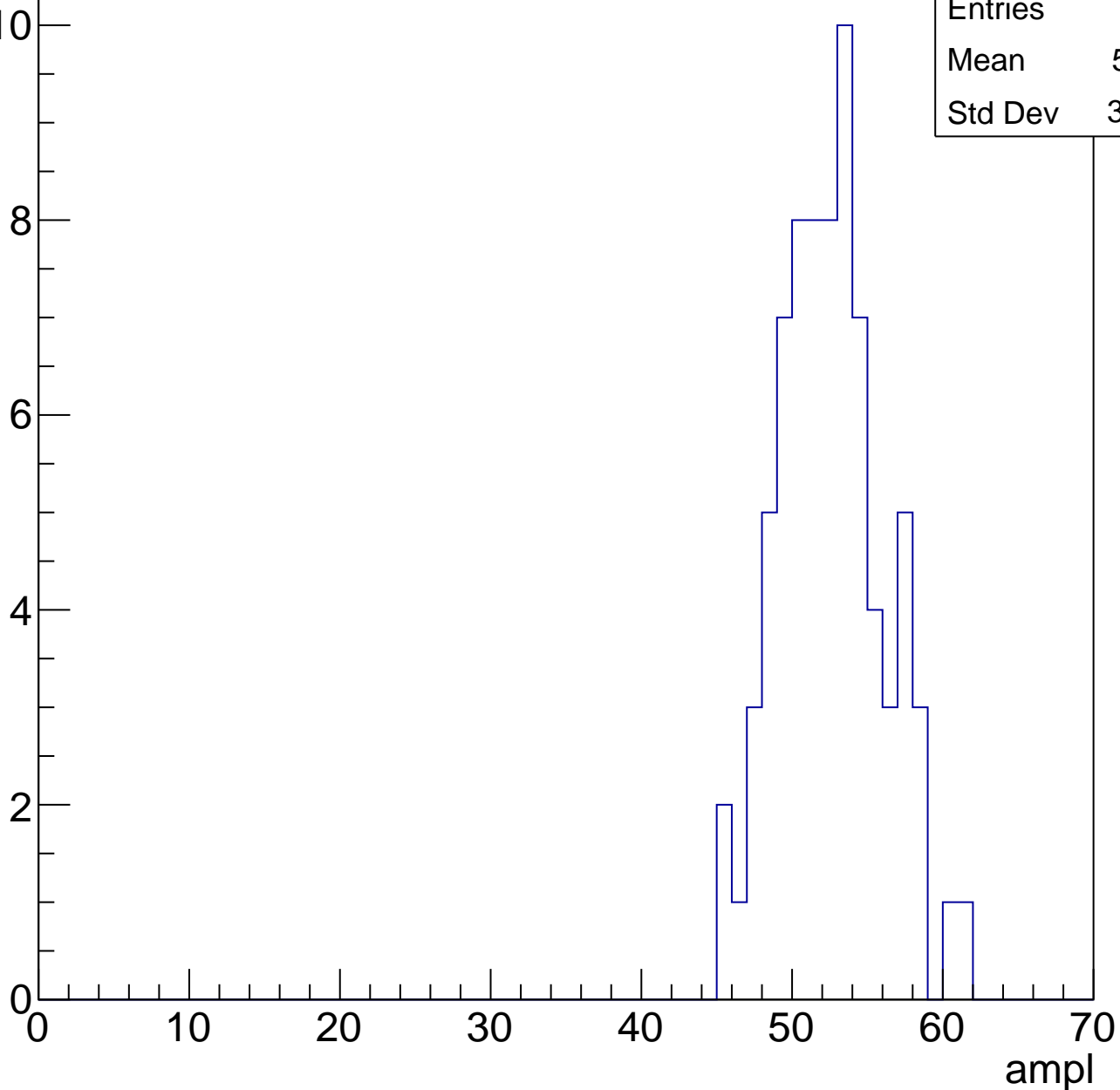


B0L102S, U12-ch40, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	52.11
Std Dev	3.424

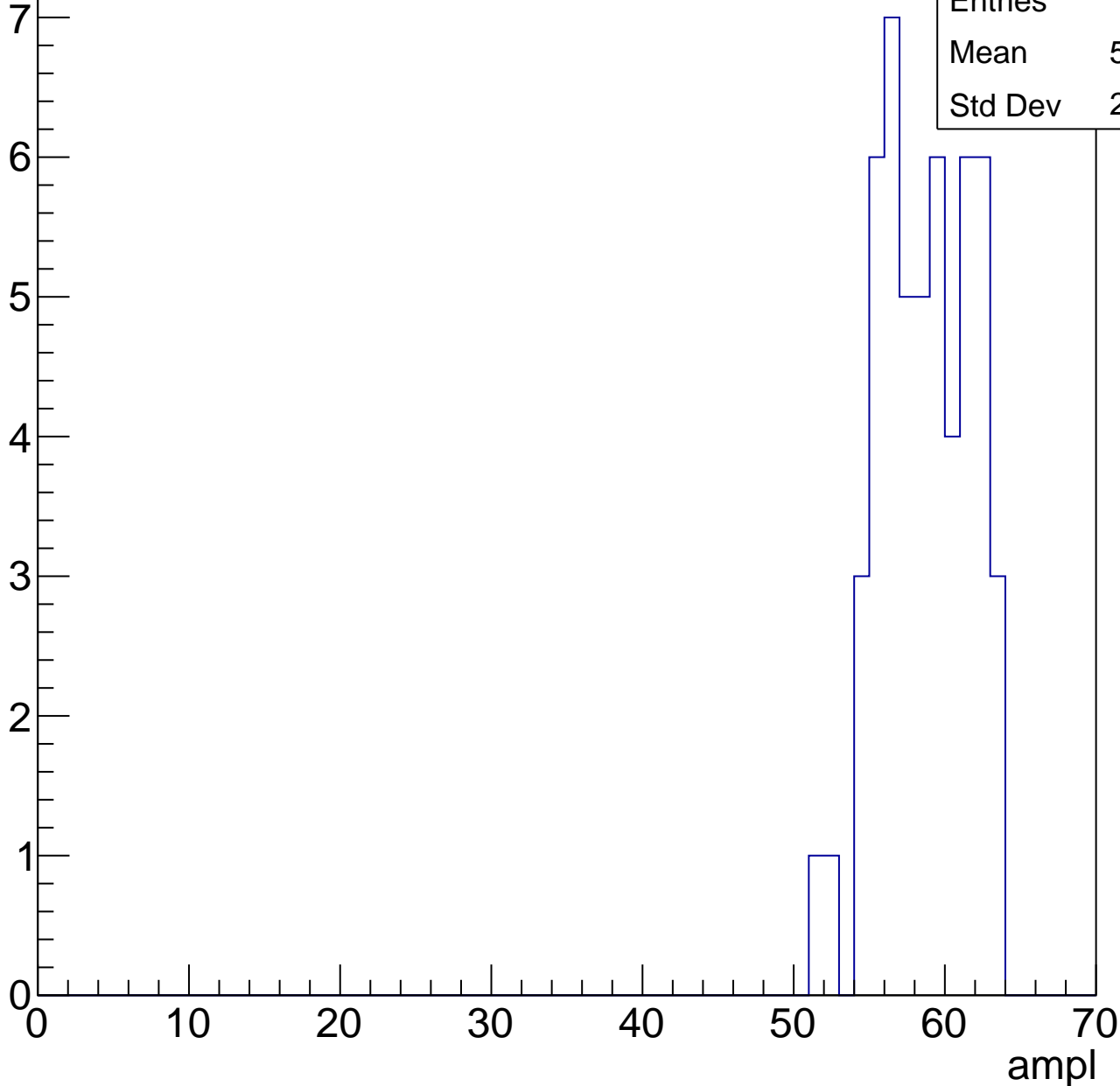


B0L102S, U12-ch40, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	58.17
Std Dev	2.964

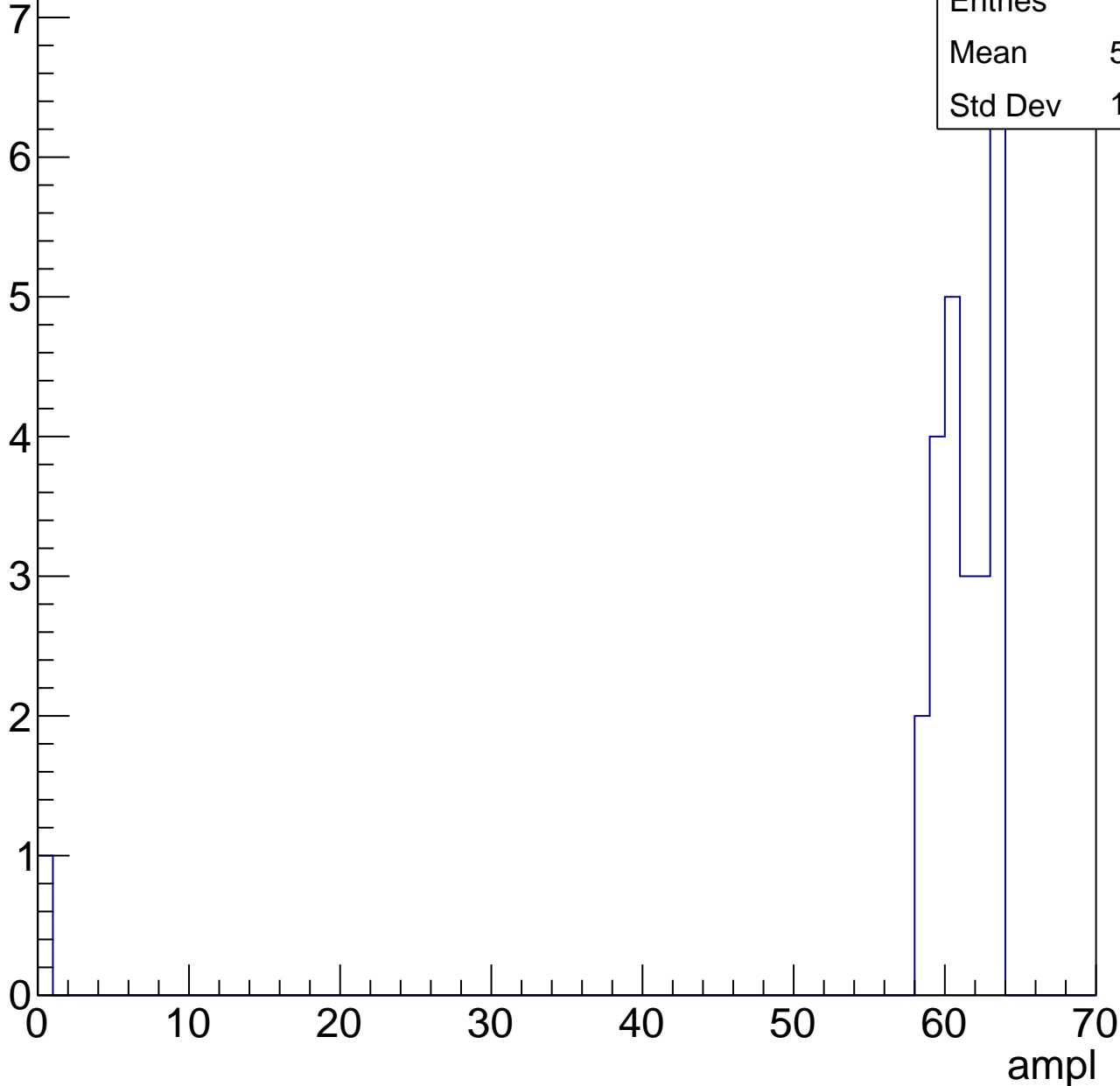


B0L102S, U12-ch40, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	25
Mean	58.48
Std Dev	12.05



B0L102S, U12-ch40, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch40, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L102S, U12-ch41, adc0

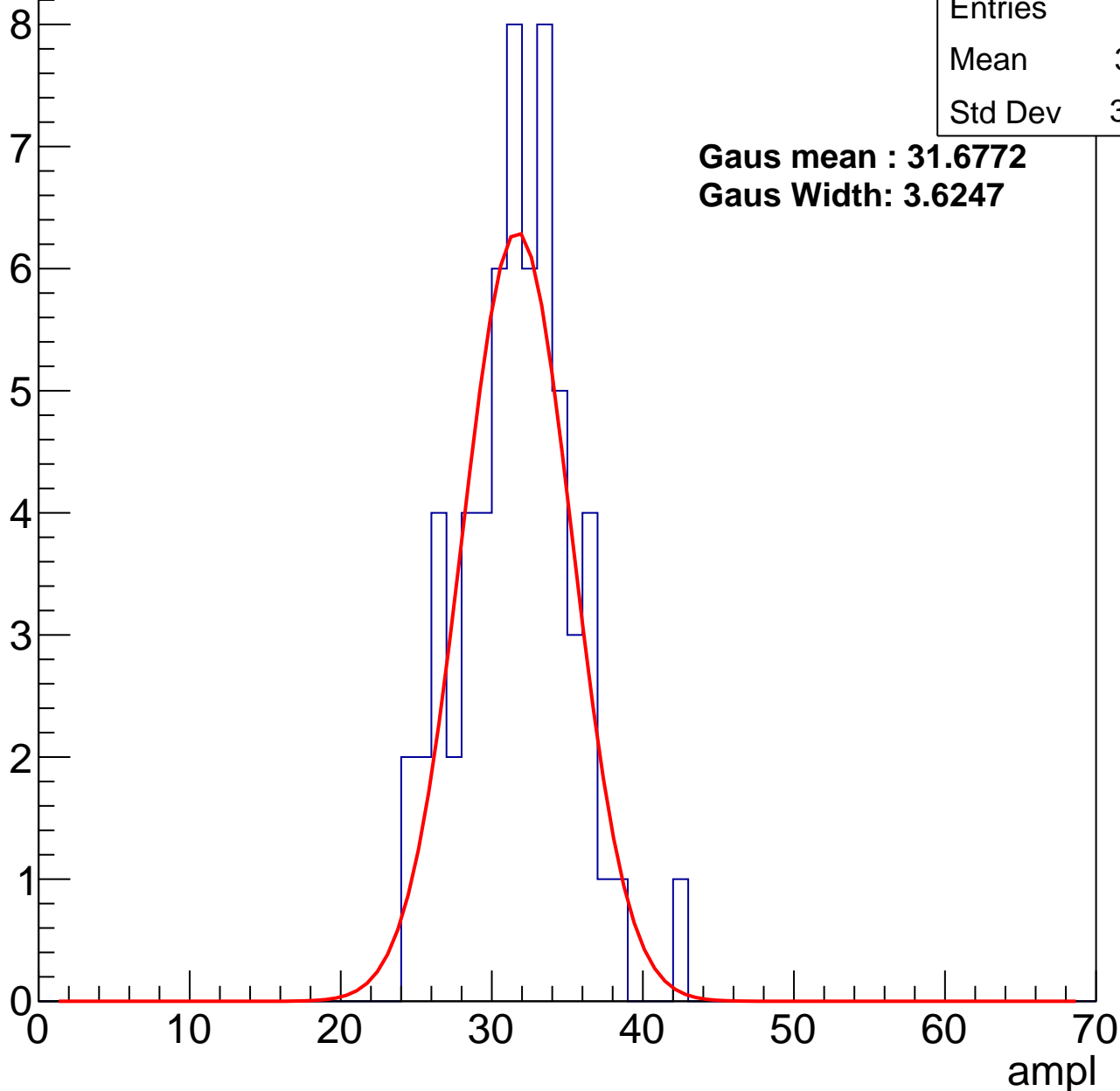
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	31.21
Std Dev	3.608

Gaus mean : 31.6772

Gaus Width: 3.6247



B0L102S, U12-ch41, adc1

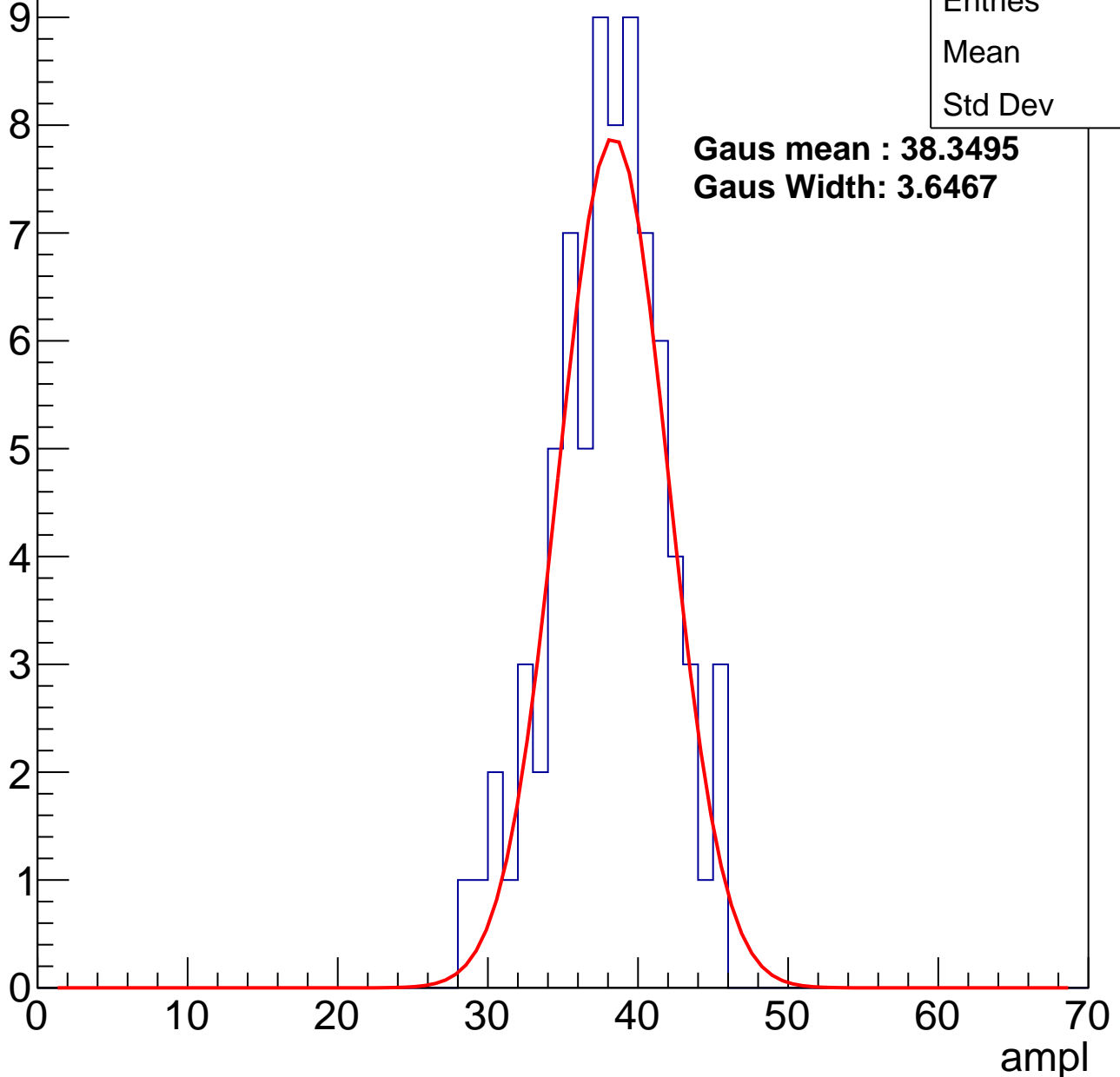
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	37.6
Std Dev	3.76

Gaus mean : 38.3495

Gaus Width: 3.6467



B0L102S, U12-ch41, adc2

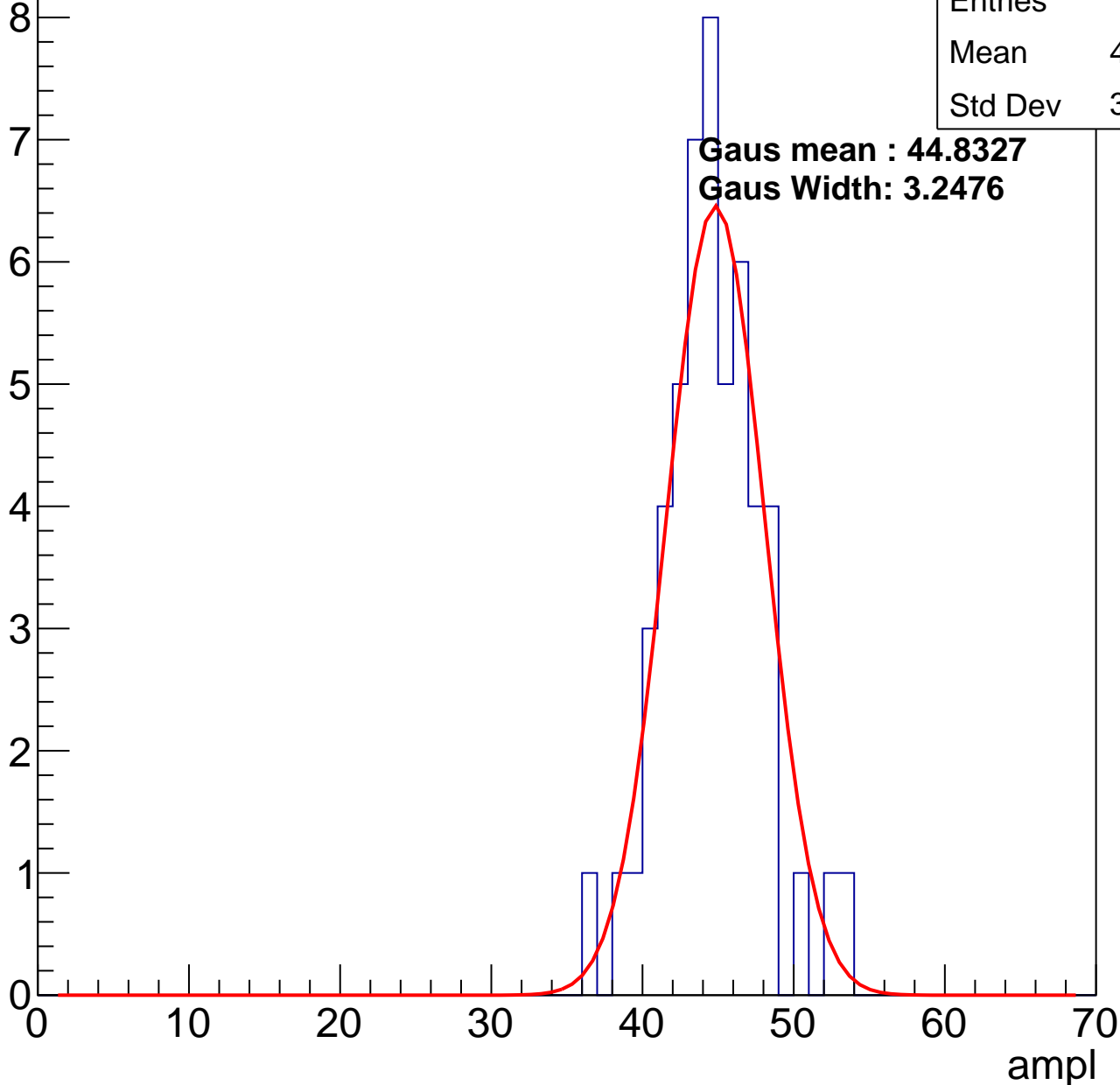
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	44.15
Std Dev	3.237

Gaus mean : 44.8327

Gaus Width: 3.2476



B0L102S, U12-ch41, adc3

calib_packv5_042523_0143.root, FC#12, port B1

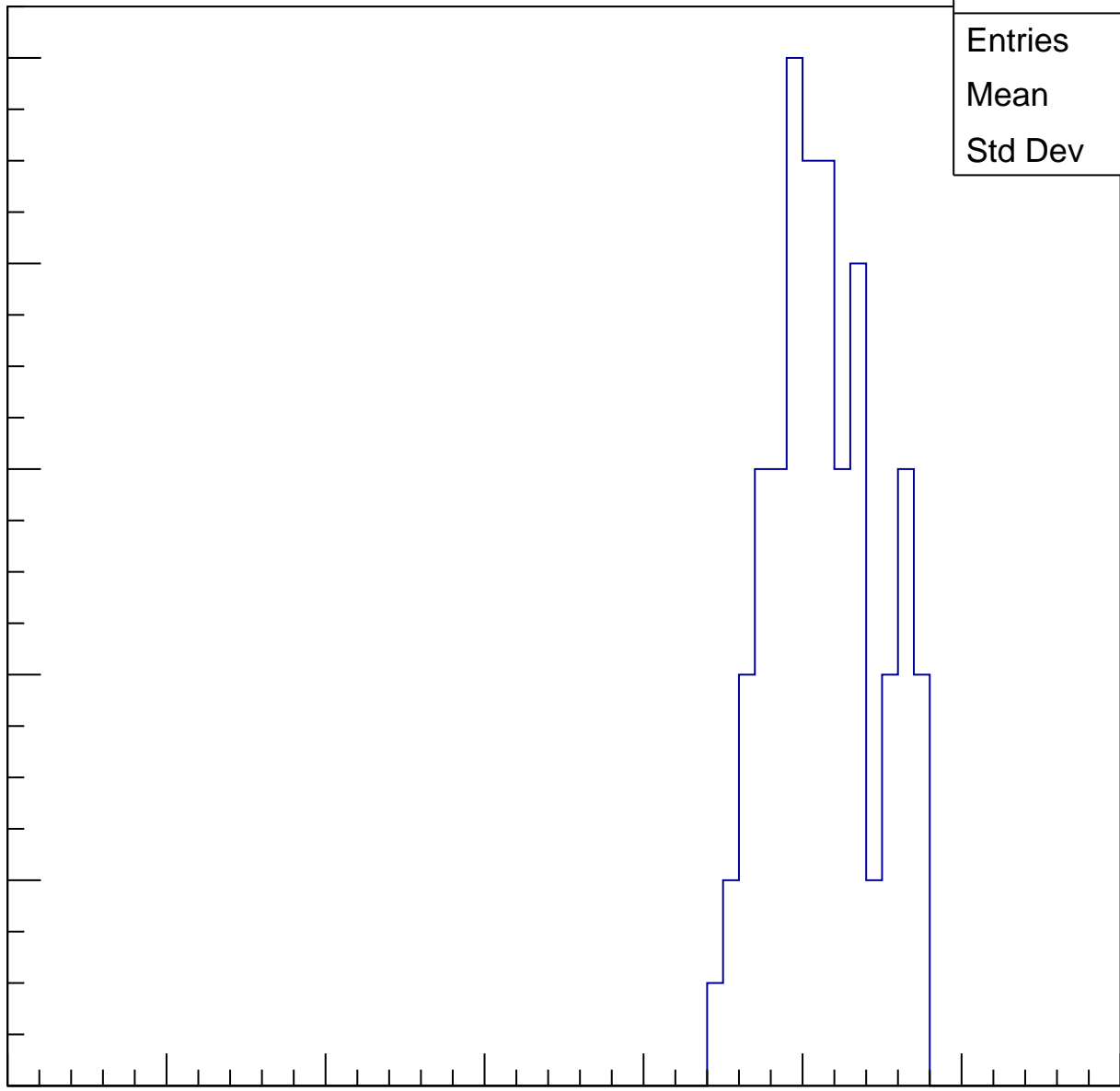
Entries	77
Mean	50.84
Std Dev	3.287

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

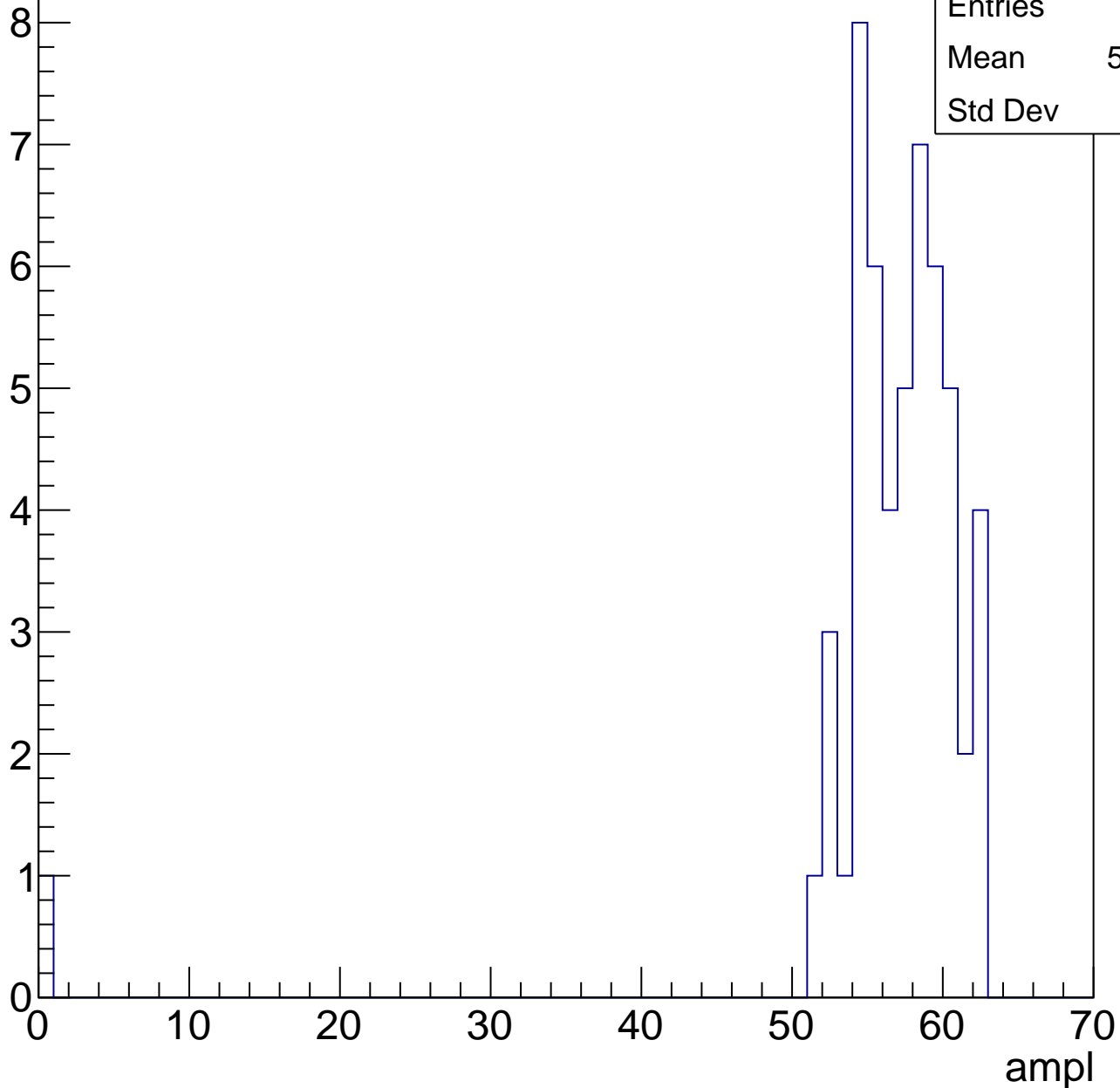


B0L102S, U12-ch41, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

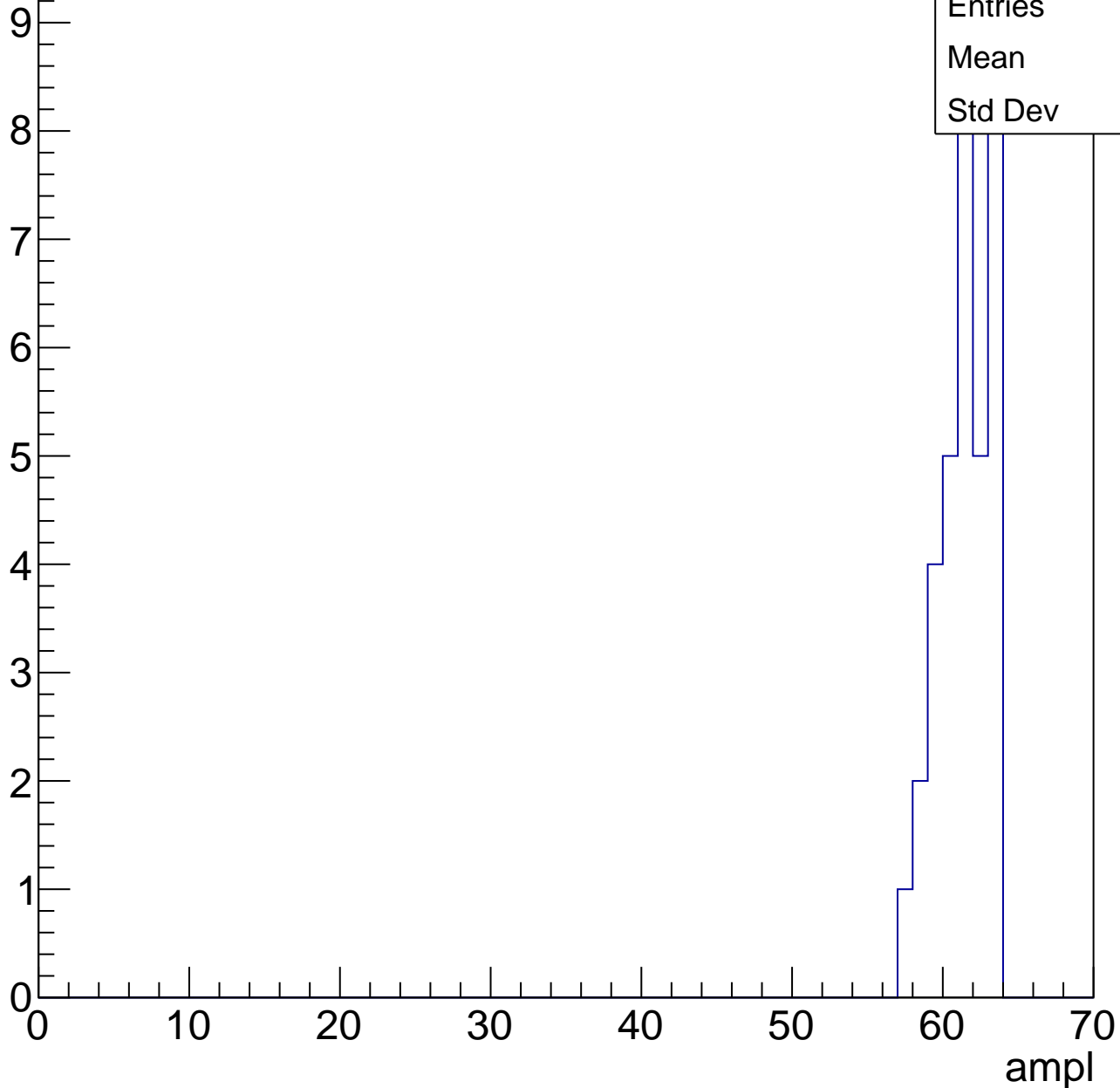
Entries	53
Mean	55.87
Std Dev	8.26



B0L102S, U12-ch41, adc5

calib_packv5_042523_0143.root, FC#12, port B1

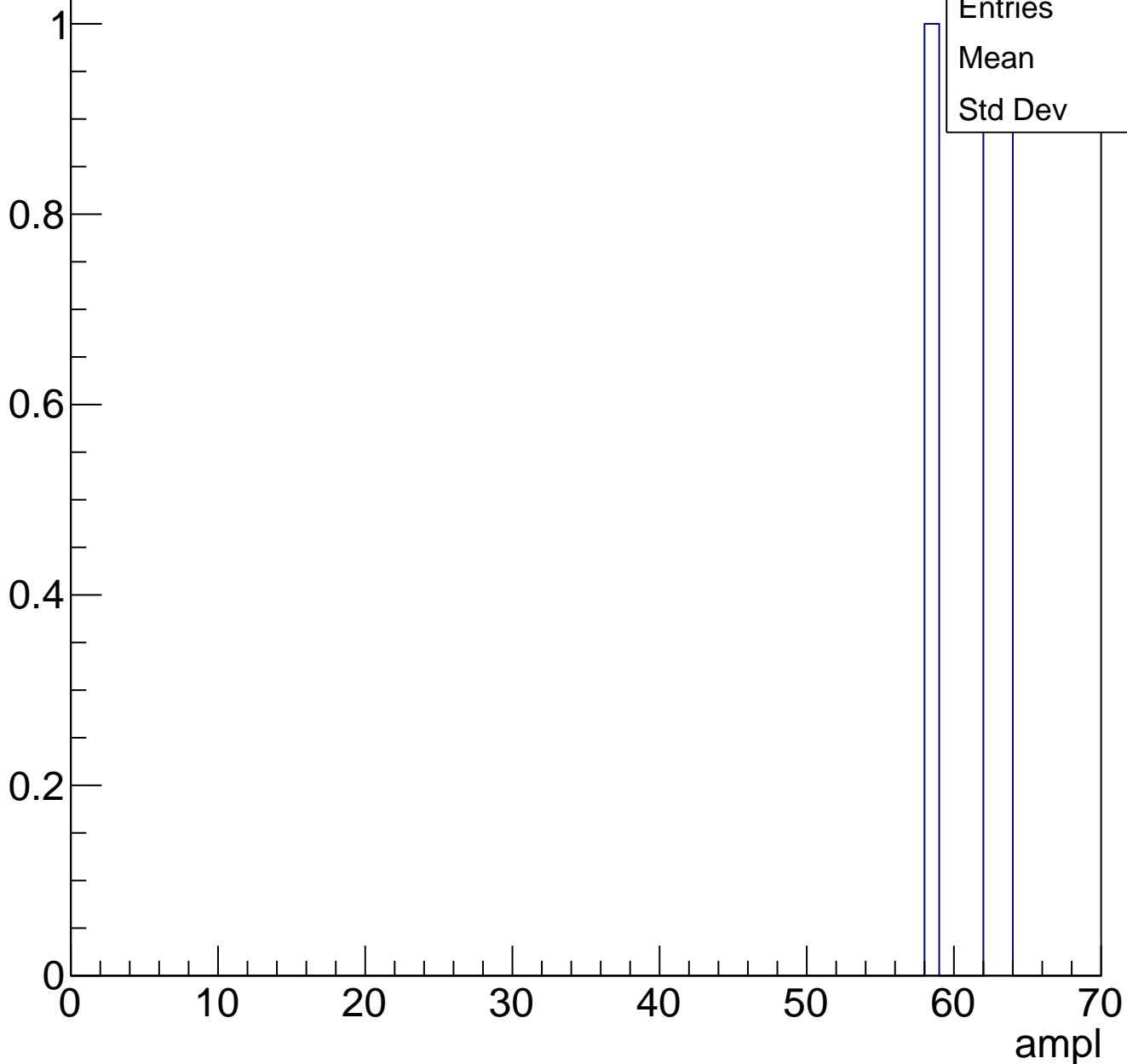
Entry



B0L102S, U12-ch41, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch41, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch42, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	58
Mean	30.36
Std Dev	3.177

Gaus mean : 30.6128

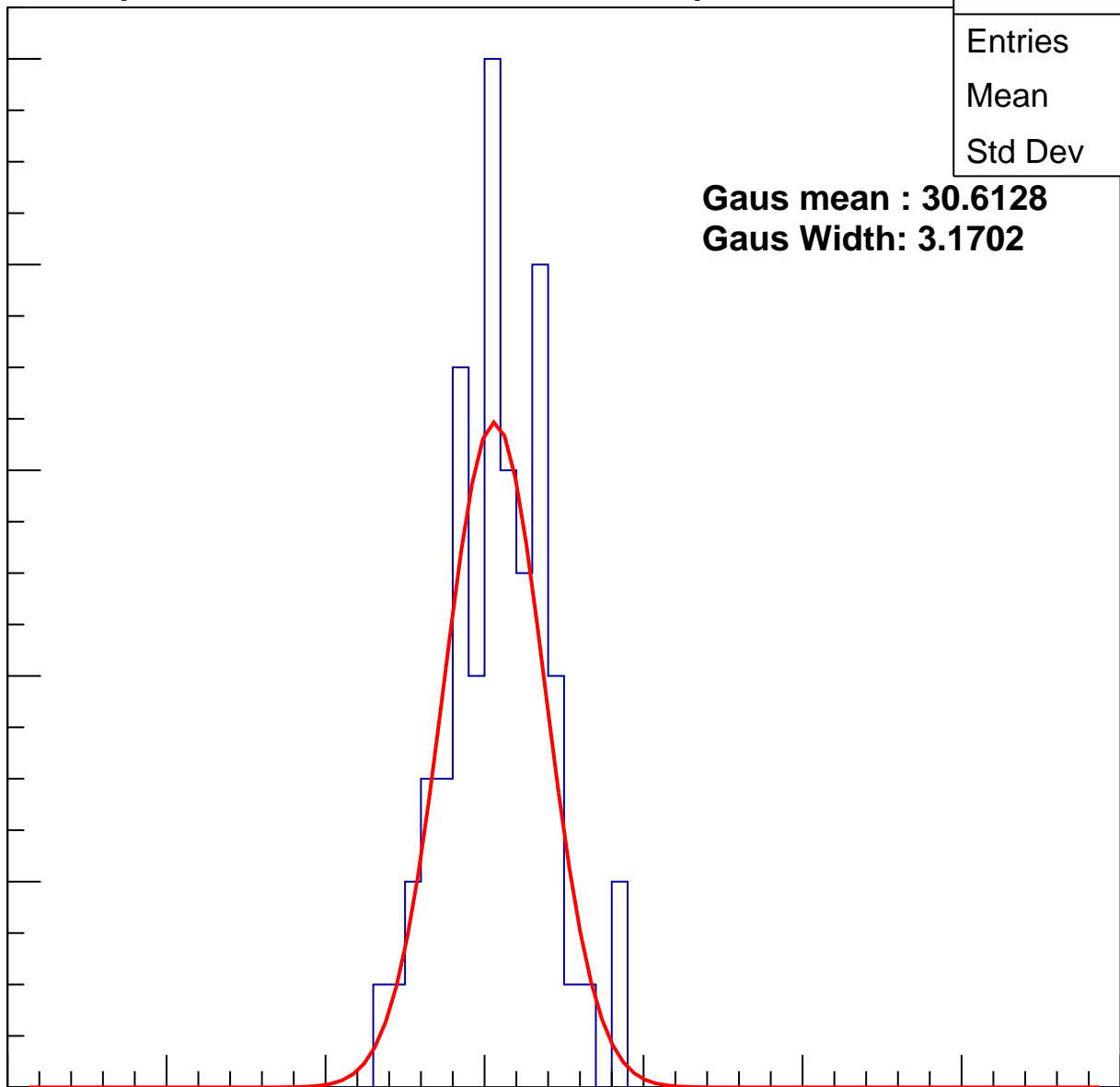
Gaus Width: 3.1702

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch42, adc1

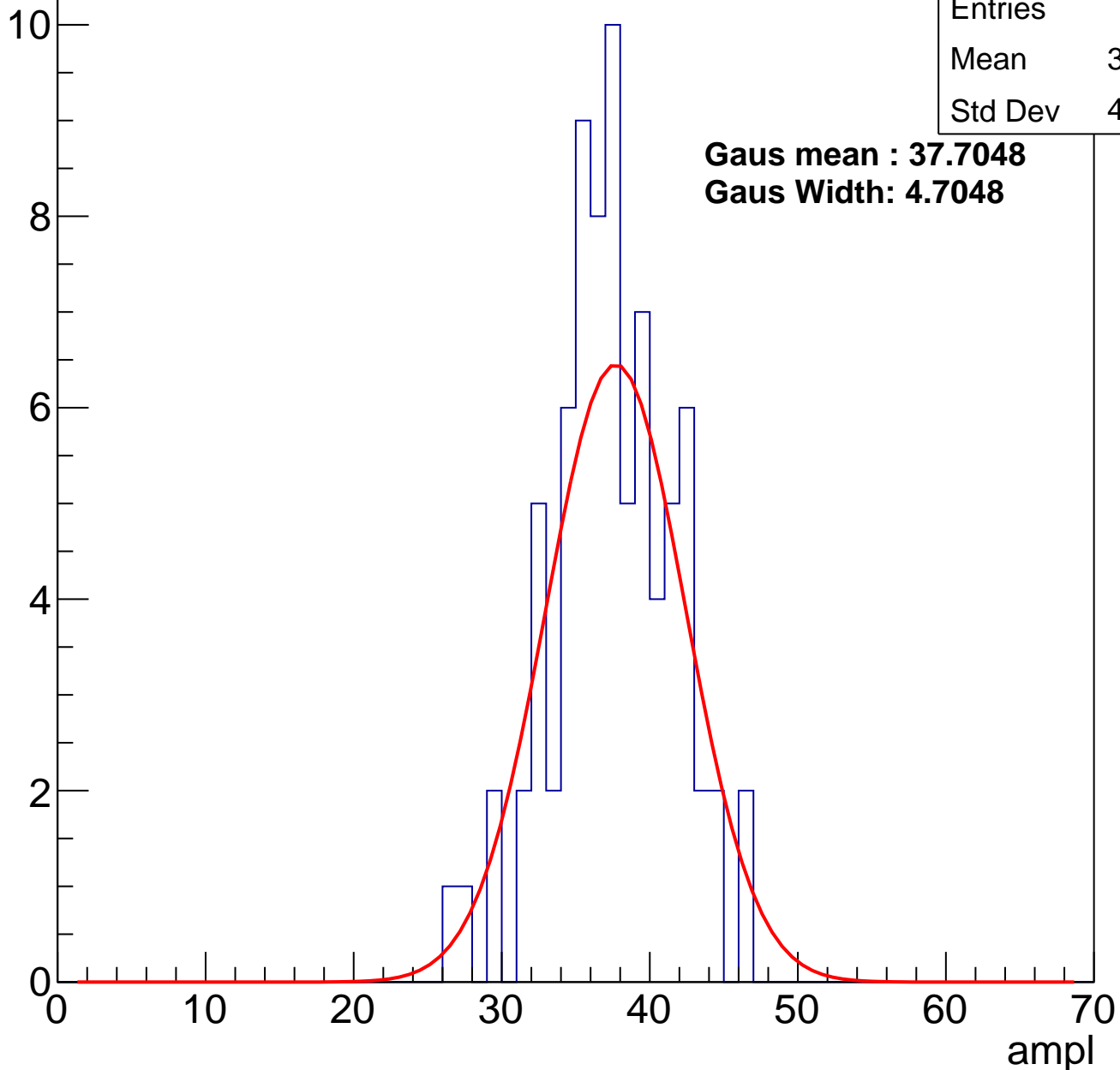
calib_packv5_042523_0143.root, FC#12, port B1

Entries	79
Mean	36.99
Std Dev	4.067

Gaus mean : 37.7048

Gaus Width: 4.7048

Entry



B0L102S, U12-ch42, adc2

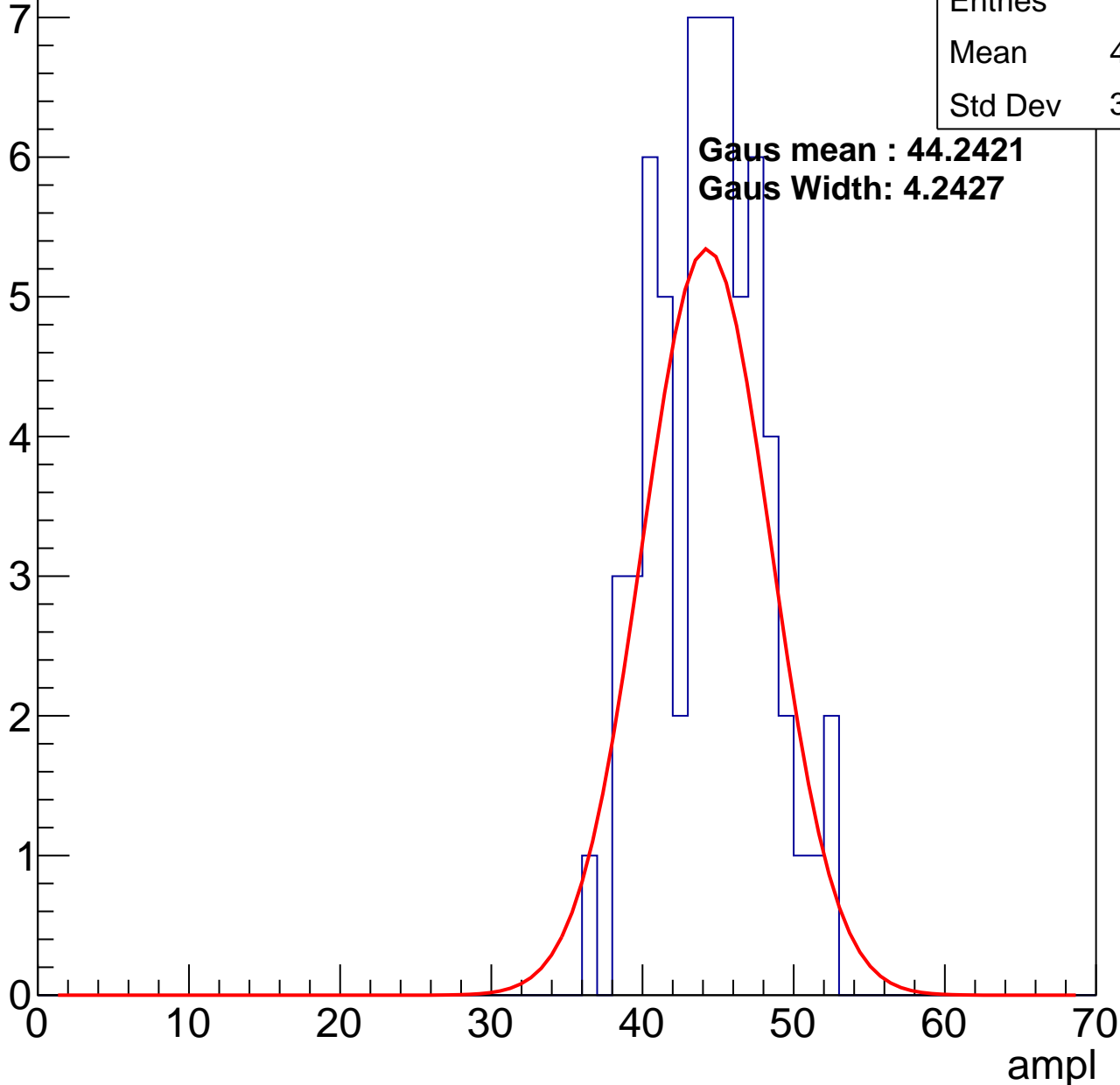
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	43.98
Std Dev	3.617

Gaus mean : 44.2421

Gaus Width: 4.2427

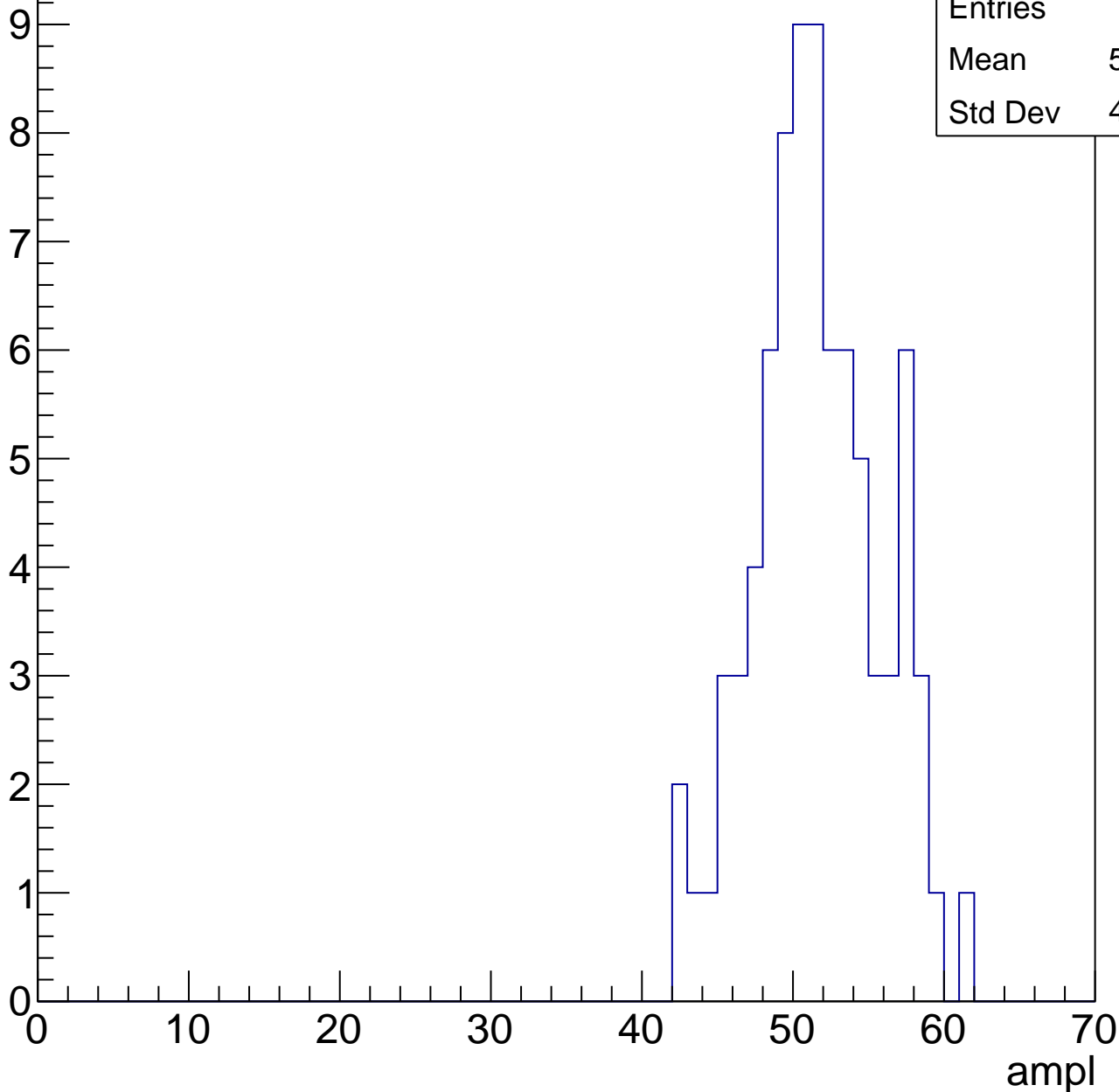


B0L102S, U12-ch42, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	51.12
Std Dev	4.094

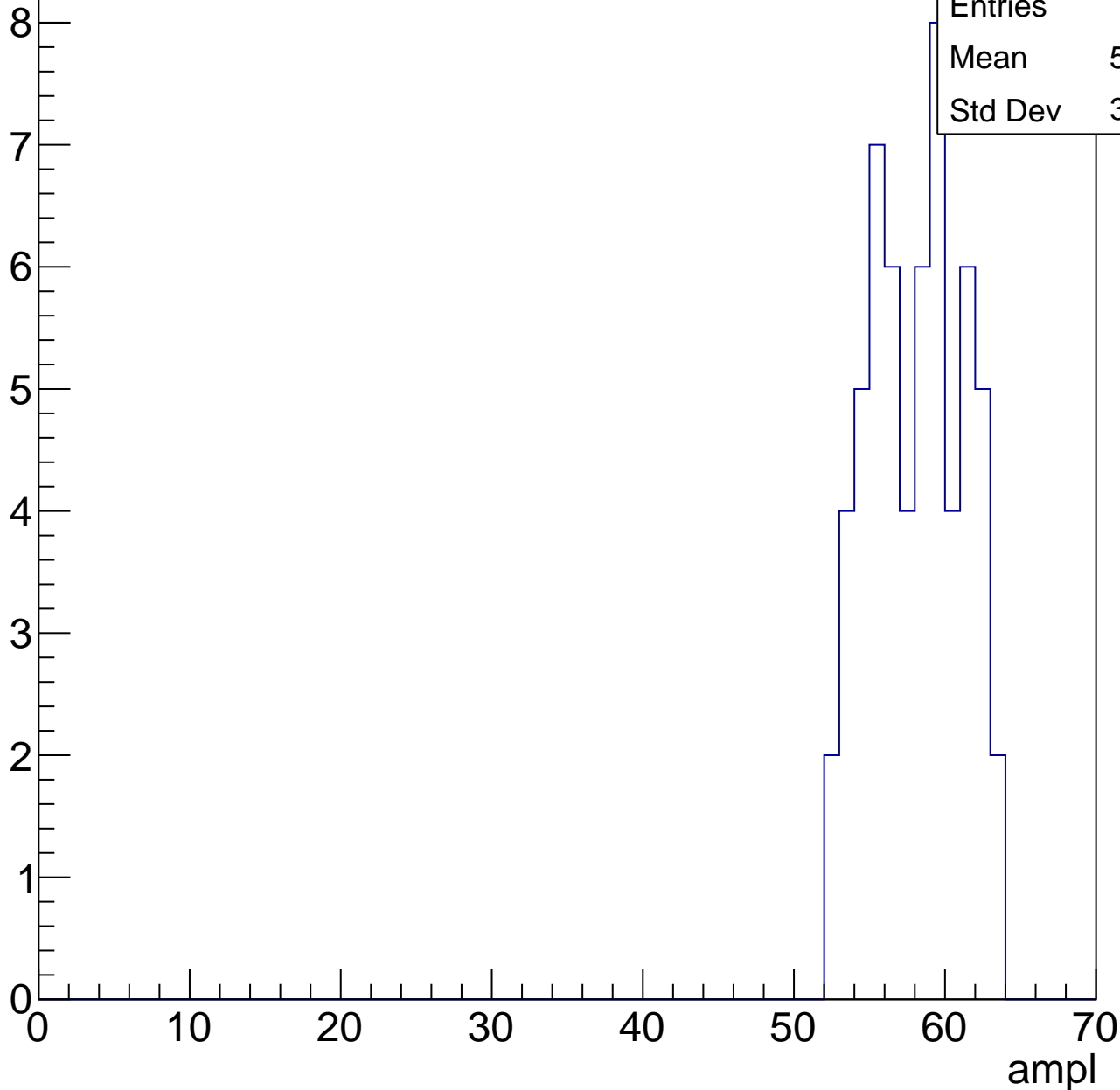


B0L102S, U12-ch42, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	57.58
Std Dev	3.026

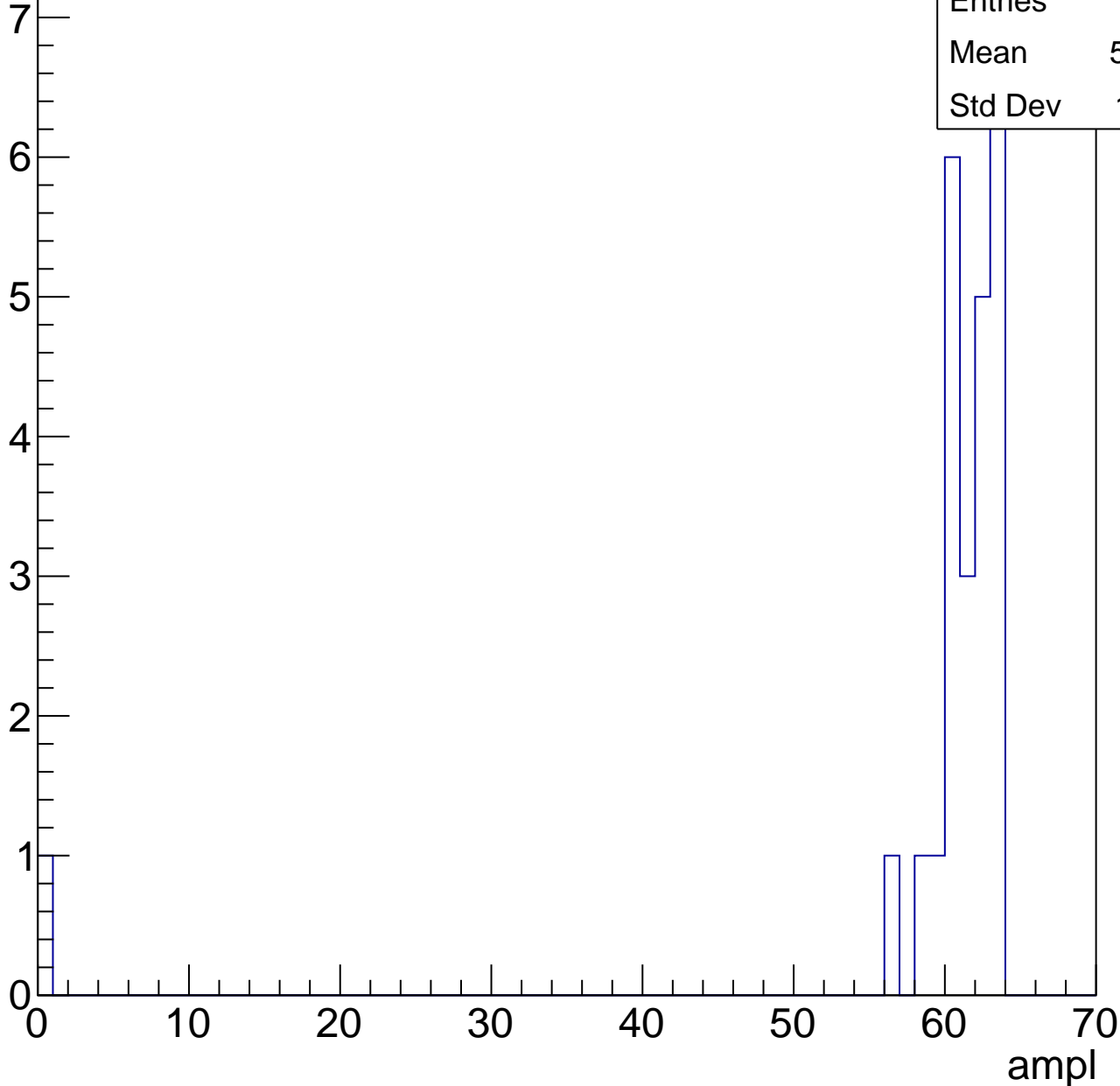


B0L102S, U12-ch42, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	25
Mean	58.68
Std Dev	12.11



B0L102S, U12-ch42, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch42, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch43, adc0

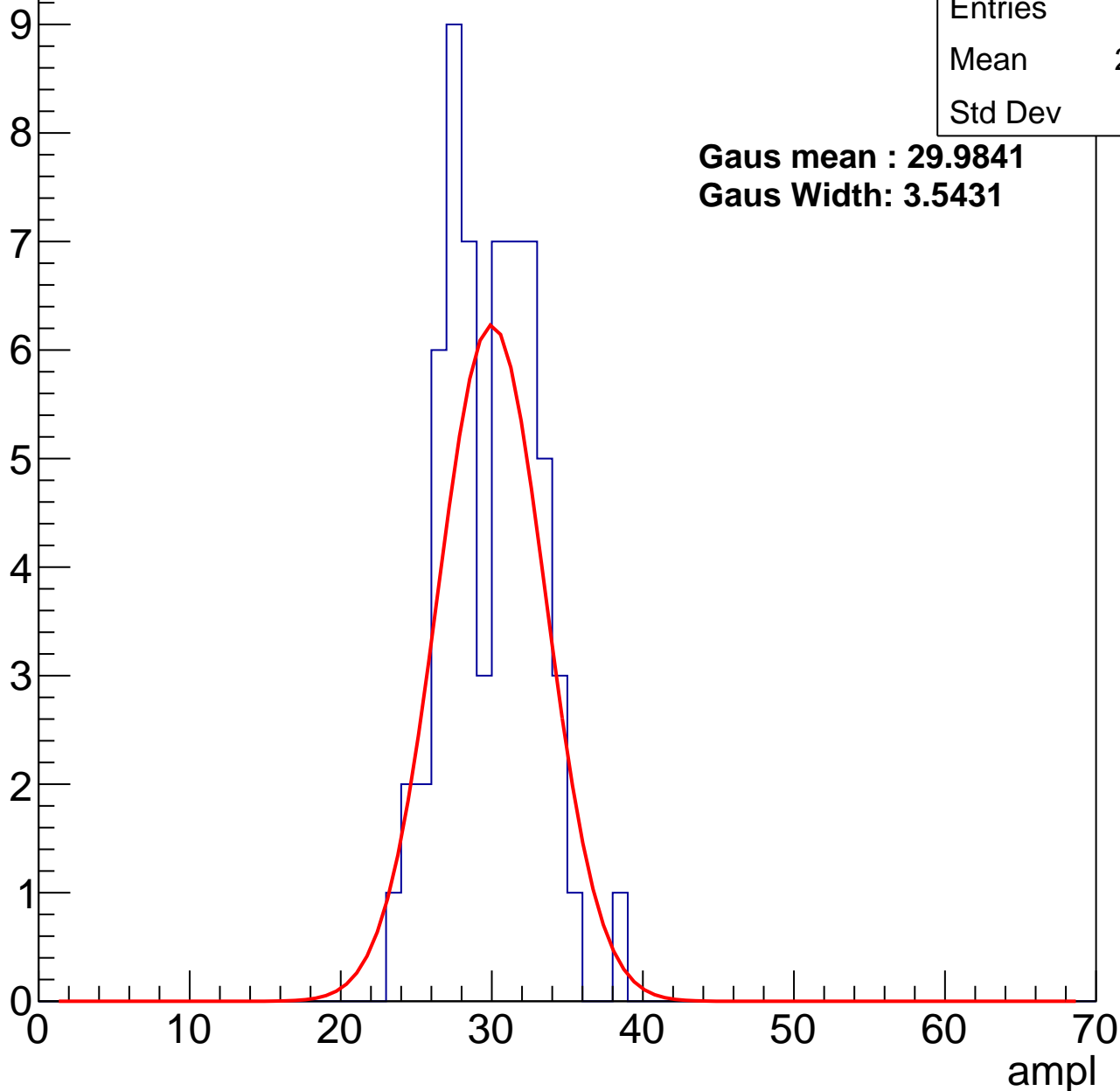
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	29.41
Std Dev	3.07

Gaus mean : 29.9841

Gaus Width: 3.5431



B0L102S, U12-ch43, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	35.83
Std Dev	3.599

Gaus mean : 36.1943

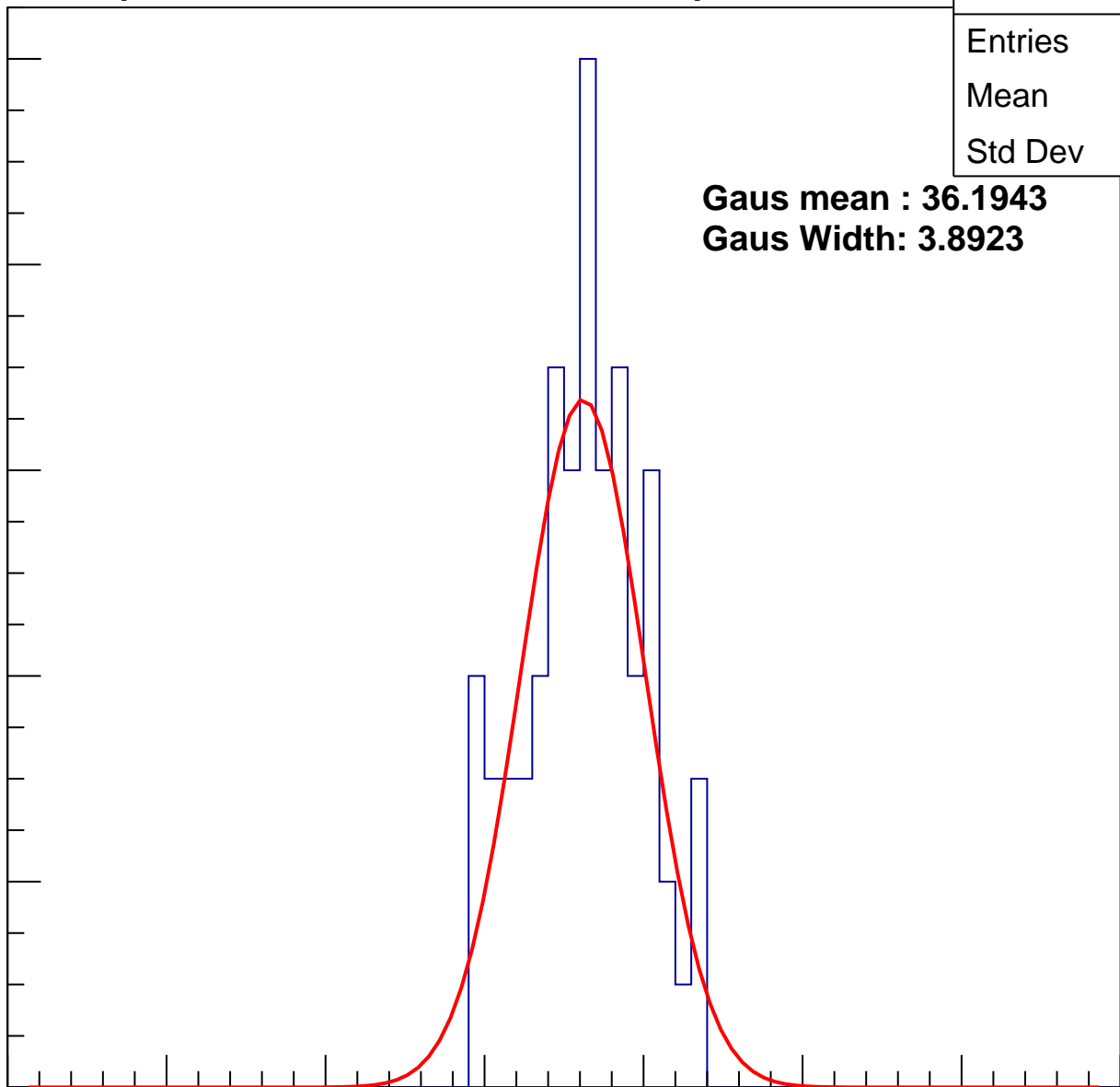
Gaus Width: 3.8923

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch43, adc2

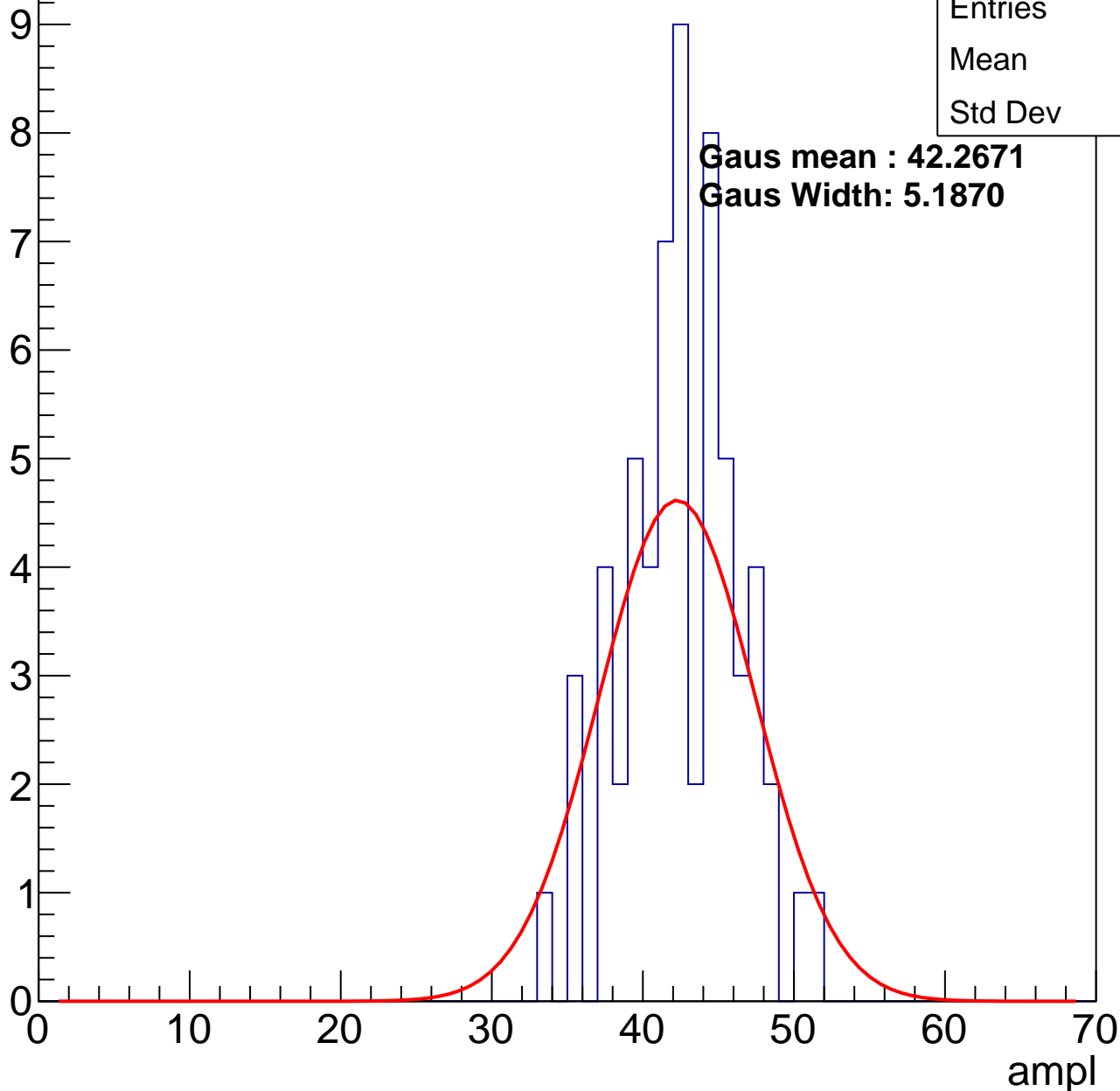
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	42.1
Std Dev	3.78

Gaus mean : 42.2671

Gaus Width: 5.1870

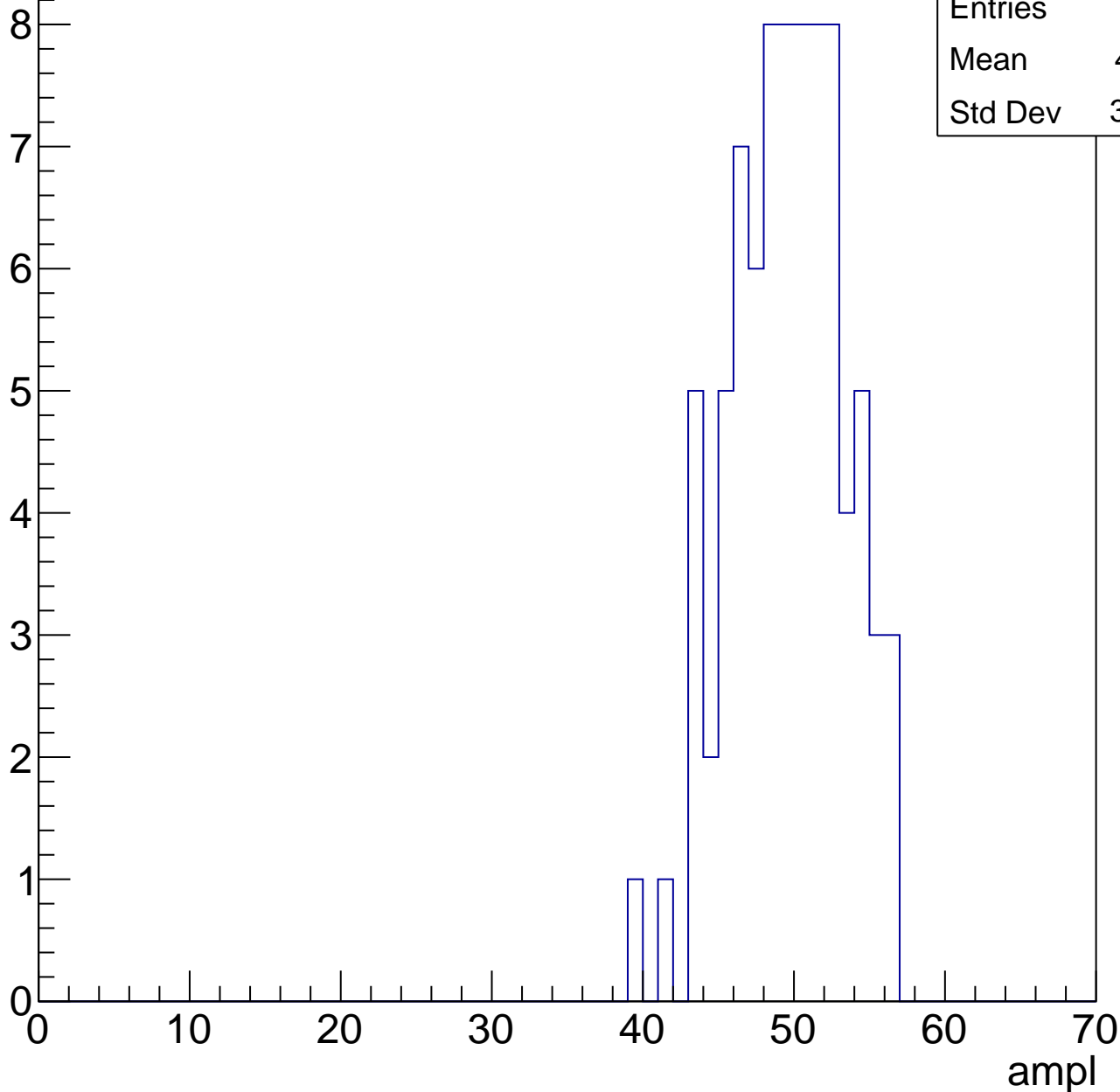


B0L102S, U12-ch43, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	49.11
Std Dev	3.702

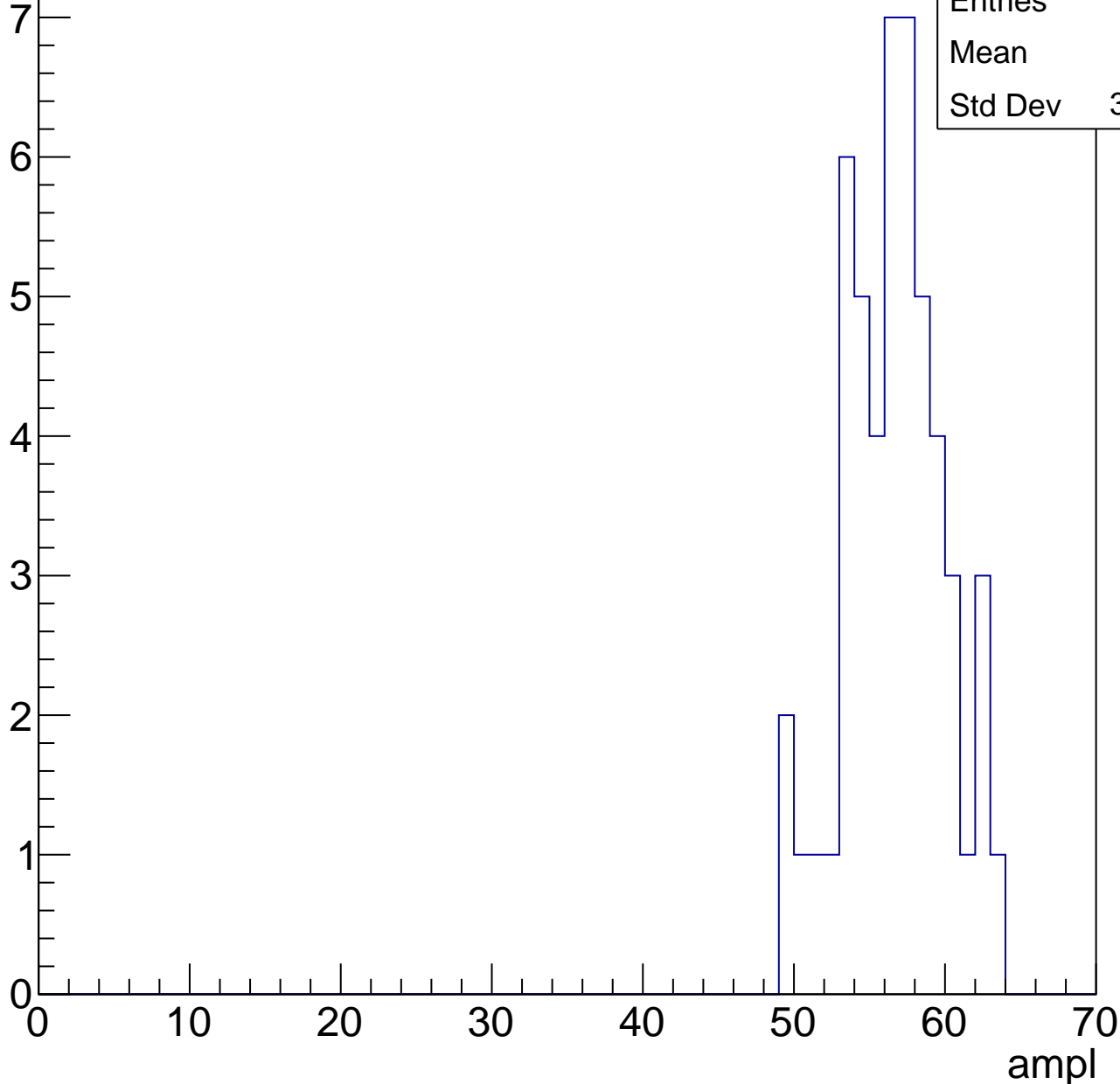


B0L102S, U12-ch43, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	56.2
Std Dev	3.266



B0L102S, U12-ch43, adc5

calib_packv5_042523_0143.root, FC#12, port B1

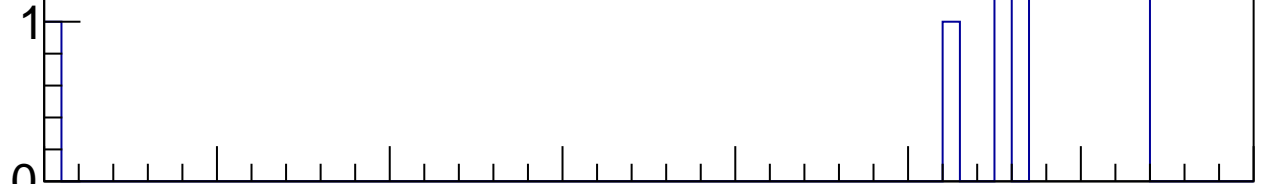
Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	58.47
Std Dev	9.159

ampl

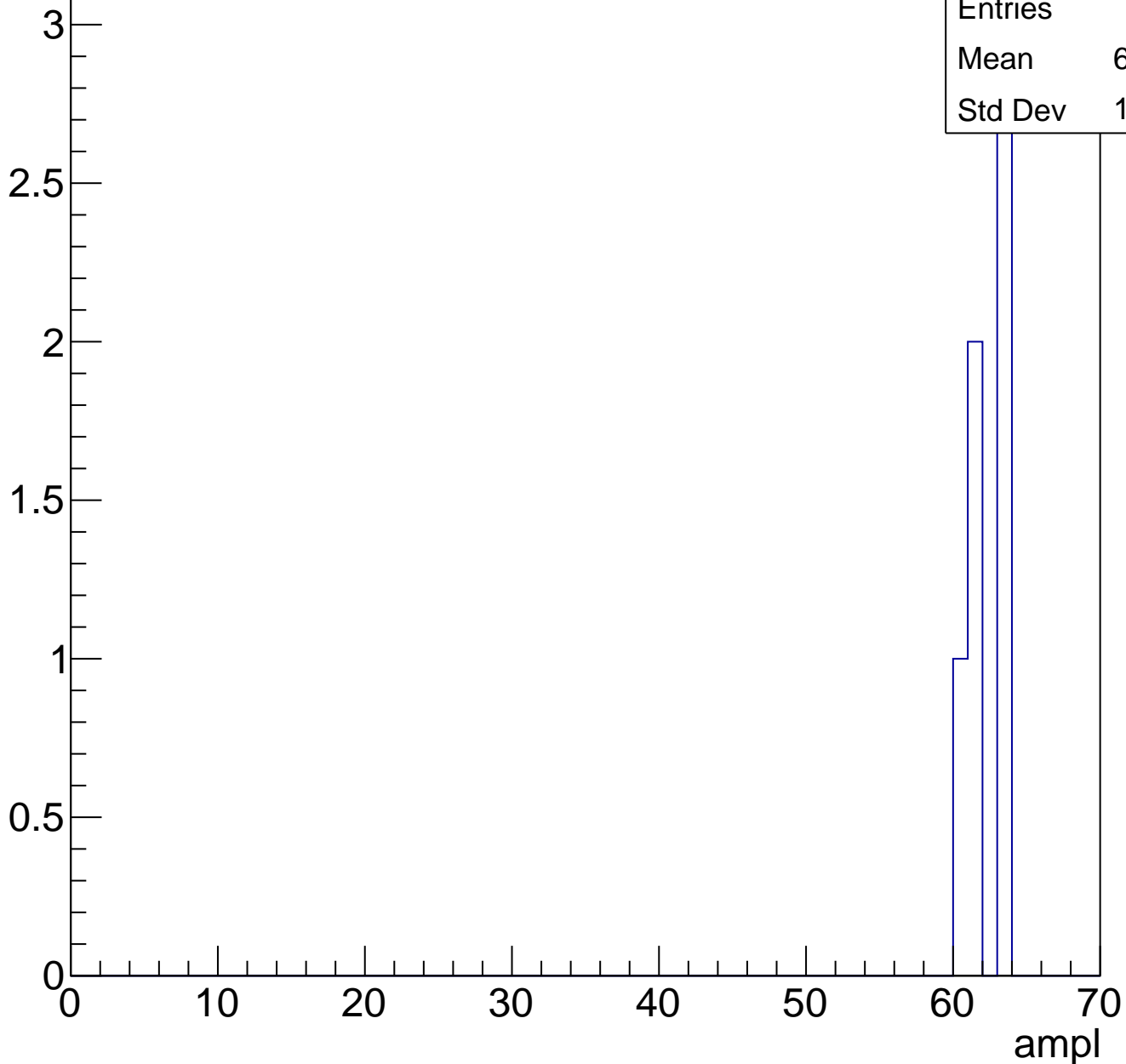
0 10 20 30 40 50 60 70



B0L102S, U12-ch43, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch43, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch44, adc0

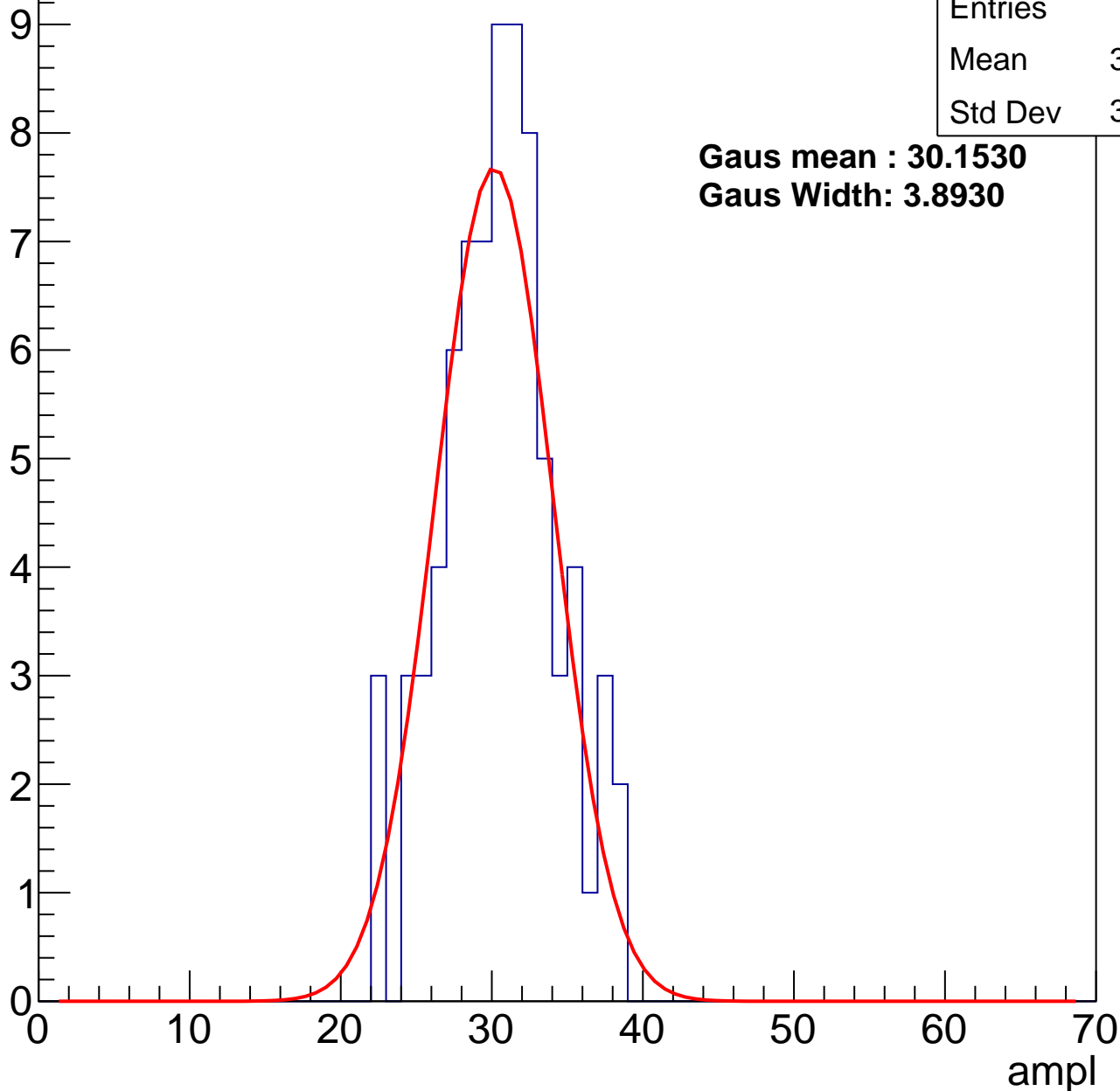
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	30.04
Std Dev	3.733

Gaus mean : 30.1530

Gaus Width: 3.8930



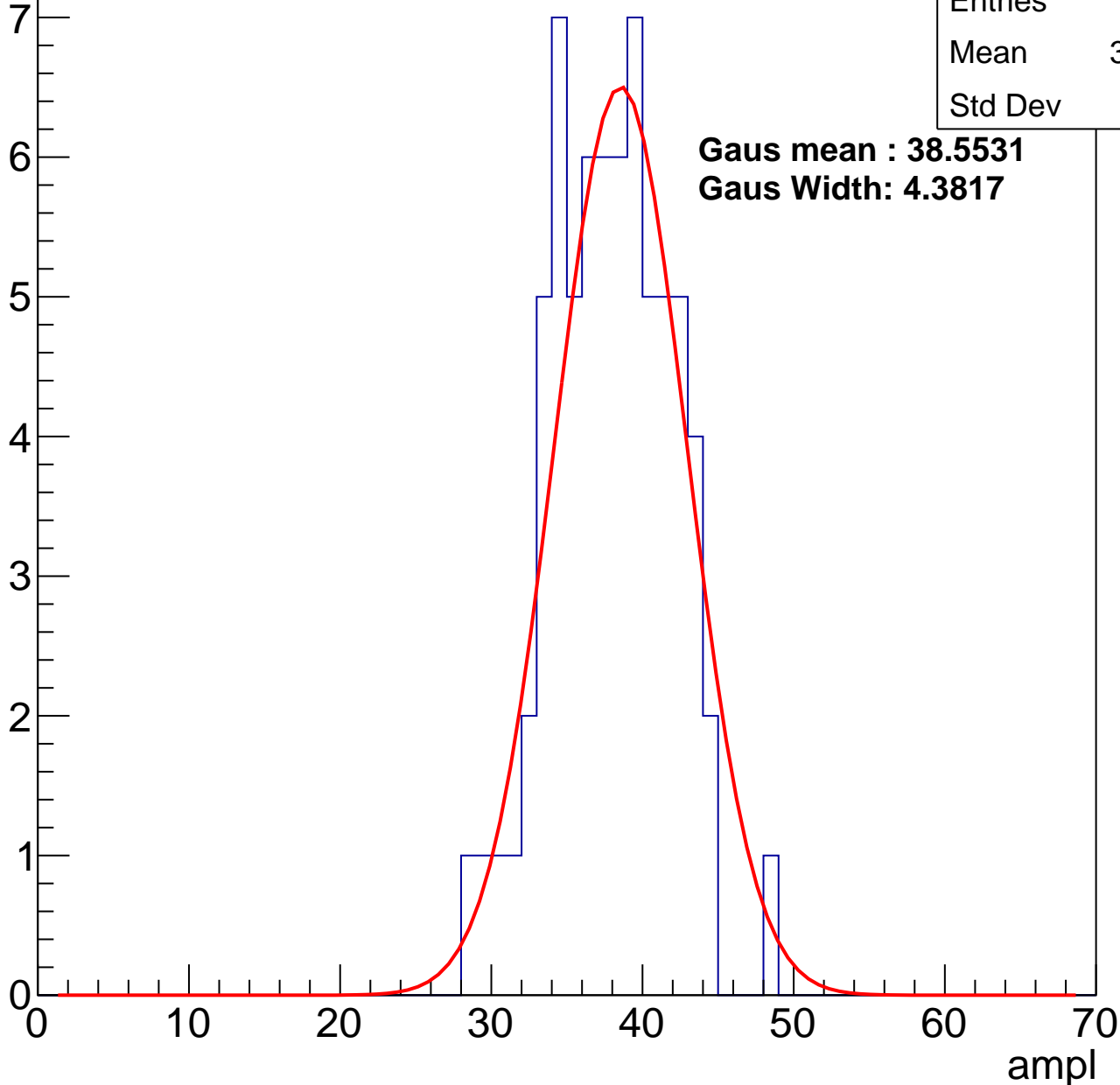
B0L102S, U12-ch44, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	37.46
Std Dev	3.93

Gaus mean : 38.5531
Gaus Width: 4.3817



B0L102S, U12-ch44, adc2

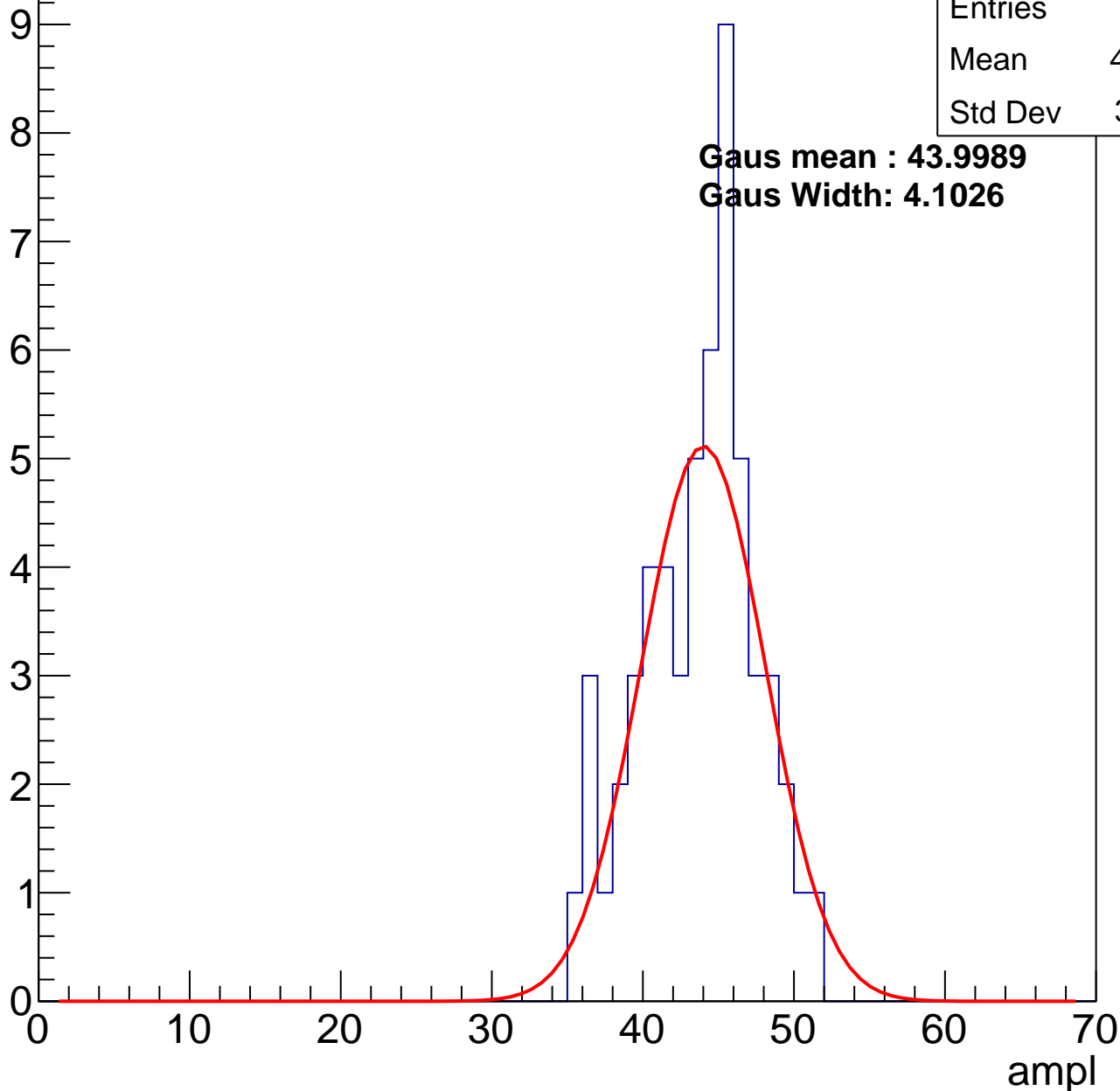
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	43.23
Std Dev	3.761

Gaus mean : 43.9989

Gaus Width: 4.1026

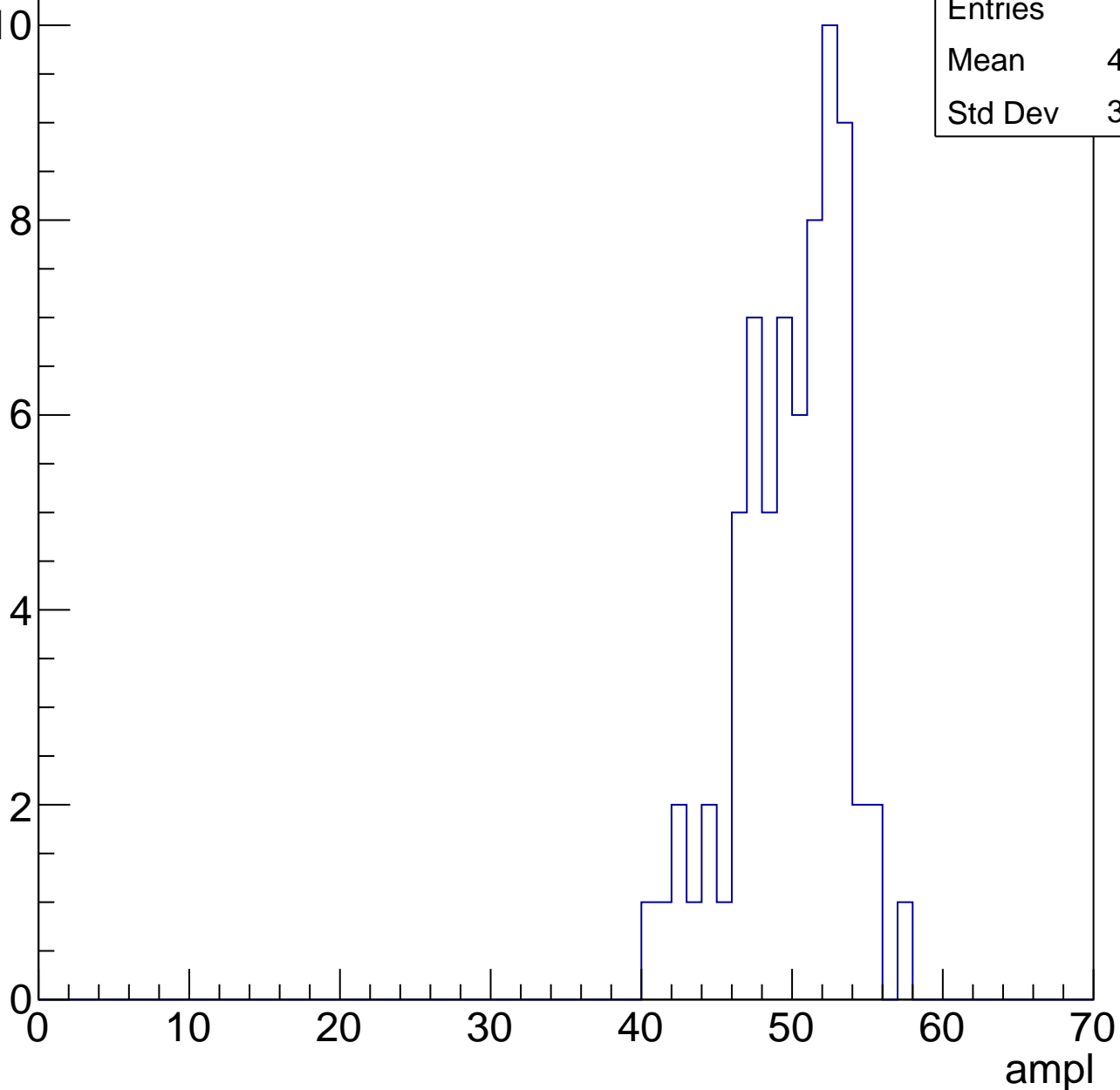


B0L102S, U12-ch44, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.47
Std Dev	3.516

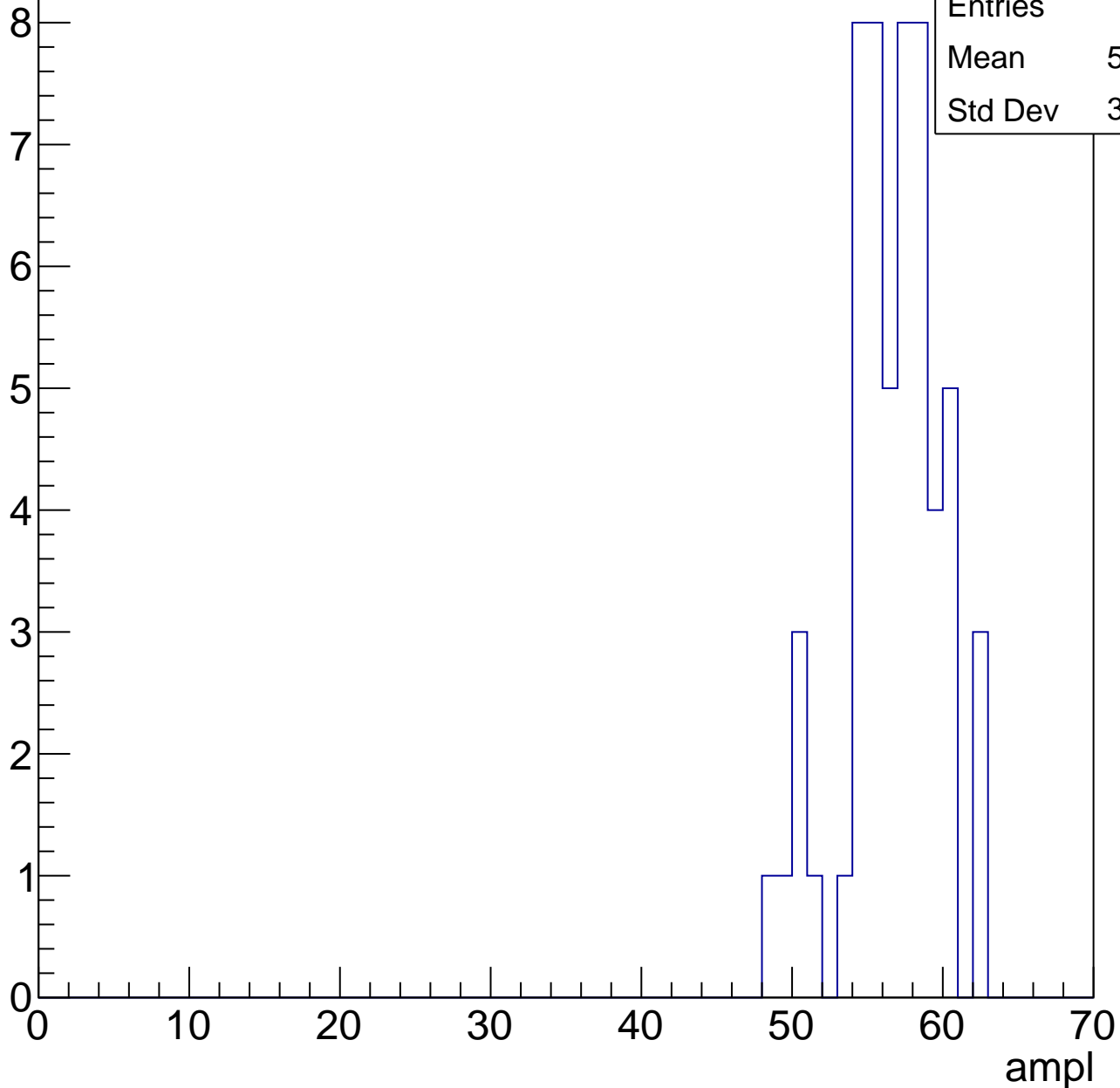


B0L102S, U12-ch44, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

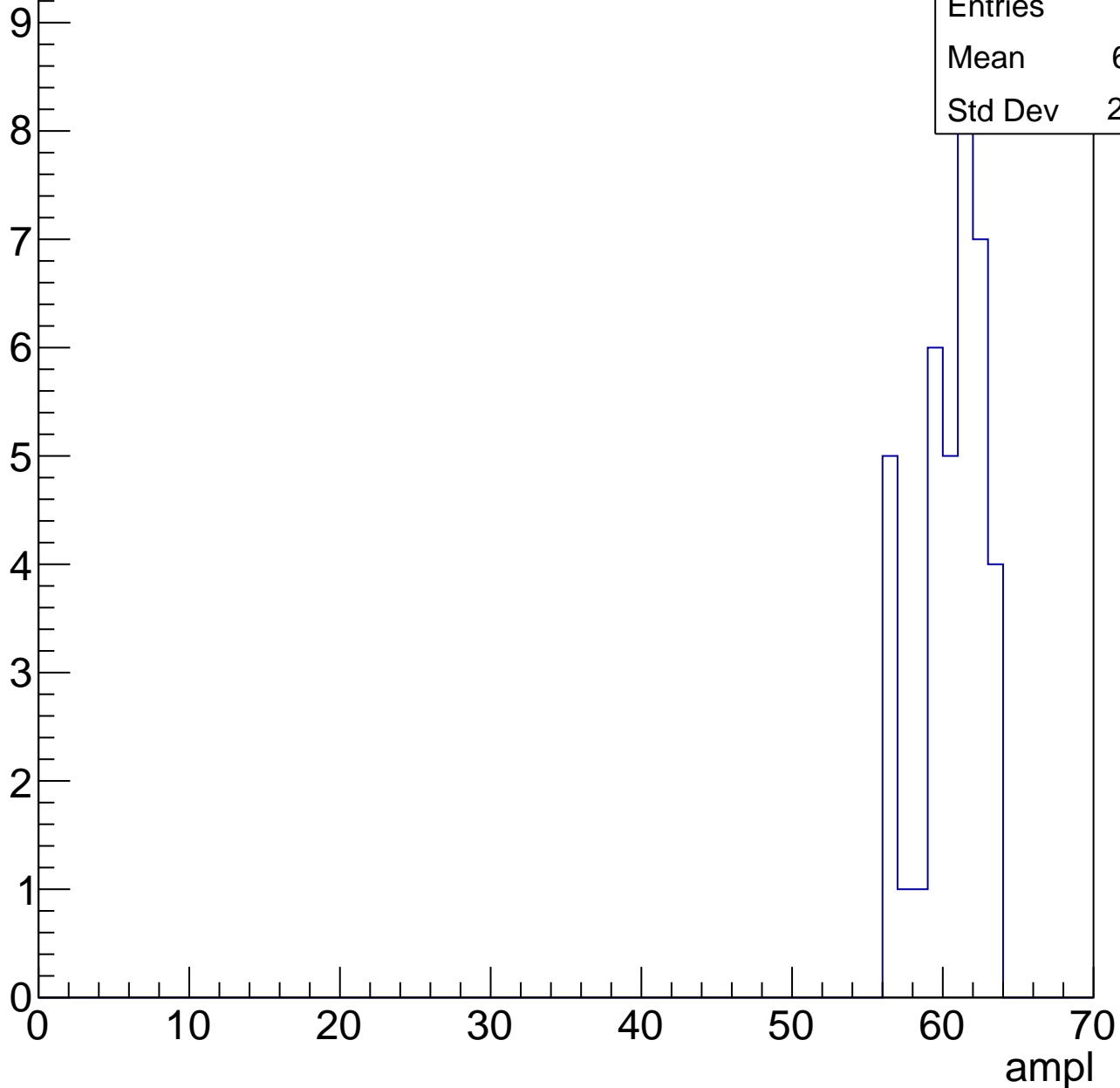
Entries	56
Mean	56.16
Std Dev	3.155



B0L102S, U12-ch44, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

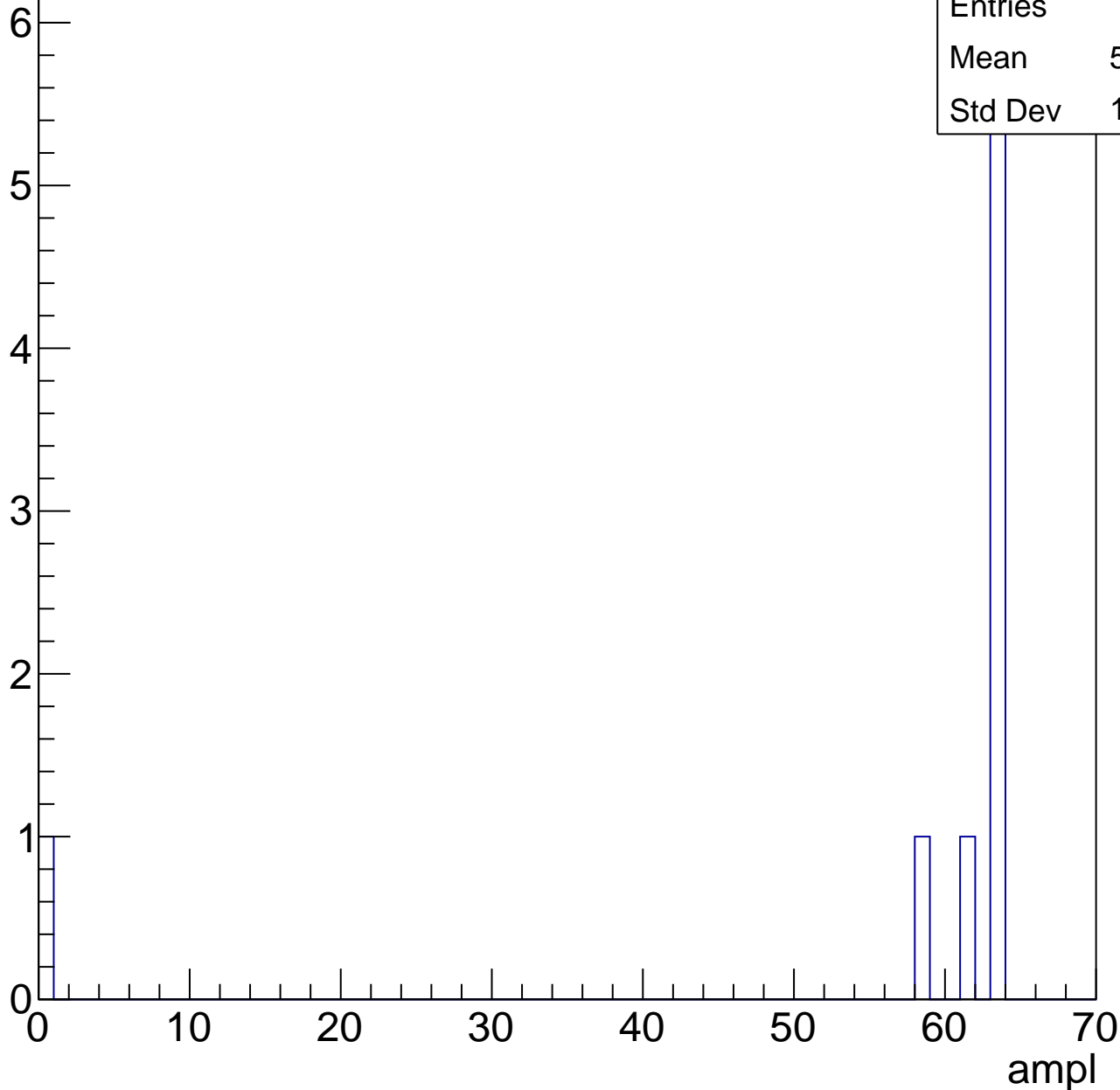


B0L102S, U12-ch44, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	9
Mean	55.22
Std Dev	19.59



B0L102S, U12-ch44, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10
Std Dev	10

B0L102S, U12-ch45, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	78
Mean	29.91
Std Dev	3.416

Gaus mean : 30.5643

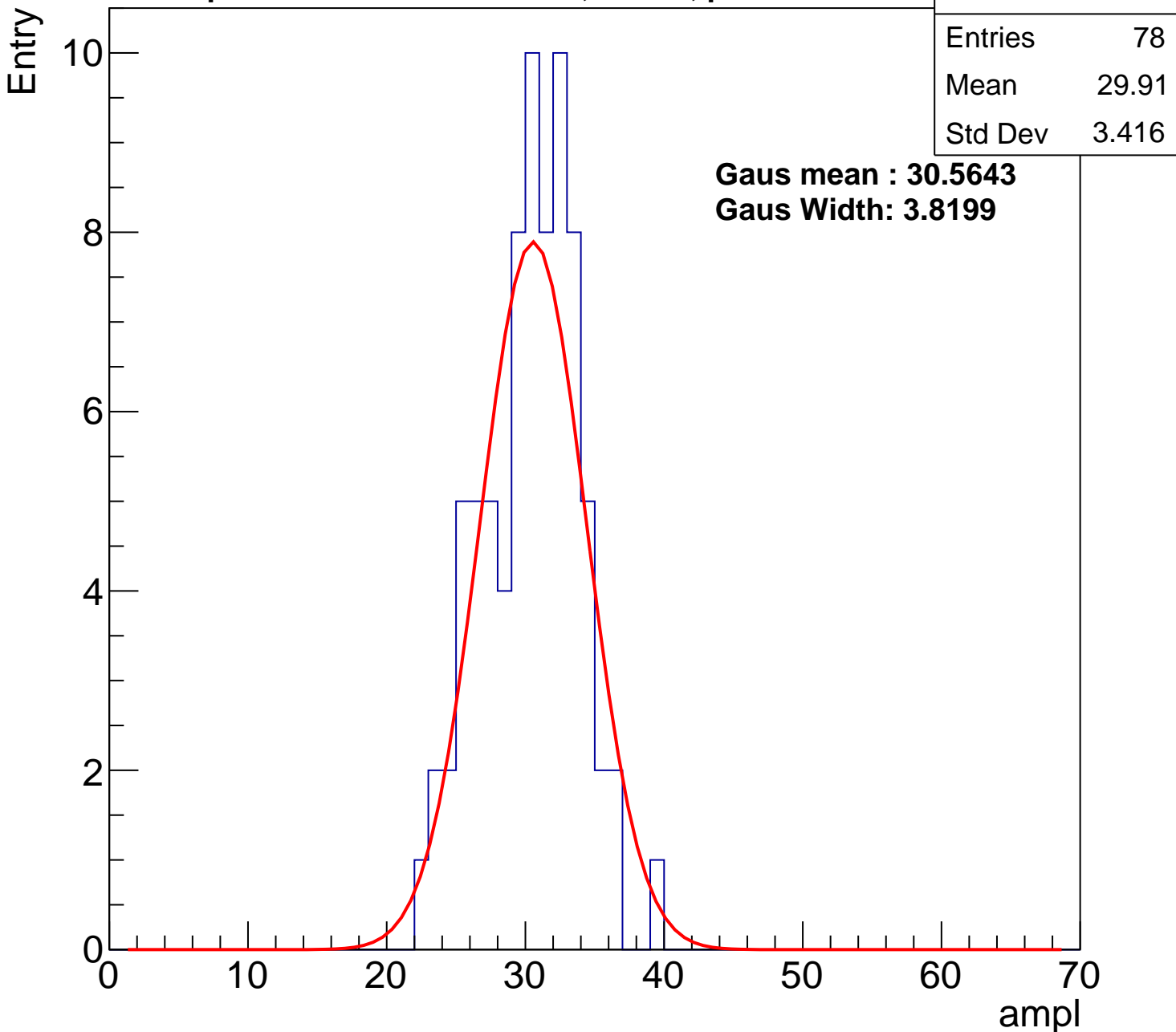
Gaus Width: 3.8199

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch45, adc1

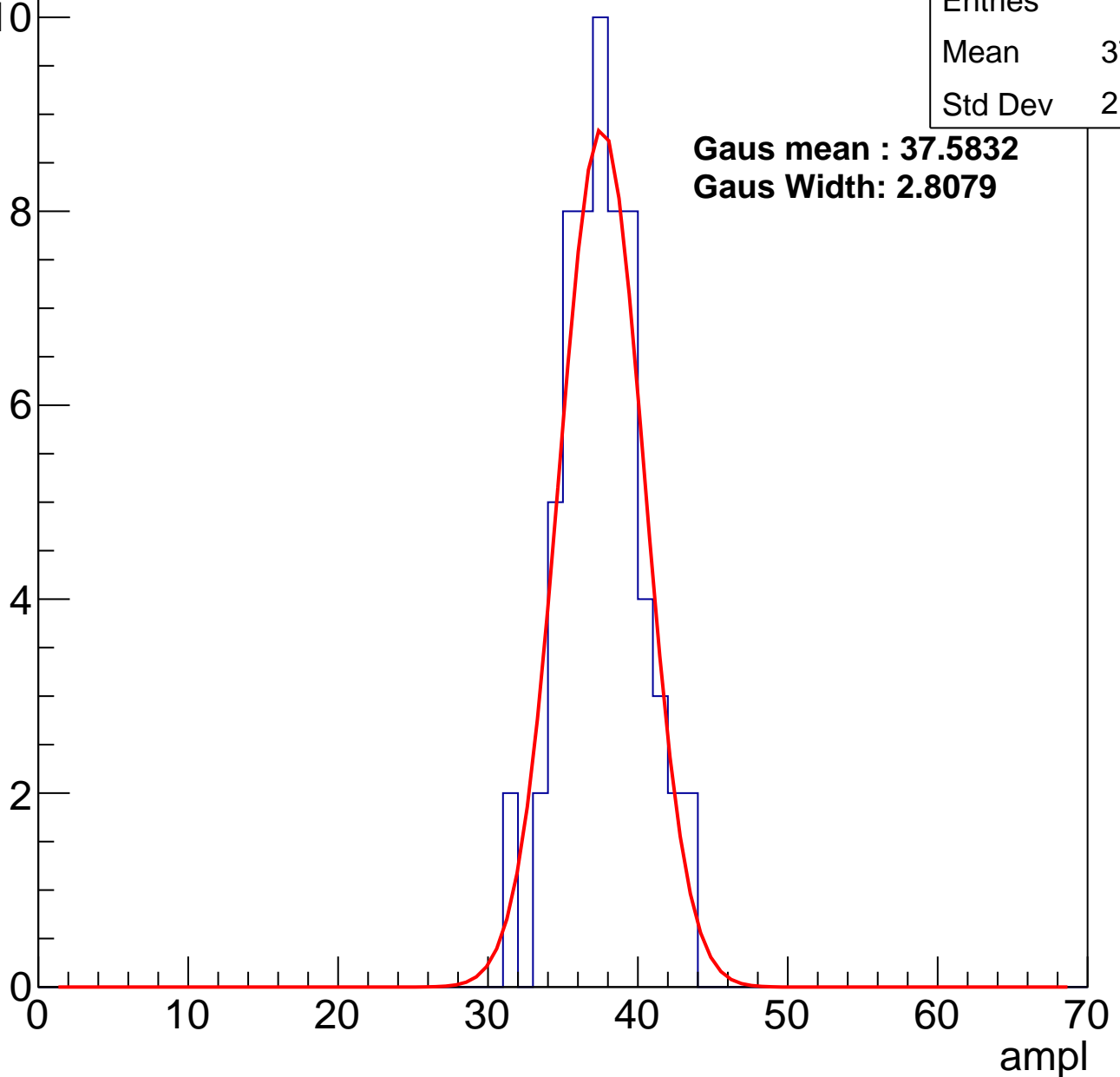
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.18
Std Dev	2.643

Gaus mean : 37.5832

Gaus Width: 2.8079



B0L102S, U12-ch45, adc2

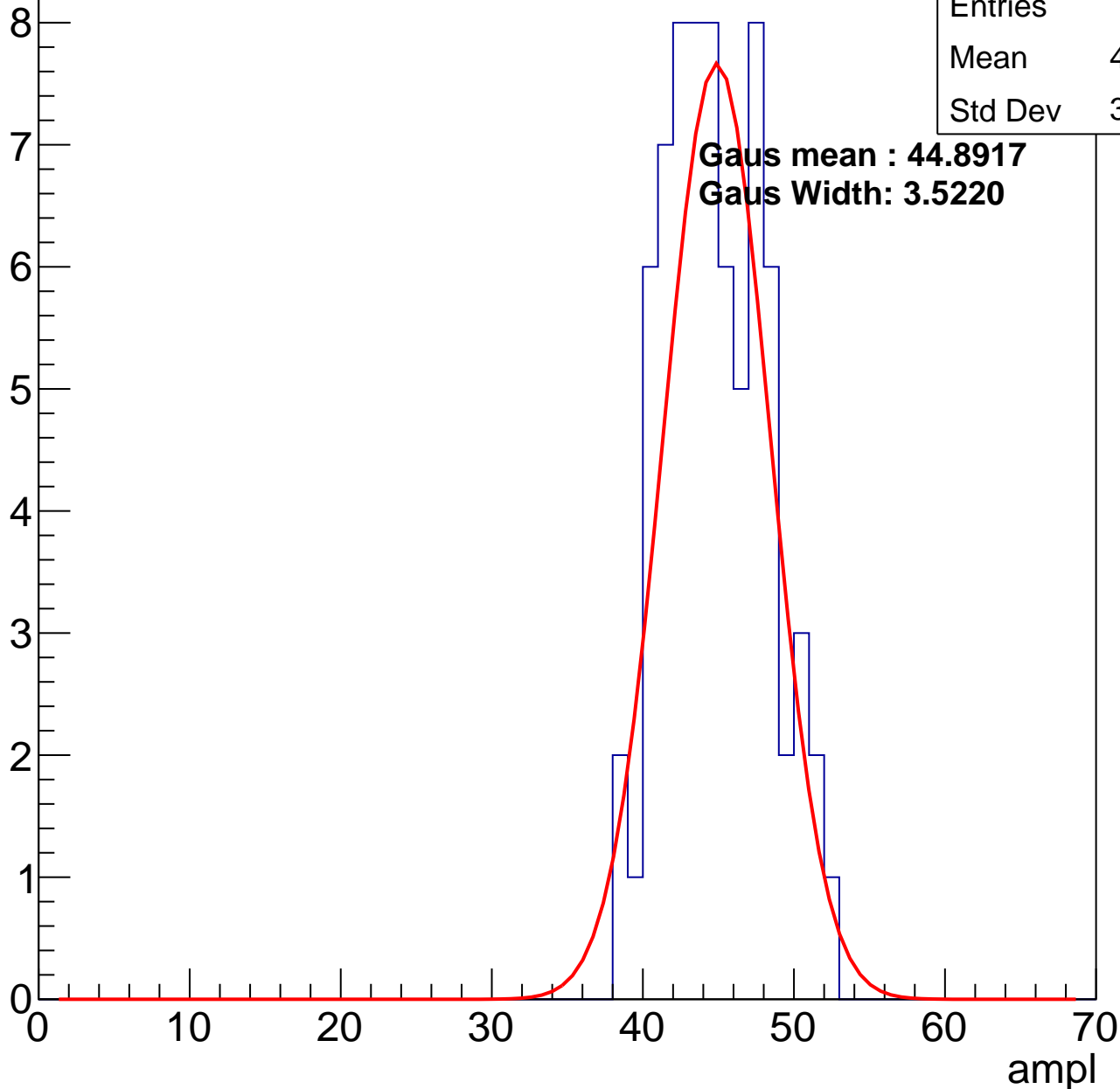
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	44.38
Std Dev	3.309

Gaus mean : 44.8917

Gaus Width: 3.5220

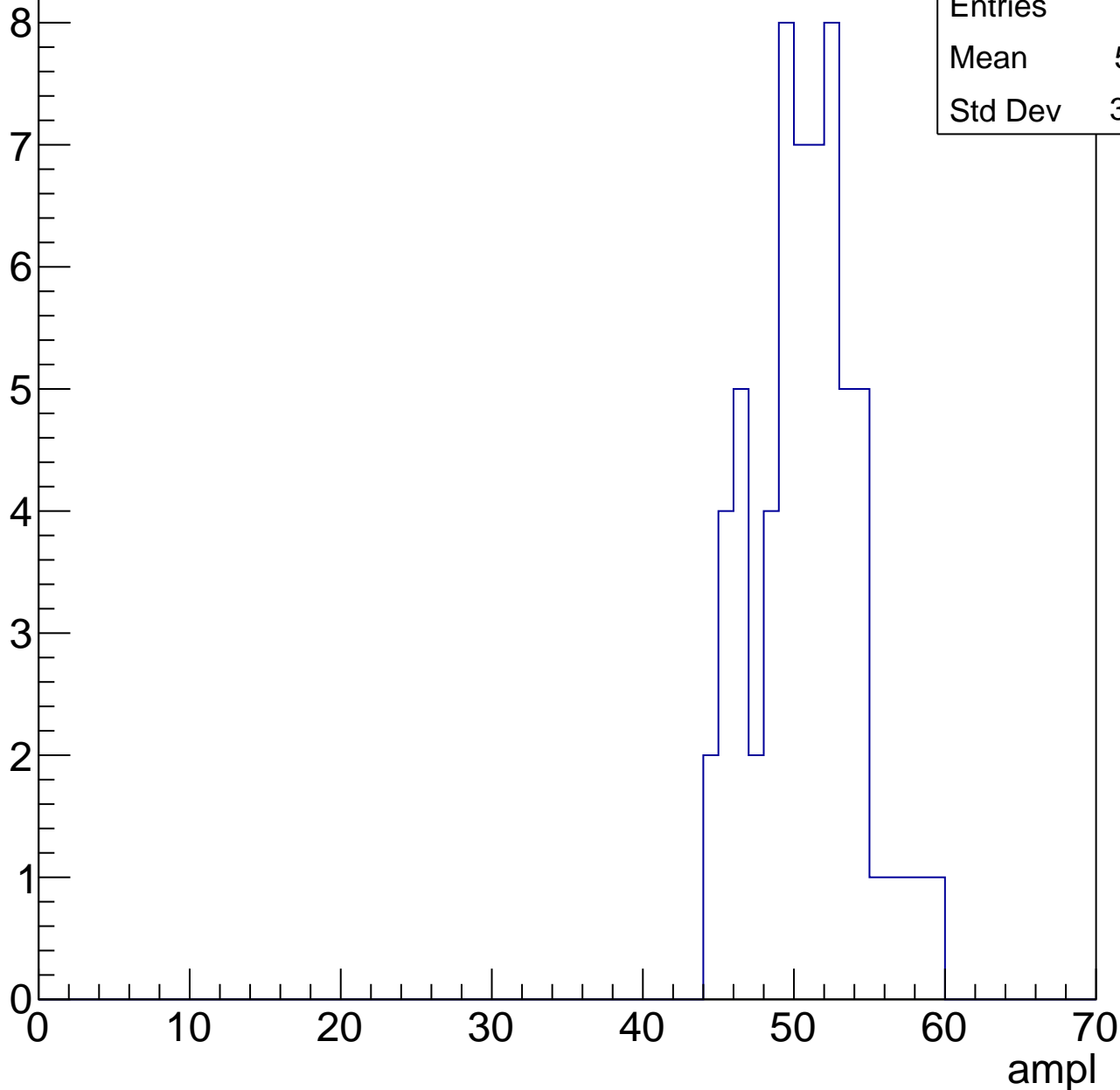


B0L102S, U12-ch45, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	50.31
Std Dev	3.377

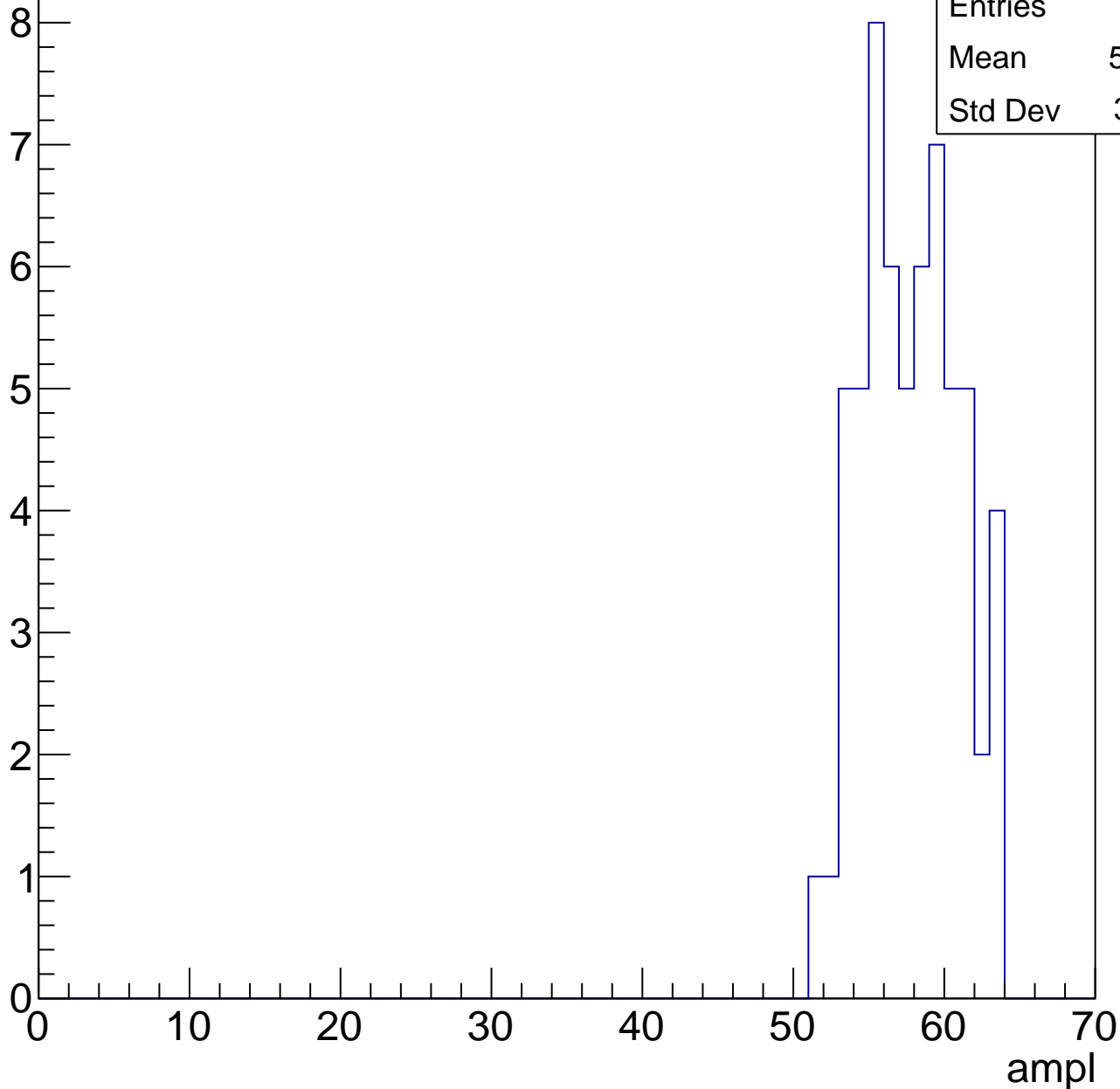


B0L102S, U12-ch45, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	57.35
Std Dev	3.081

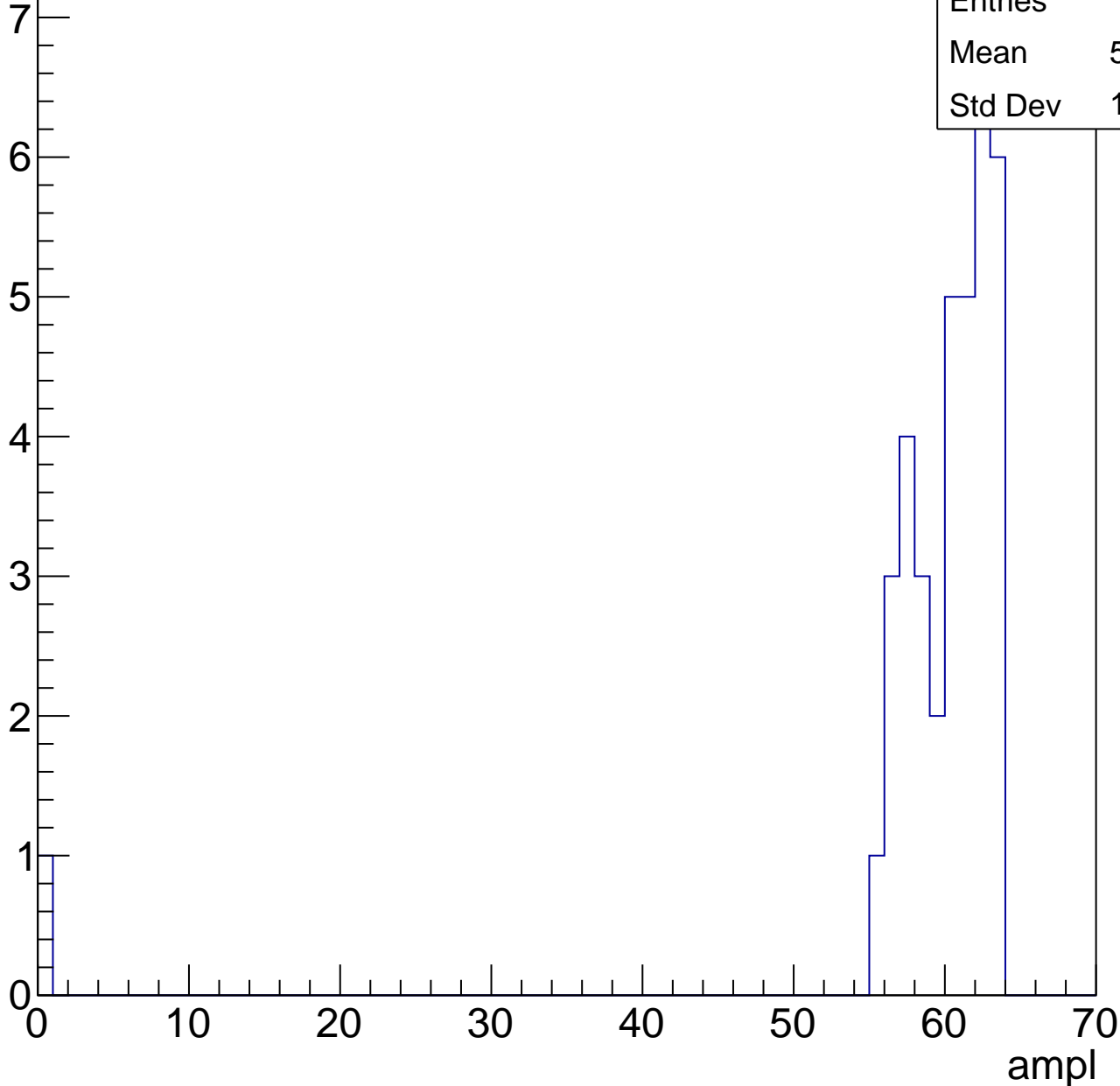


B0L102S, U12-ch45, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	37
Mean	58.38
Std Dev	10.02



B0L102S, U12-ch45, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch45, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch46, adc0

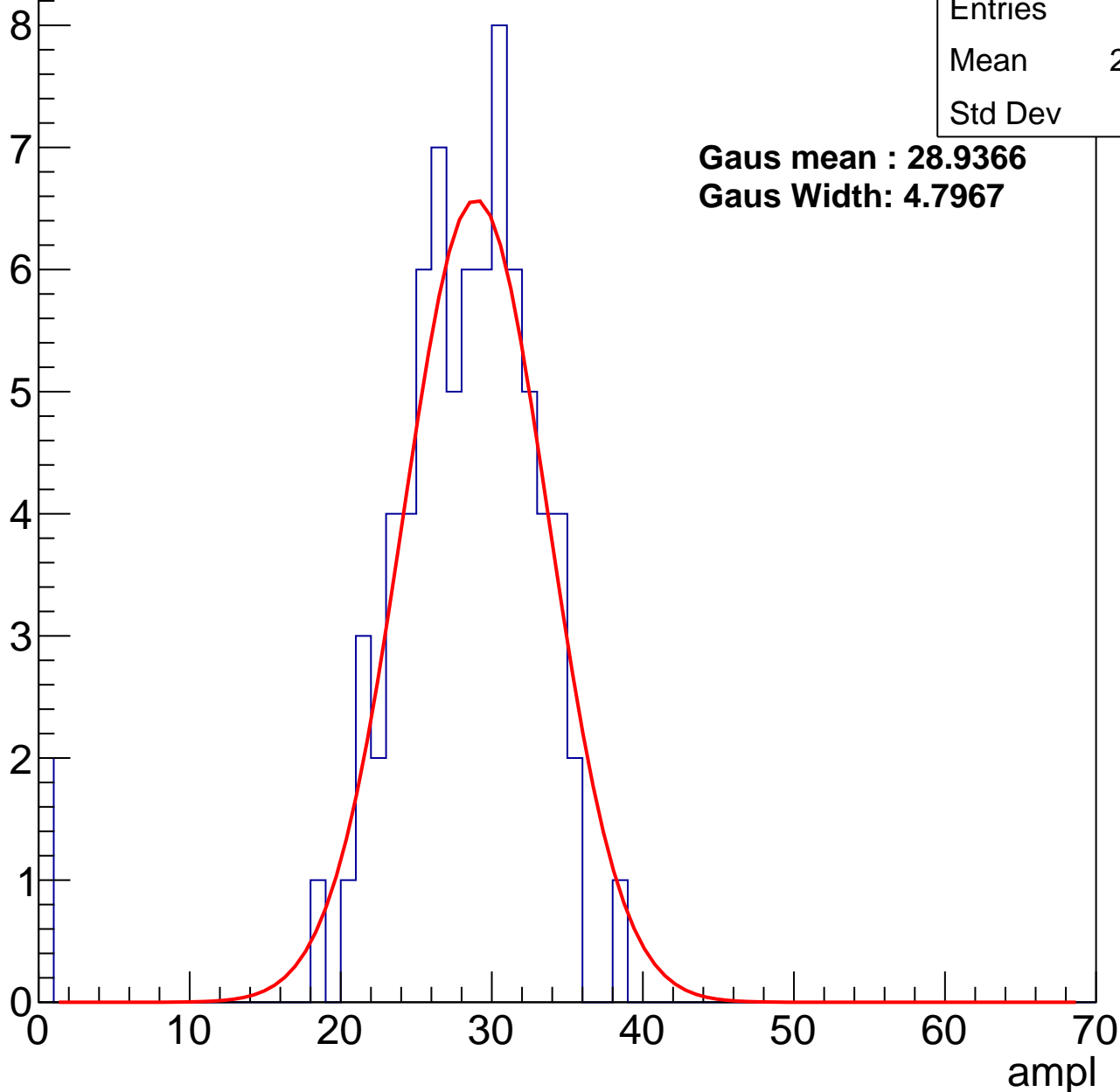
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	27.32
Std Dev	6.01

Gaus mean : 28.9366

Gaus Width: 4.7967



B0L102S, U12-ch46, adc1

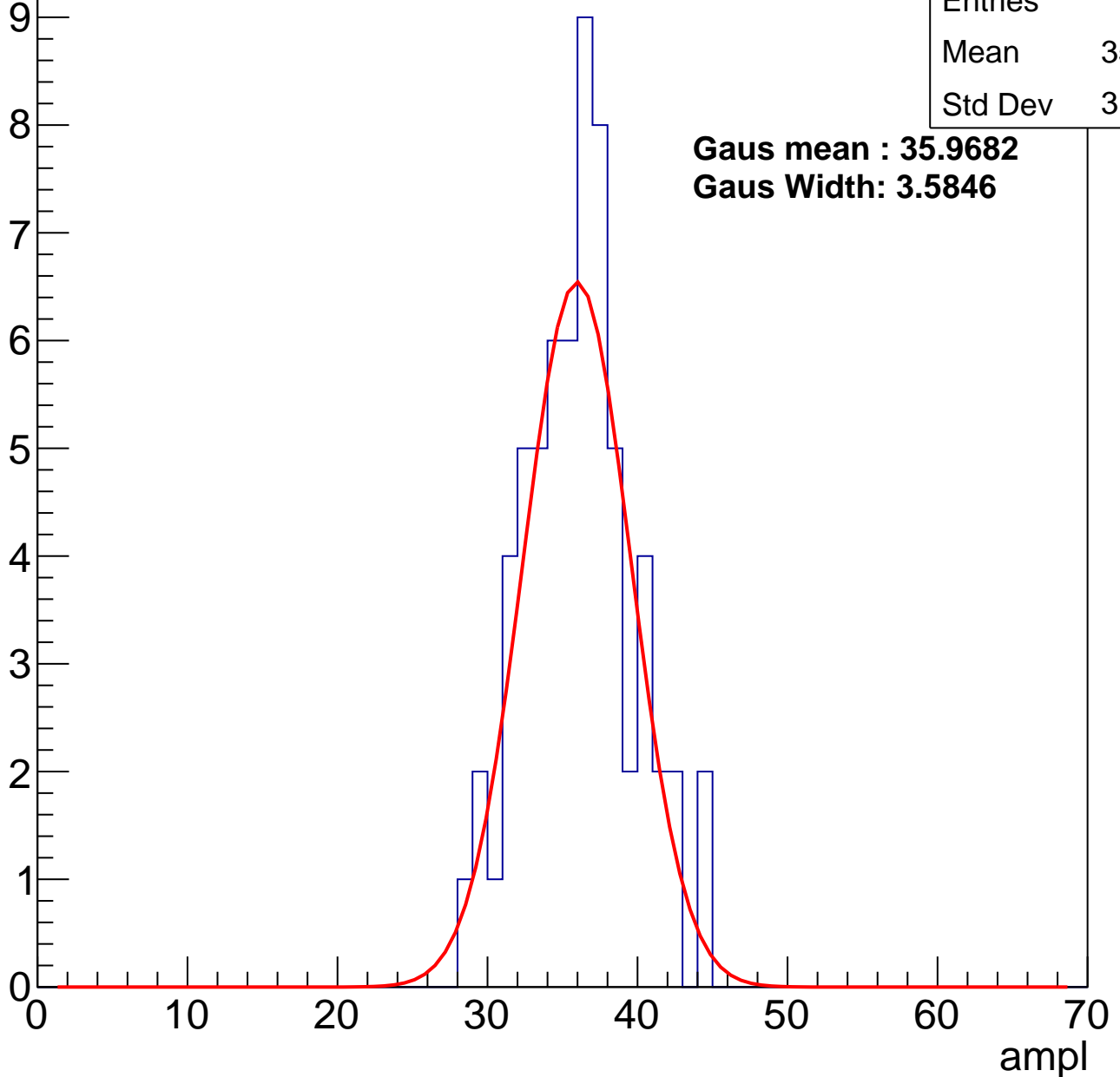
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.64
Std Dev	3.546

Gaus mean : 35.9682

Gaus Width: 3.5846



B0L102S, U12-ch46, adc2

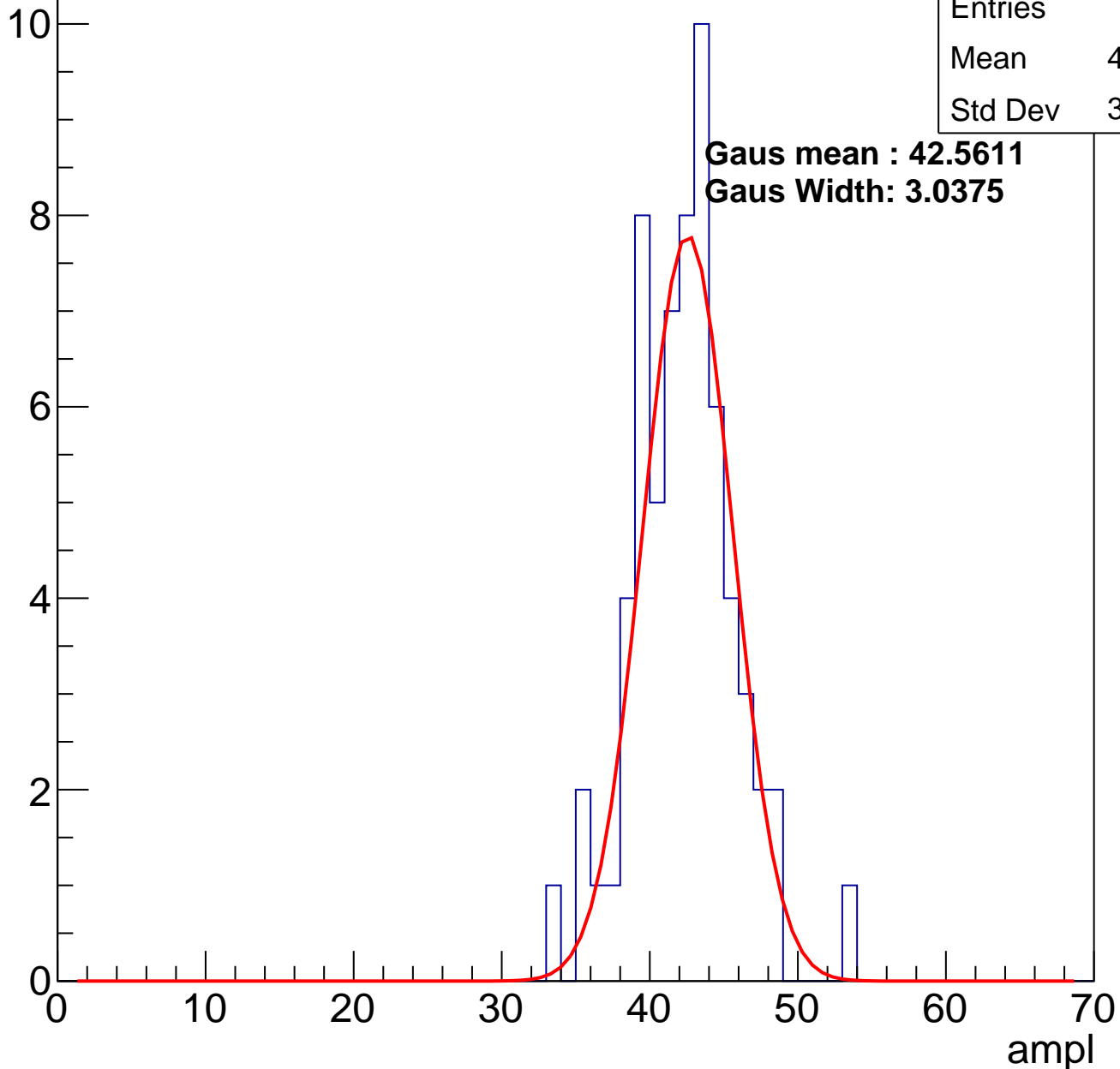
calib_packv5_042523_0143.root, FC#12, port B1

Entries	65
Mean	41.82
Std Dev	3.423

Gaus mean : 42.5611

Gaus Width: 3.0375

Entry

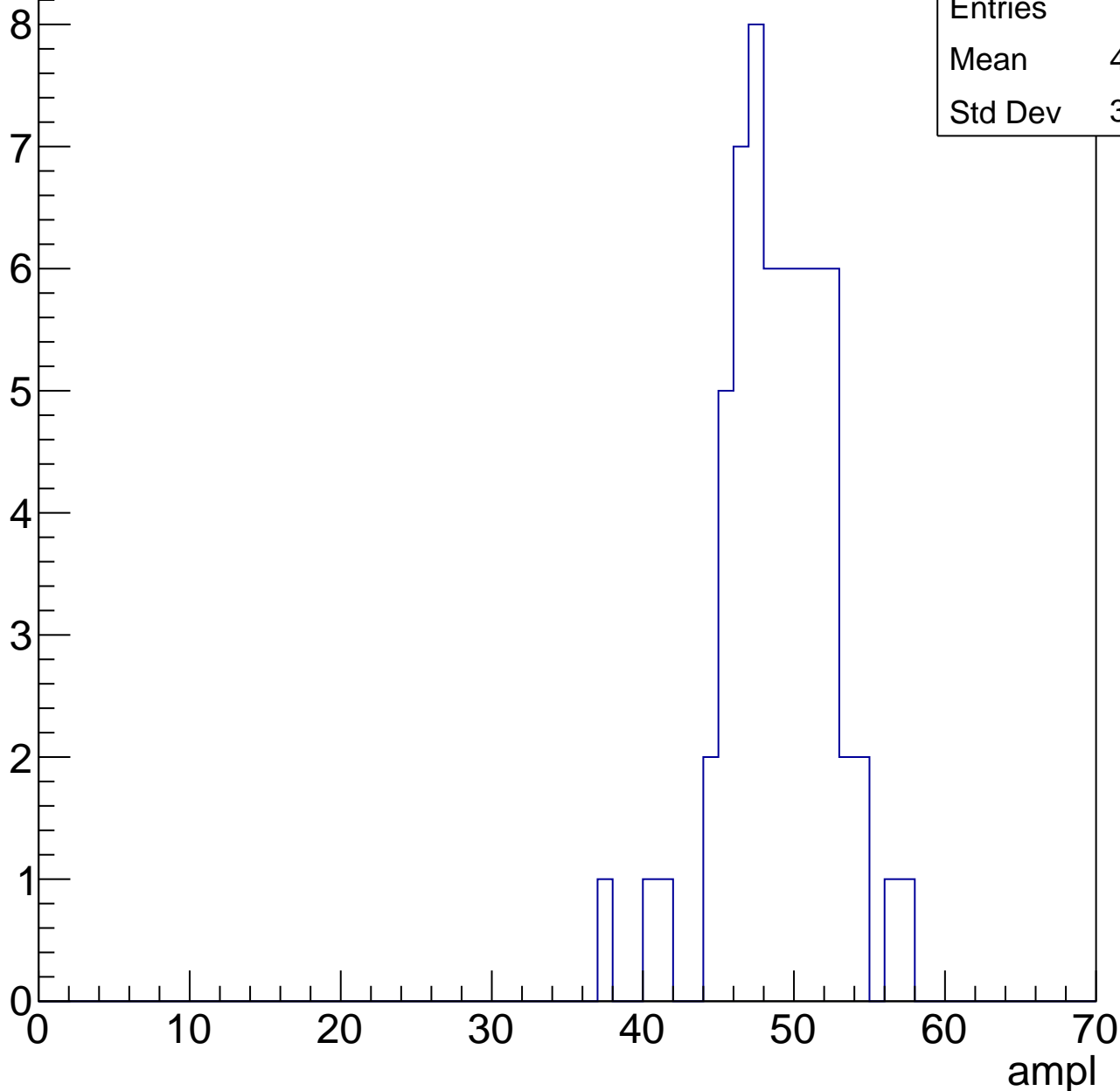


B0L102S, U12-ch46, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	48.46
Std Dev	3.579



B0L102S, U12-ch46, adc4

calib_packv5_042523_0143.root, FC#12, port B1

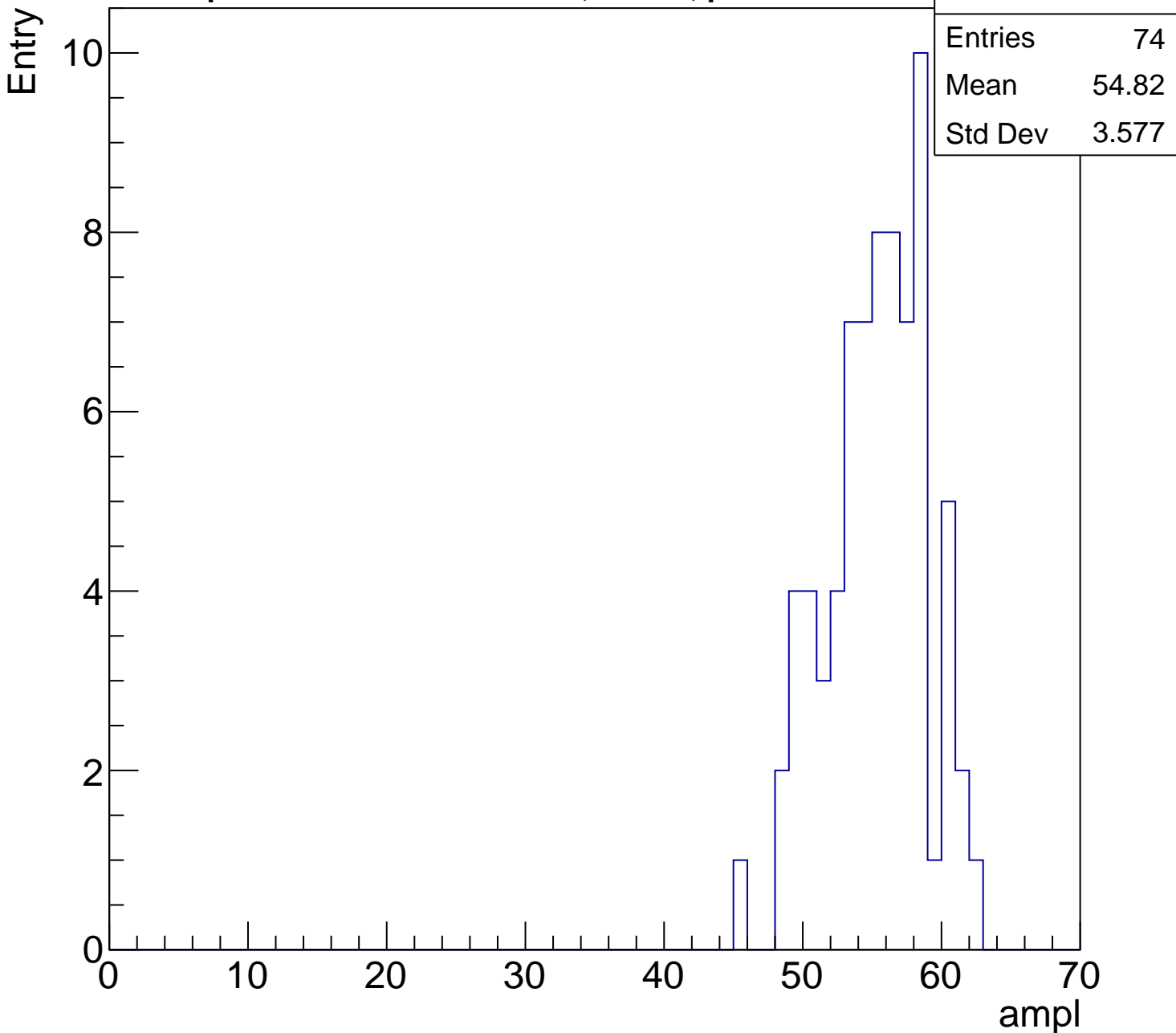
Entries	74
Mean	54.82
Std Dev	3.577

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

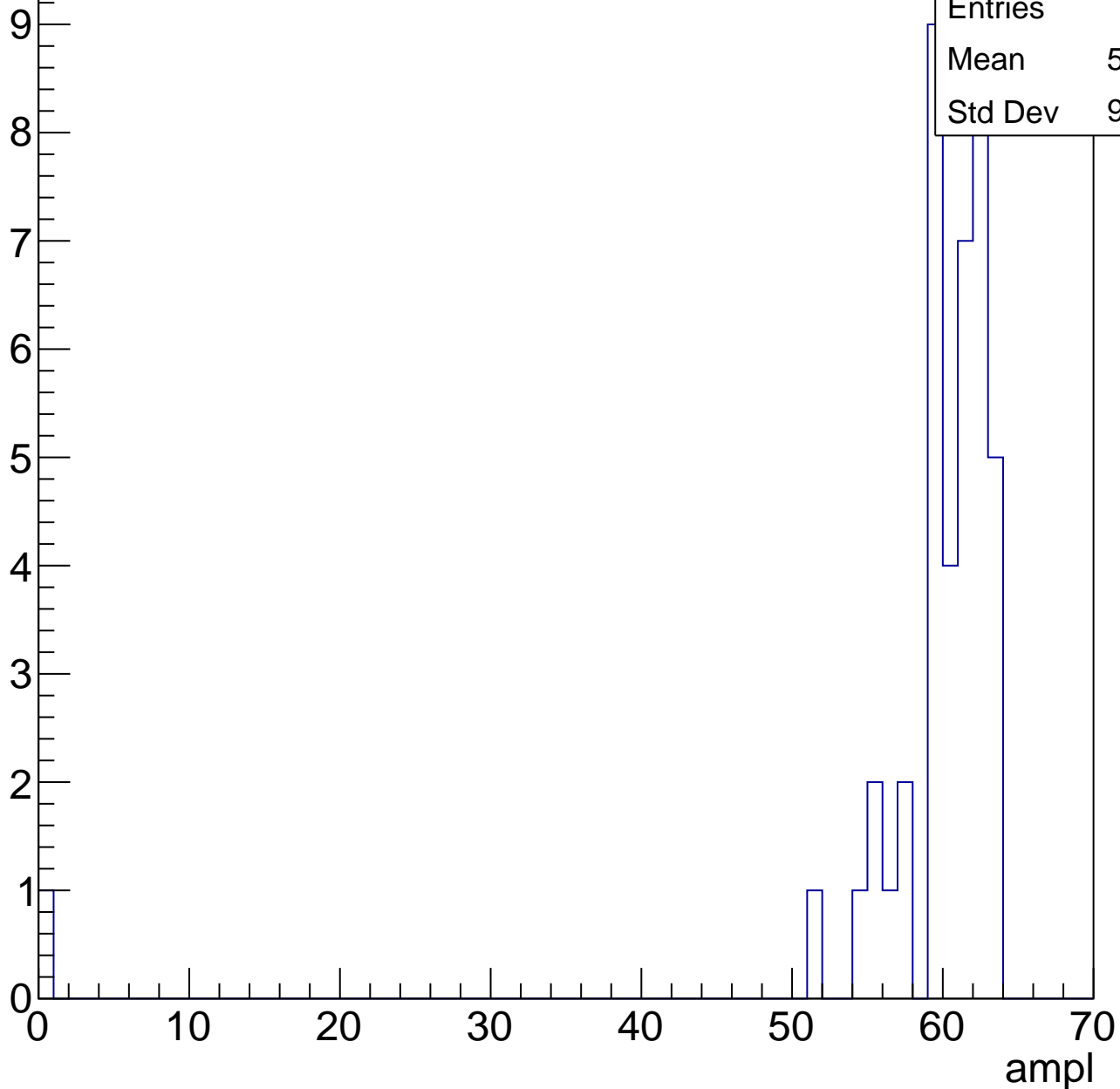
ampl



B0L102S, U12-ch46, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

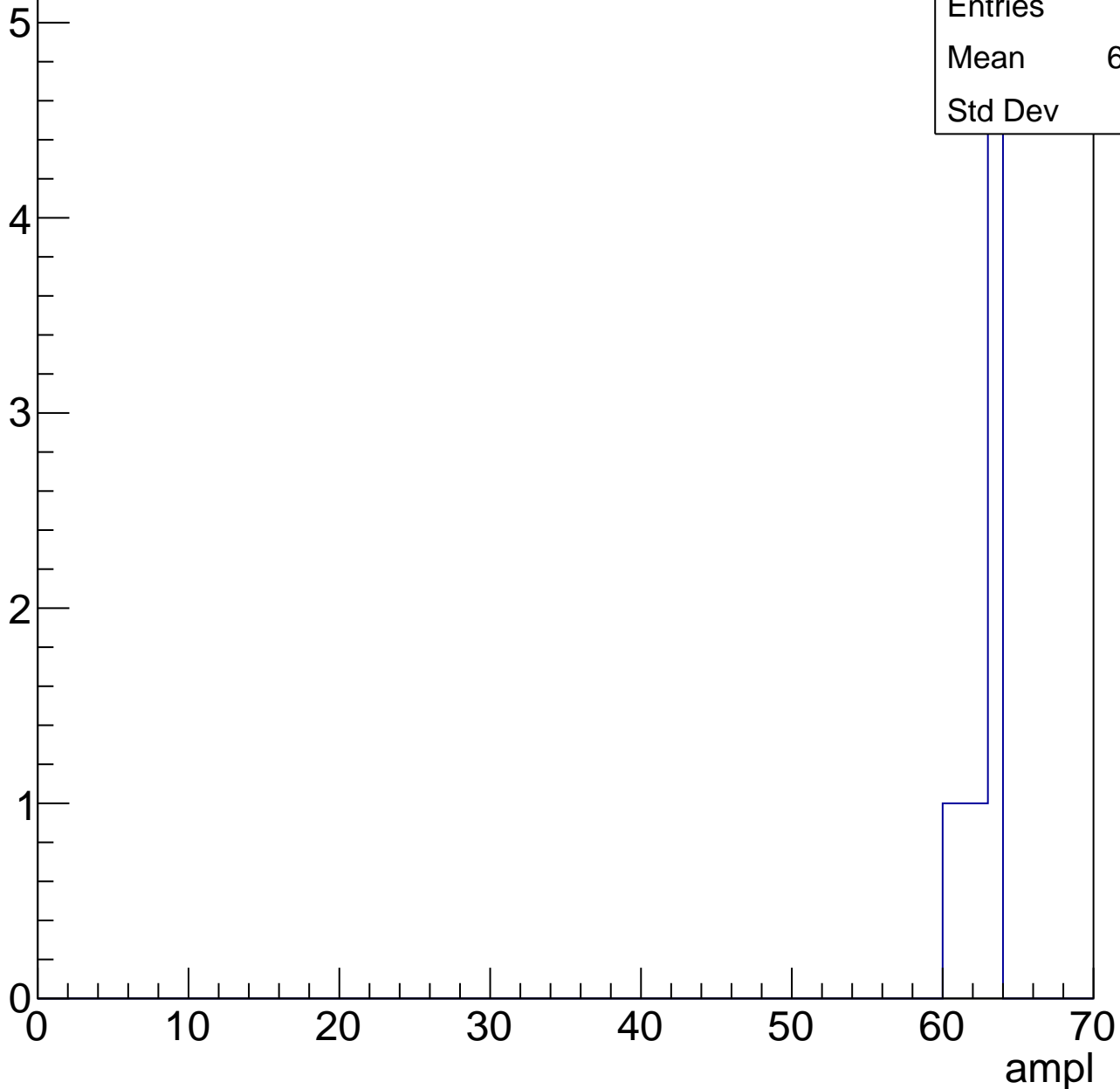


B0L102S, U12-ch46, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	8
Mean	62.25
Std Dev	1.09



B0L102S, U12-ch46, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch47, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	62
Mean	29.6
Std Dev	3.554

Gaus mean : 29.9460

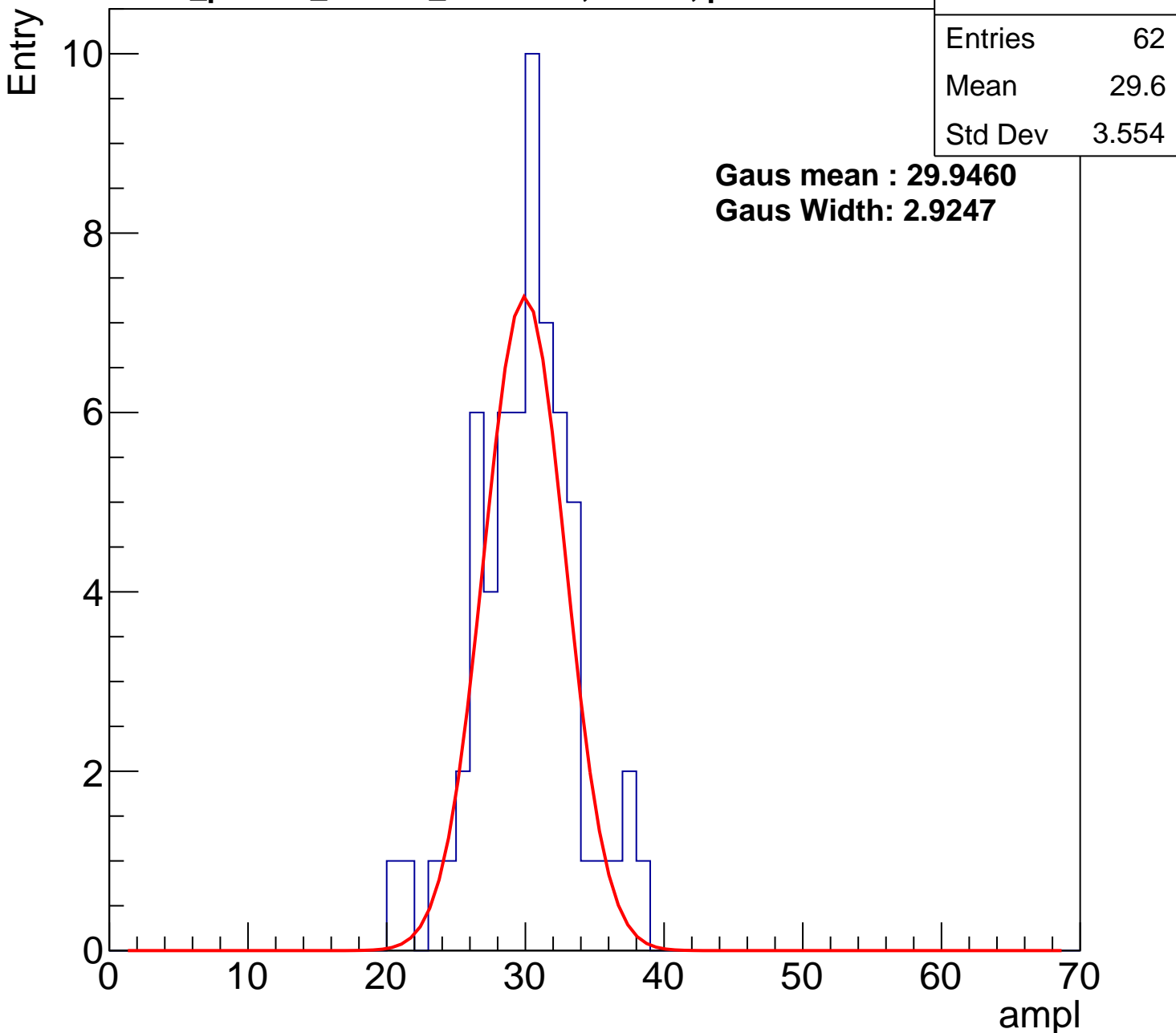
Gaus Width: 2.9247

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch47, adc1

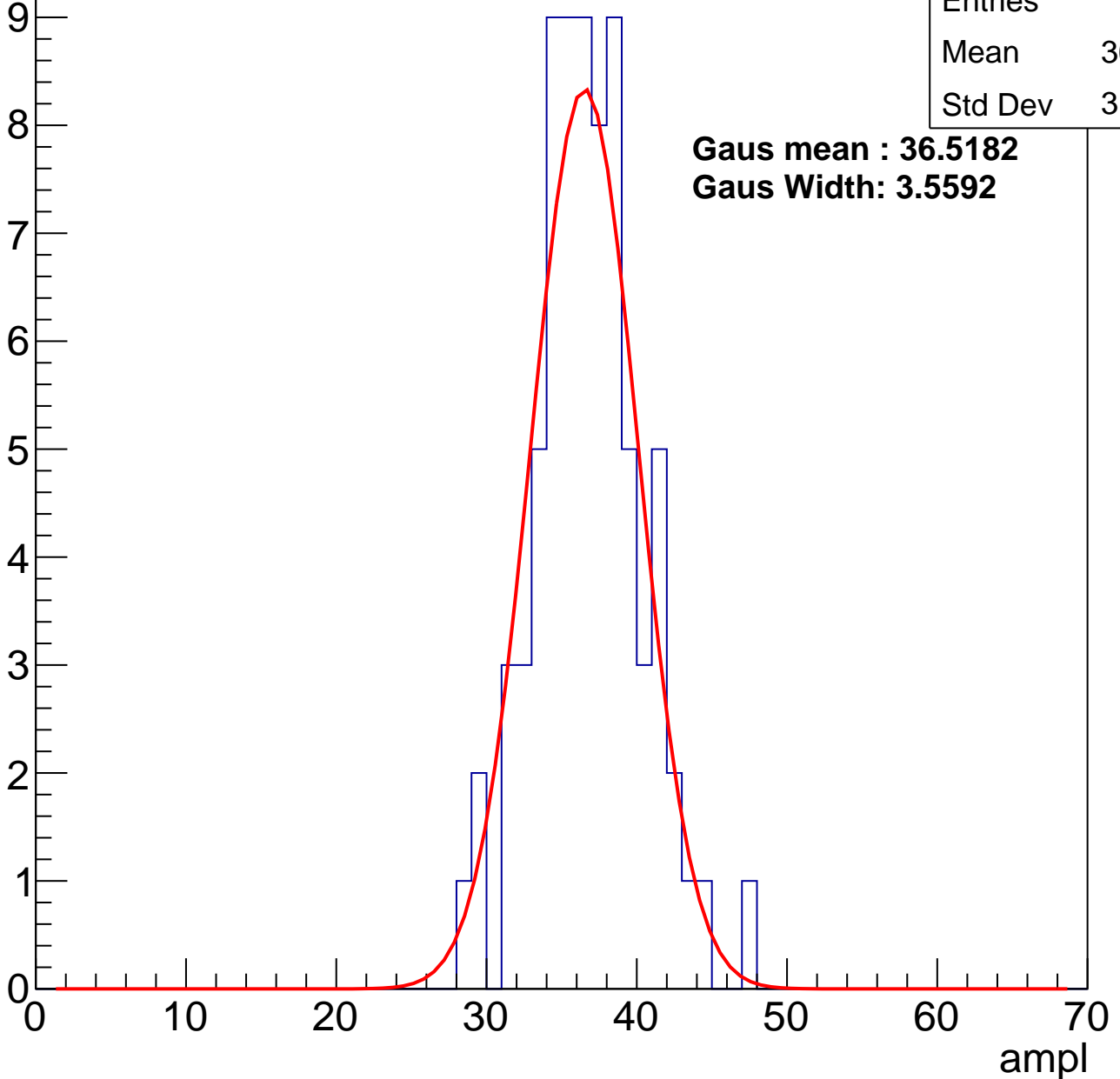
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	36.33
Std Dev	3.507

Gaus mean : 36.5182

Gaus Width: 3.5592



B0L102S, U12-ch47, adc2

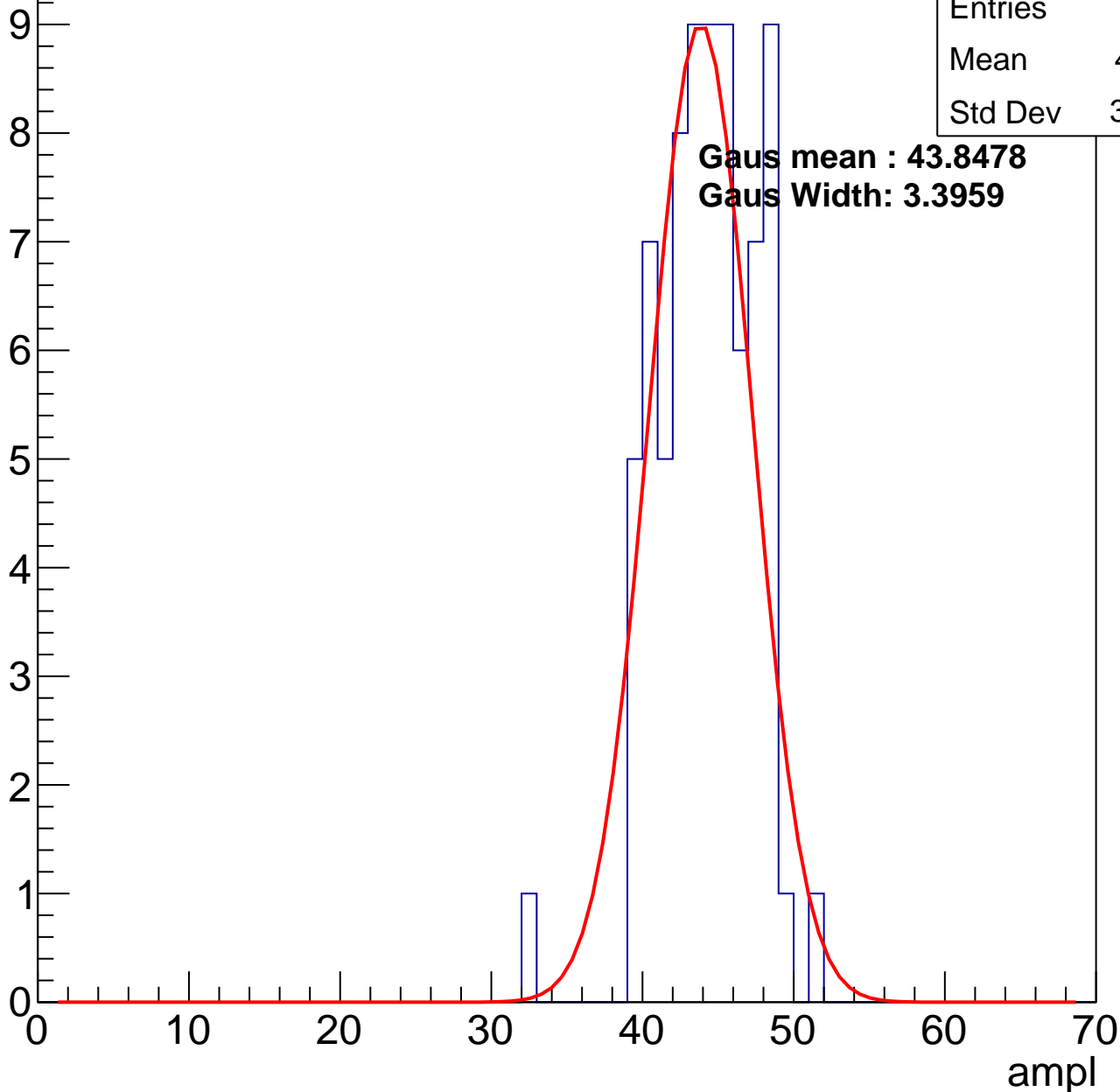
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	43.81
Std Dev	3.179

Gaus mean : 43.8478

Gaus Width: 3.3959

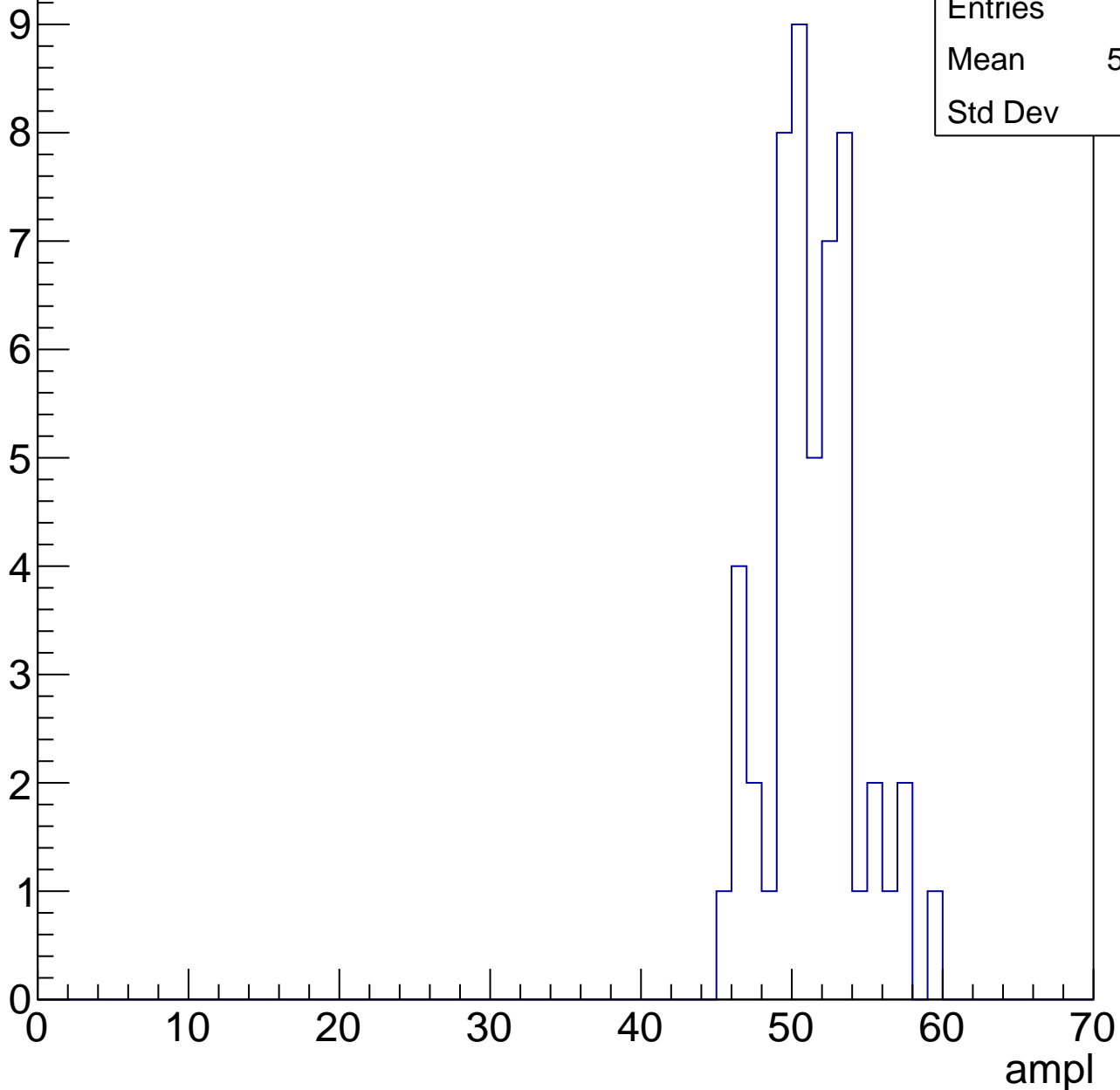


B0L102S, U12-ch47, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	50.94
Std Dev	2.97

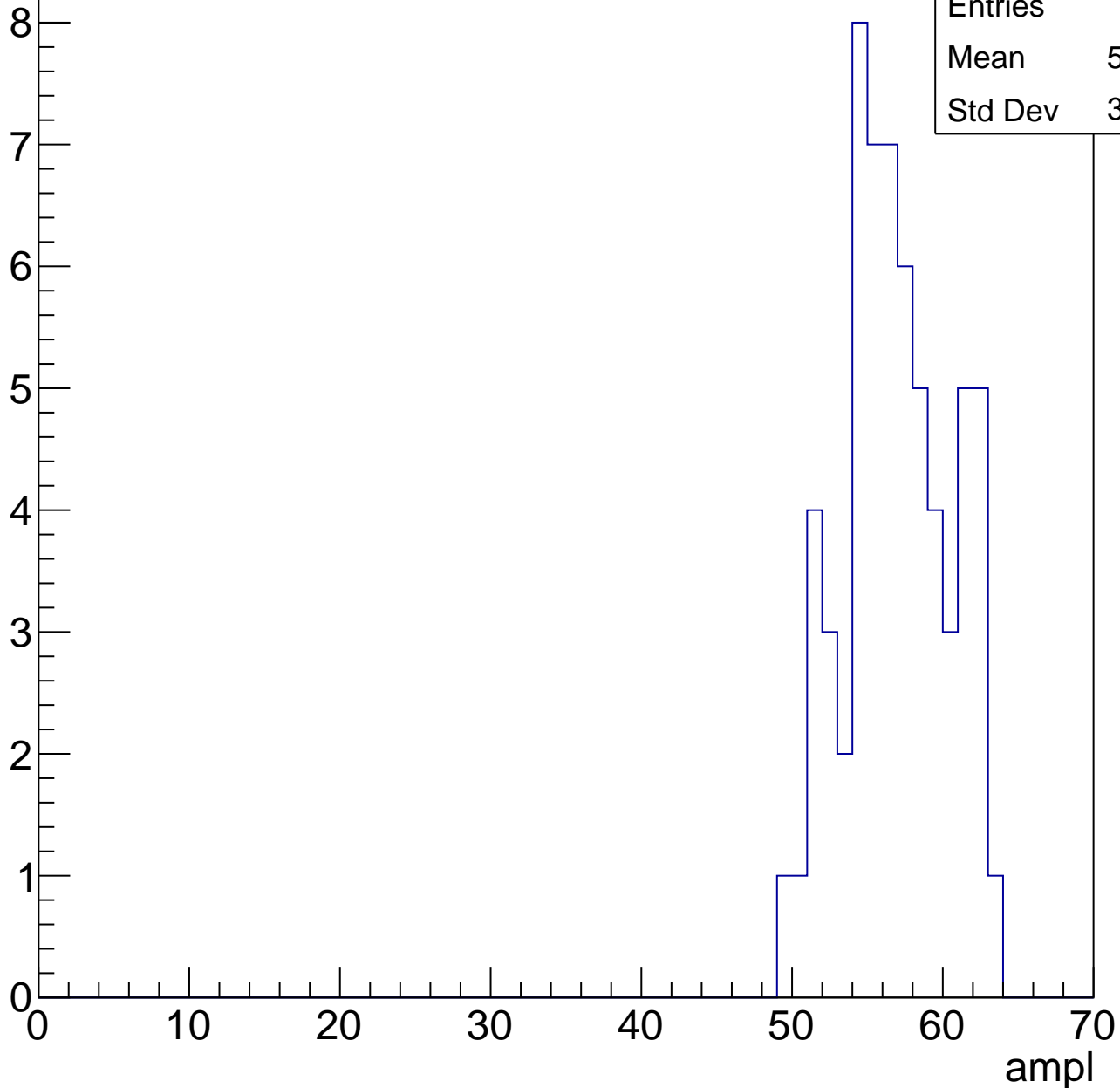


B0L102S, U12-ch47, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	56.45
Std Dev	3.458

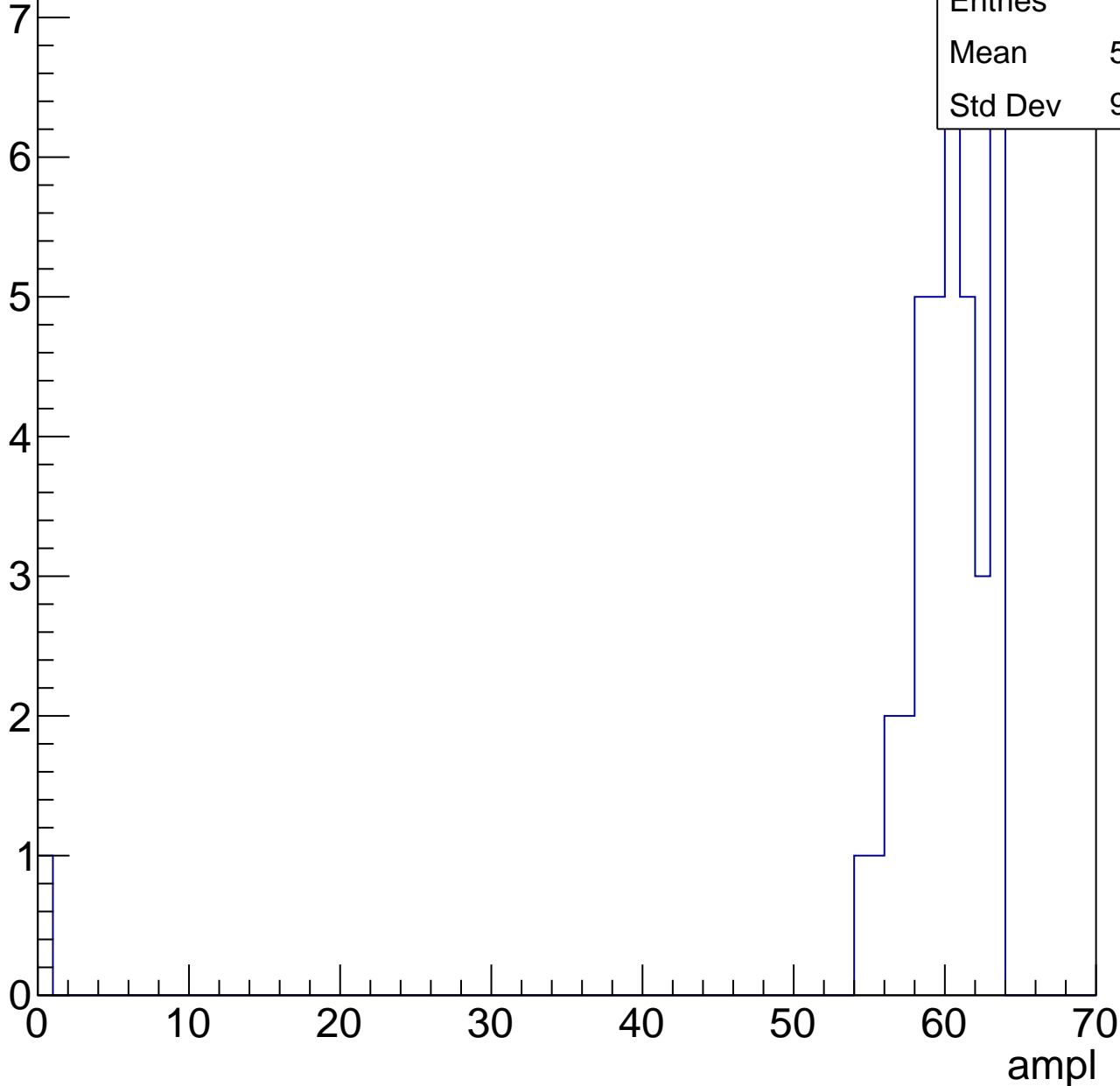


B0L102S, U12-ch47, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.26
Std Dev	9.737



B0L102S, U12-ch47, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch47, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch48, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	81
Mean	27.78
Std Dev	6.398

Gaus mean : 29.6819

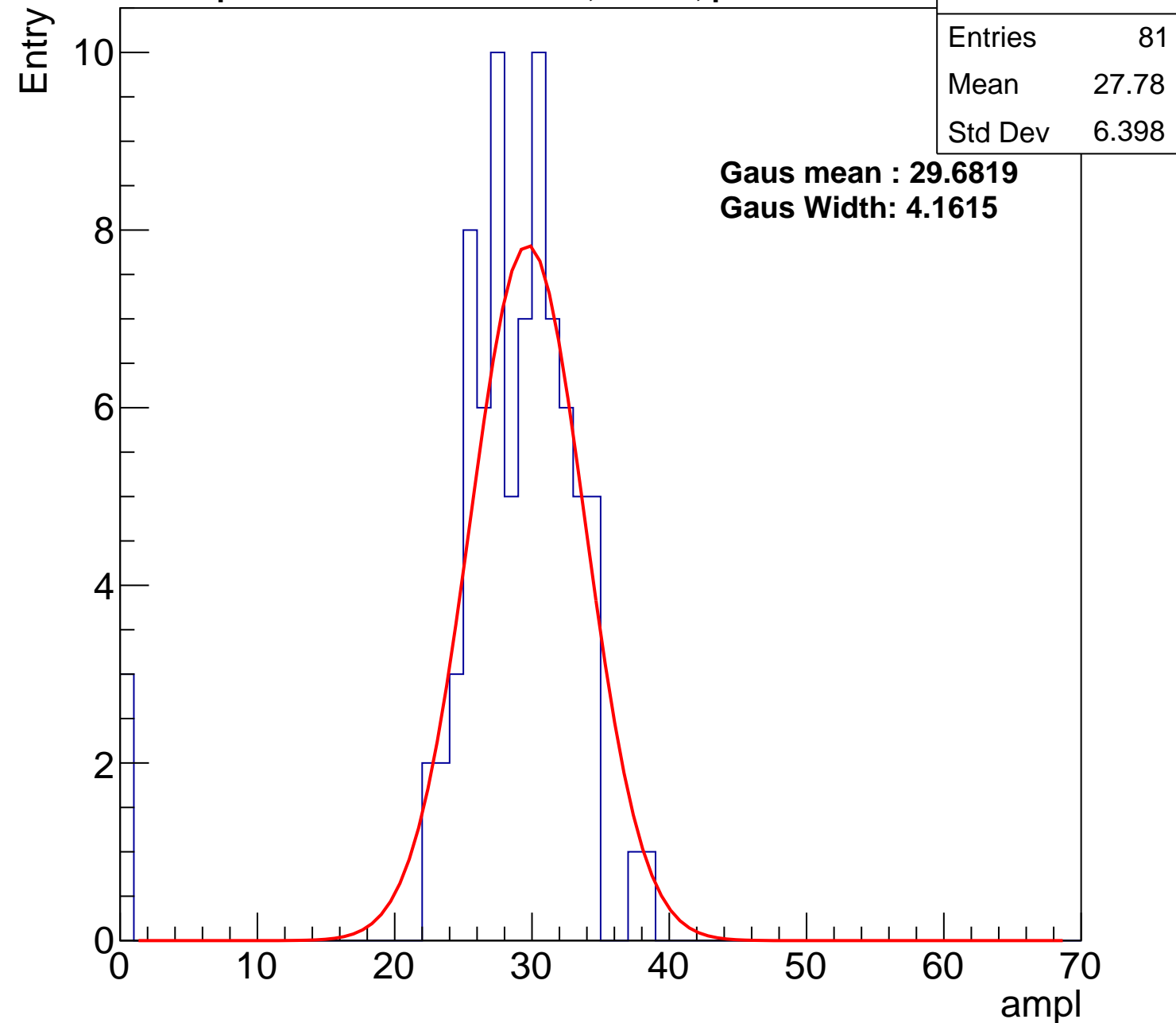
Gaus Width: 4.1615

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch48, adc1

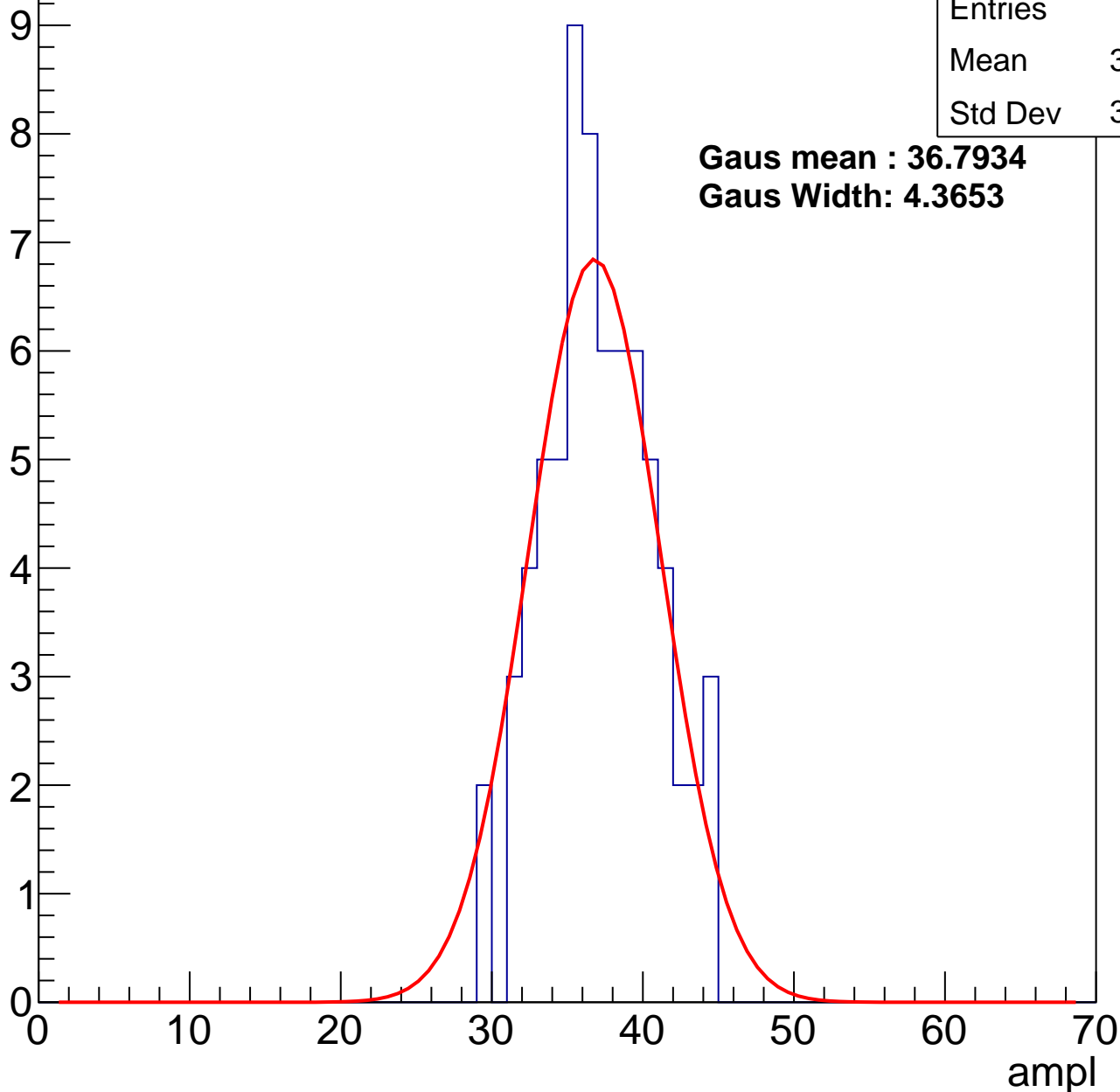
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	36.67
Std Dev	3.597

Gaus mean : 36.7934

Gaus Width: 4.3653



B0L102S, U12-ch48, adc2

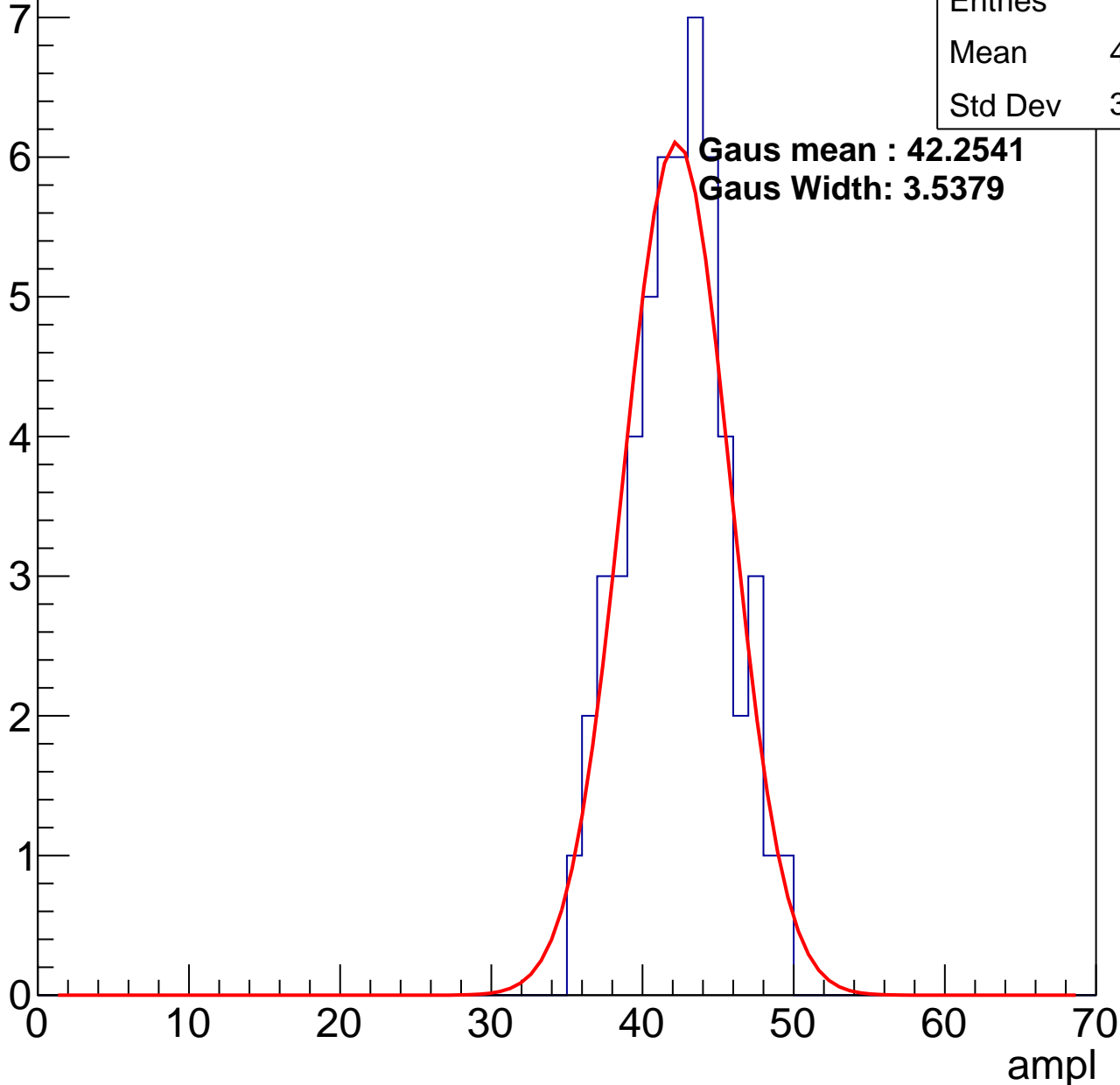
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	41.87
Std Dev	3.232

Gaus mean : 42.2541

Gaus Width: 3.5379

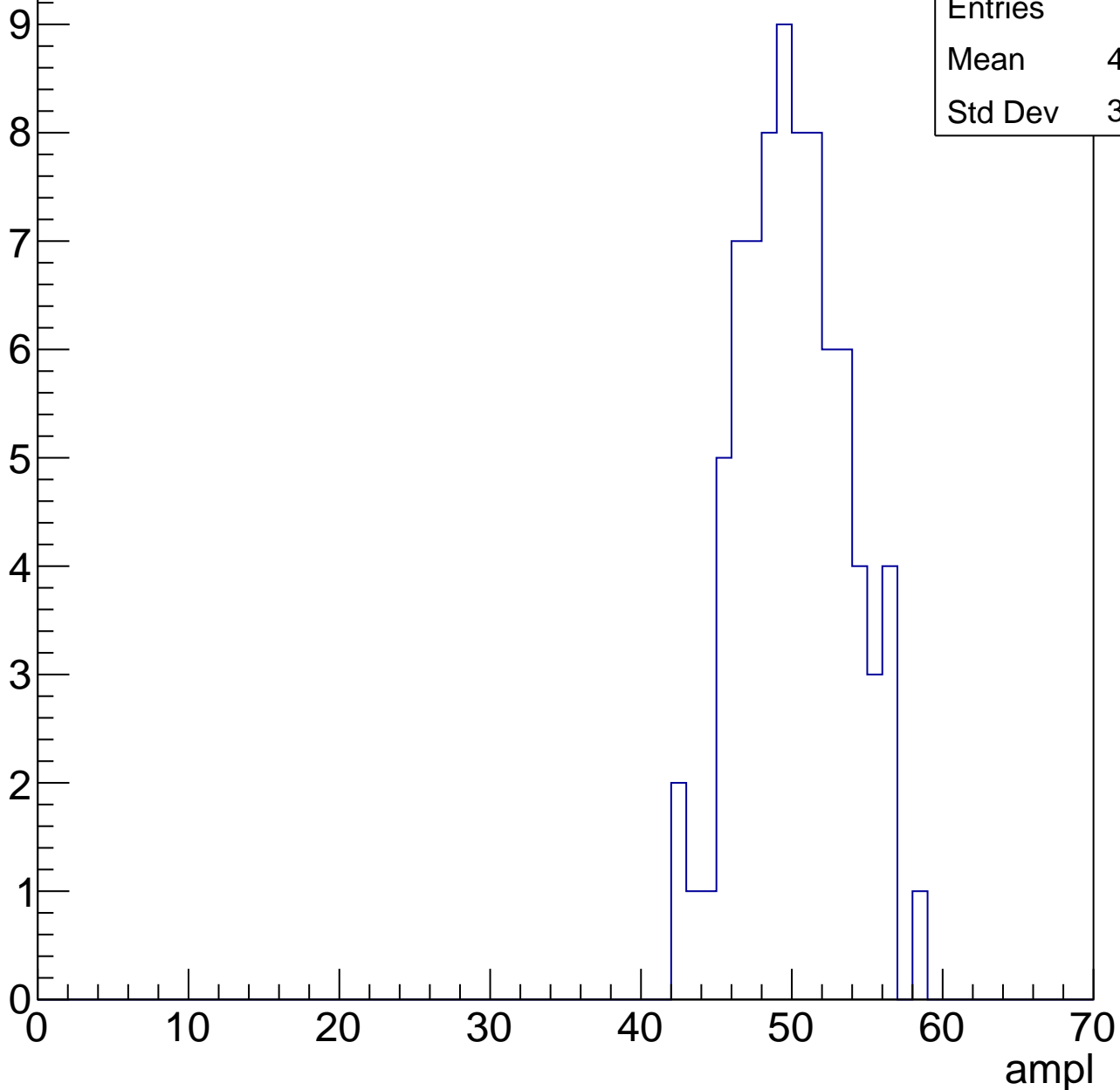


B0L102S, U12-ch48, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	49.66
Std Dev	3.496

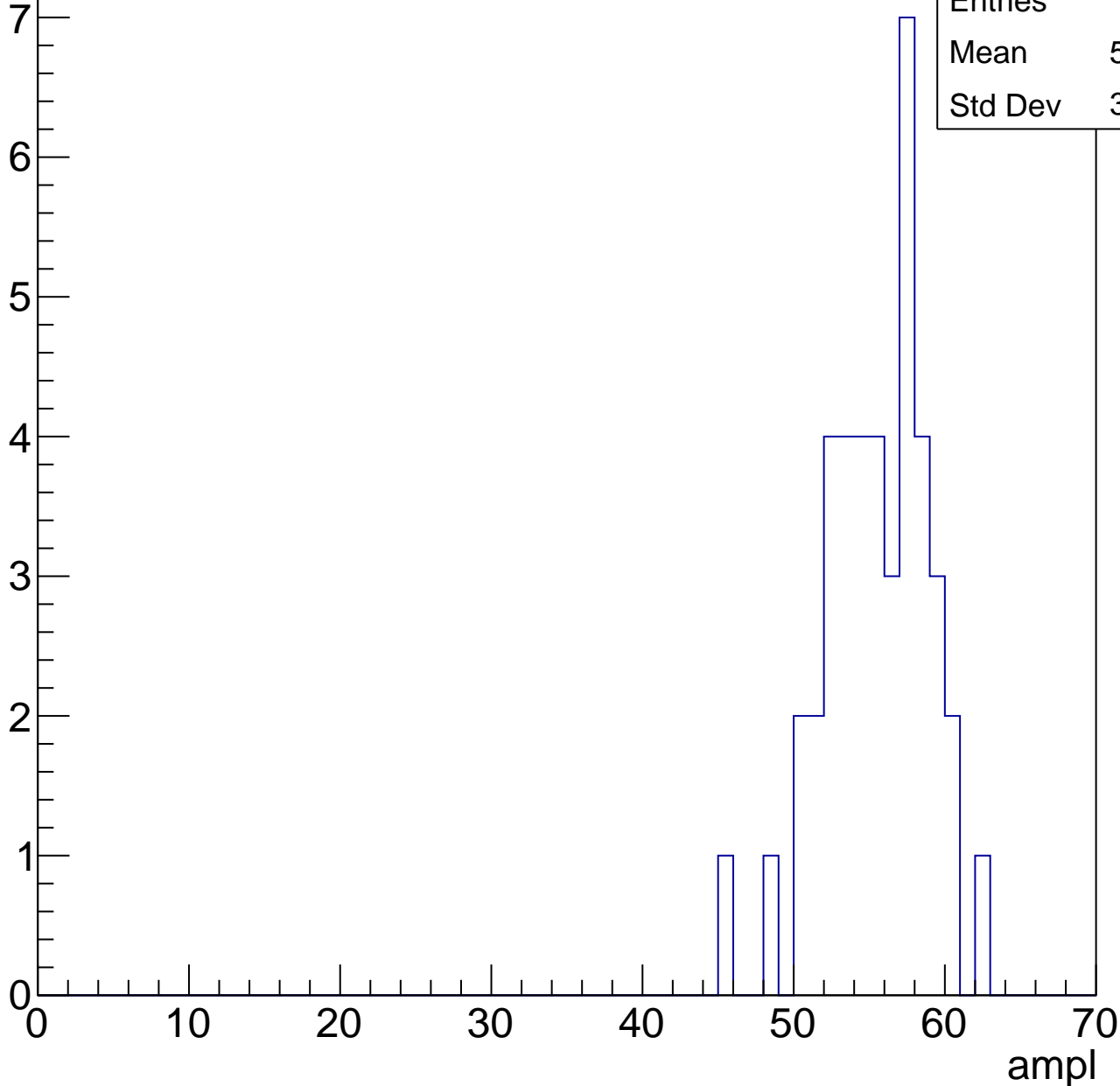


B0L102S, U12-ch48, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	54.98
Std Dev	3.454

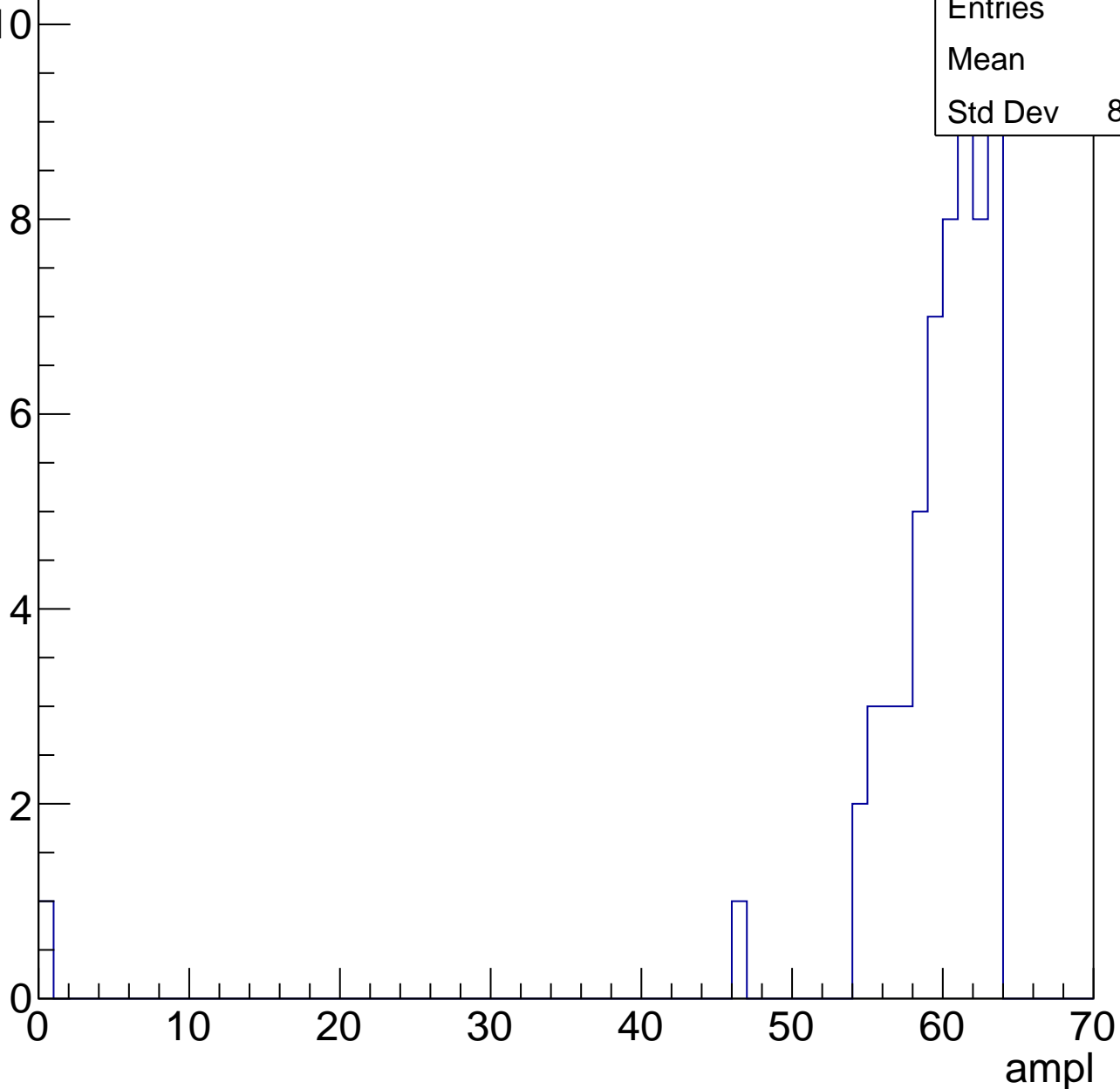


B0L102S, U12-ch48, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	58.6
Std Dev	8.224



B0L102S, U12-ch48, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch48, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch49, adc0

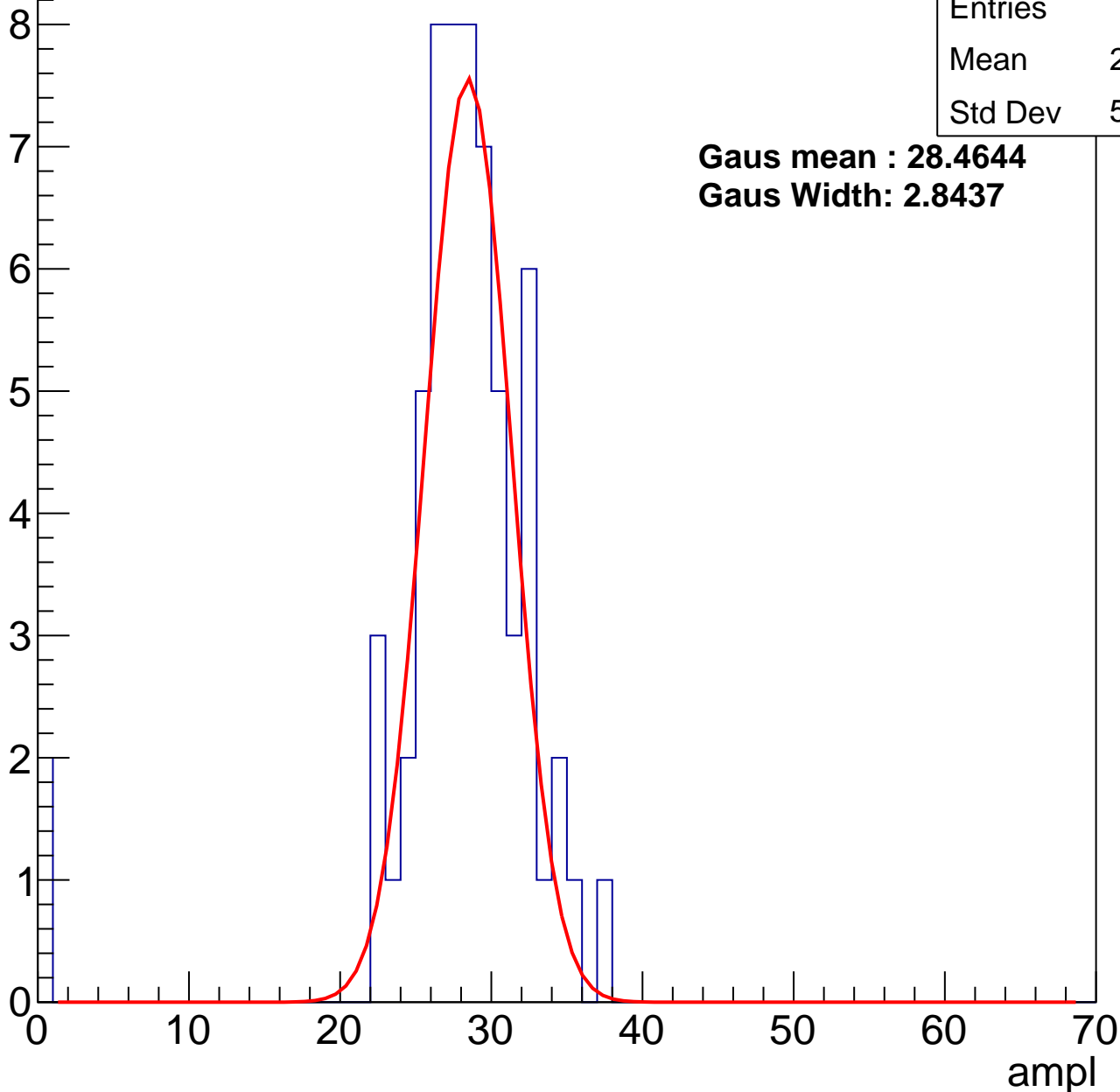
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	27.32
Std Dev	5.863

Gaus mean : 28.4644

Gaus Width: 2.8437

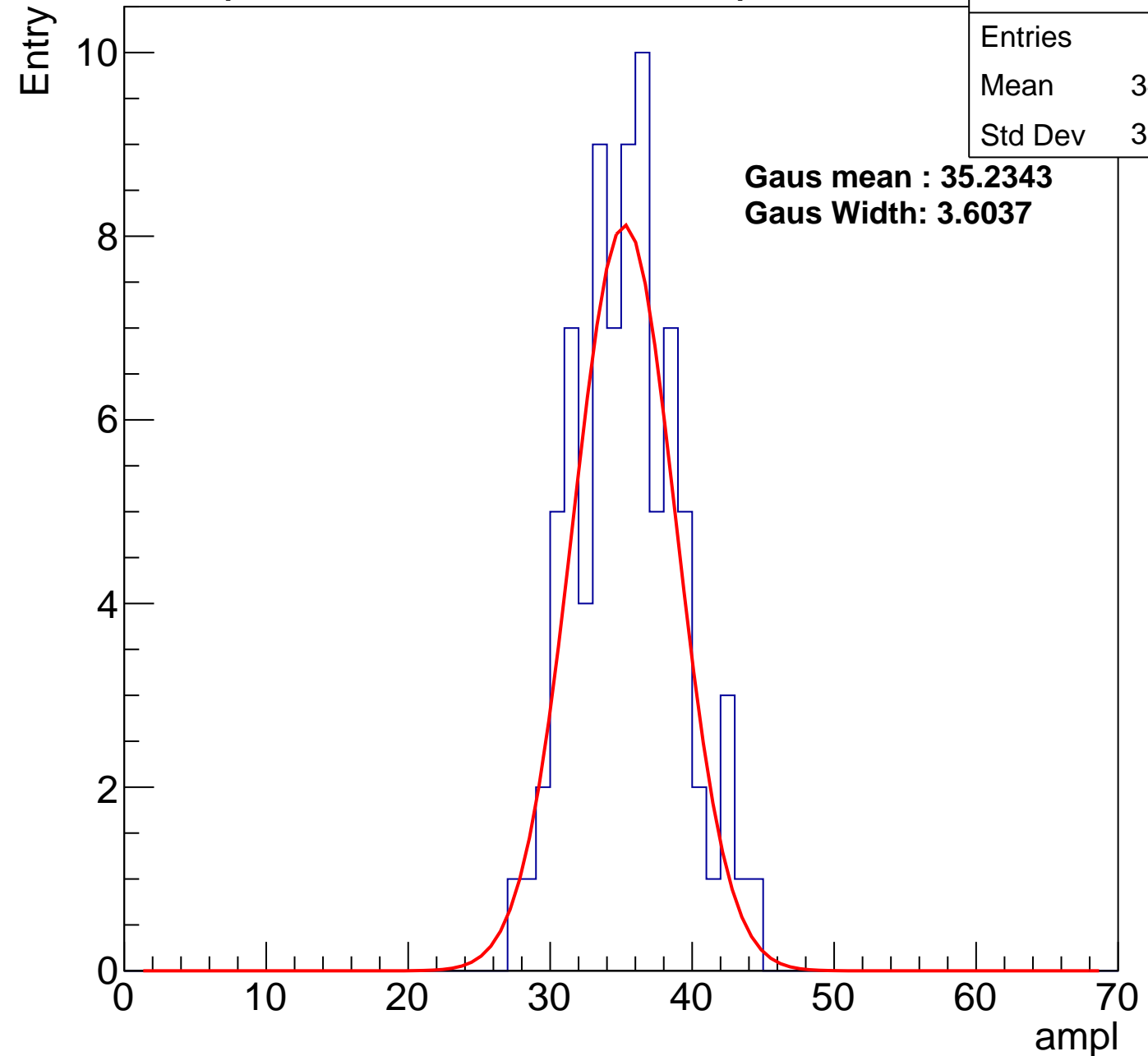


B0L102S, U12-ch49, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	80
Mean	34.98
Std Dev	3.626

Gaus mean : 35.2343
Gaus Width: 3.6037



B0L102S, U12-ch49, adc2

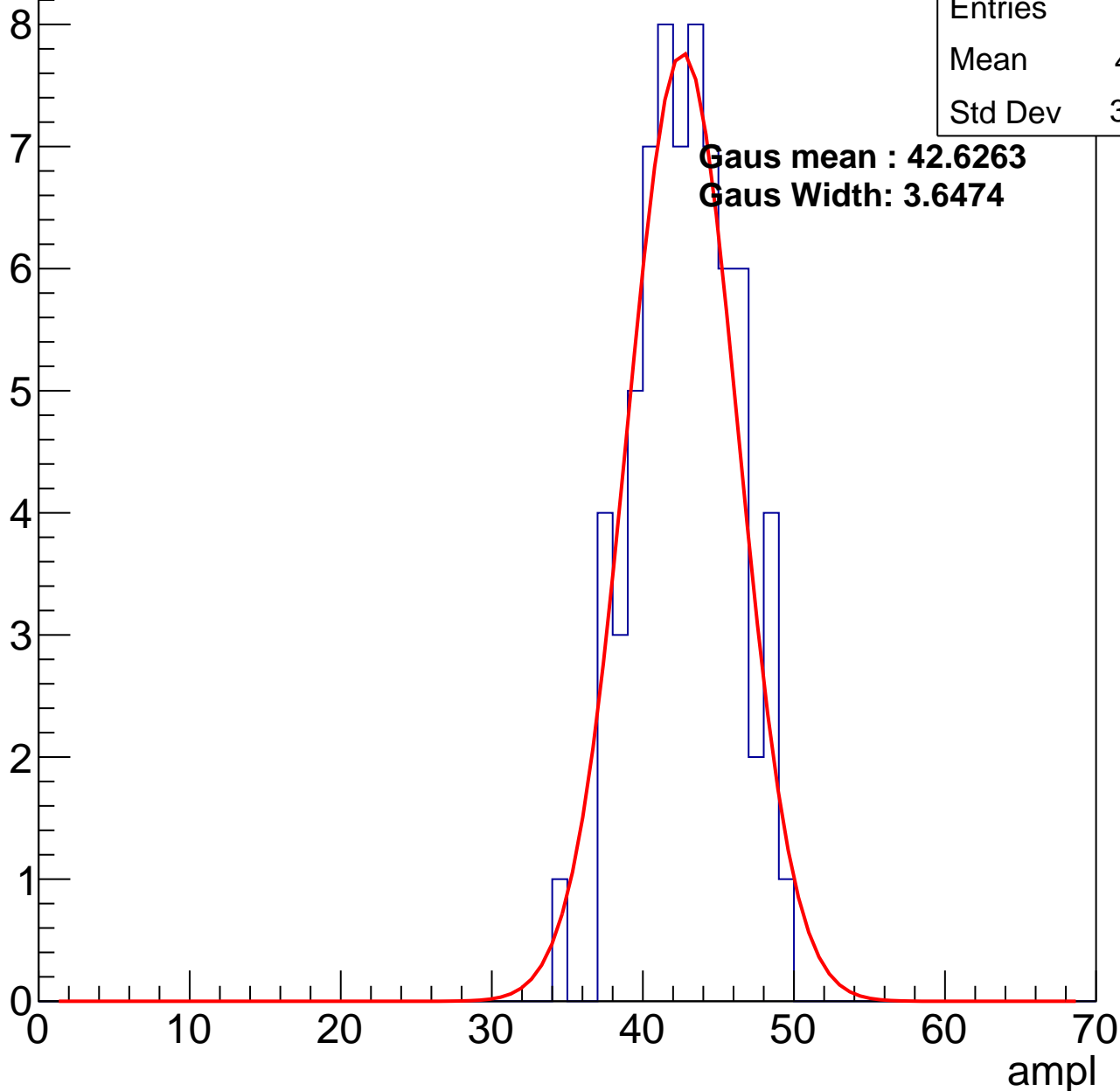
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	42.41
Std Dev	3.209

Gaus mean : 42.6263

Gaus Width: 3.6474

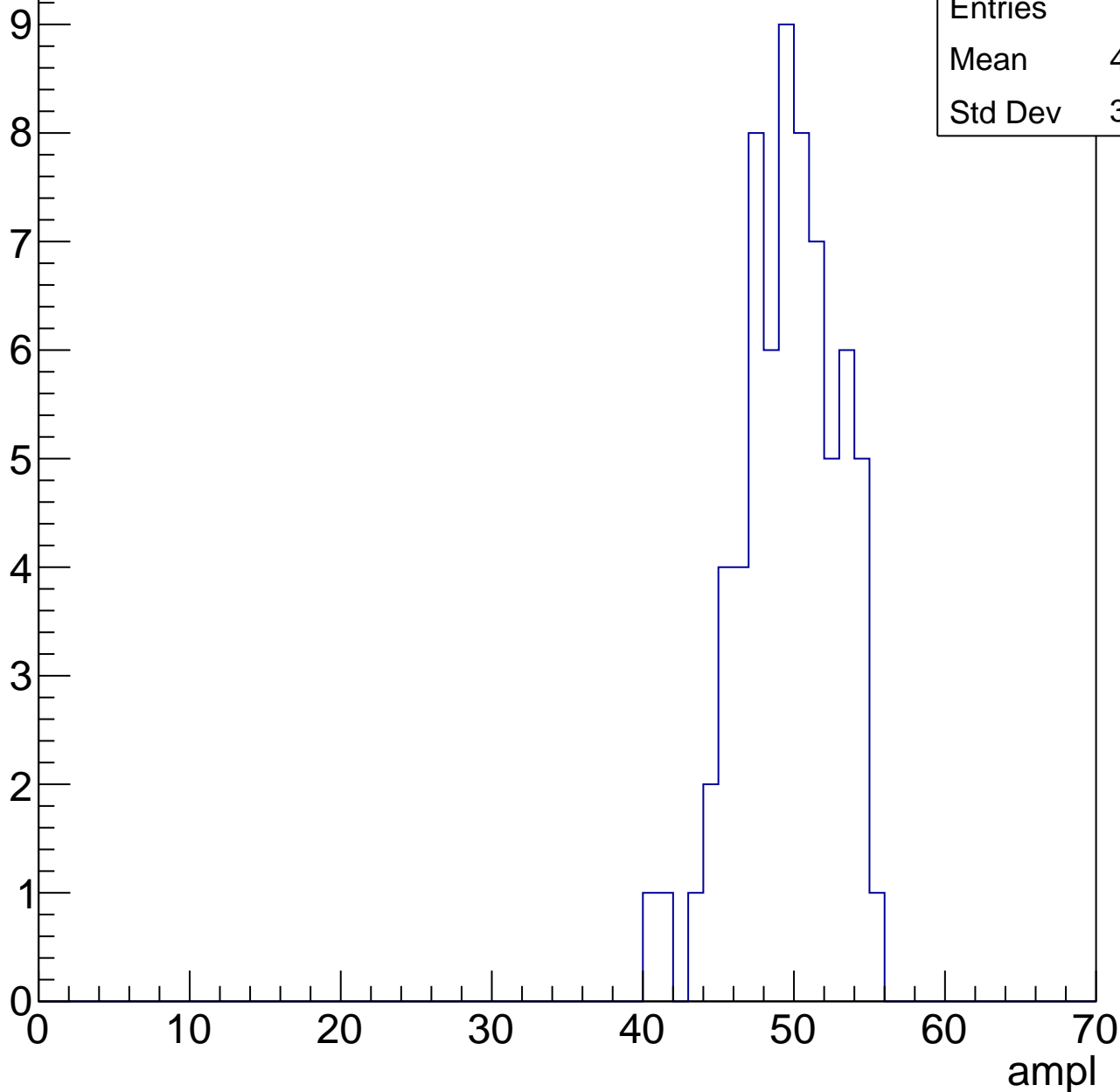


B0L102S, U12-ch49, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.13
Std Dev	3.222

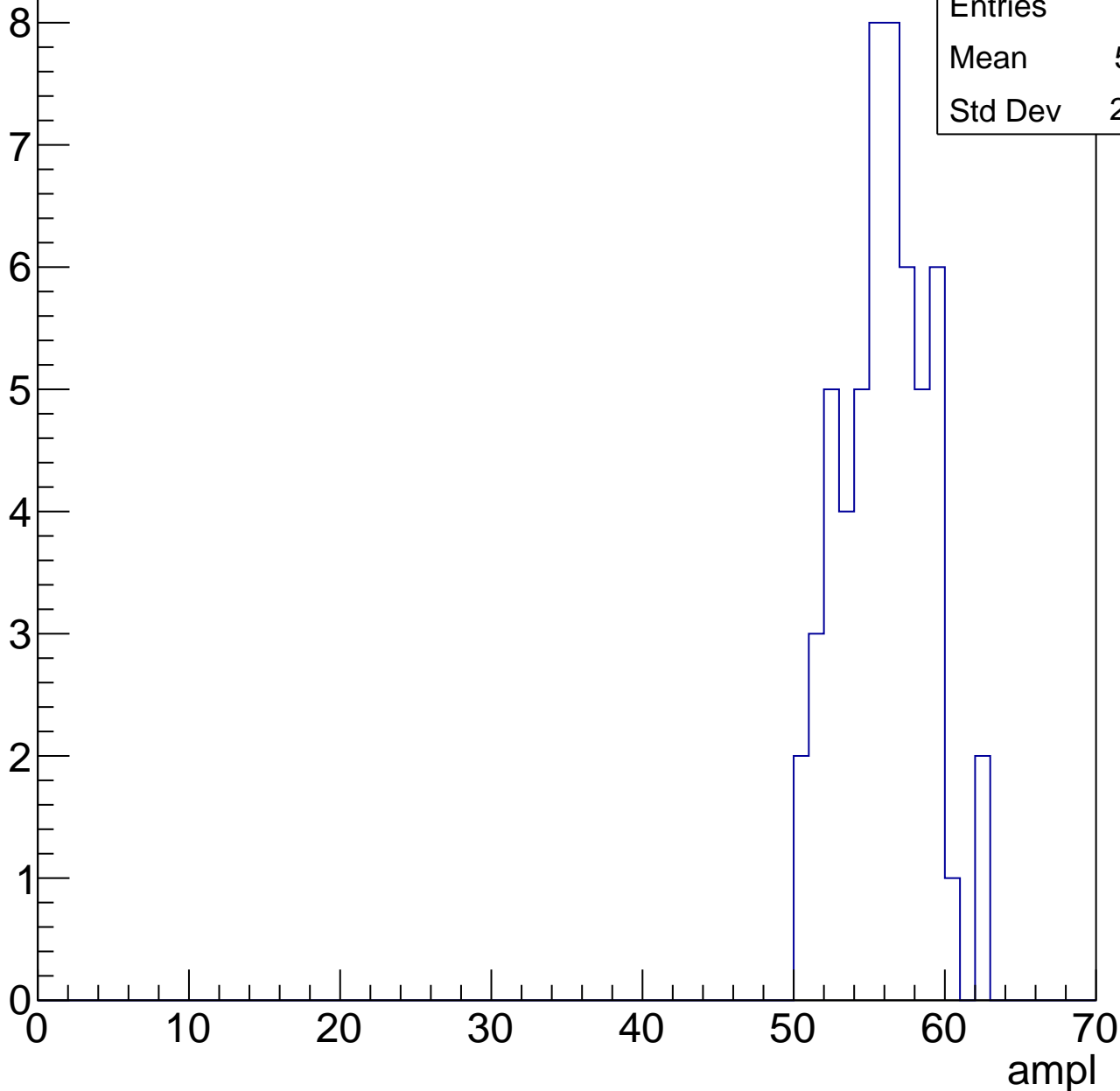


B0L102S, U12-ch49, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	55.51
Std Dev	2.847

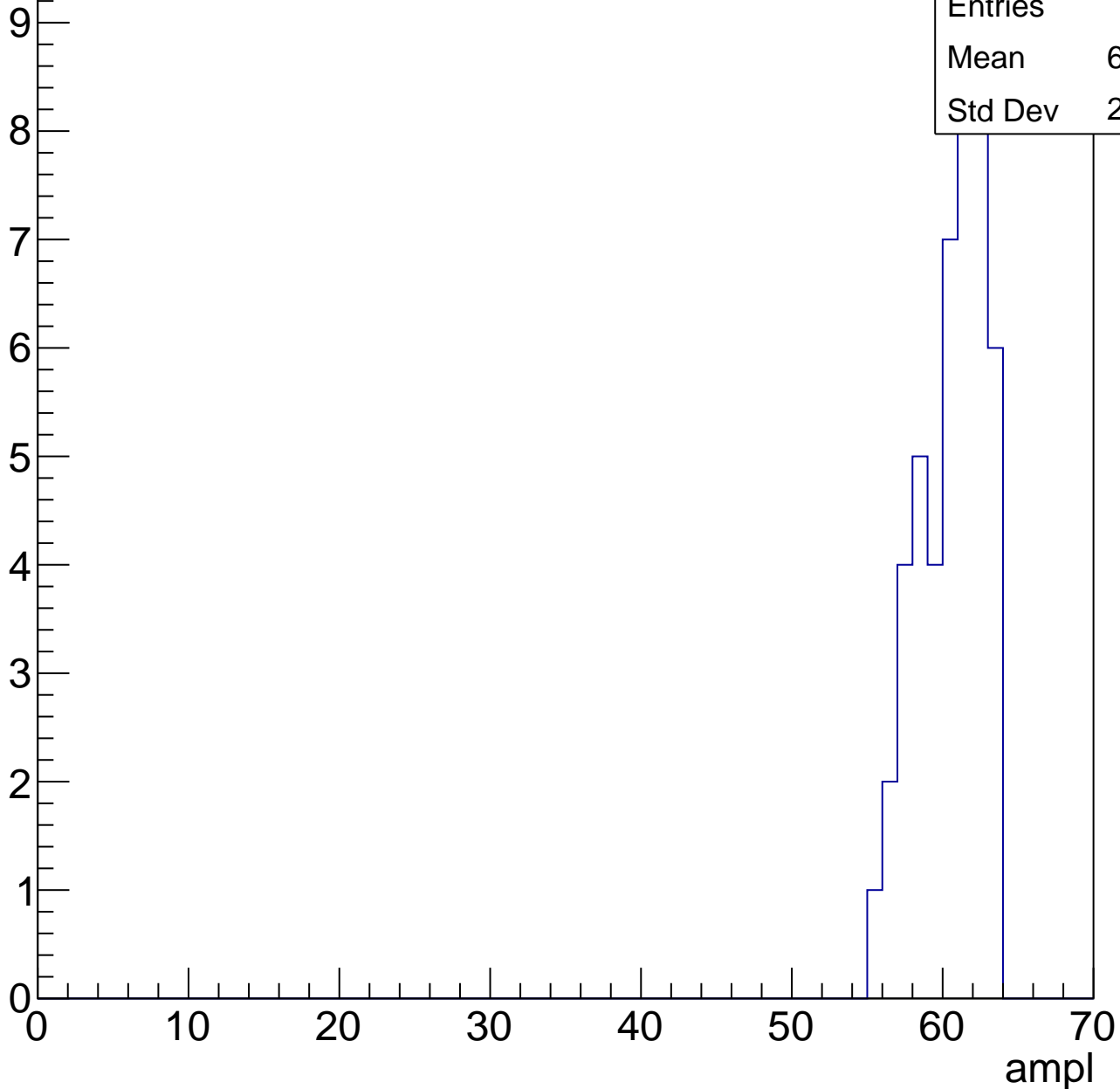


B0L102S, U12-ch49, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

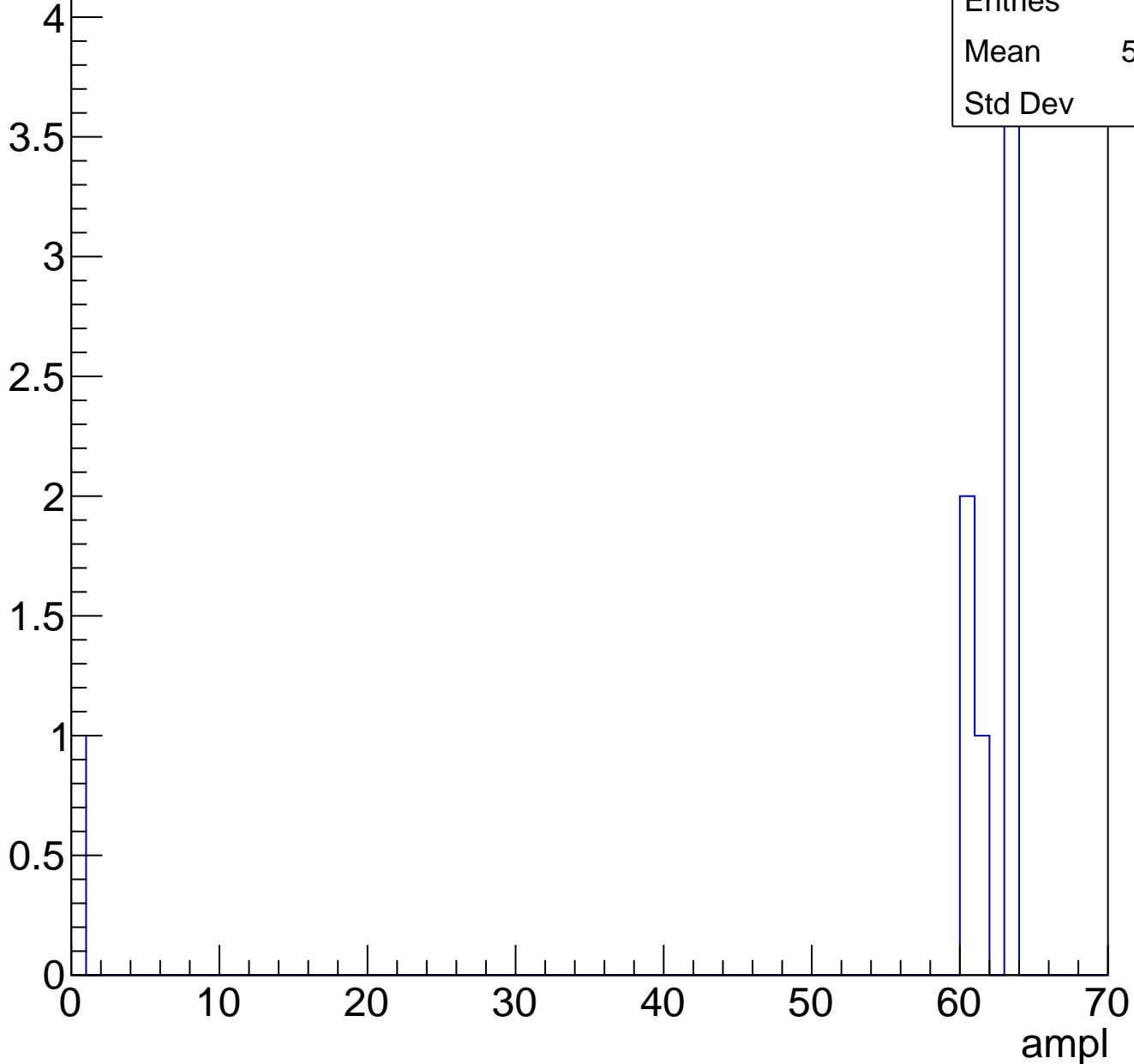
Entries	46
Mean	60.09
Std Dev	2.145



B0L102S, U12-ch49, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch49, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch50, adc0

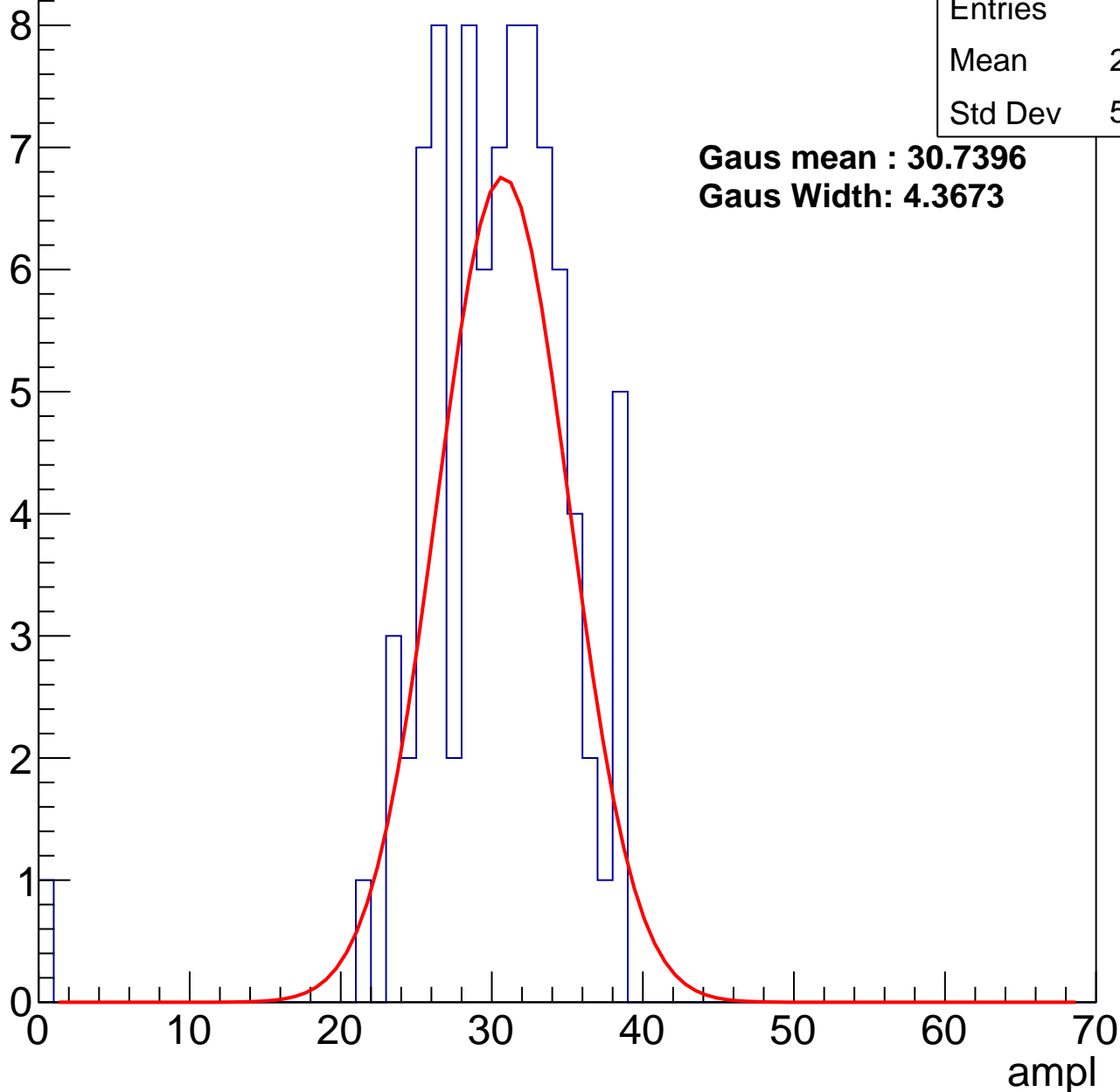
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	29.78
Std Dev	5.166

Gaus mean : 30.7396

Gaus Width: 4.3673



B0L102S, U12-ch50, adc1

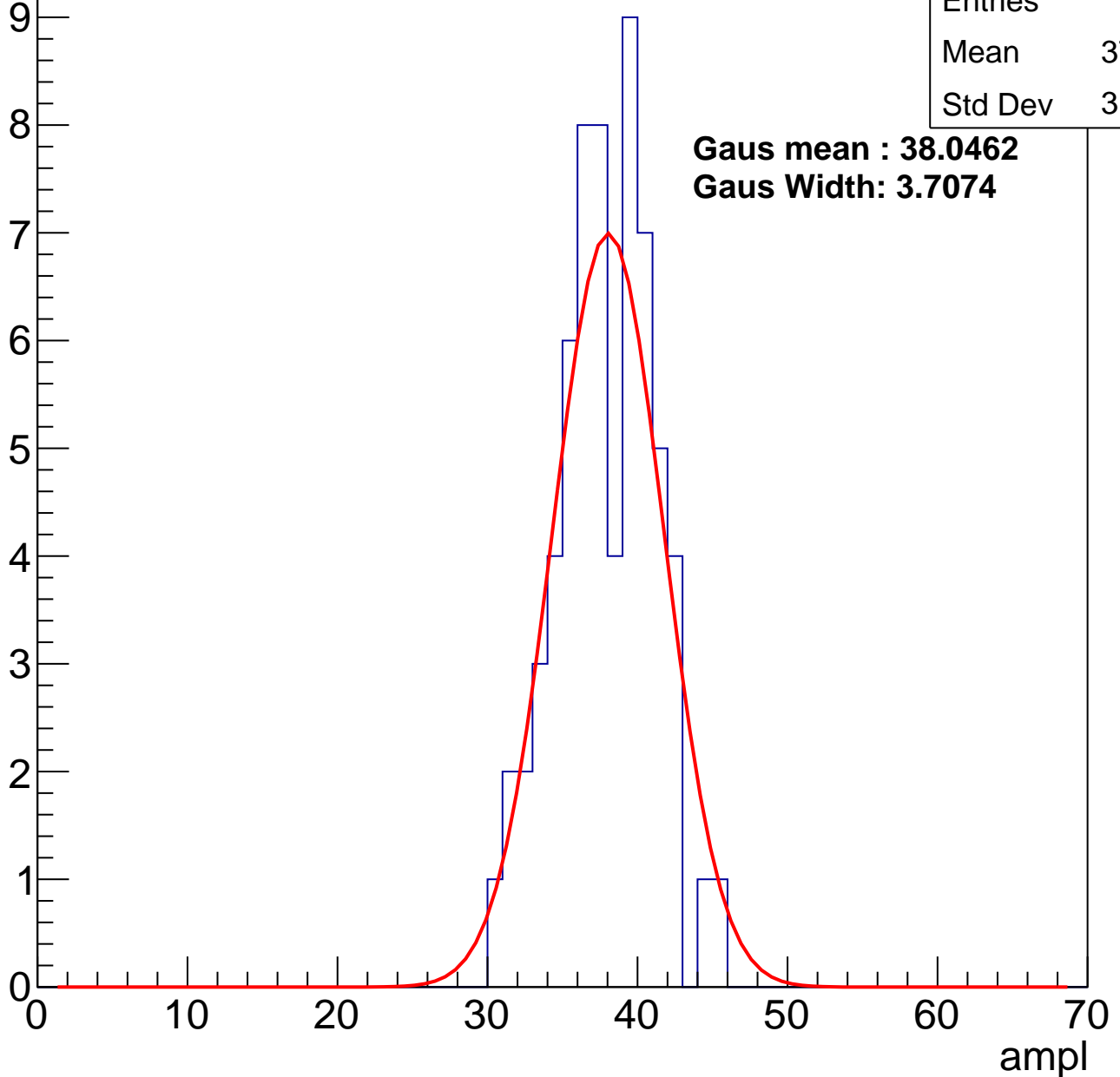
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.38
Std Dev	3.219

Gaus mean : 38.0462

Gaus Width: 3.7074



B0L102S, U12-ch50, adc2

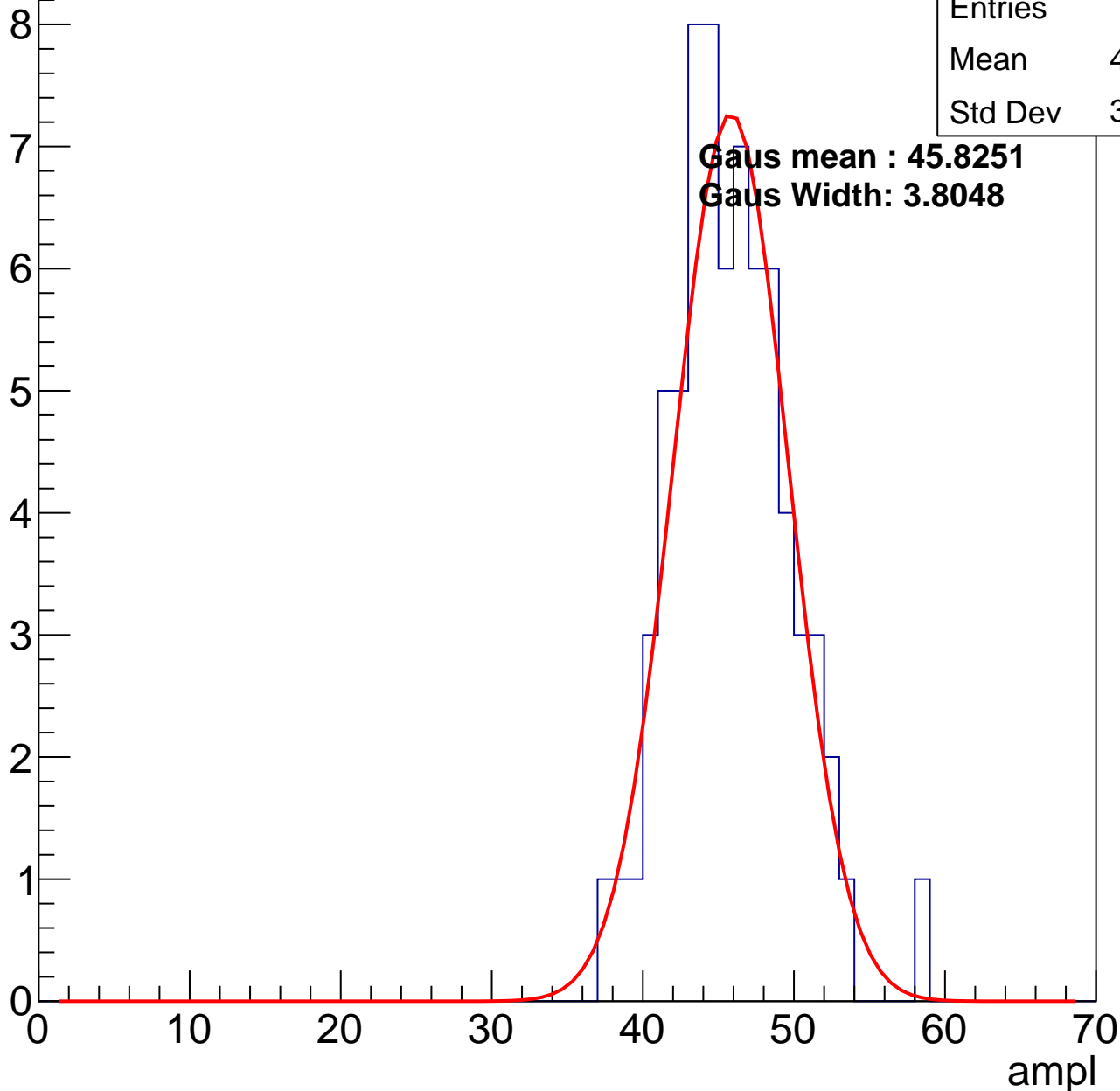
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	45.37
Std Dev	3.832

Gaus mean : 45.8251

Gaus Width: 3.8048



B0L102S, U12-ch50, adc3

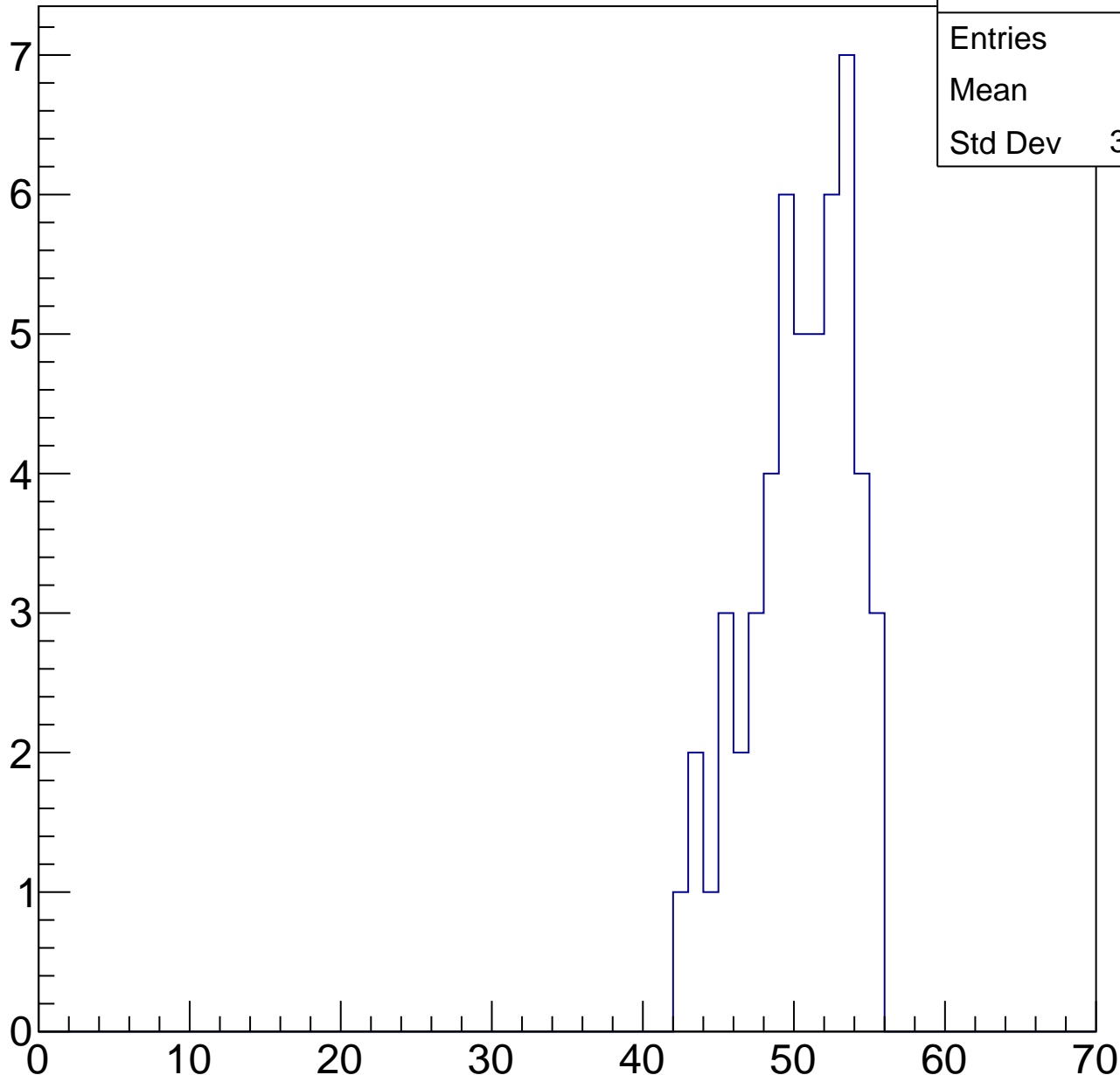
calib_packv5_042523_0143.root, FC#12, port B1

Entry

7
6
5
4
3
2
1
0

Entries	52
Mean	49.9
Std Dev	3.353

ampl



B0L102S, U12-ch50, adc4

calib_packv5_042523_0143.root, FC#12, port B1

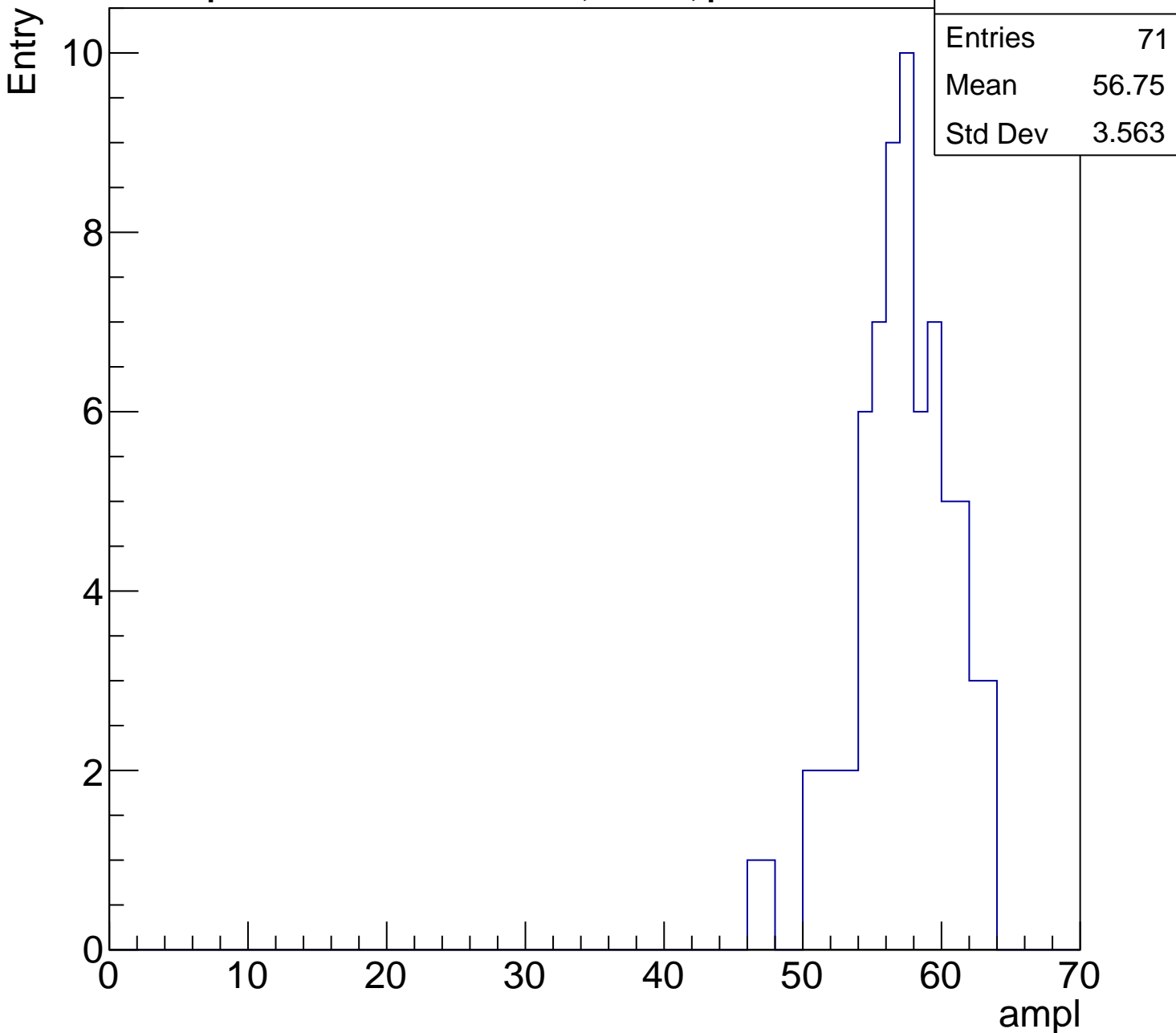
Entries	71
Mean	56.75
Std Dev	3.563

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

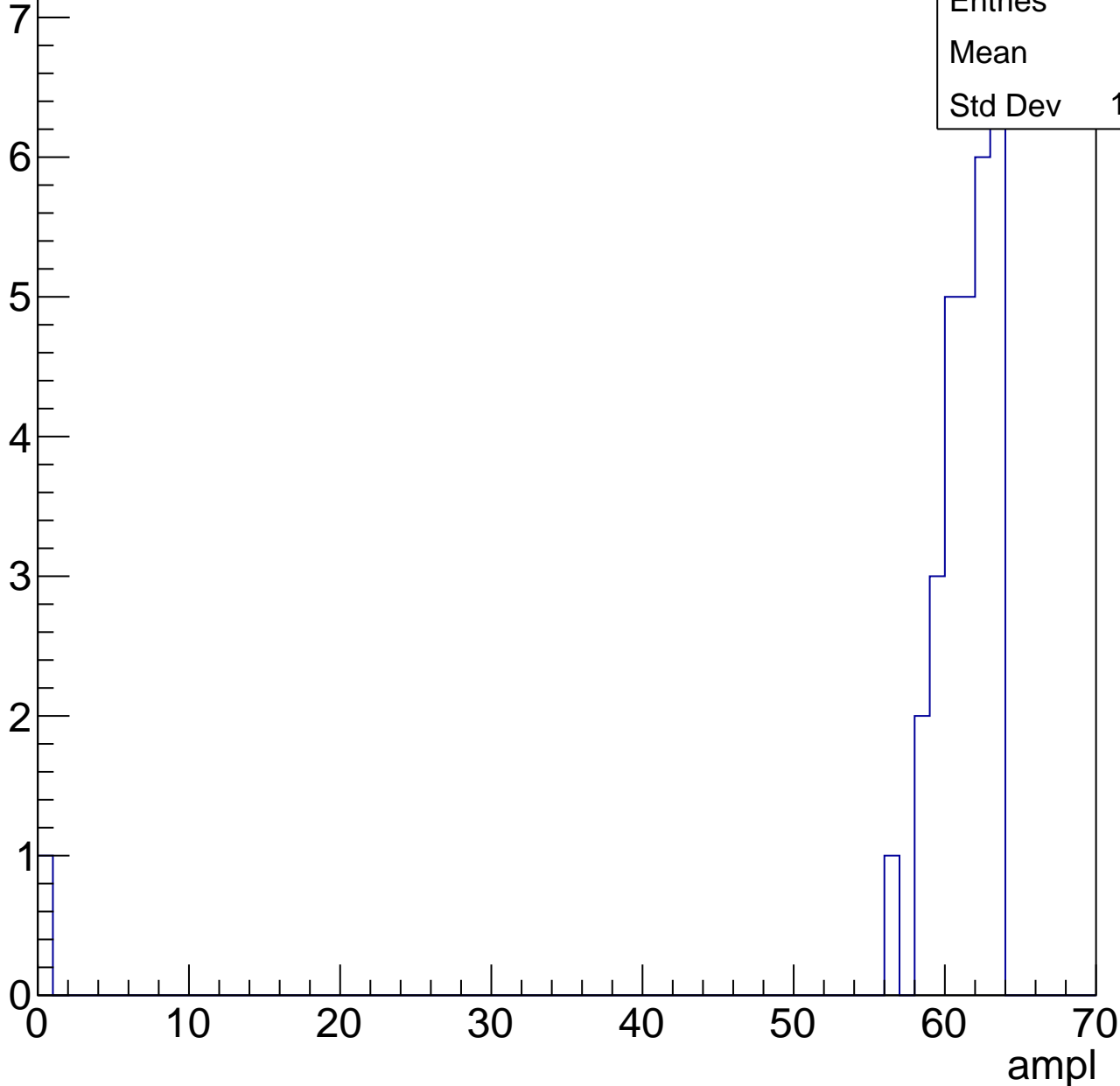


B0L102S, U12-ch50, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

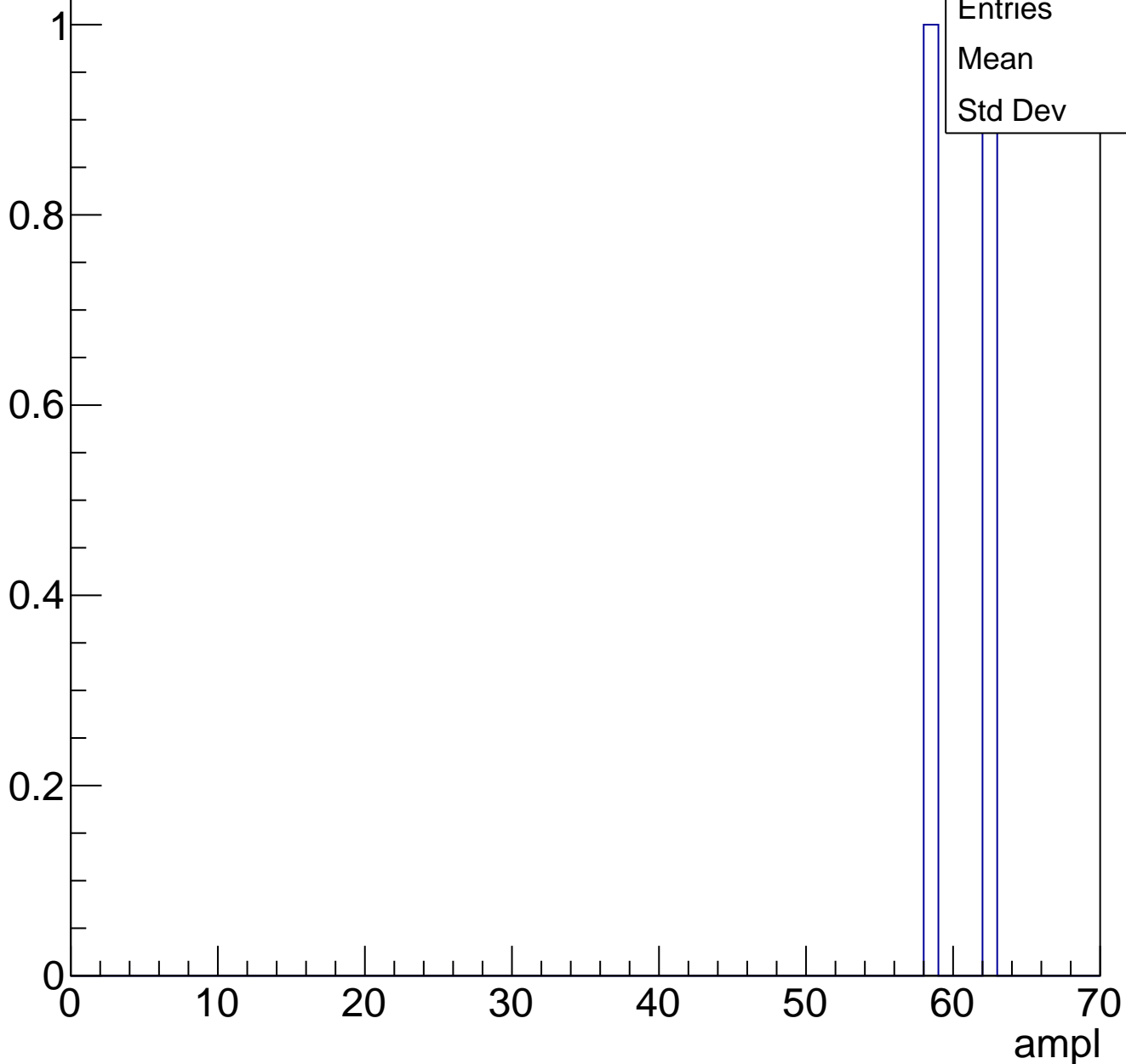
Entries	30
Mean	58.9
Std Dev	11.08



B0L102S, U12-ch50, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch50, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch51, adc0

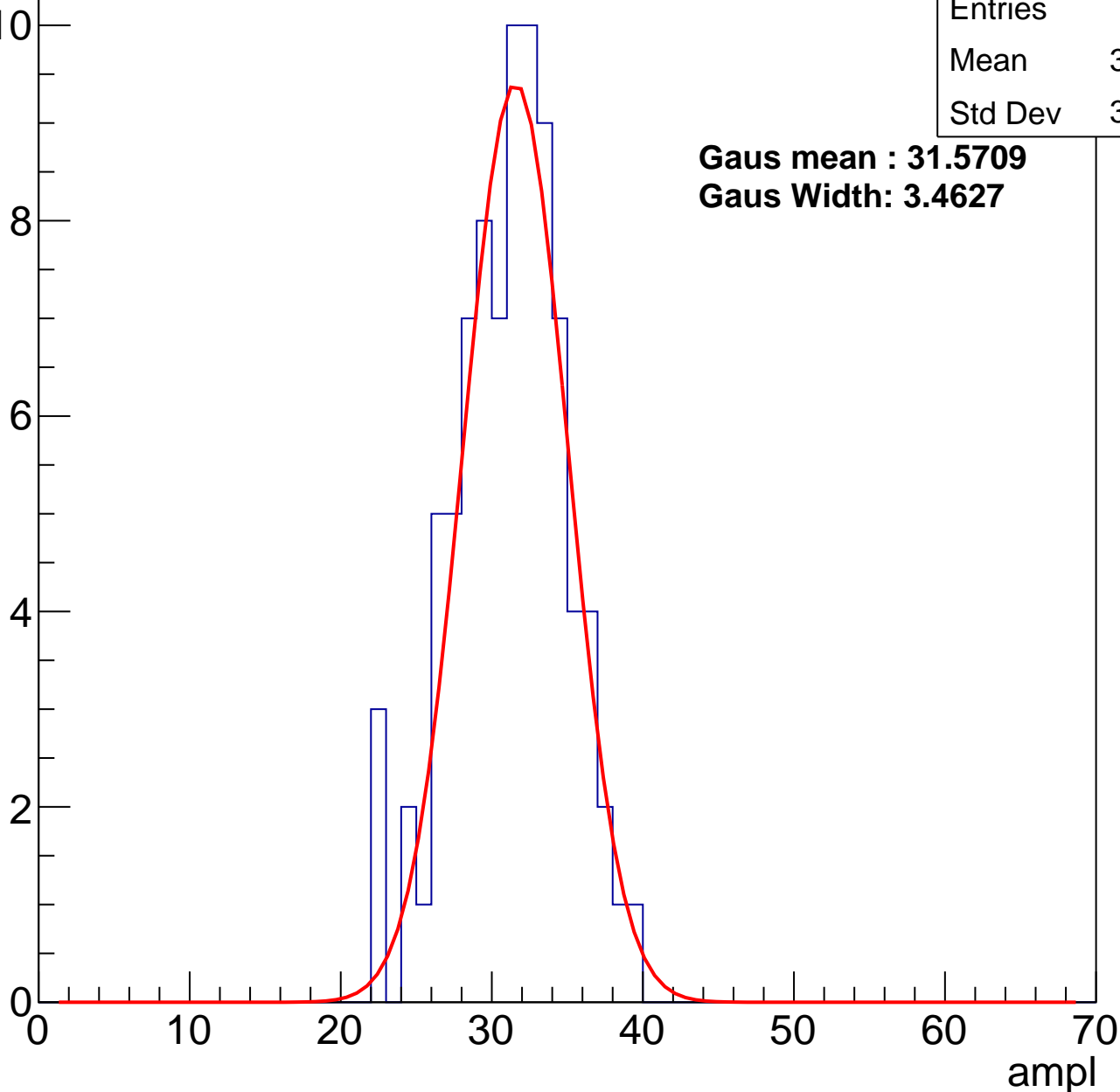
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	30.72
Std Dev	3.633

Gaus mean : 31.5709

Gaus Width: 3.4627



B0L102S, U12-ch51, adc1

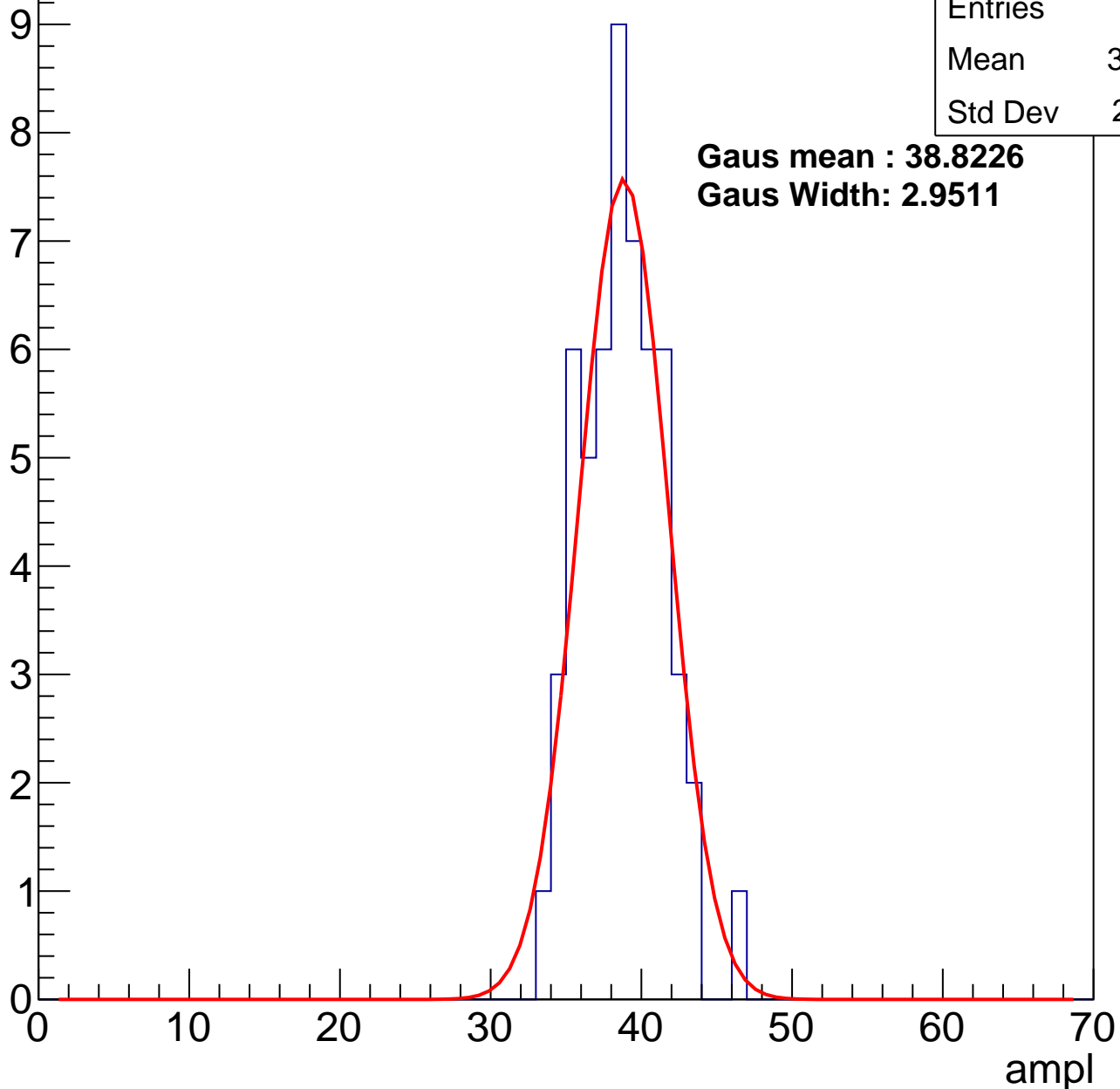
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	38.29
Std Dev	2.681

Gaus mean : 38.8226

Gaus Width: 2.9511



B0L102S, U12-ch51, adc2

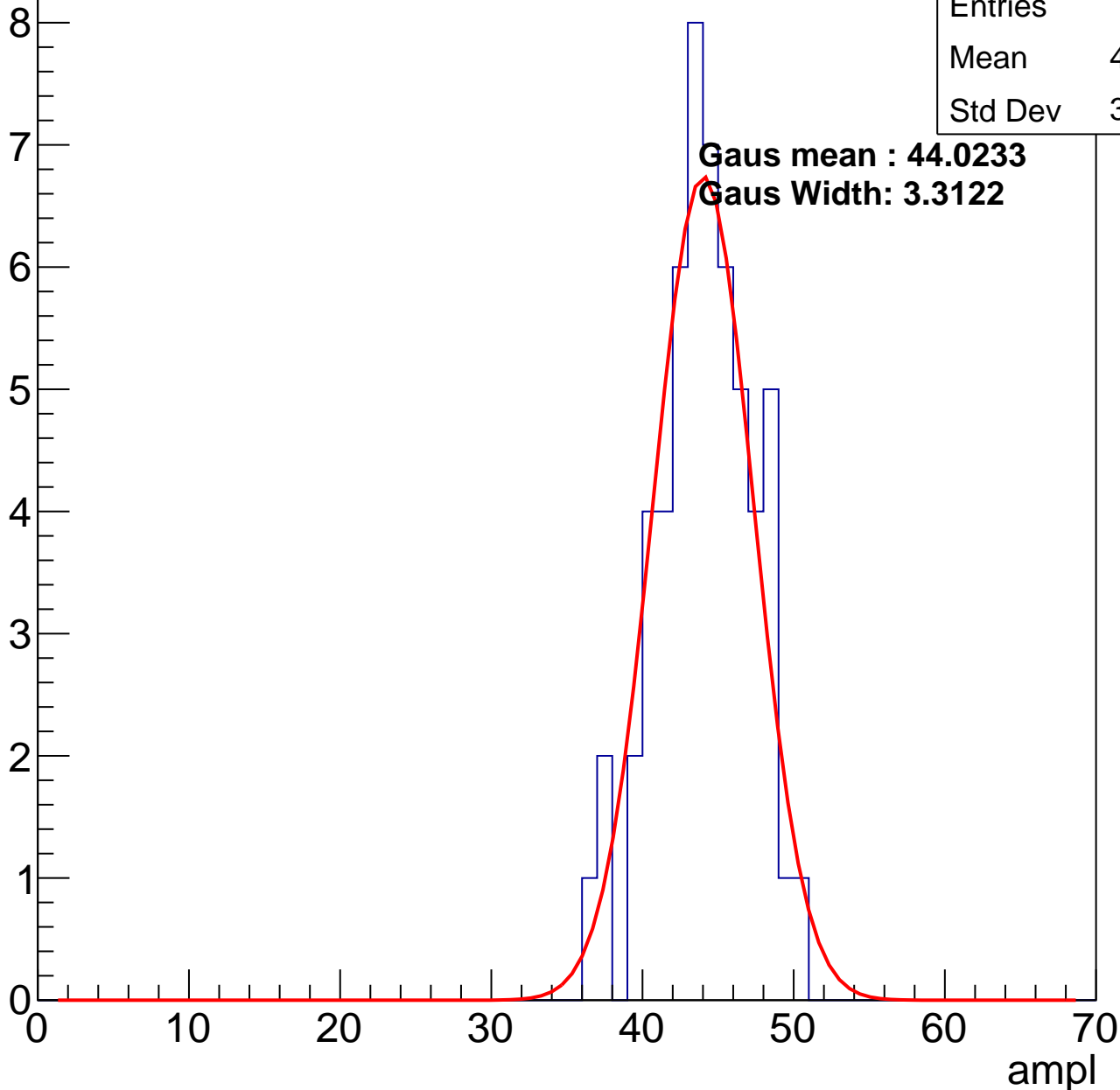
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	43.62
Std Dev	3.103

Gaus mean : 44.0233

Gaus Width: 3.3122

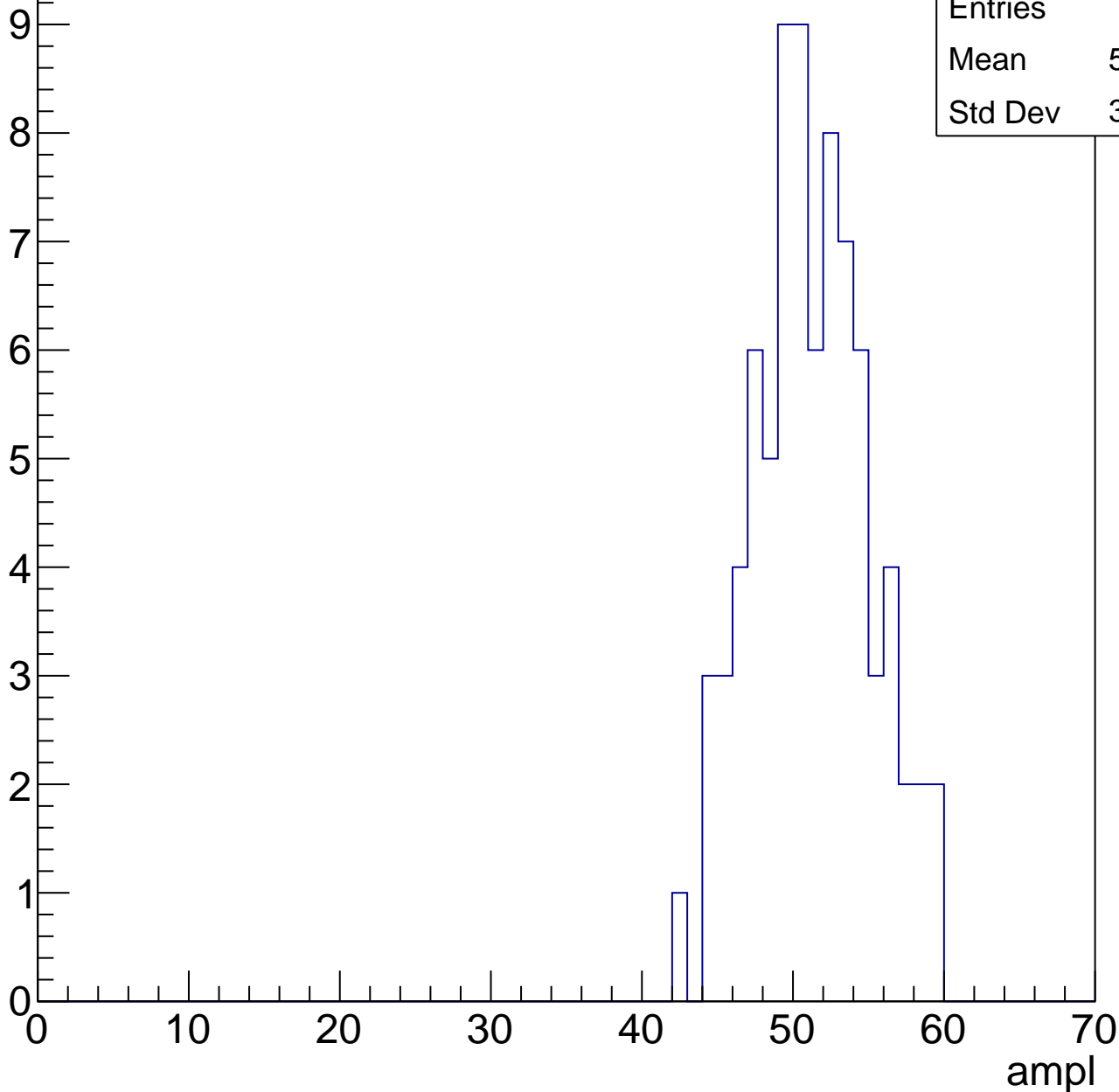


B0L102S, U12-ch51, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	50.75
Std Dev	3.796

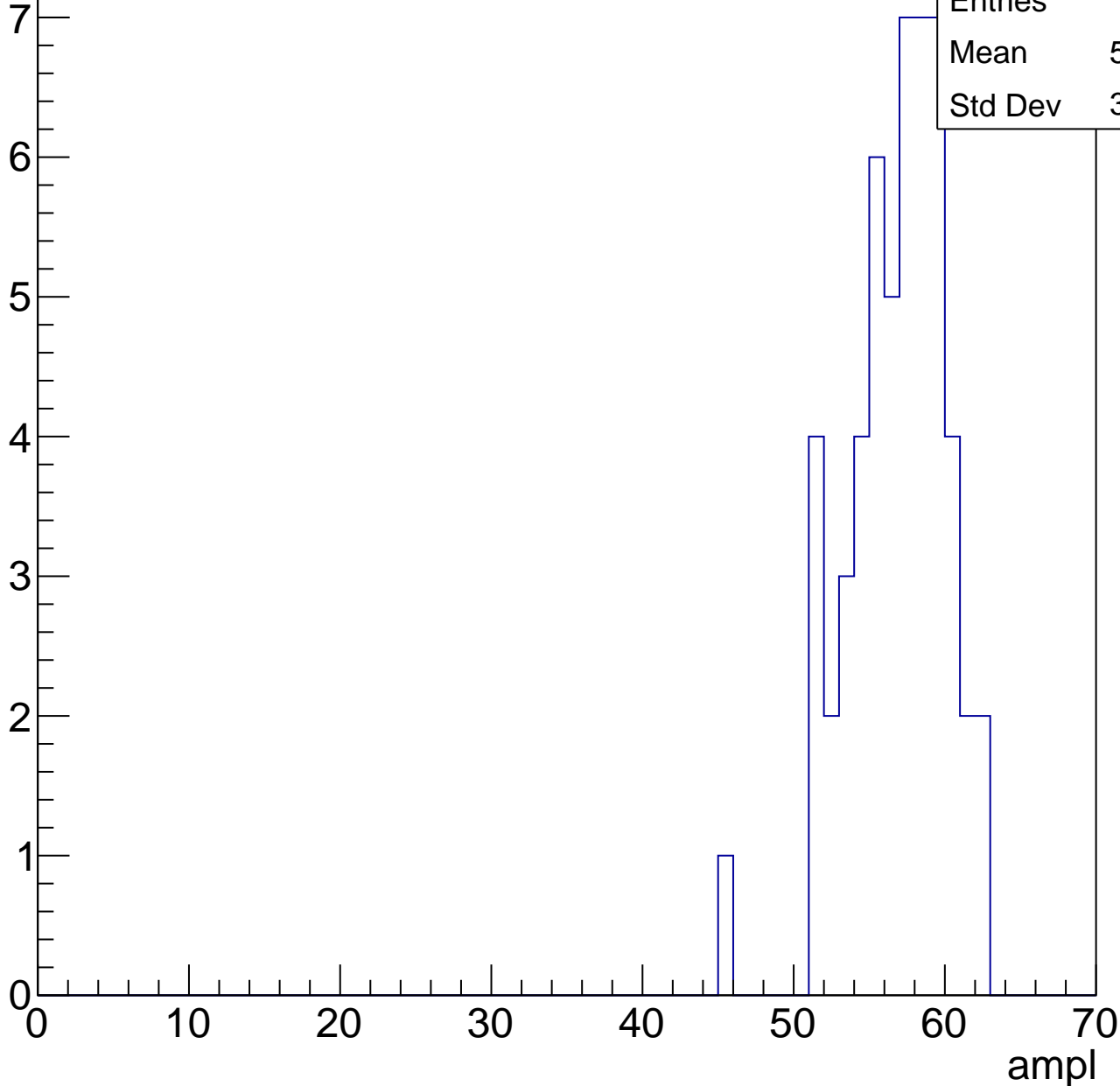


B0L102S, U12-ch51, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	56.33
Std Dev	3.277



B0L102S, U12-ch51, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	38
Mean	59.45
Std Dev	9.965

Entry

10

8

6

4

2

0

0

10

20

30

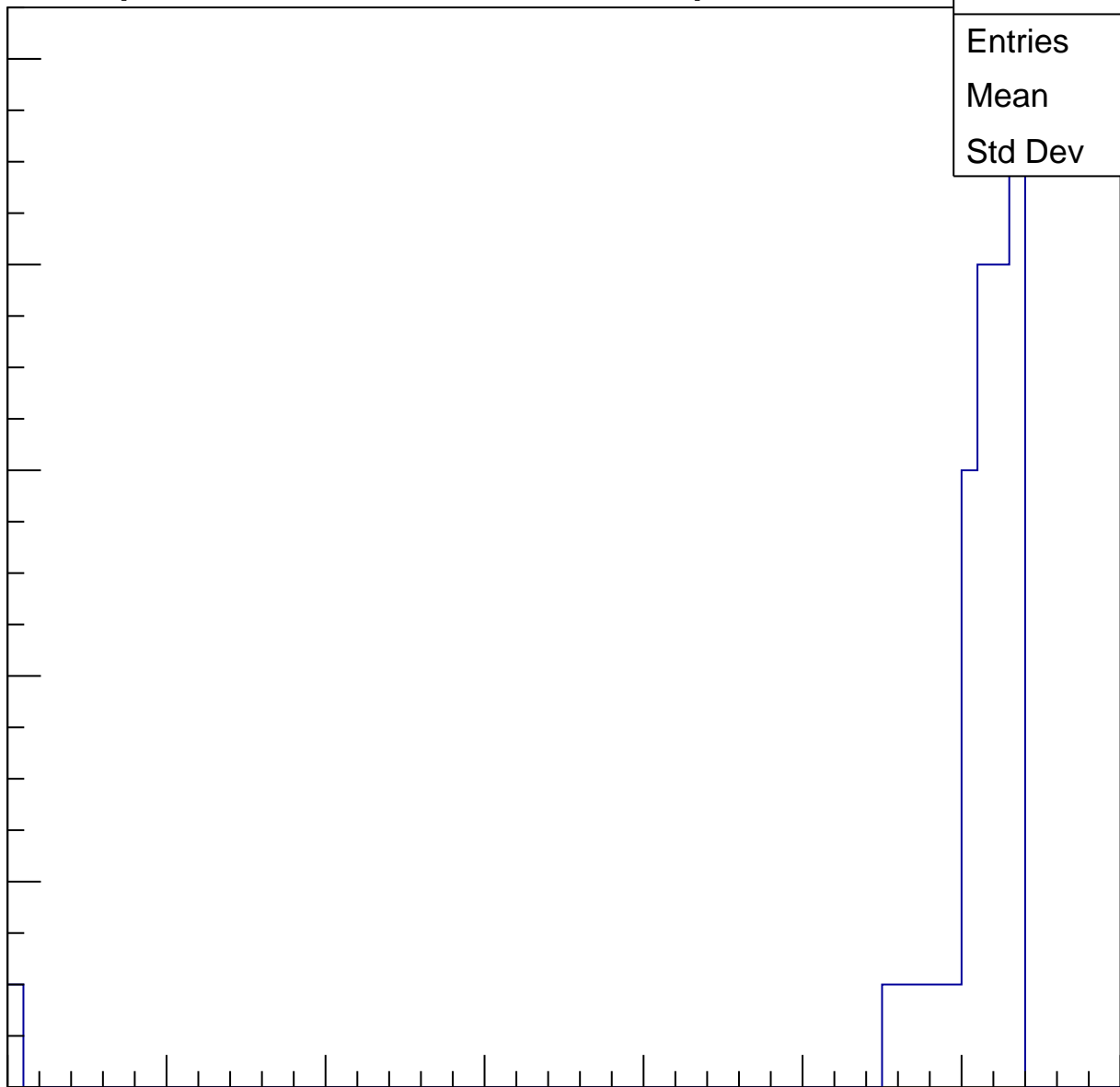
40

50

60

70

ampl



B0L102S, U12-ch51, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch51, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch52, adc0

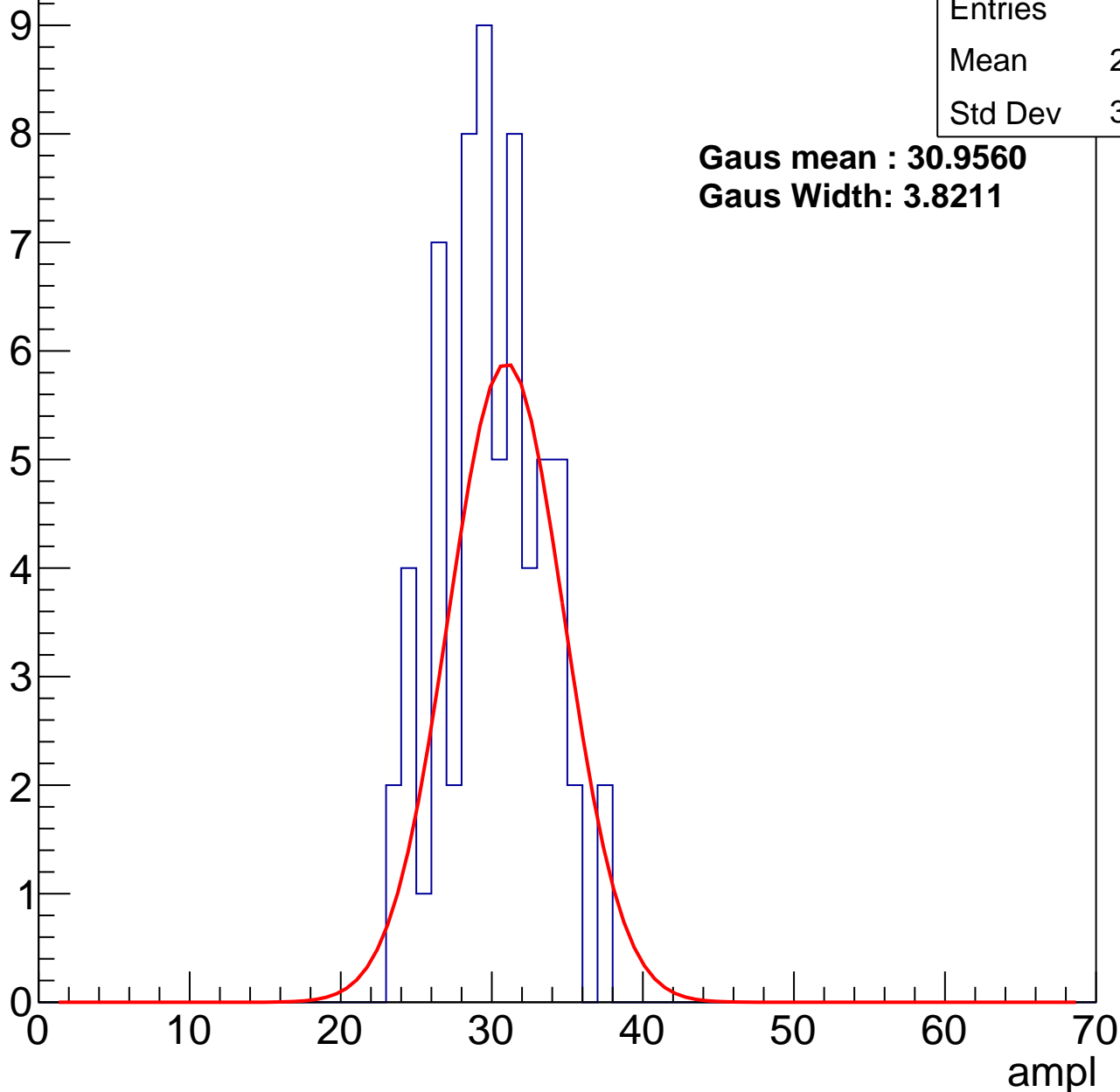
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.58
Std Dev	3.362

Gaus mean : 30.9560

Gaus Width: 3.8211



B0L102S, U12-ch52, adc1

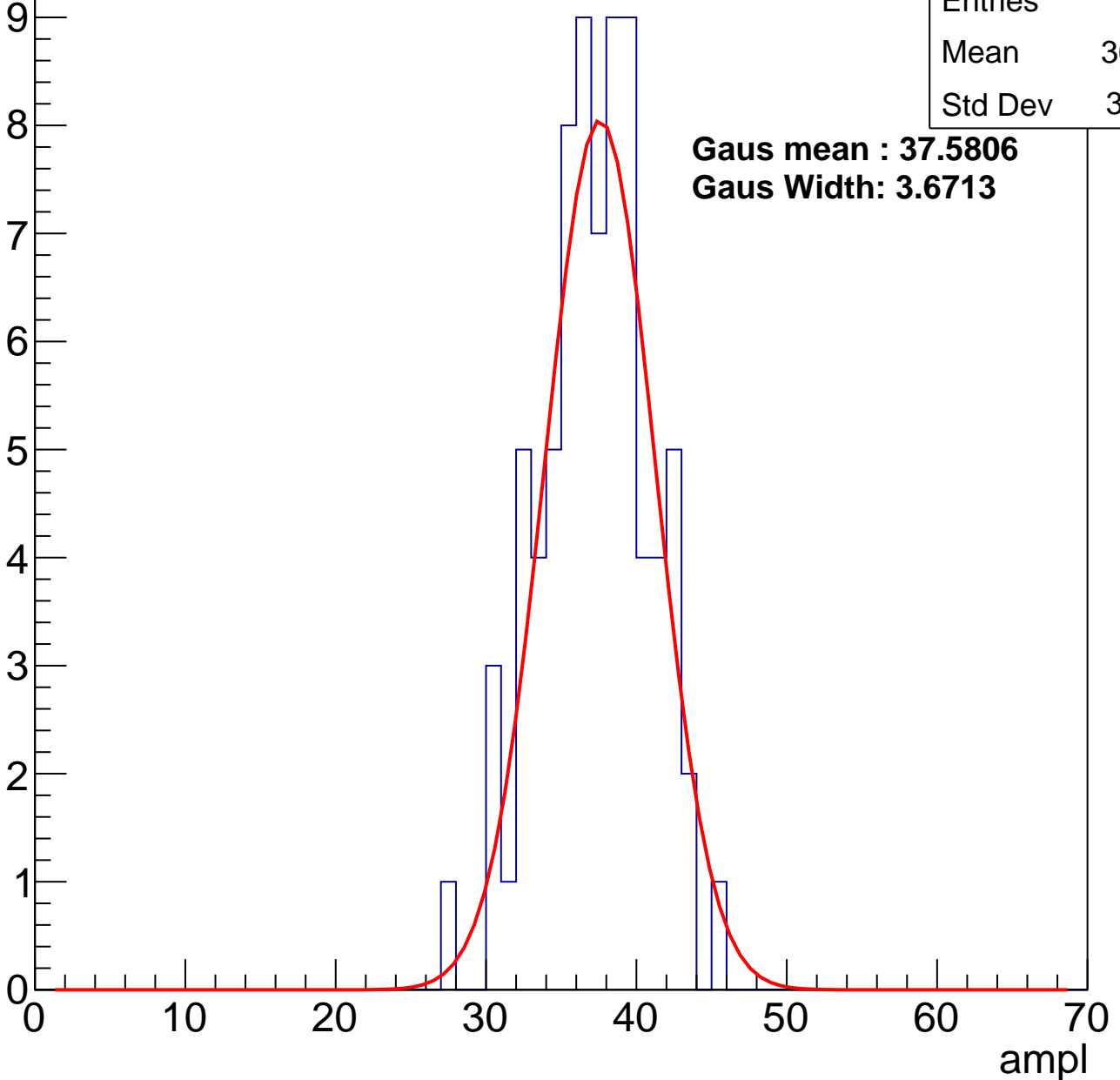
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	36.77
Std Dev	3.531

Gaus mean : 37.5806

Gaus Width: 3.6713



B0L102S, U12-ch52, adc2

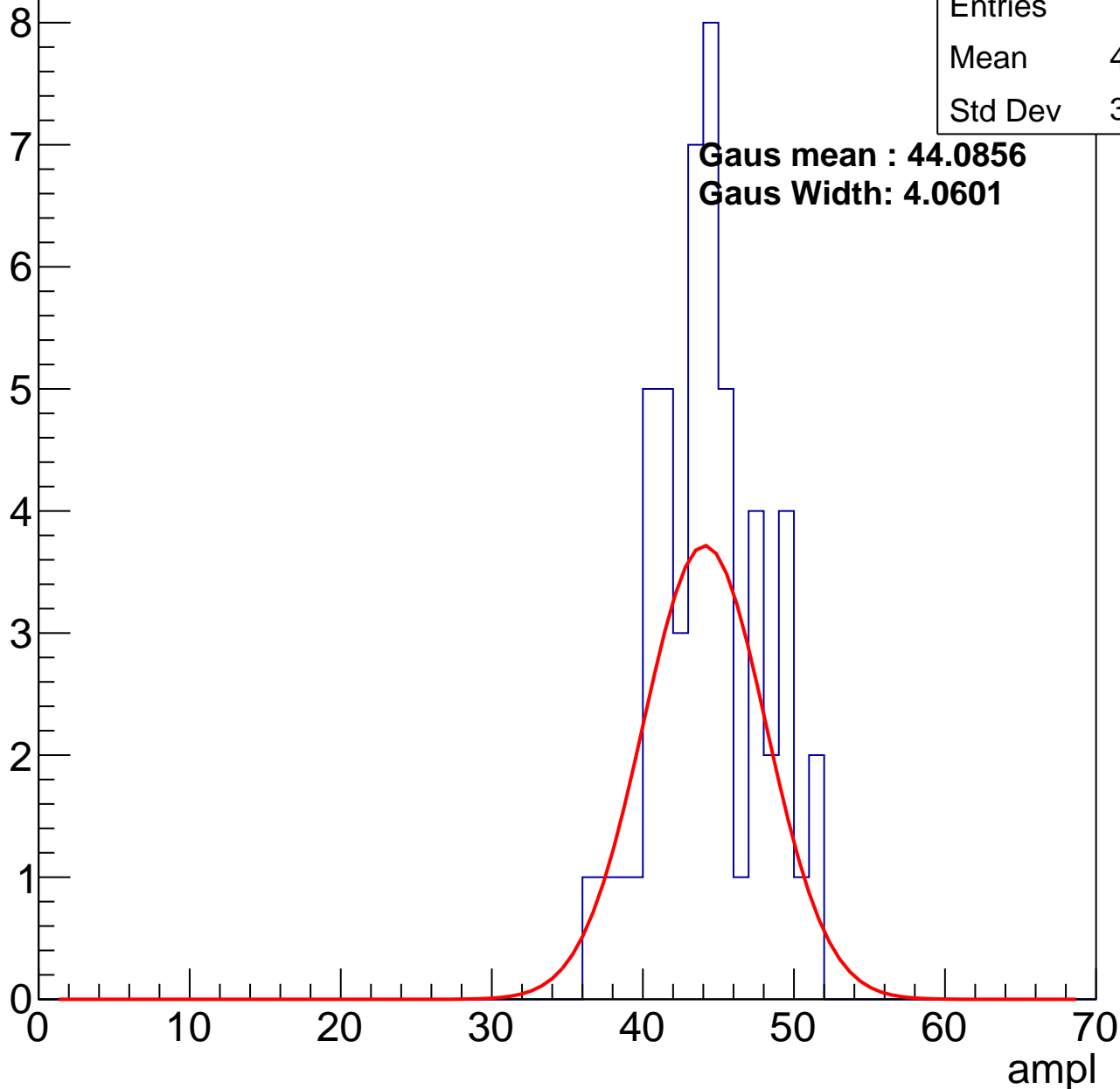
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	43.86
Std Dev	3.509

Gaus mean : 44.0856

Gaus Width: 4.0601

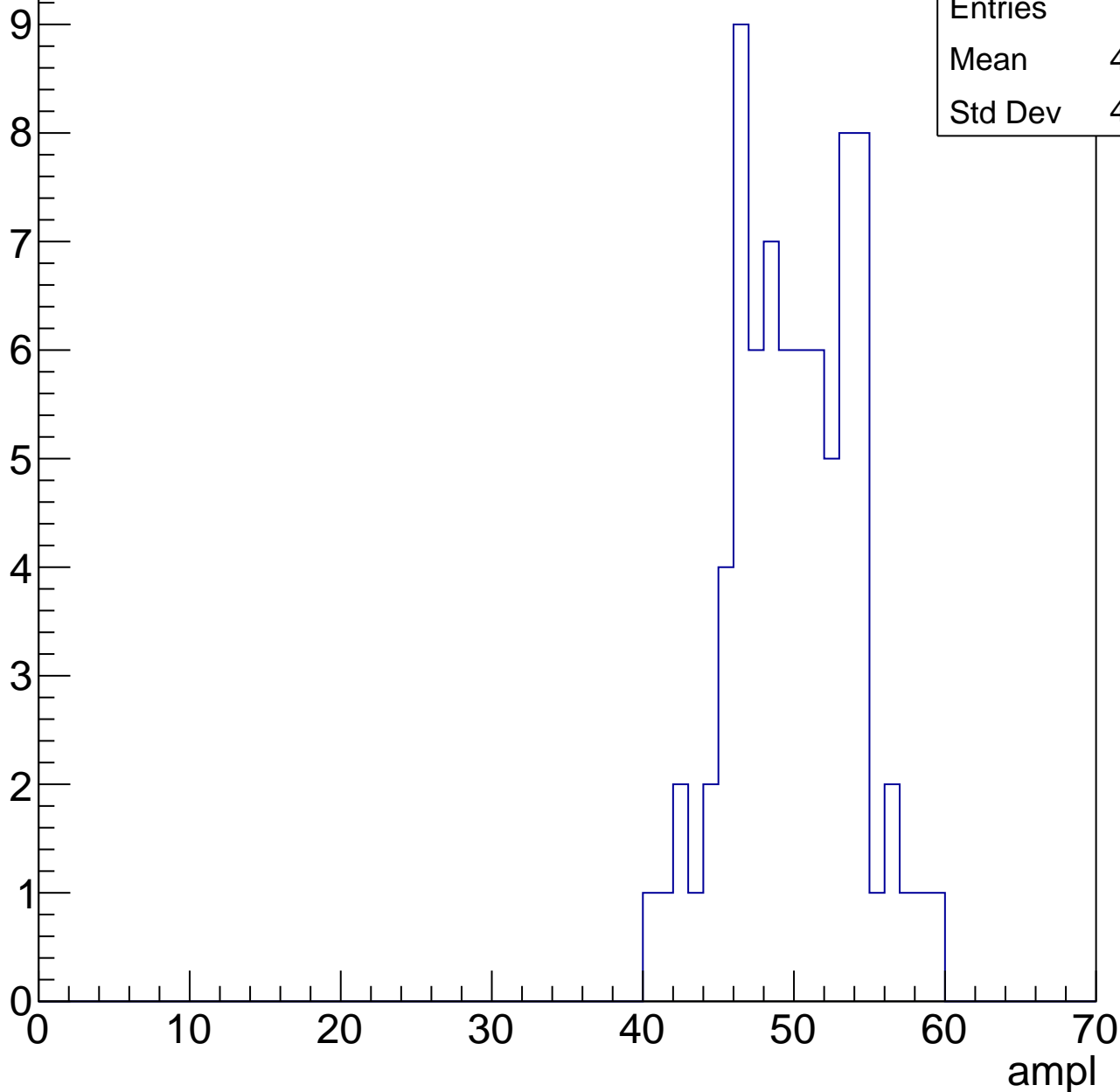


B0L102S, U12-ch52, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	49.55
Std Dev	4.024

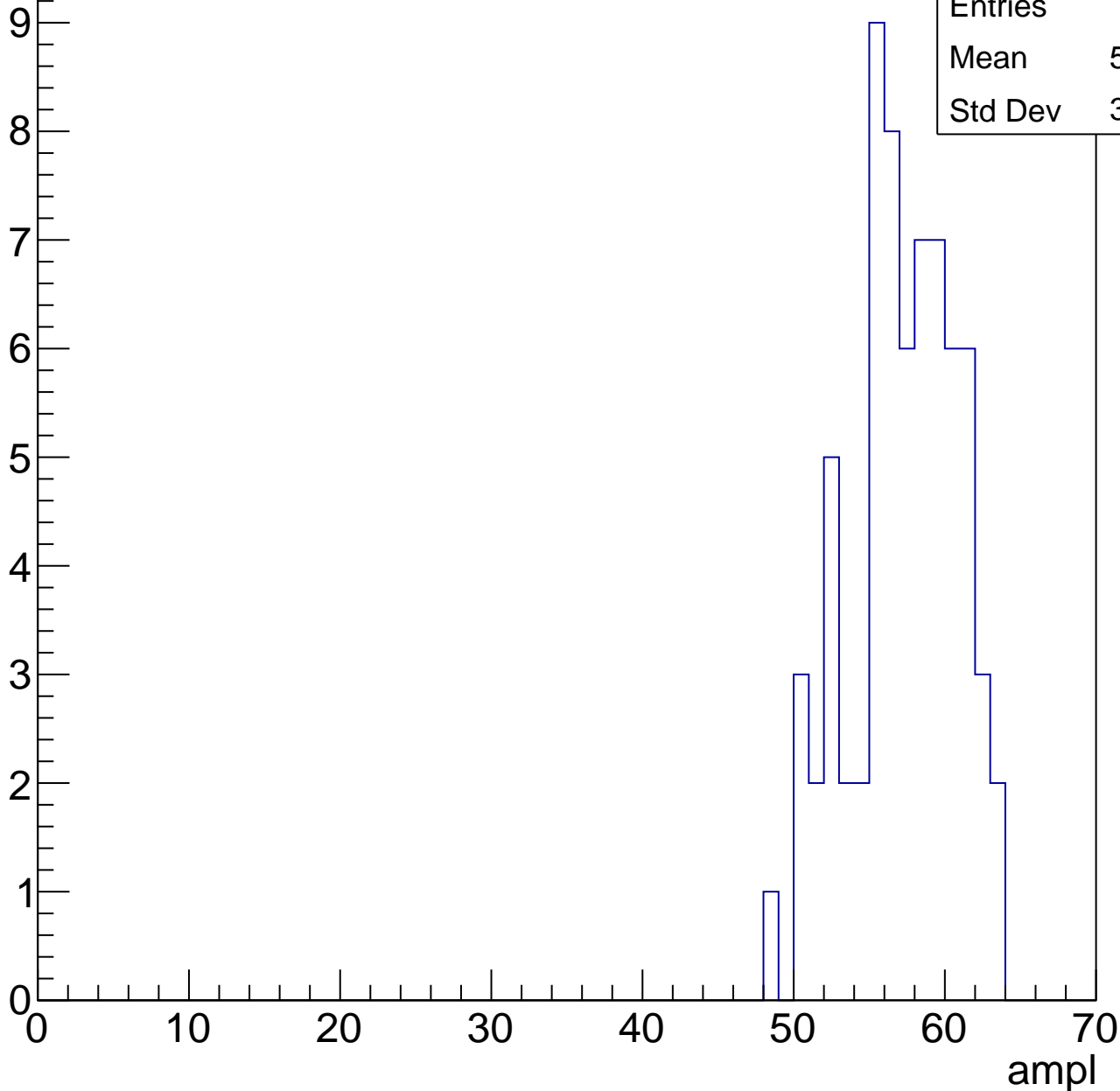


B0L102S, U12-ch52, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	56.75
Std Dev	3.503

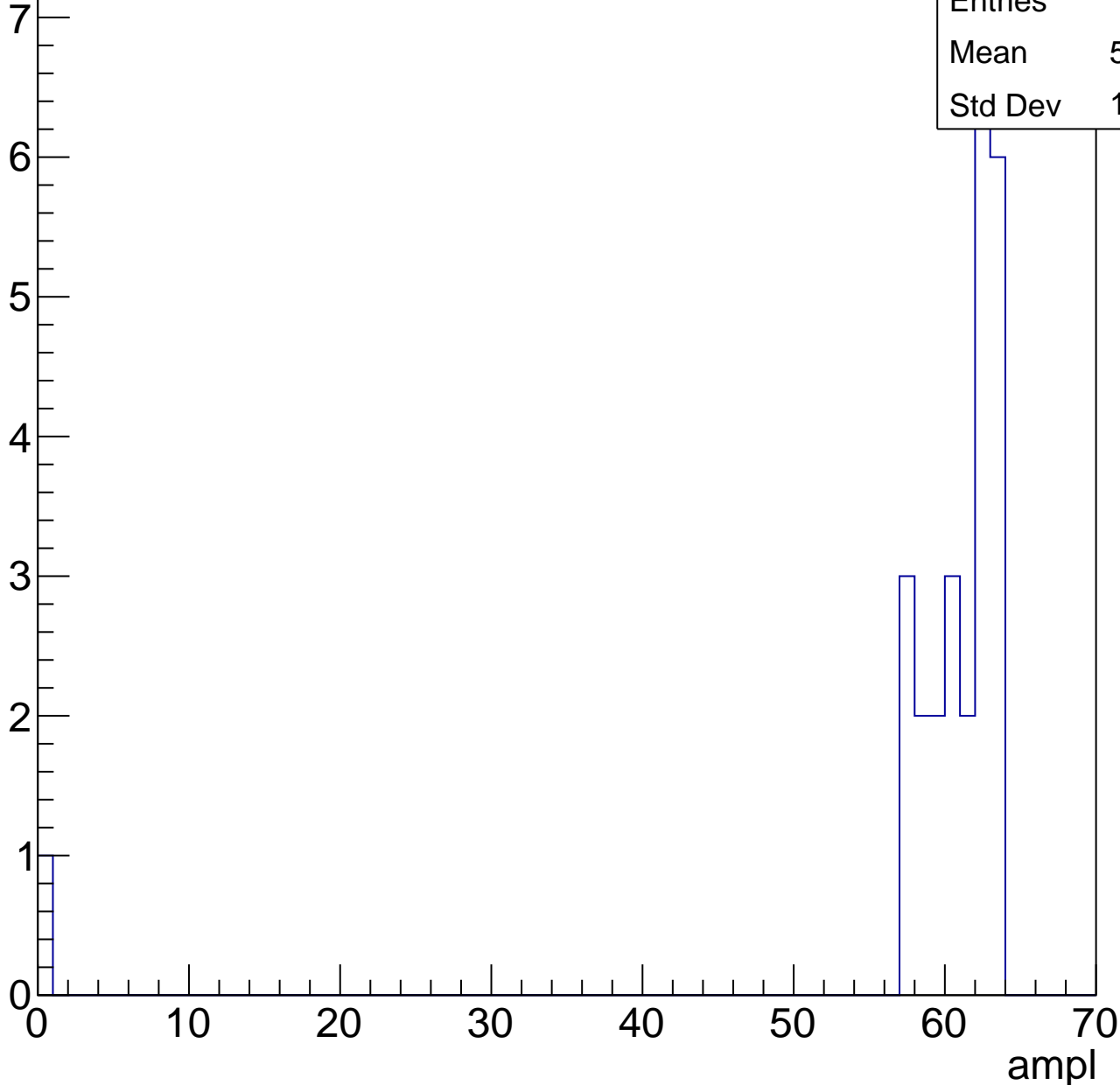


B0L102S, U12-ch52, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	26
Mean	58.42
Std Dev	11.86



B0L102S, U12-ch52, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.2

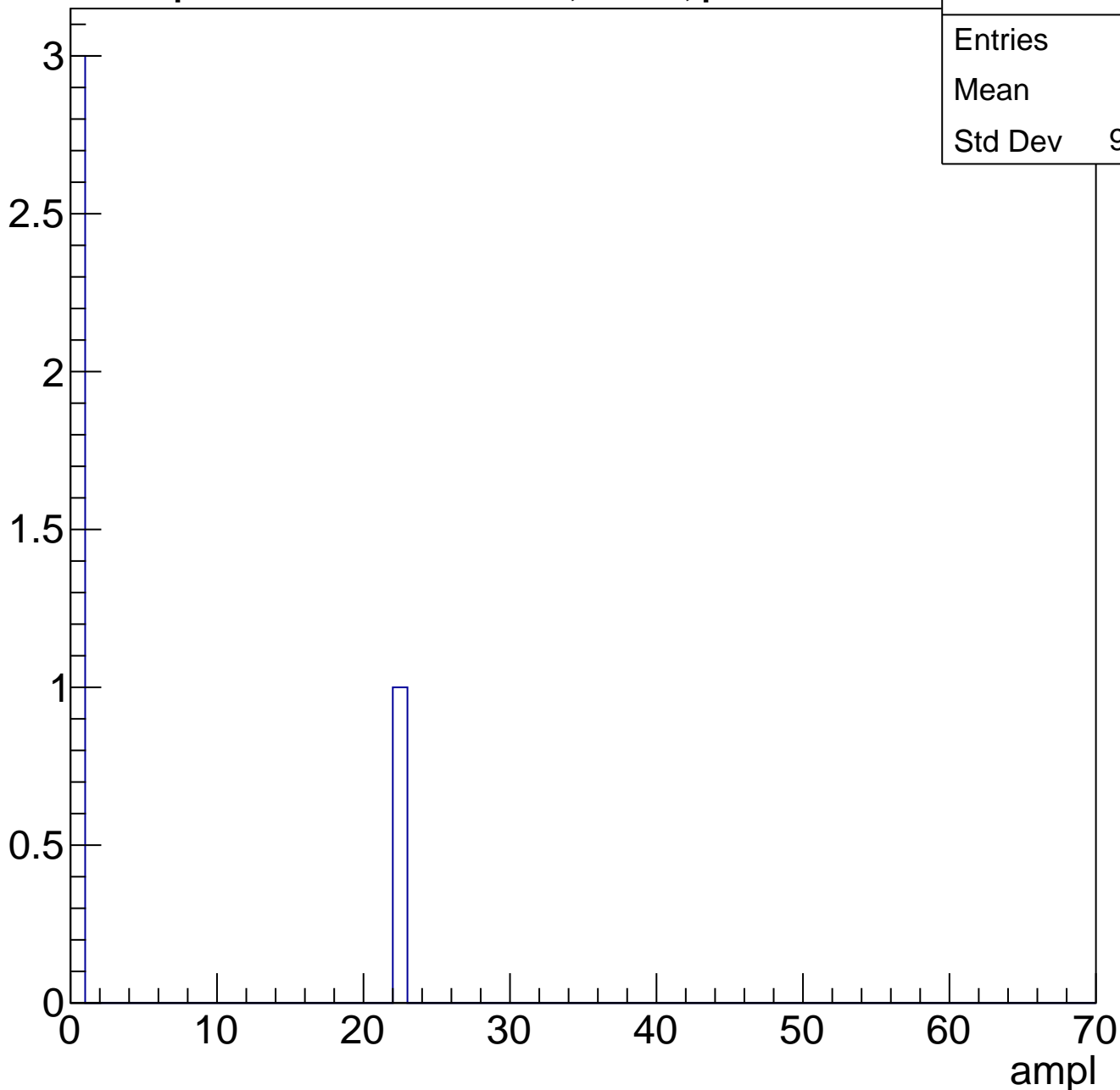
ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch52, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch53, adc0

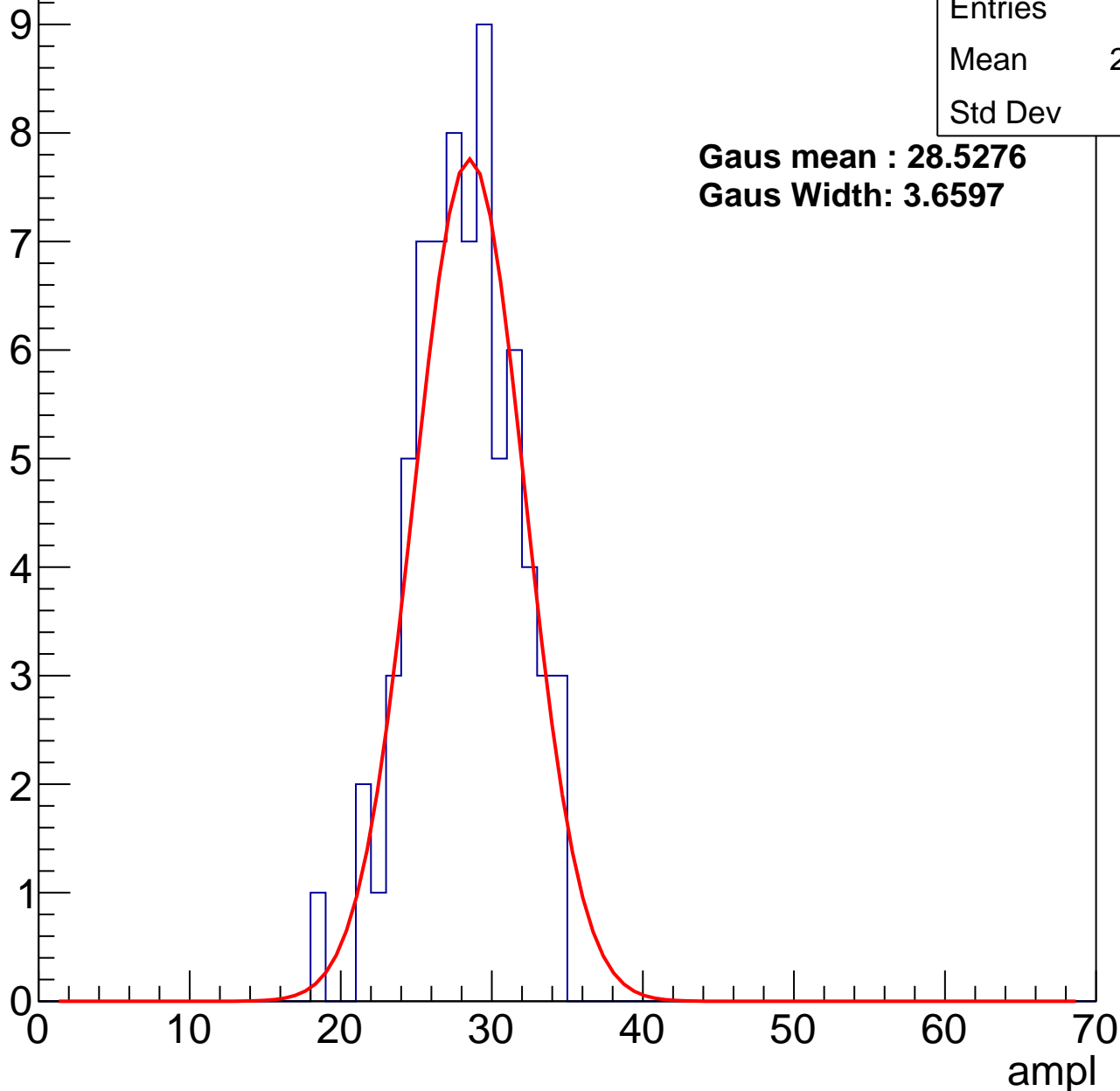
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	27.69
Std Dev	3.38

Gaus mean : 28.5276

Gaus Width: 3.6597



B0L102S, U12-ch53, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	35.78
Std Dev	3.522

Gaus mean : 35.7632

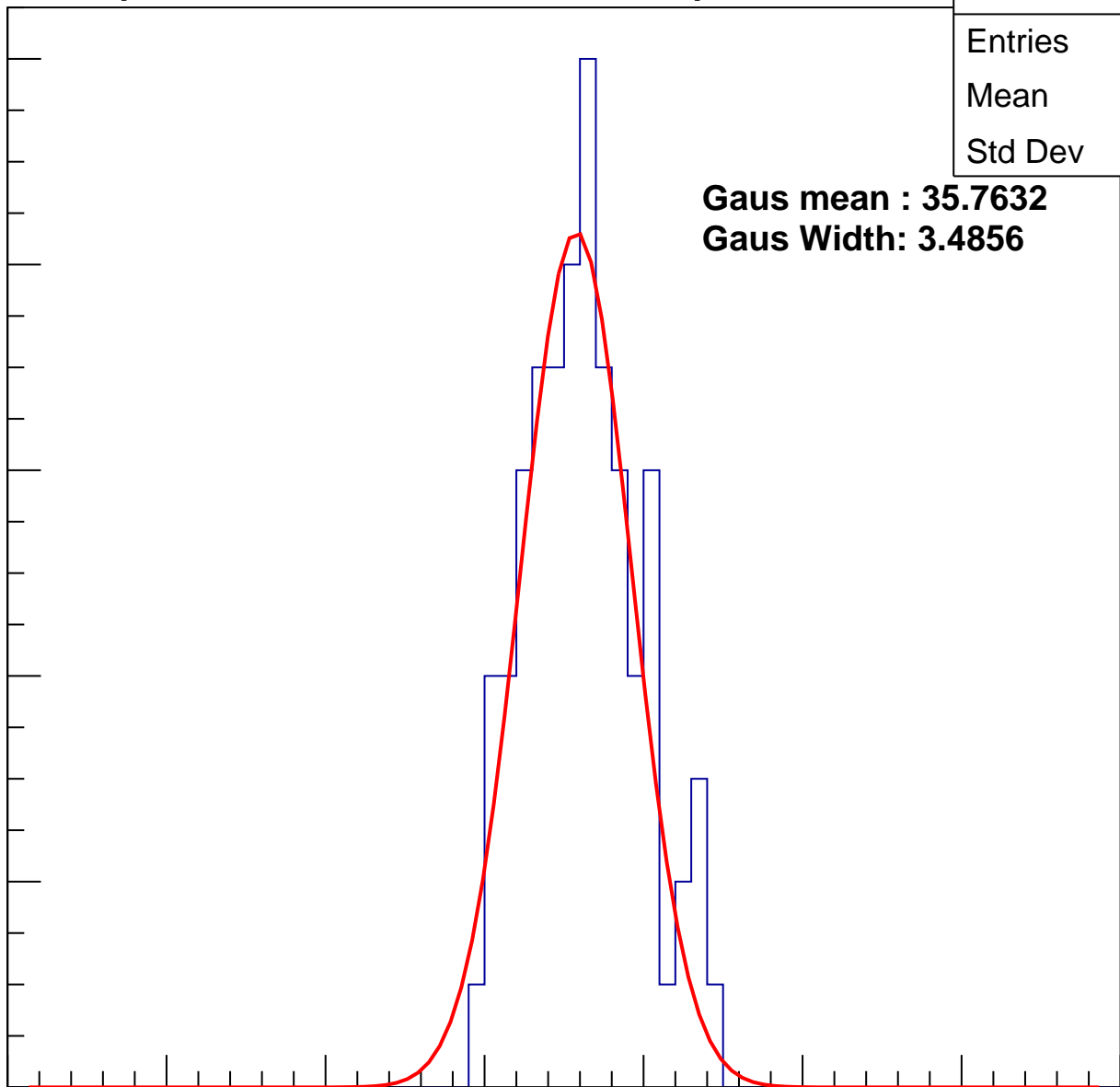
Gaus Width: 3.4856

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U12-ch53, adc2

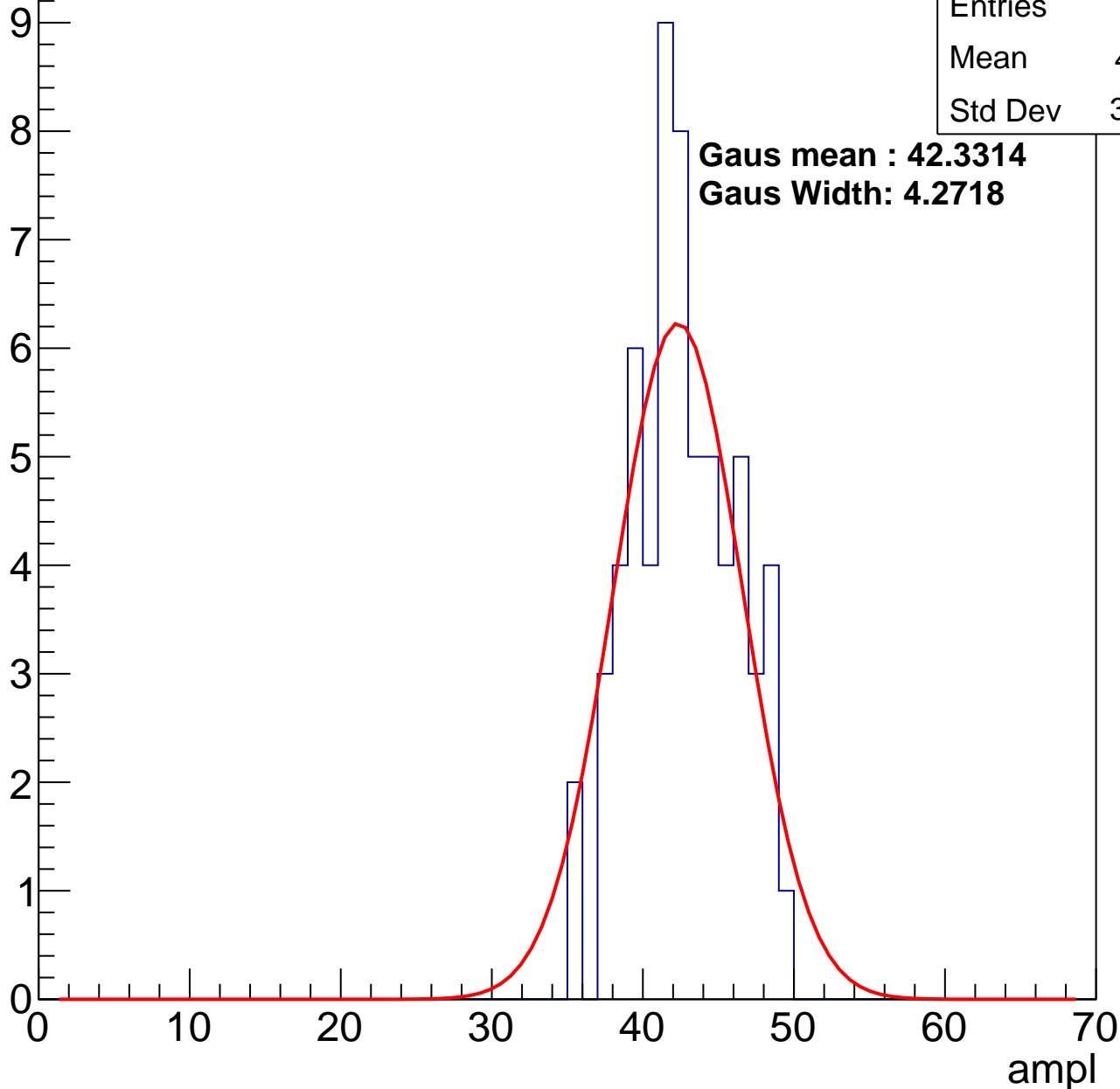
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	42.21
Std Dev	3.386

Gaus mean : 42.3314

Gaus Width: 4.2718



B0L102S, U12-ch53, adc3

calib_packv5_042523_0143.root, FC#12, port B1

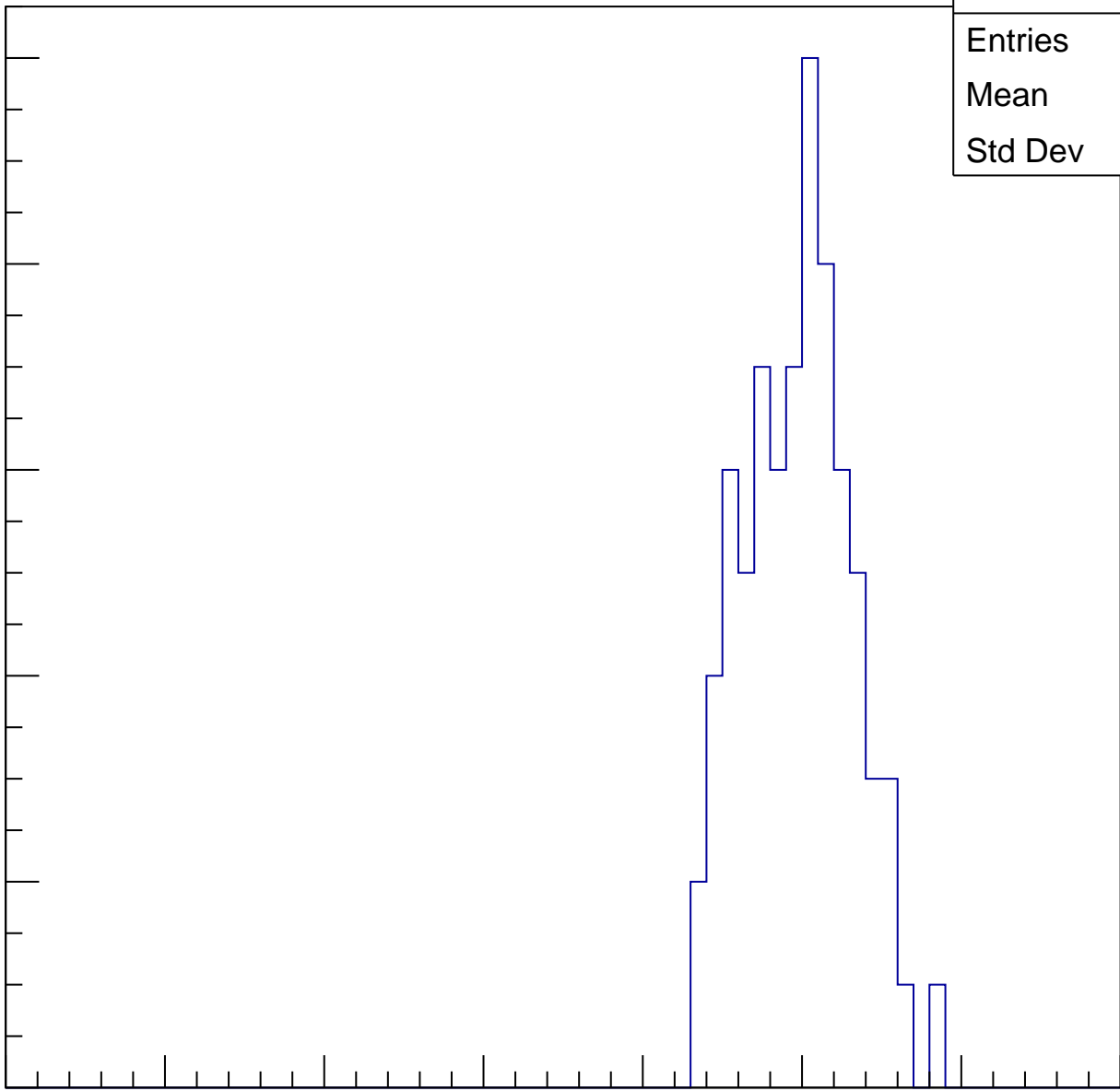
Entries	74
Mean	49.3
Std Dev	3.348

Entry

10
8
6
4
2
0

ampl

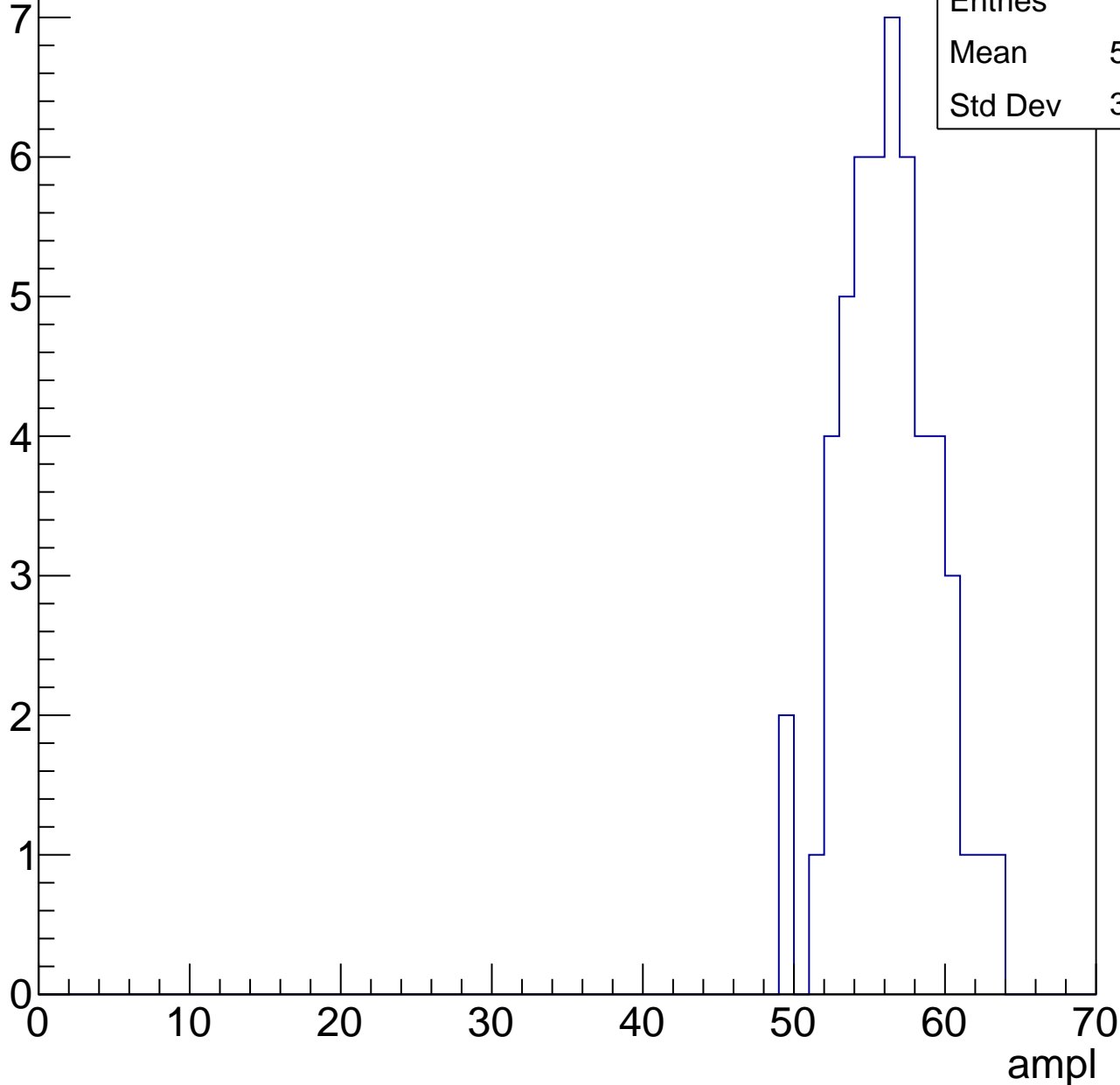
0 10 20 30 40 50 60 70



B0L102S, U12-ch53, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

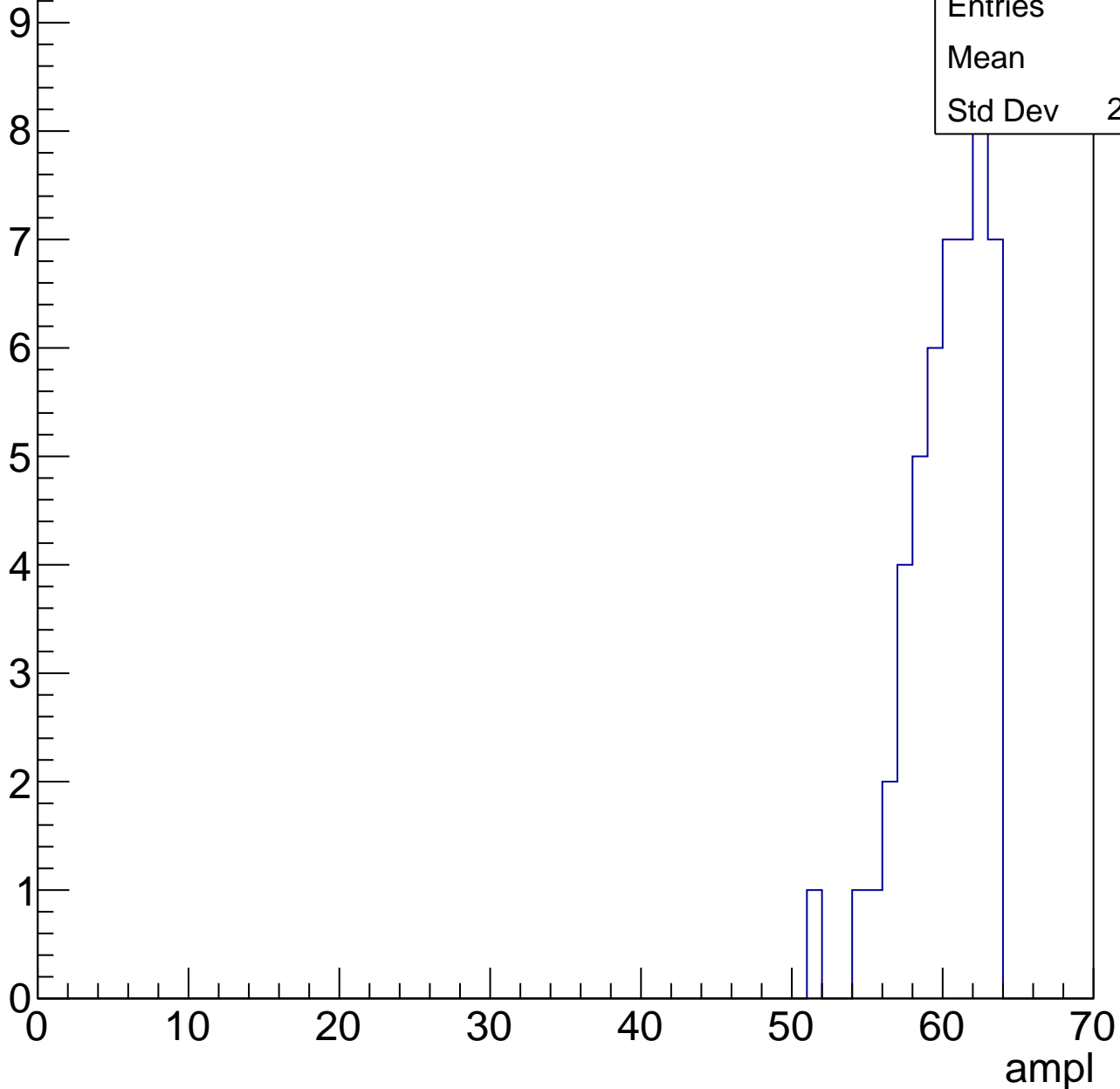


B0L102S, U12-ch53, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

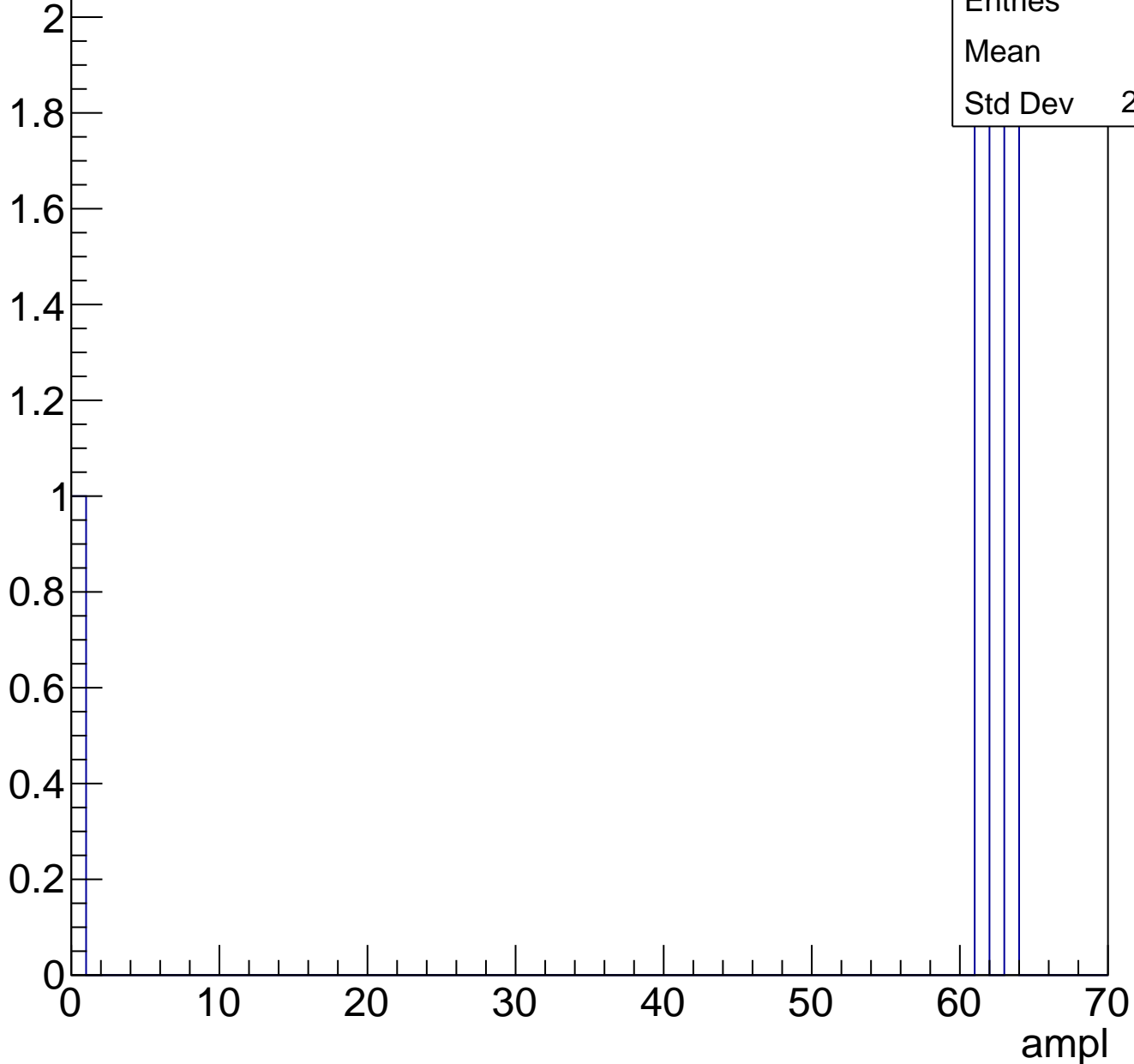
Entries	50
Mean	59.8
Std Dev	2.608



B0L102S, U12-ch53, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch53, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch54, adc0

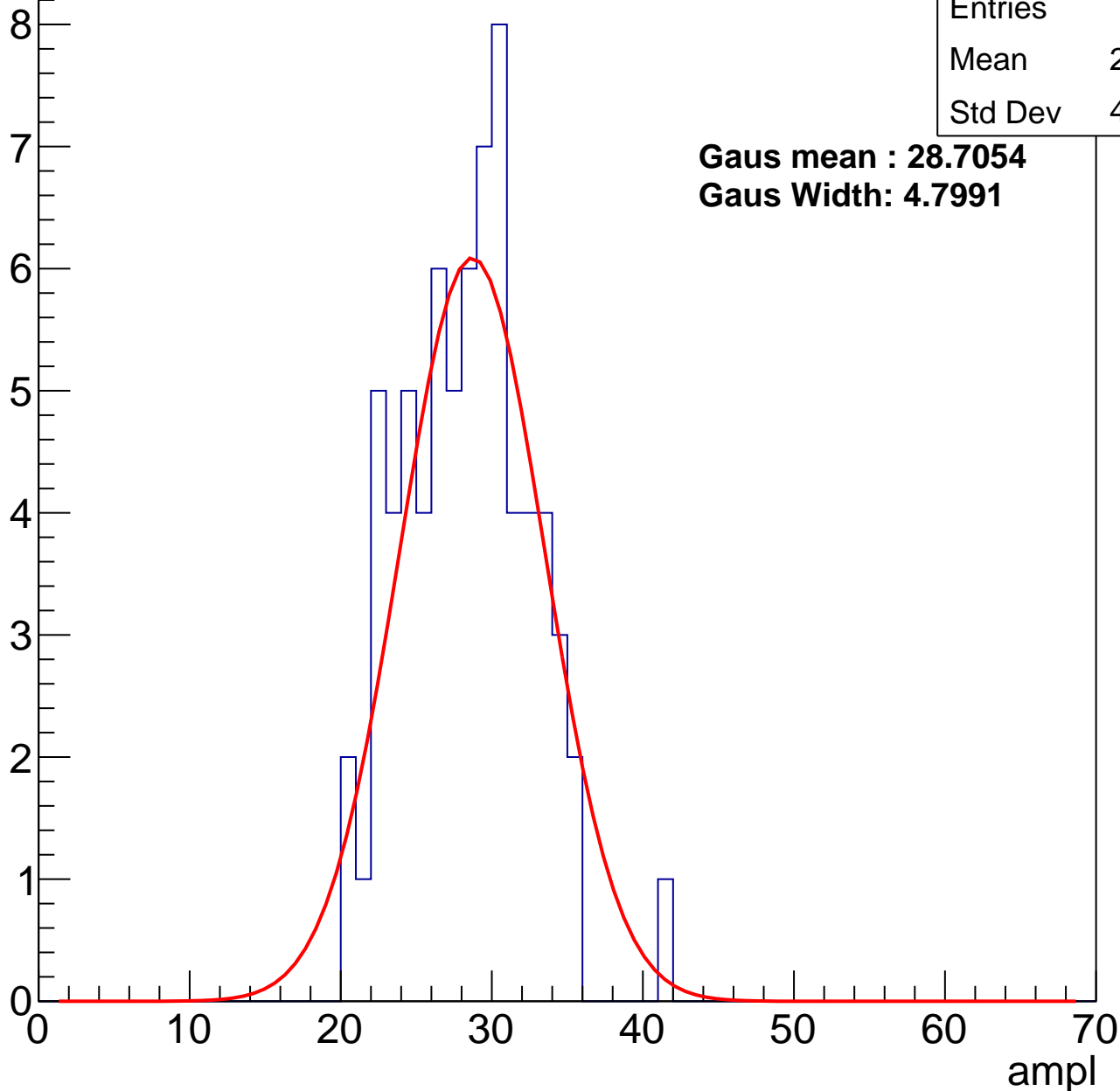
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	27.92
Std Dev	4.138

Gaus mean : 28.7054

Gaus Width: 4.7991



B0L102S, U12-ch54, adc1

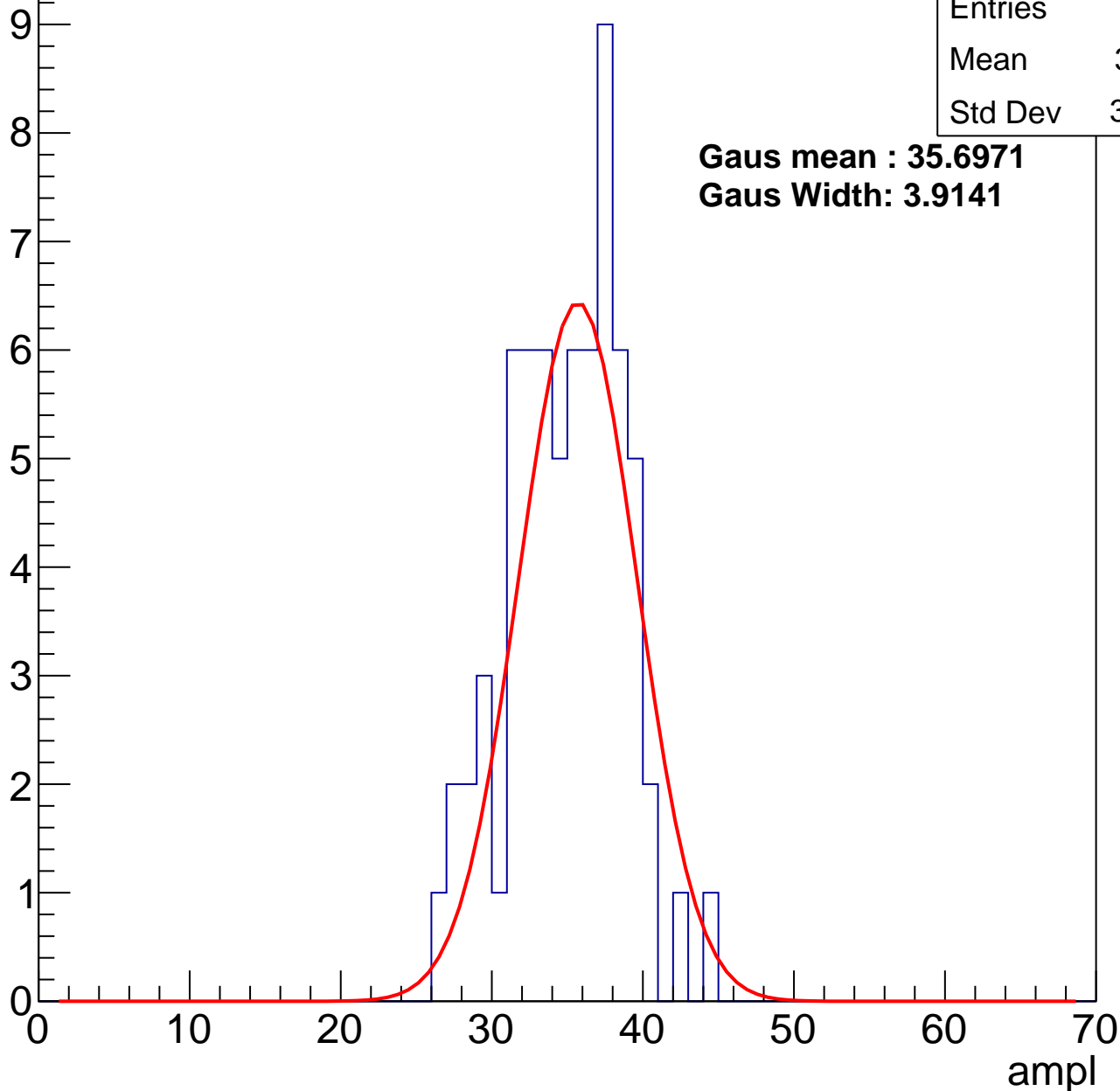
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	34.51
Std Dev	3.755

Gaus mean : 35.6971

Gaus Width: 3.9141



B0L102S, U12-ch54, adc2

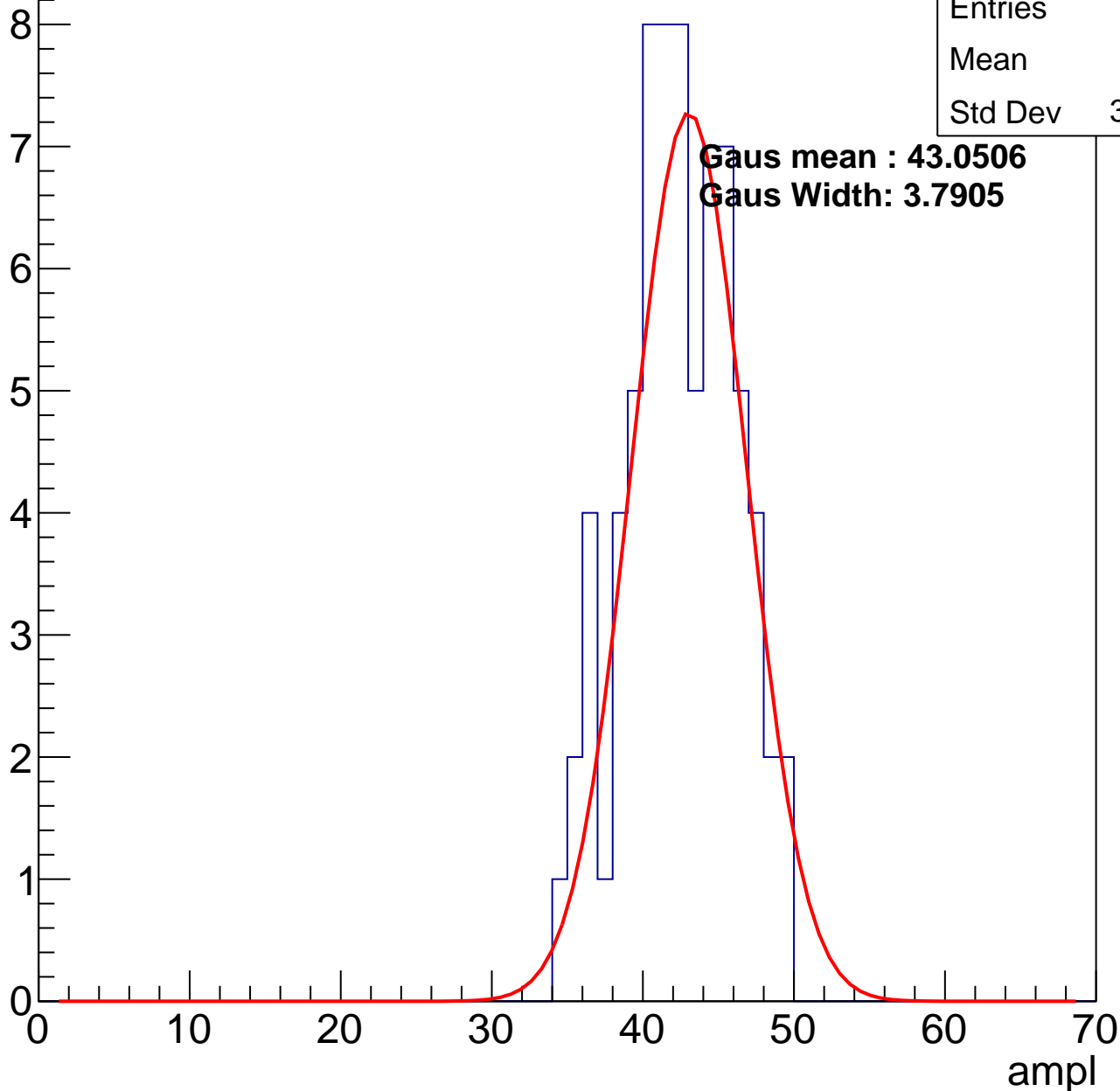
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	42
Std Dev	3.562

Gaus mean : 43.0506

Gaus Width: 3.7905

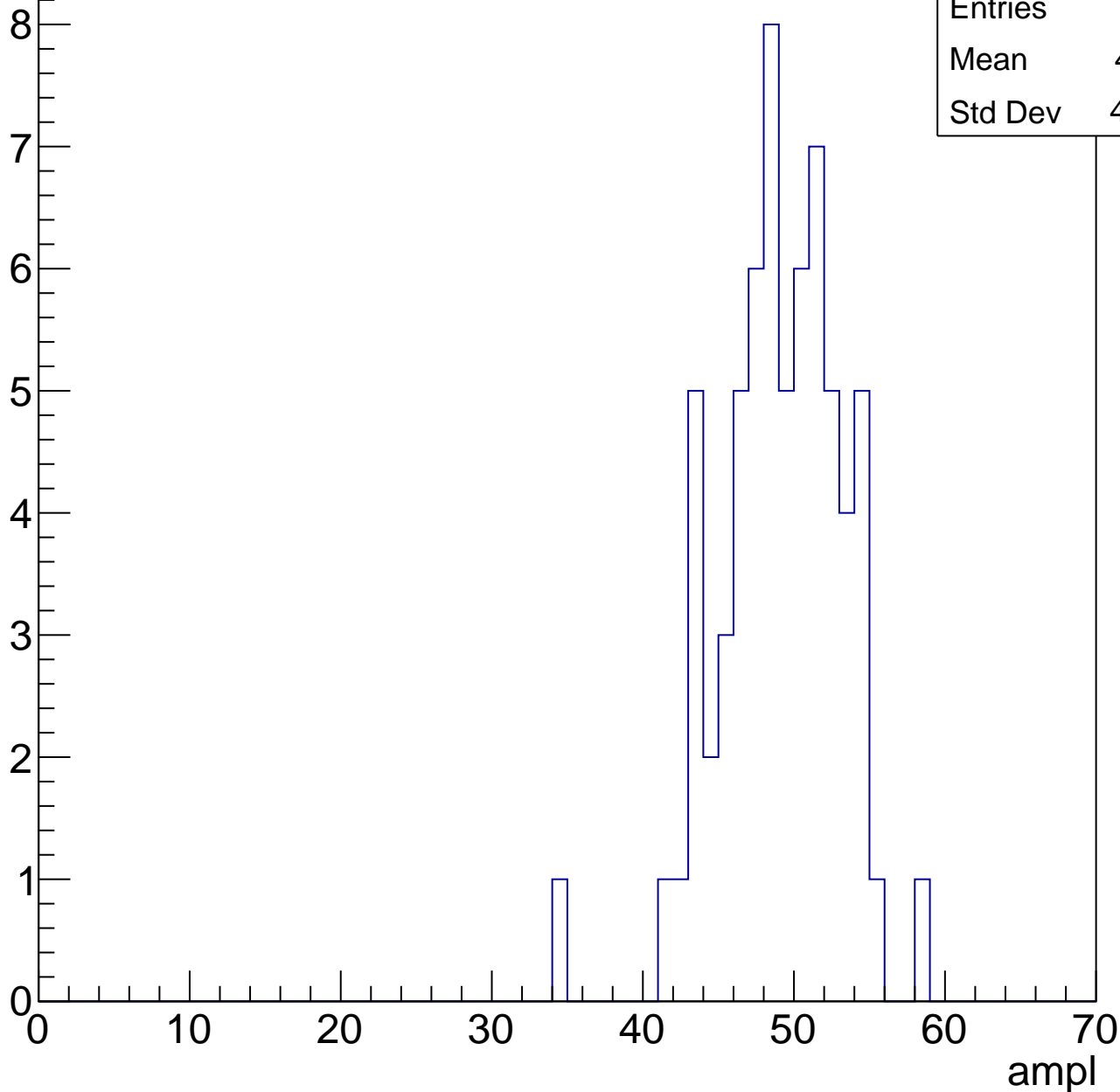


B0L102S, U12-ch54, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

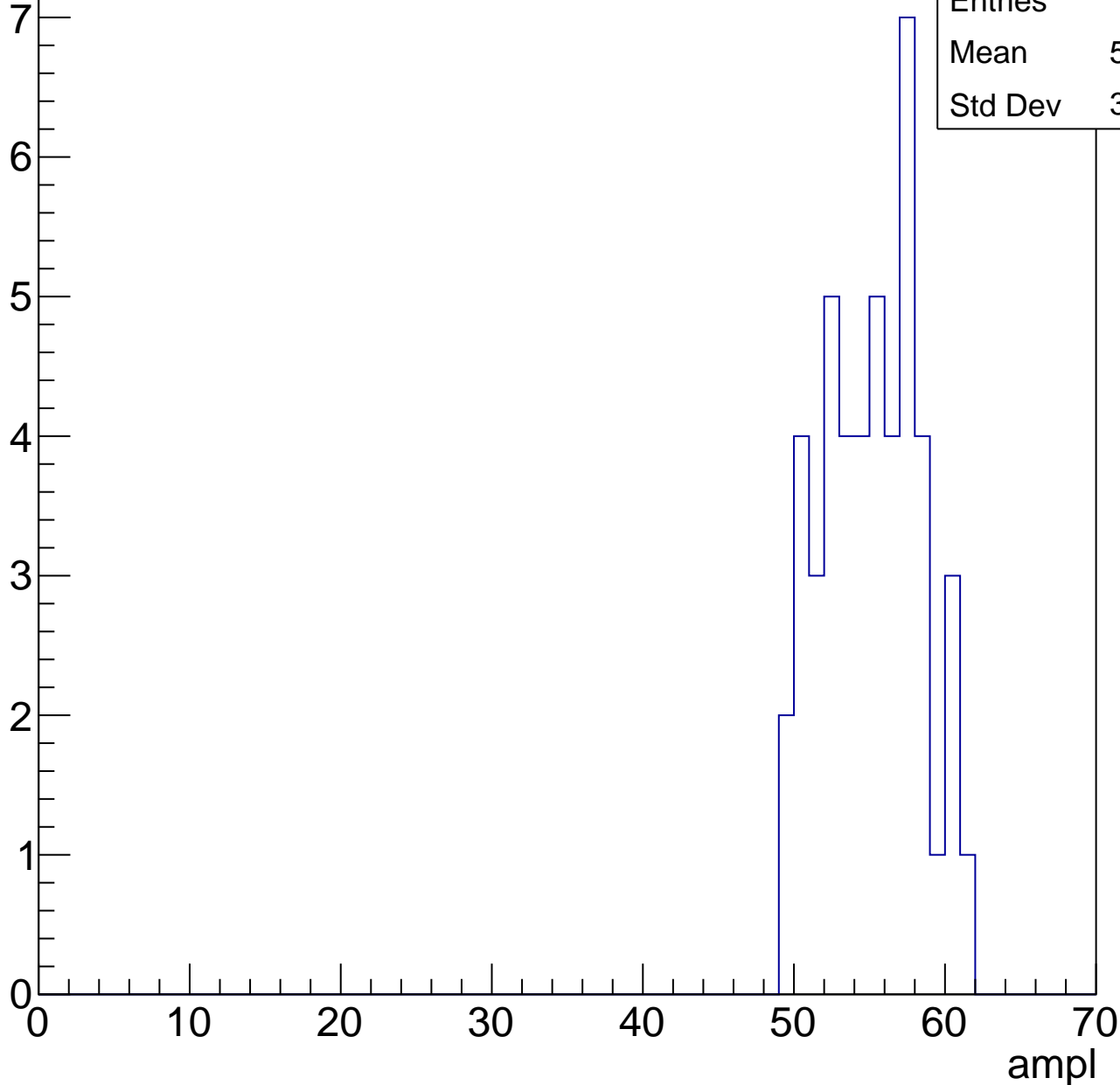
Entries	66
Mean	48.61
Std Dev	4.022



B0L102S, U12-ch54, adc4

calib_packv5_042523_0143.root, FC#12, port B1

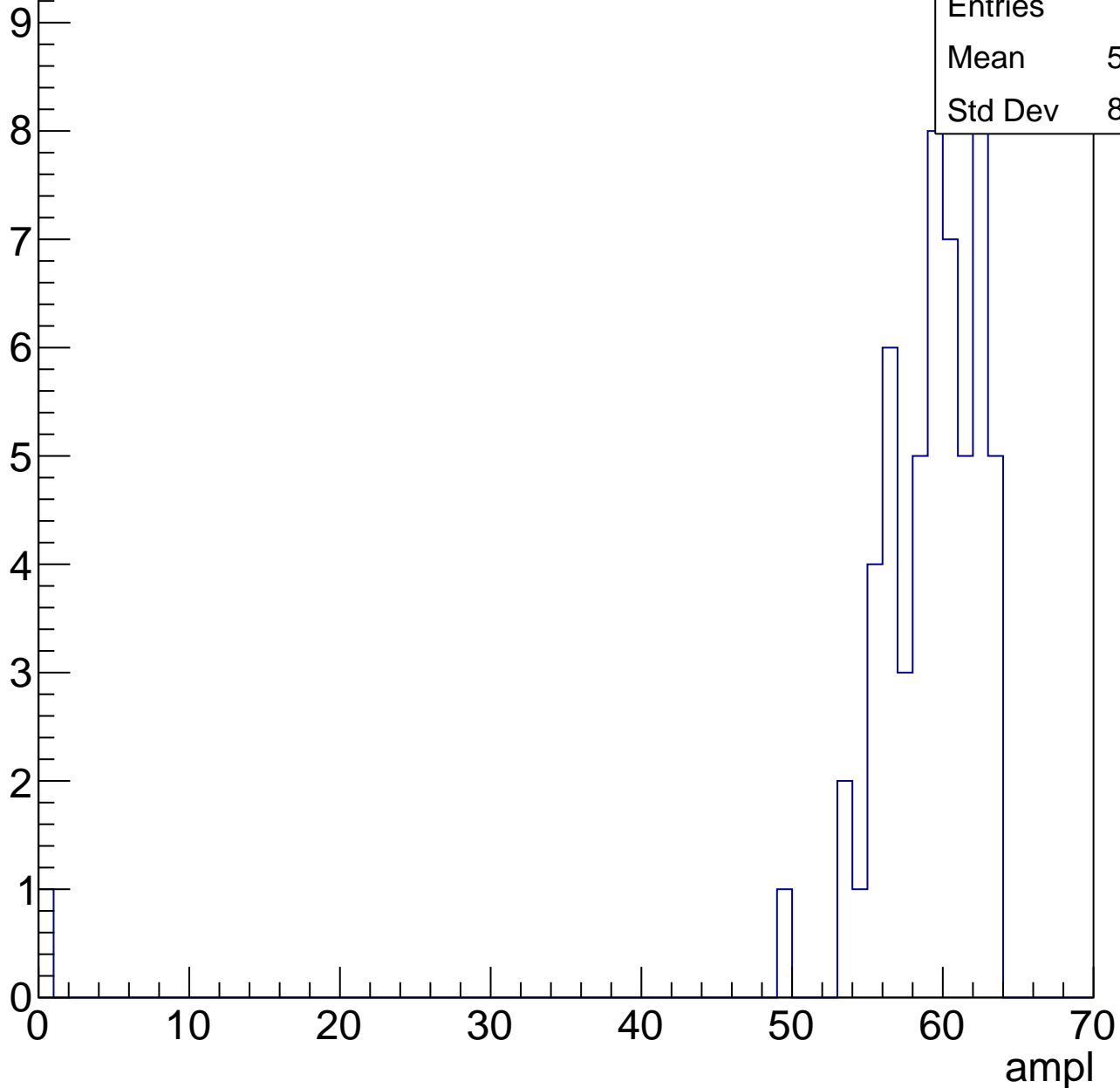
Entry



B0L102S, U12-ch54, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



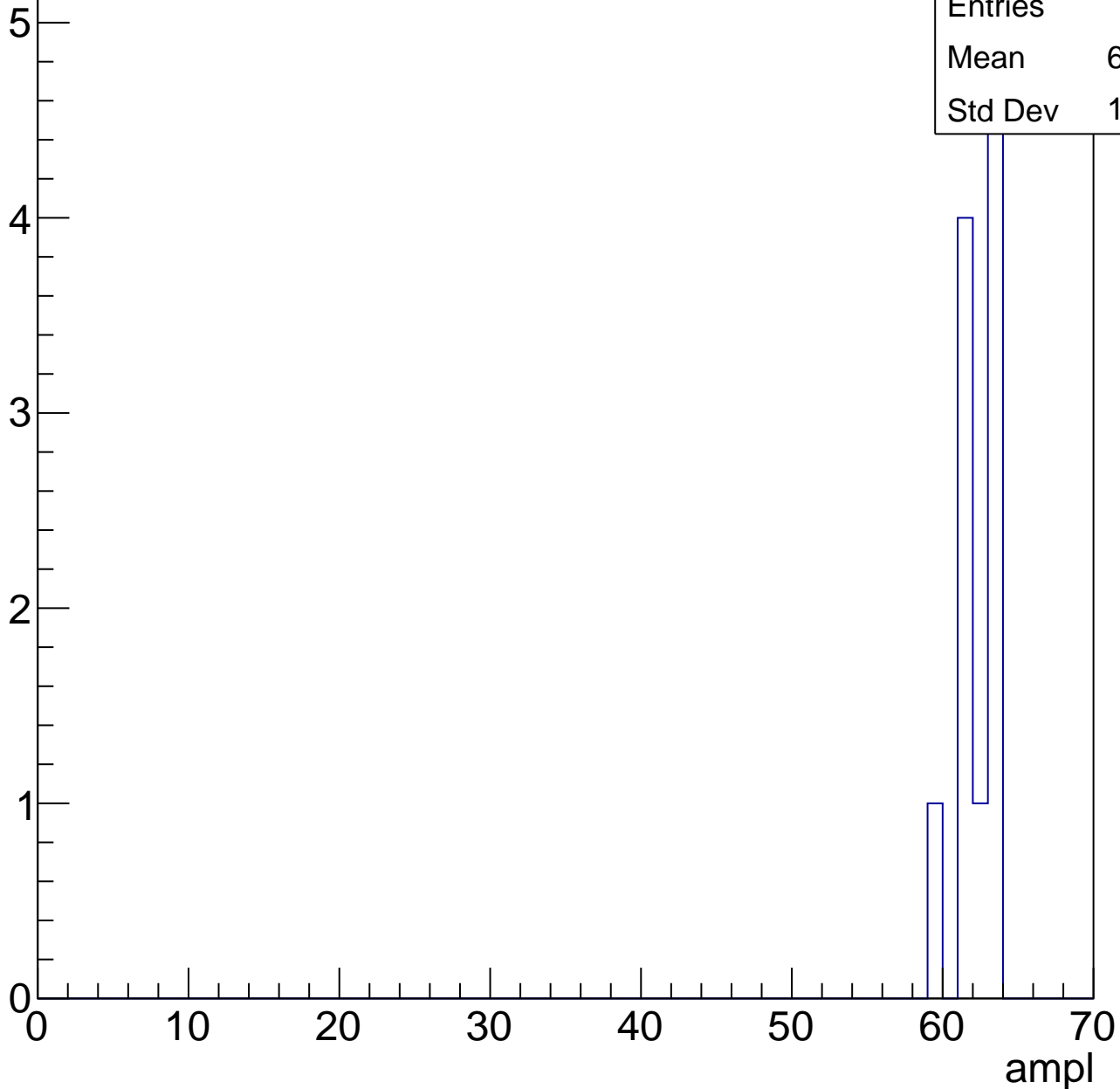
Entries	57
Mean	57.82
Std Dev	8.295

B0L102S, U12-ch54, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.82
Std Dev	1.266



B0L102S, U12-ch54, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch55, adc0

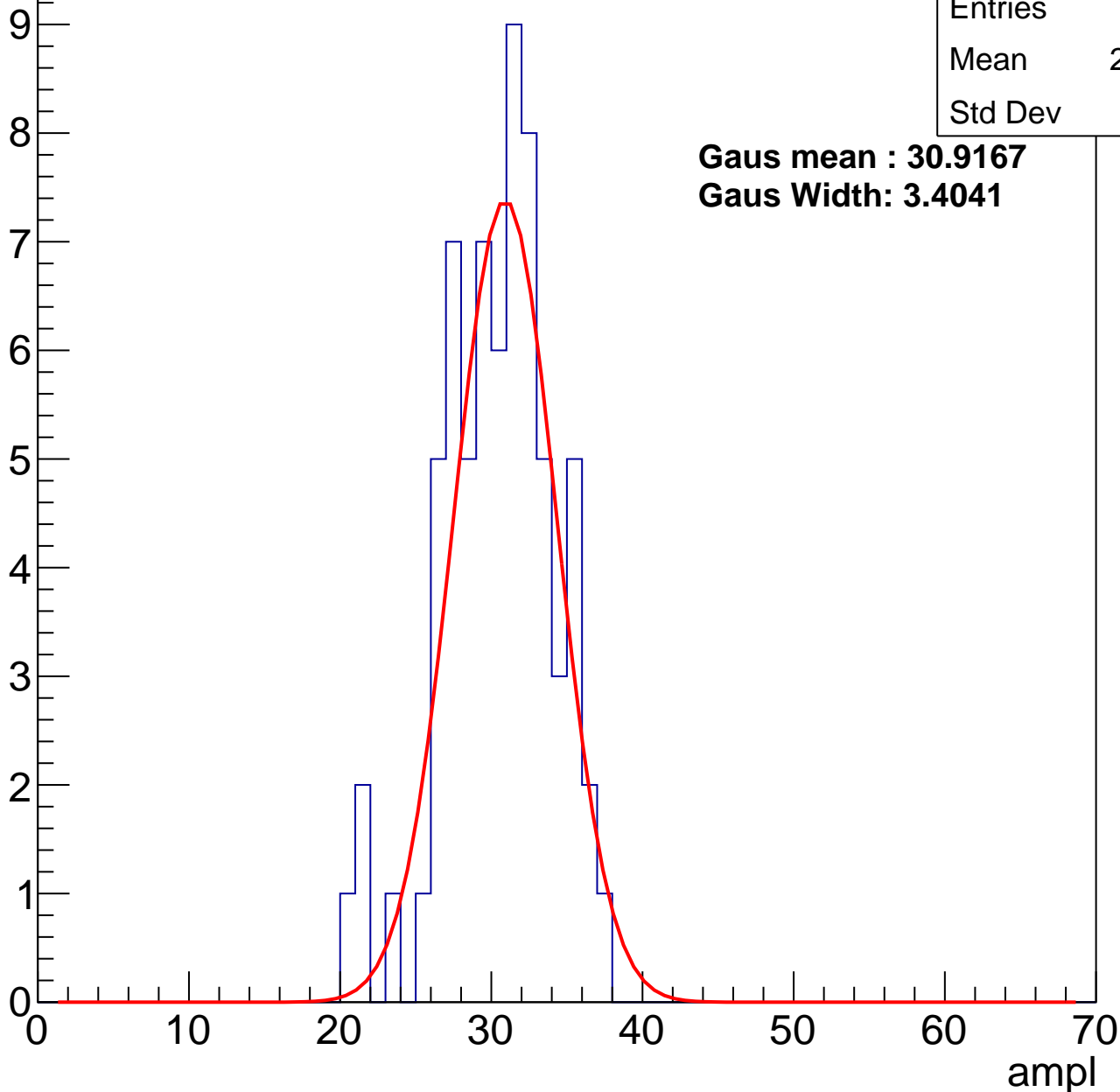
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.97
Std Dev	3.61

Gaus mean : 30.9167

Gaus Width: 3.4041

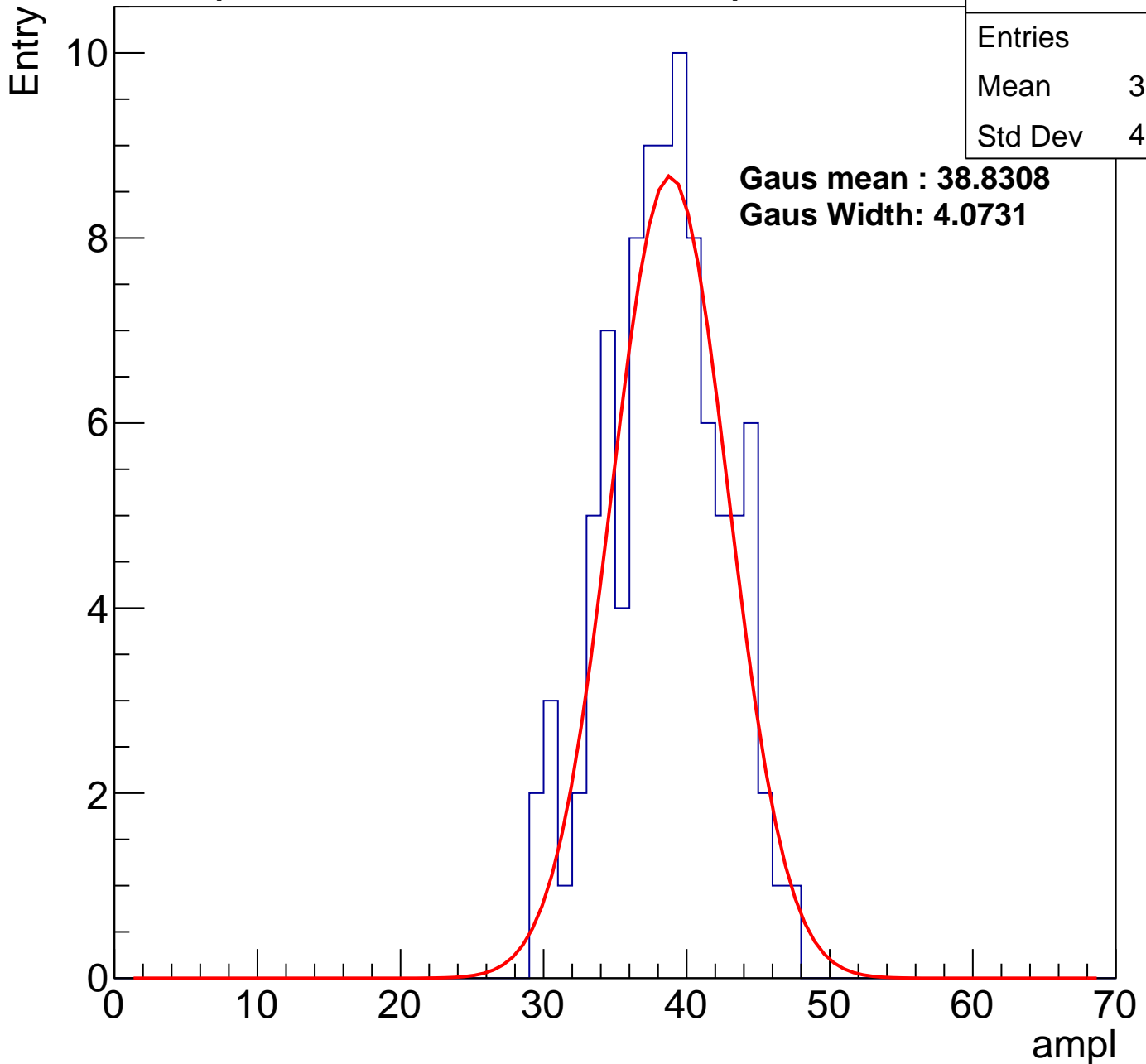


B0L102S, U12-ch55, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	94
Mean	38.05
Std Dev	4.059

Gaus mean : 38.8308
Gaus Width: 4.0731



B0L102S, U12-ch55, adc2

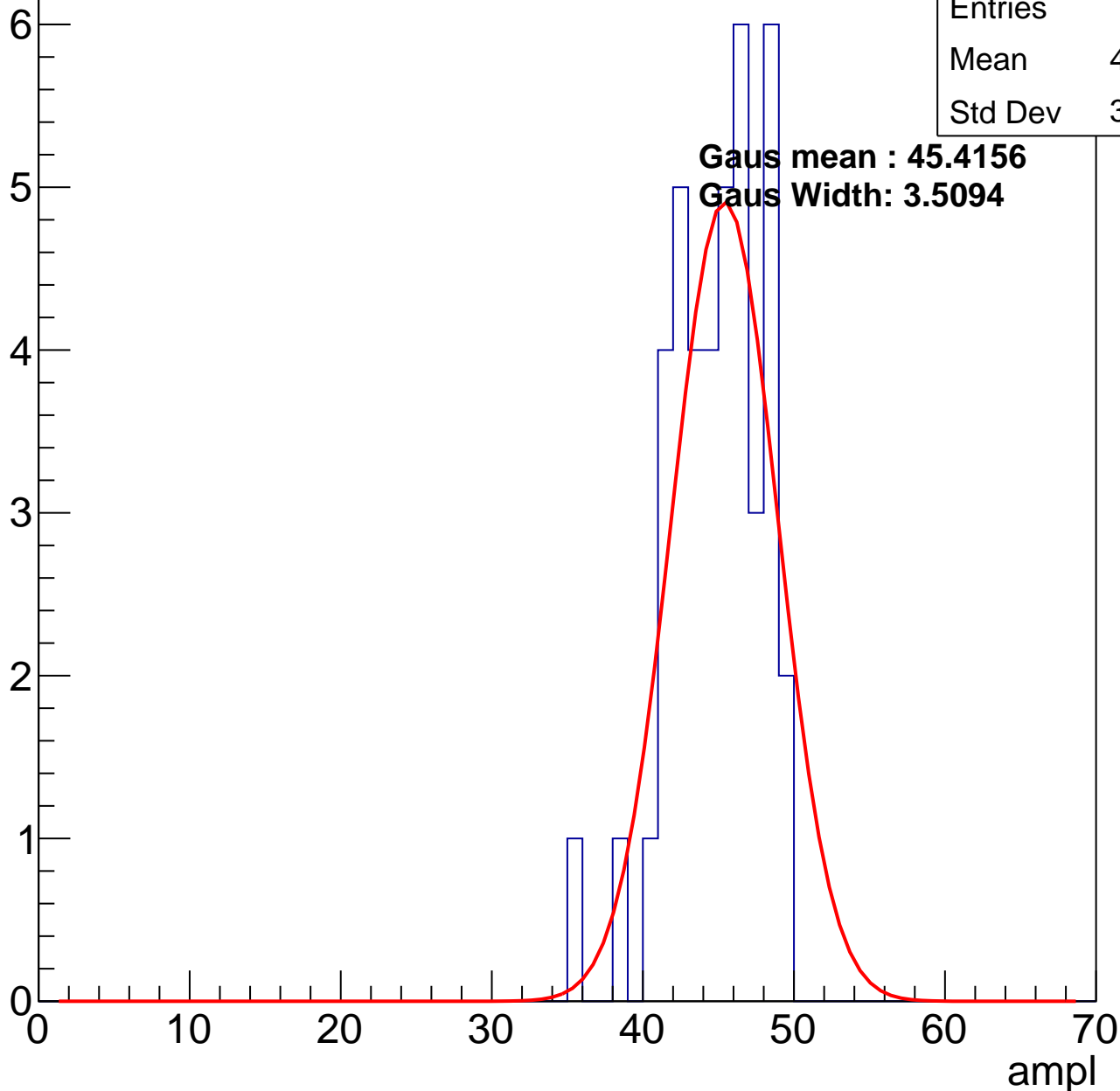
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	44.36
Std Dev	3.046

Gaus mean : 45.4156

Gaus Width: 3.5094

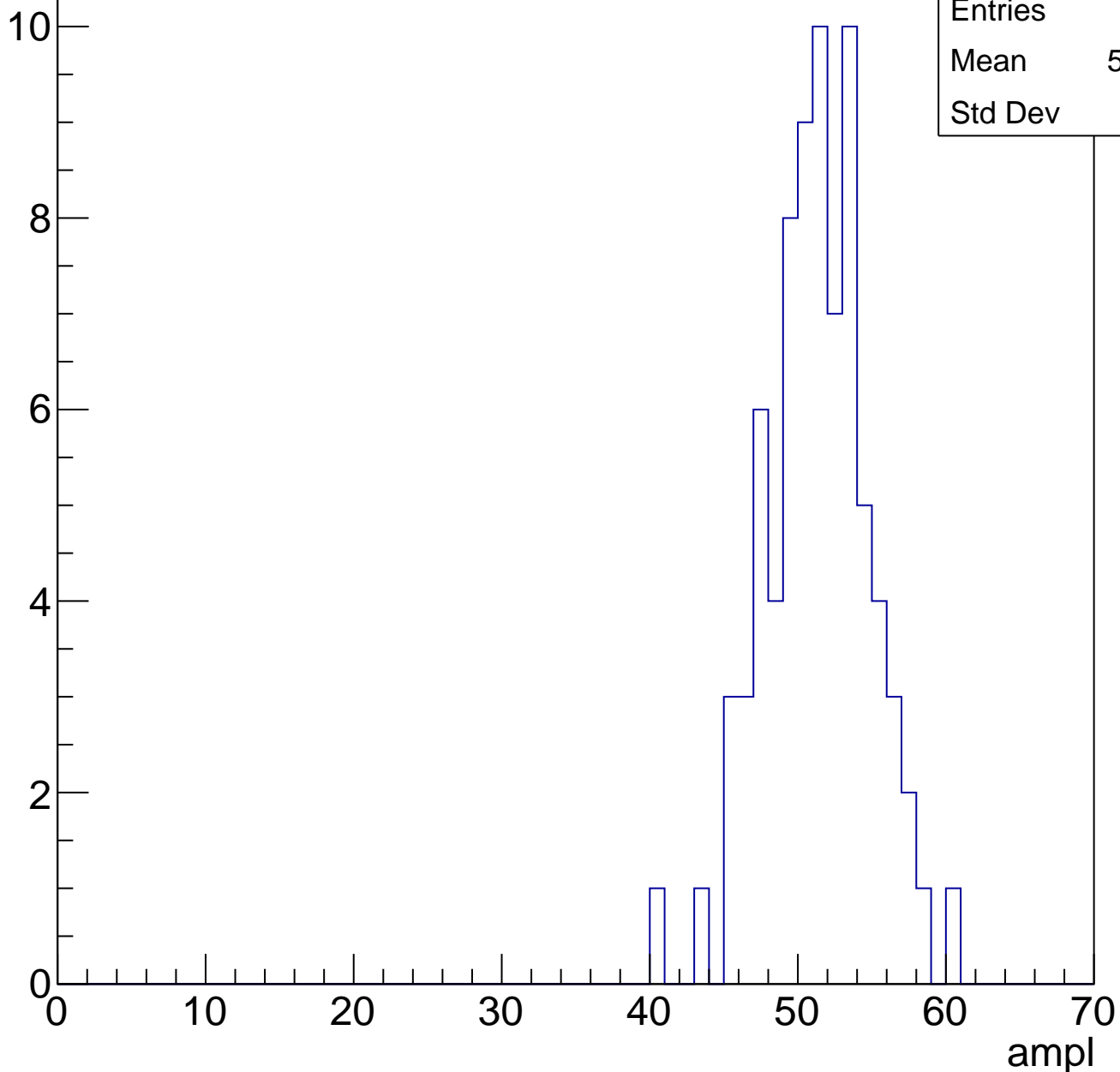


B0L102S, U12-ch55, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

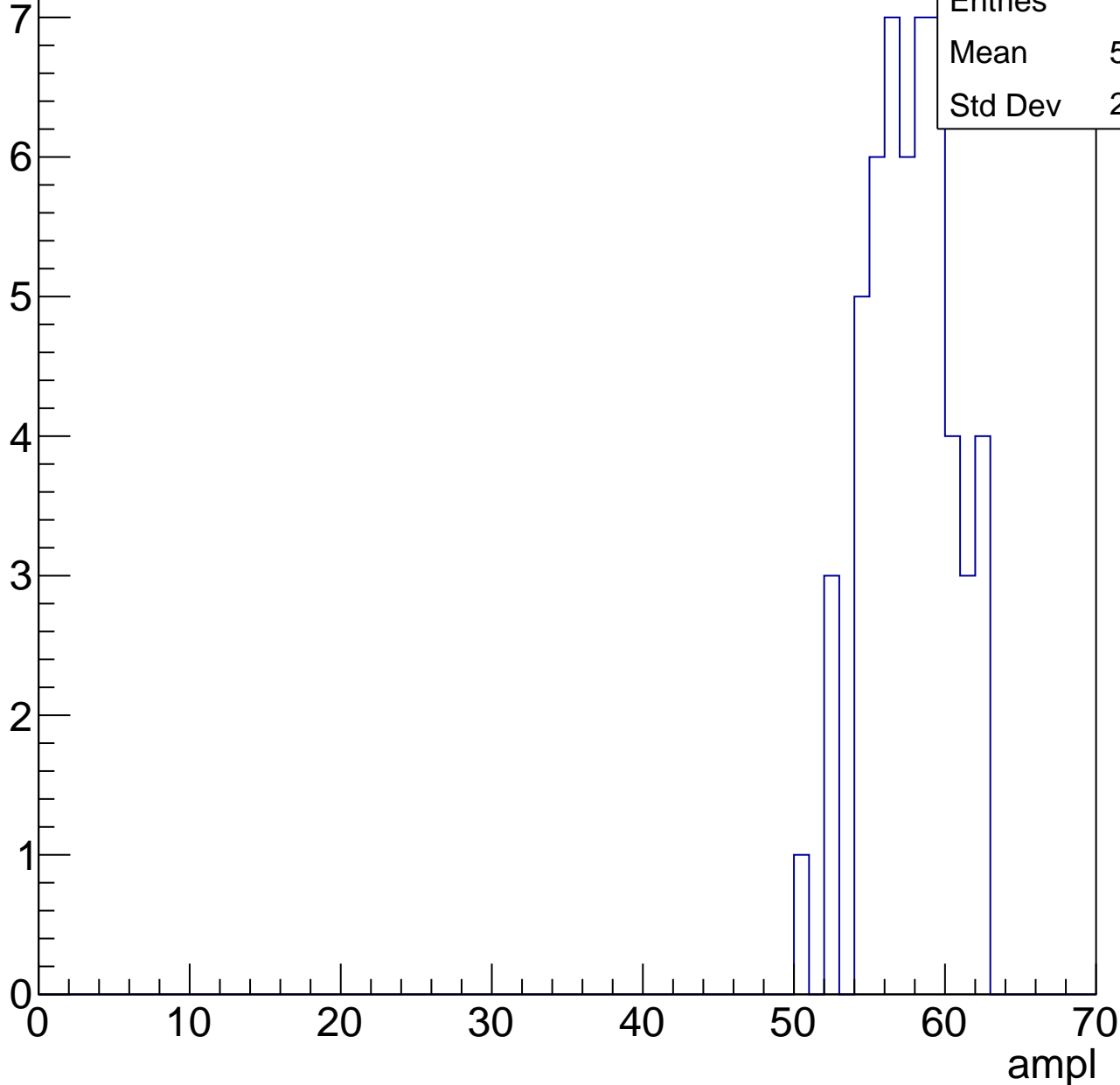
Entries	78
Mean	50.85
Std Dev	3.53



B0L102S, U12-ch55, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

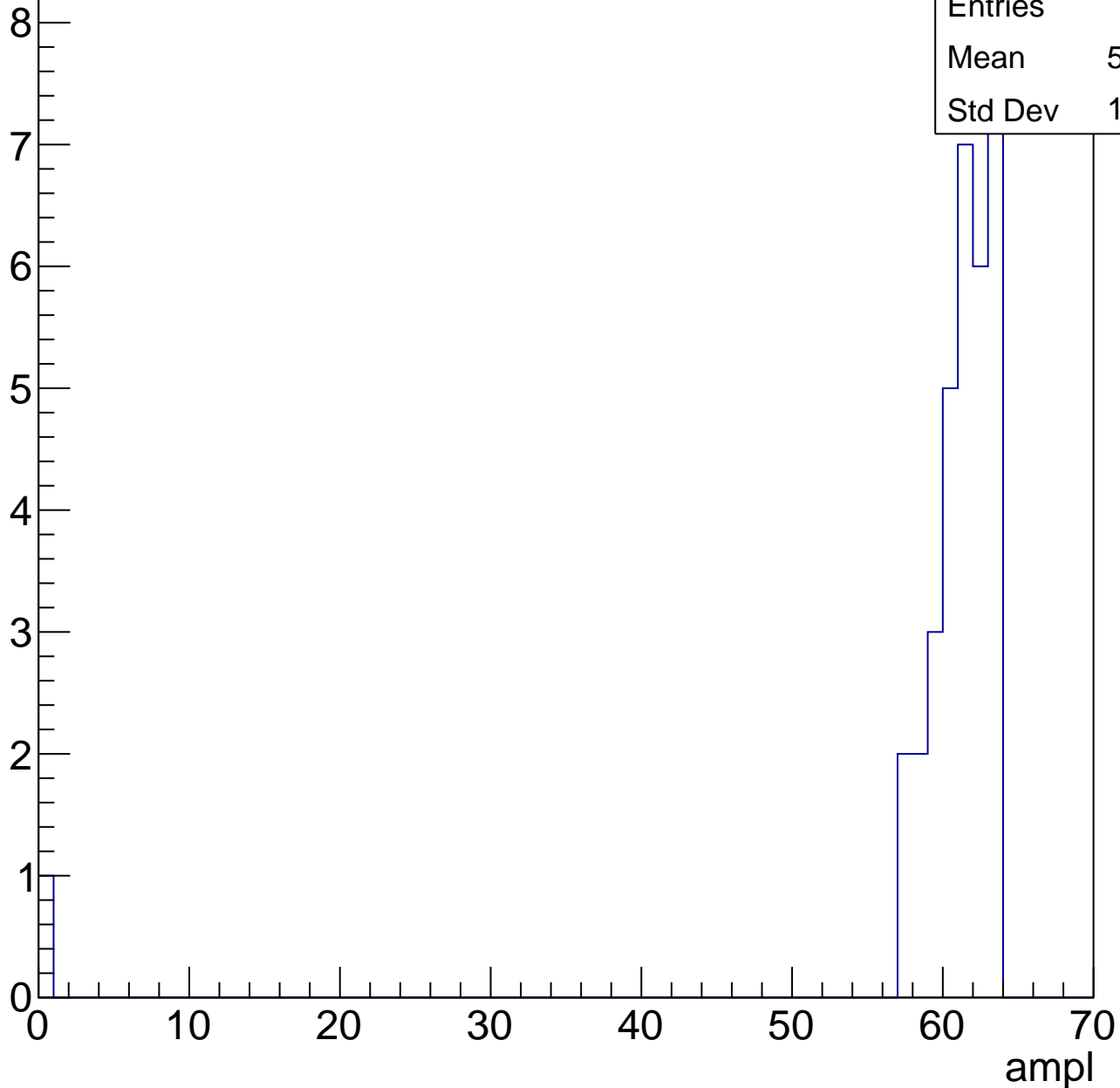


B0L102S, U12-ch55, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	34
Mean	59.12
Std Dev	10.44



B0L102S, U12-ch55, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch55, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U12-ch56, adc0

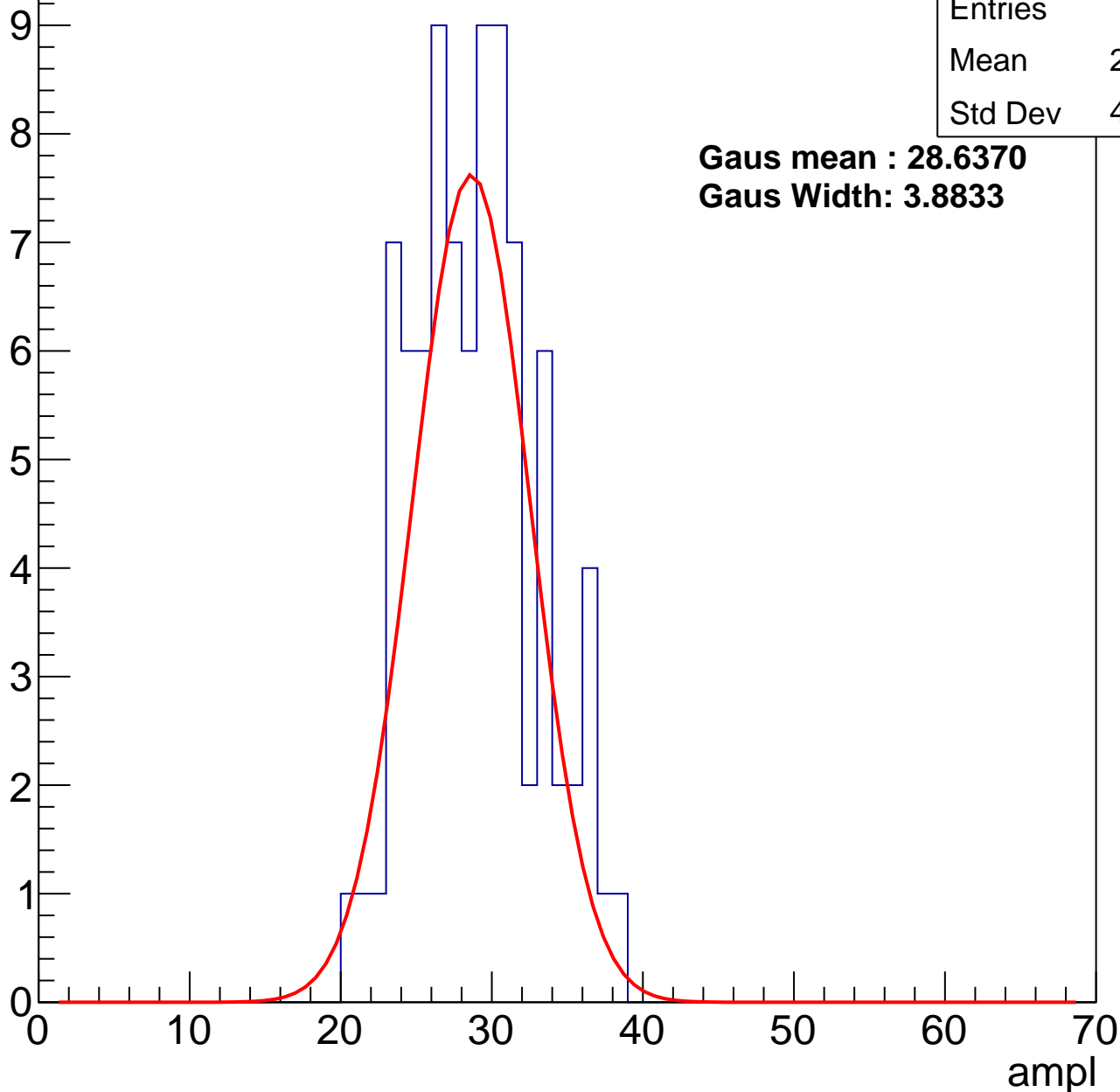
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	28.46
Std Dev	4.005

Gaus mean : 28.6370

Gaus Width: 3.8833



B0L102S, U12-ch56, adc1

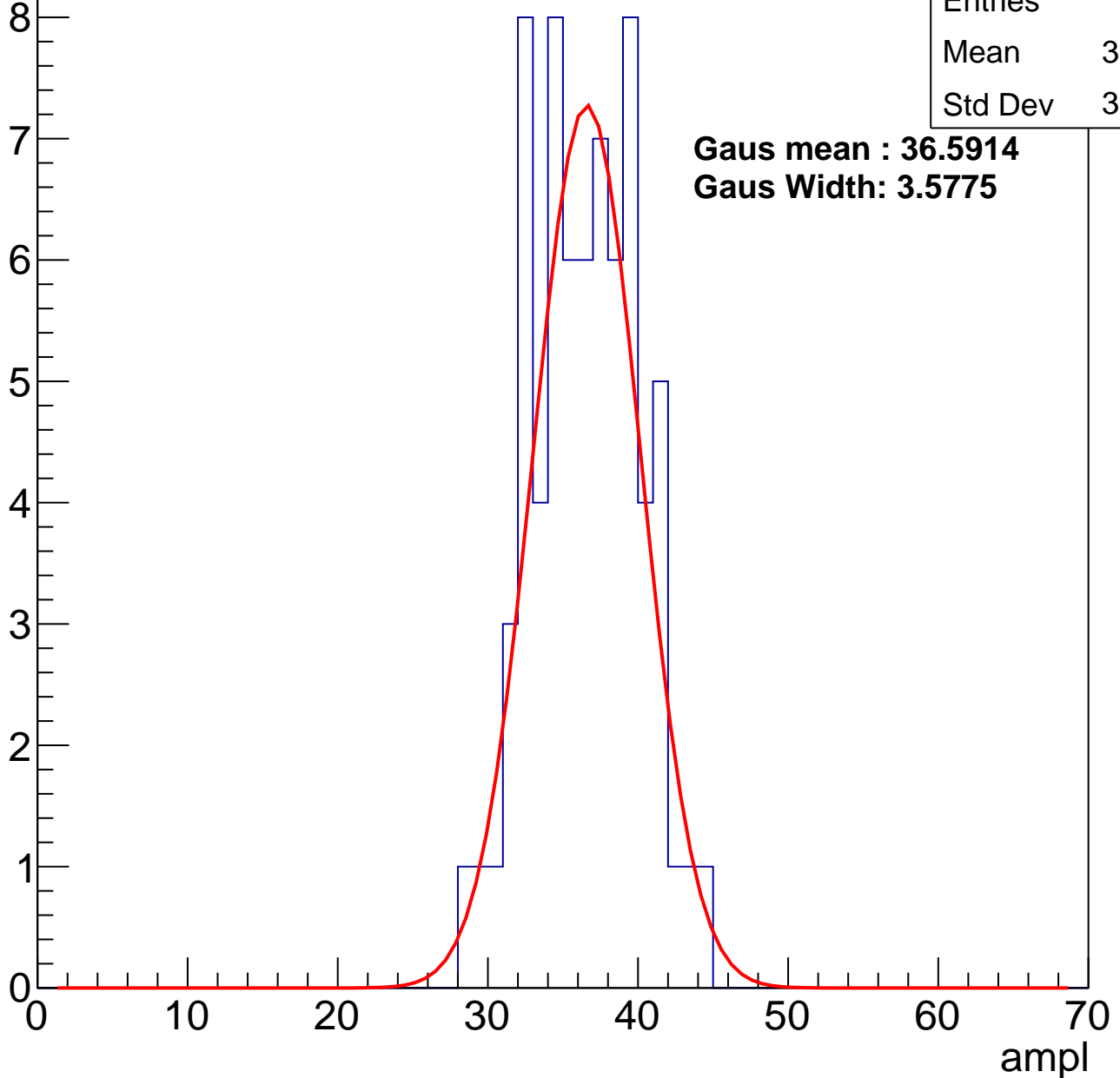
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.04
Std Dev	3.494

Gaus mean : 36.5914

Gaus Width: 3.5775



B0L102S, U12-ch56, adc2

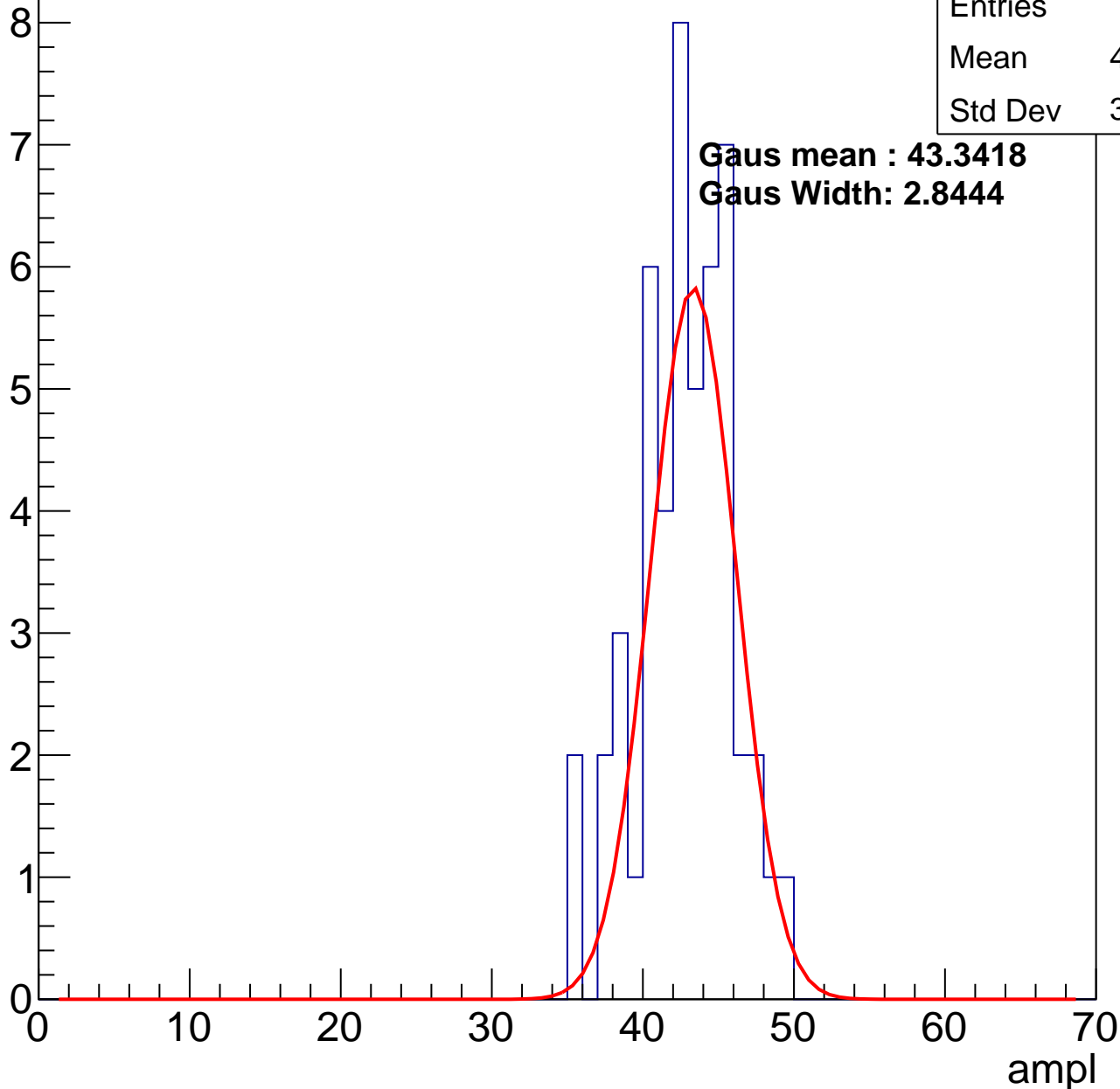
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	42.28
Std Dev	3.124

Gaus mean : 43.3418

Gaus Width: 2.8444

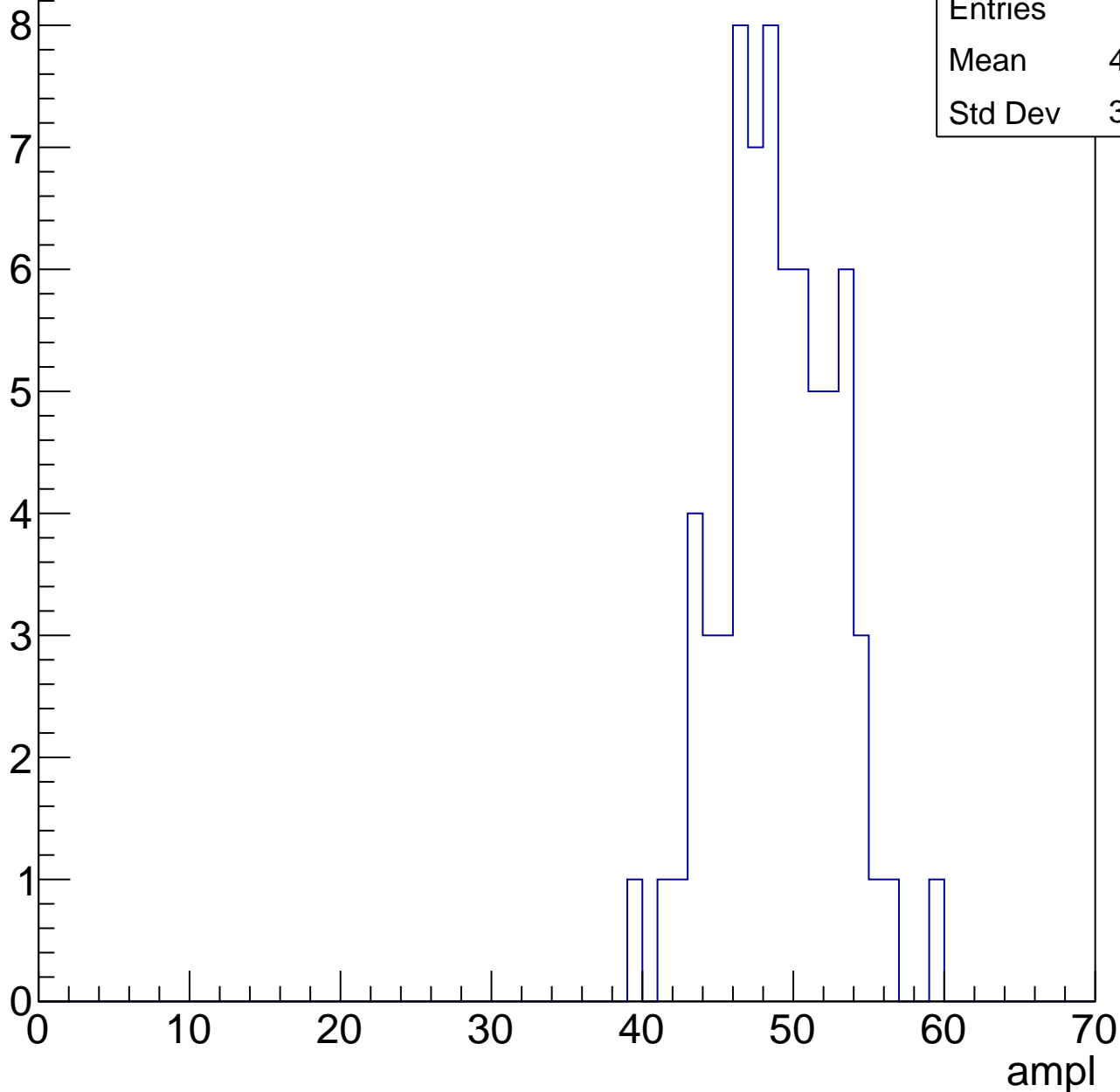


B0L102S, U12-ch56, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

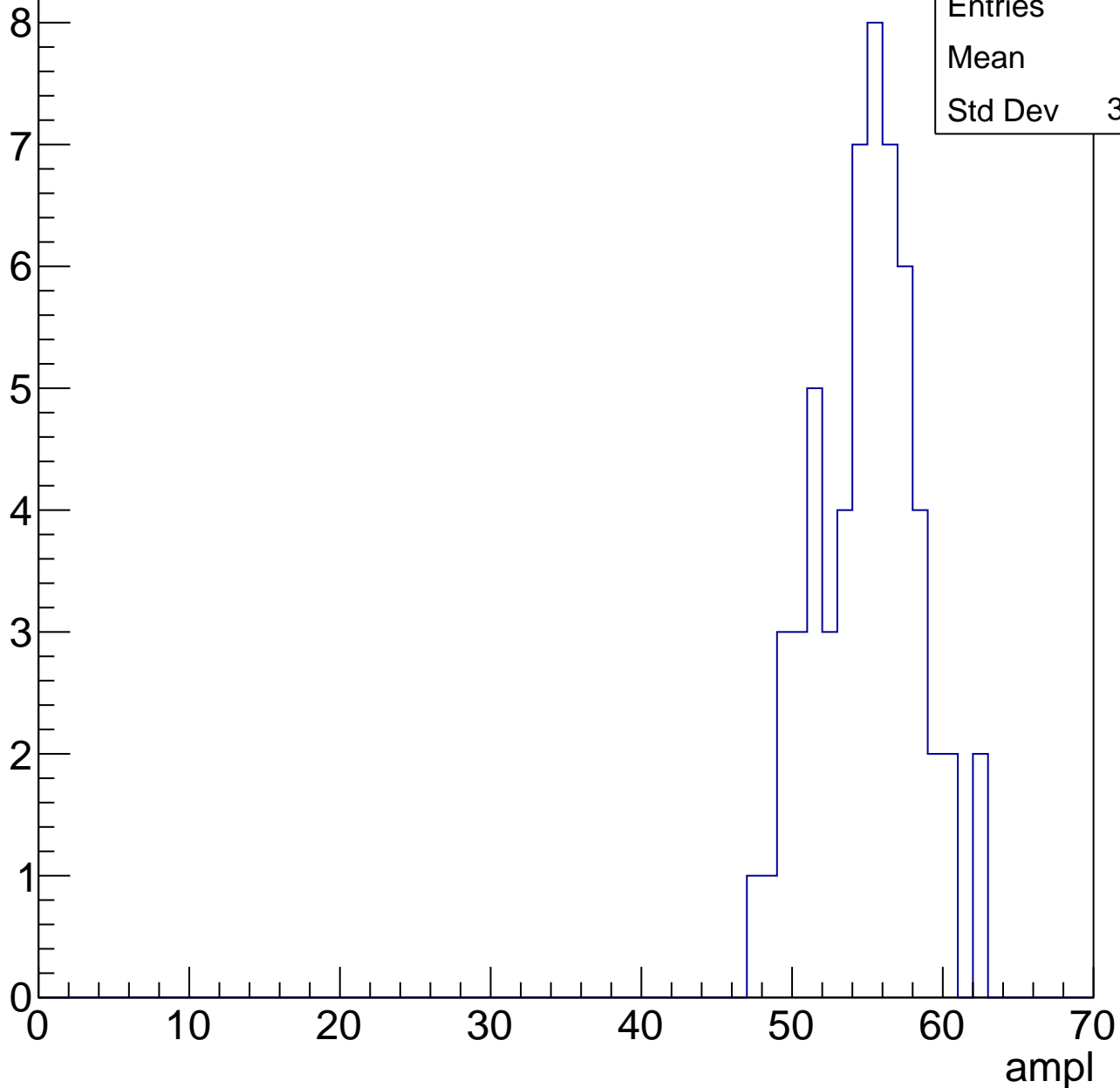
Entries	70
Mean	48.59
Std Dev	3.785



B0L102S, U12-ch56, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



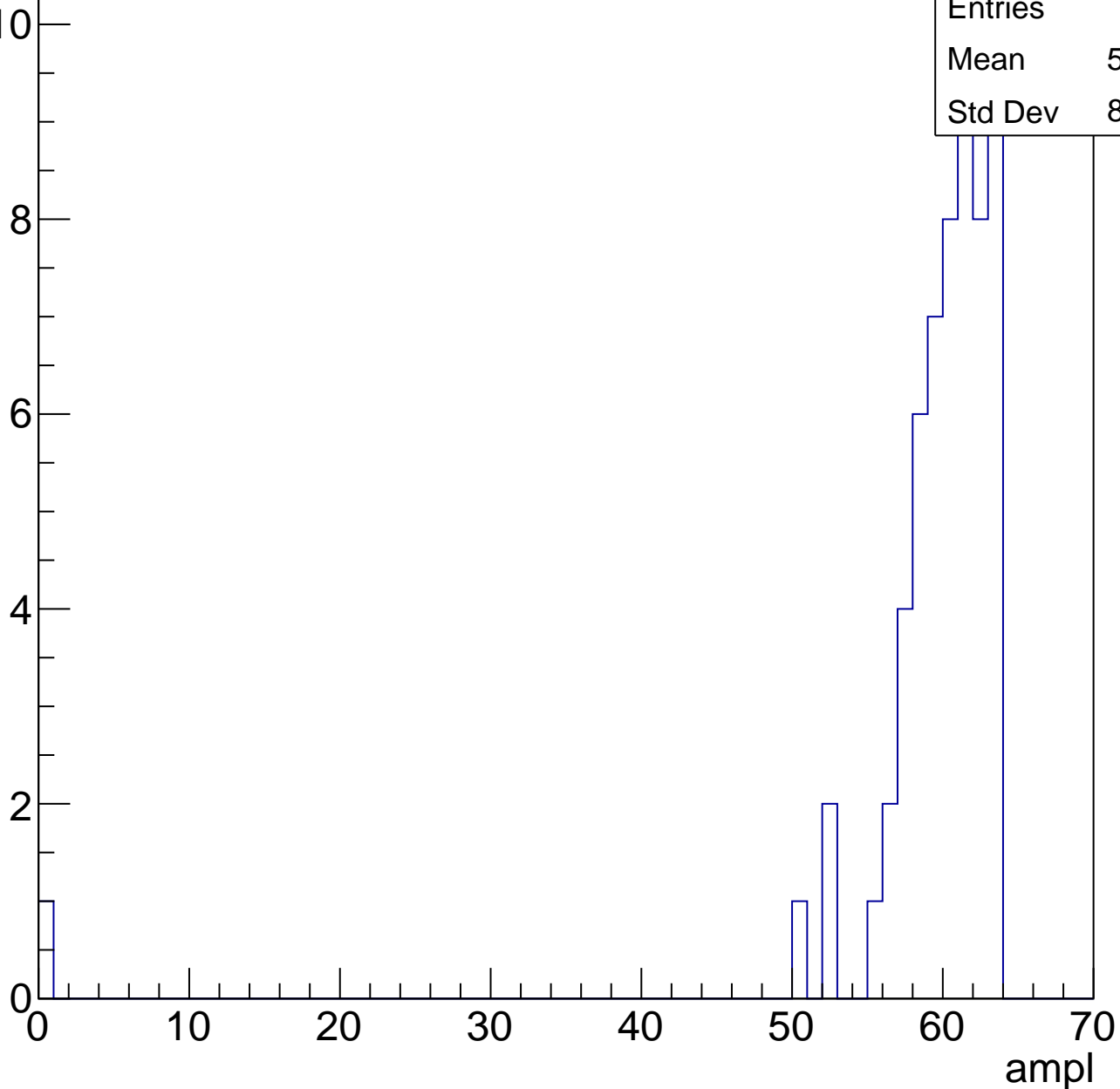
Entries	58
Mean	54.5
Std Dev	3.369

B0L102S, U12-ch56, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	58.69
Std Dev	8.208



B0L102S, U12-ch56, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch56, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch57, adc0

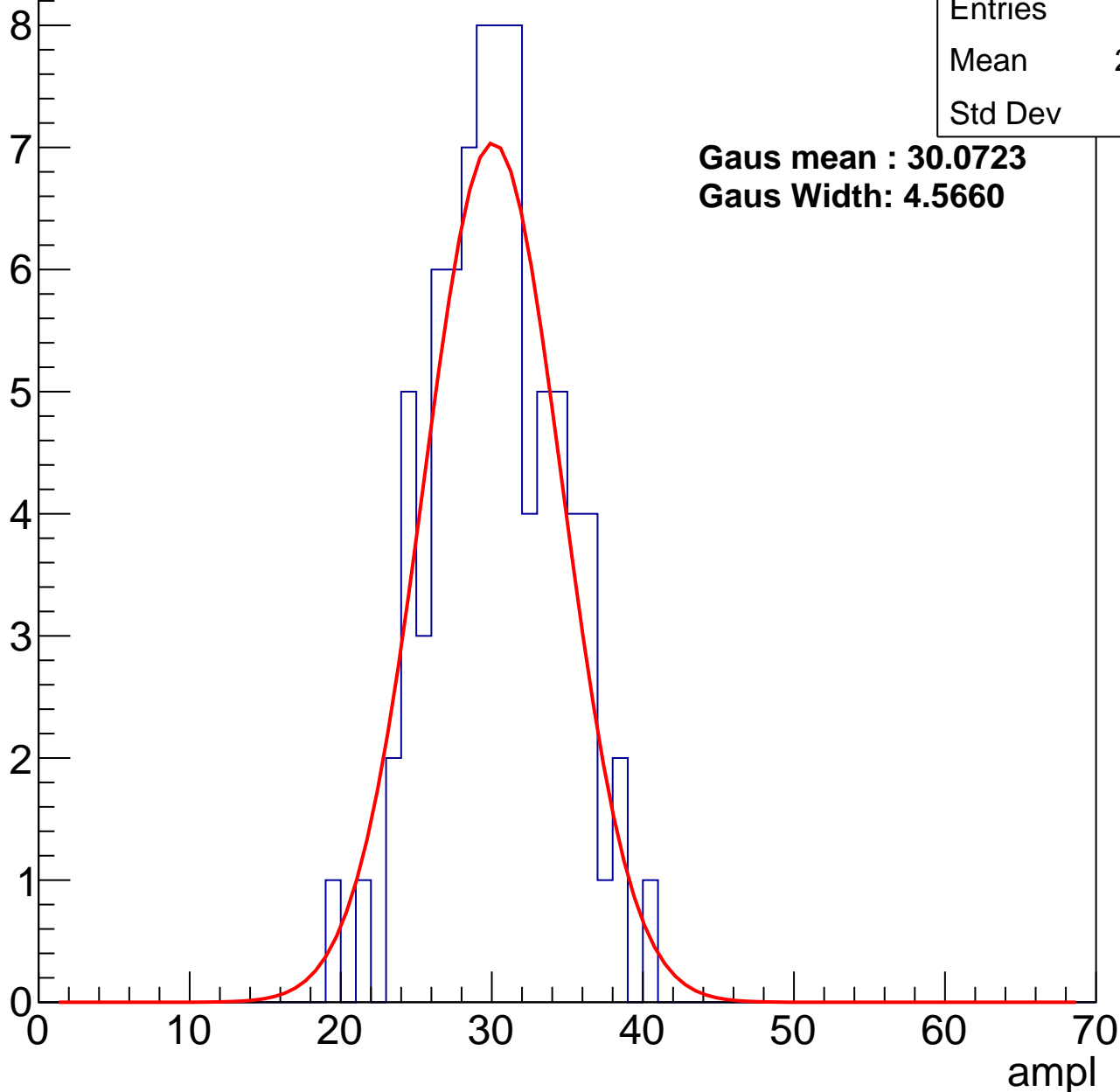
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.81
Std Dev	4.17

Gaus mean : 30.0723

Gaus Width: 4.5660



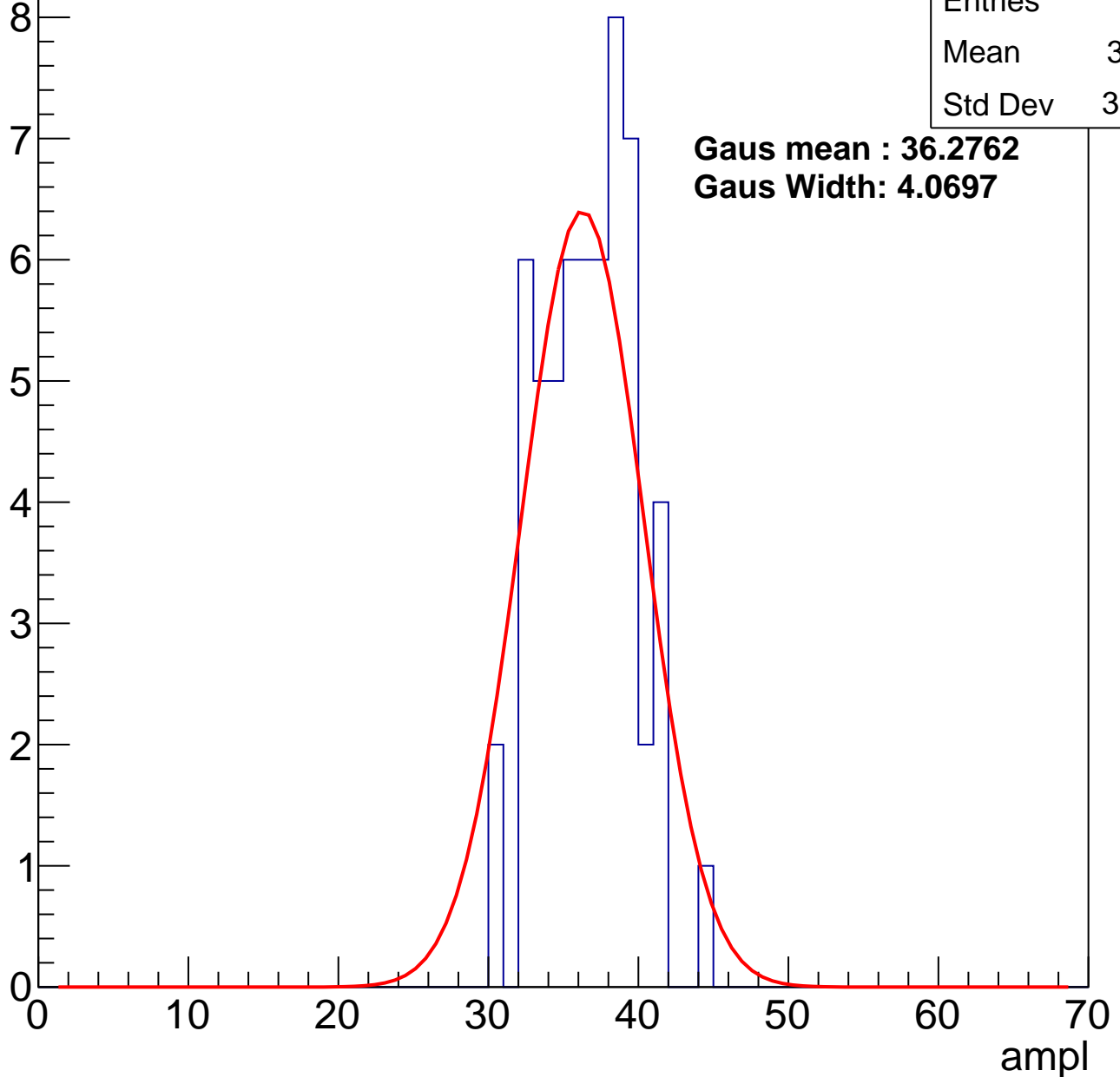
B0L102S, U12-ch57, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	36.21
Std Dev	3.033

Gaus mean : 36.2762
Gaus Width: 4.0697



B0L102S, U12-ch57, adc2

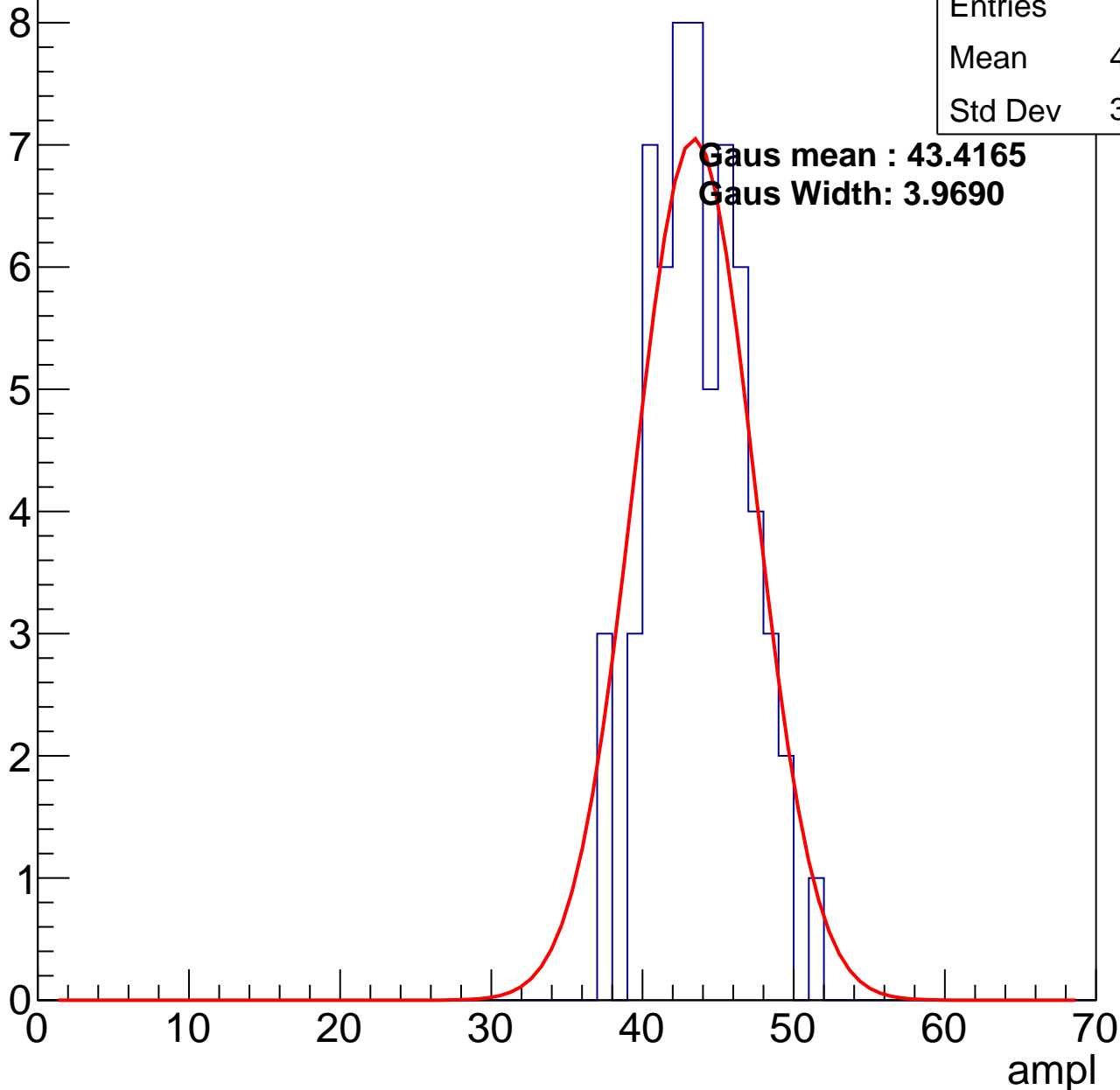
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.27
Std Dev	3.108

Gaus mean : 43.4165

Gaus Width: 3.9690

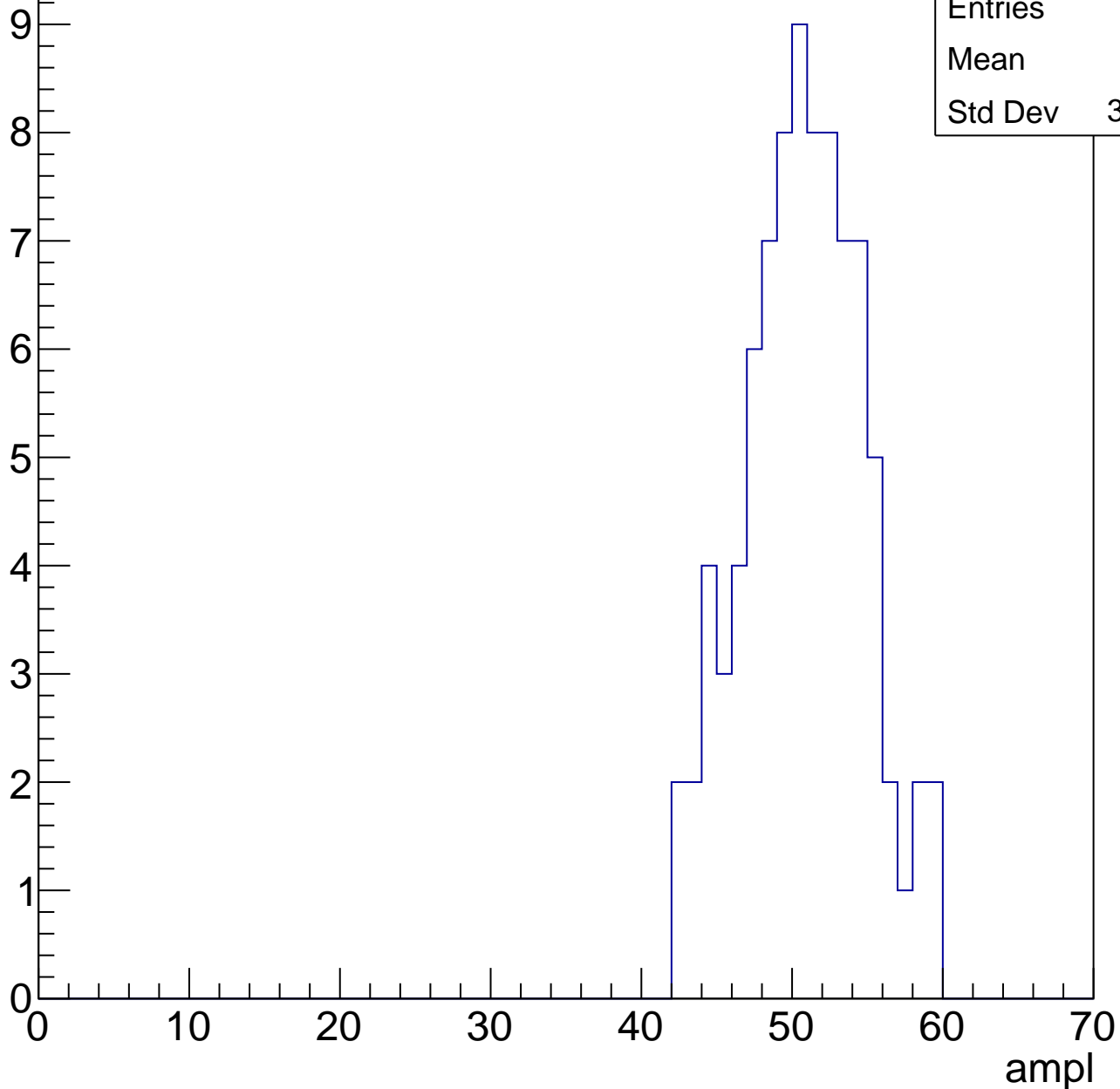


B0L102S, U12-ch57, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

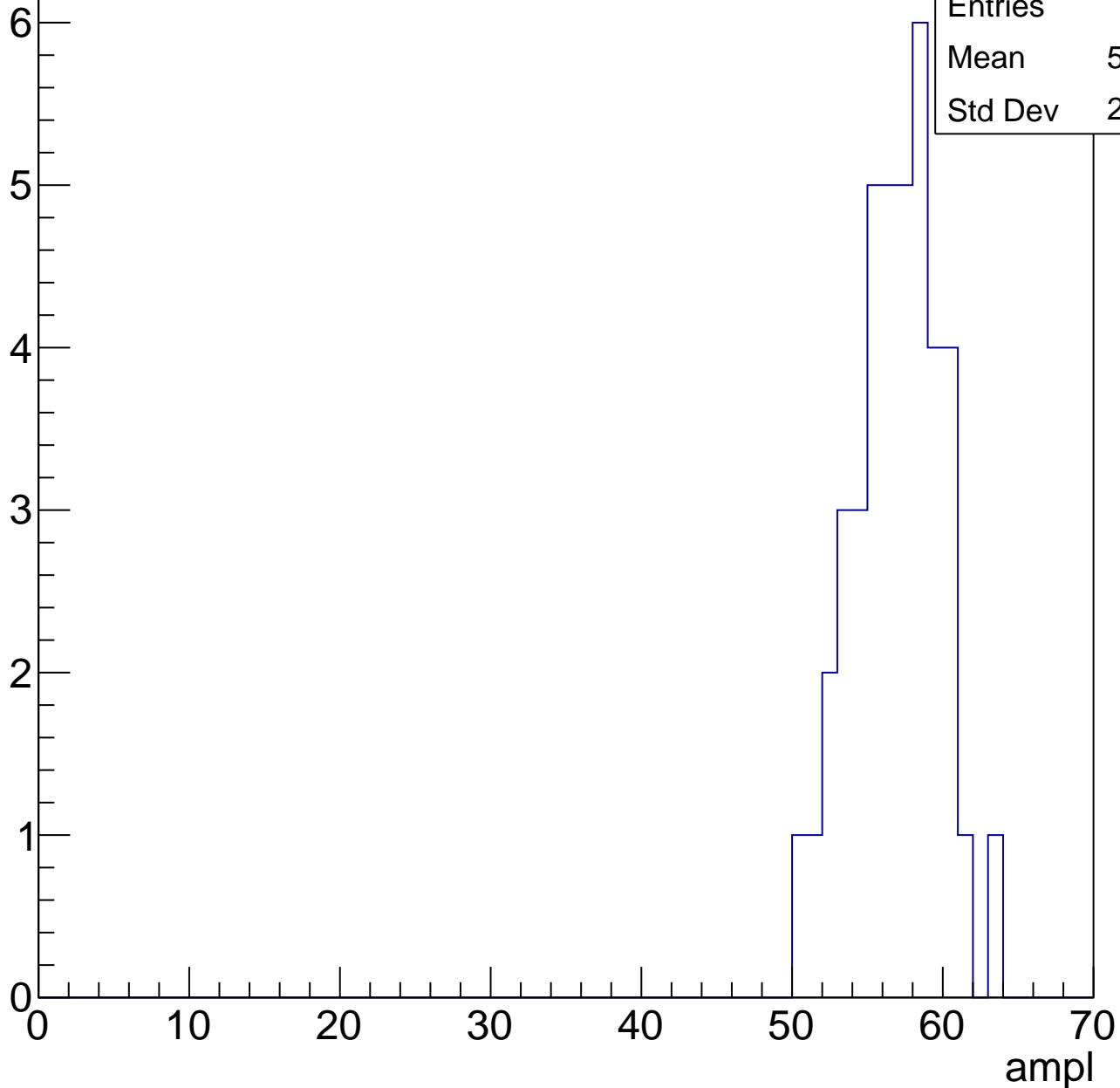
Entries	87
Mean	50.3
Std Dev	3.928



B0L102S, U12-ch57, adc4

calib_packv5_042523_0143.root, FC#12, port B1

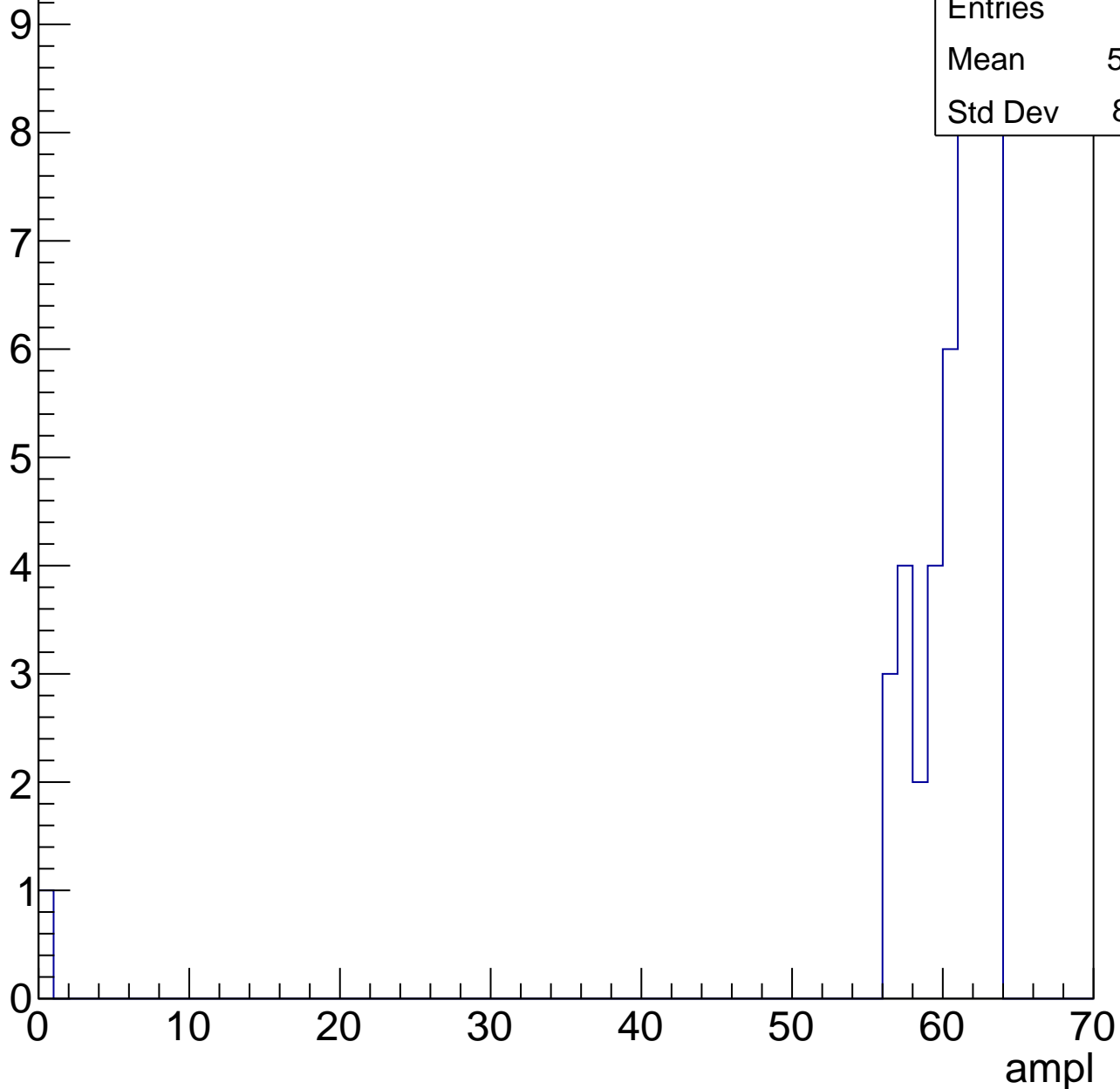
Entry



B0L102S, U12-ch57, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch57, adc6

calib_packv5_042523_0143.root, FC#12, port B1

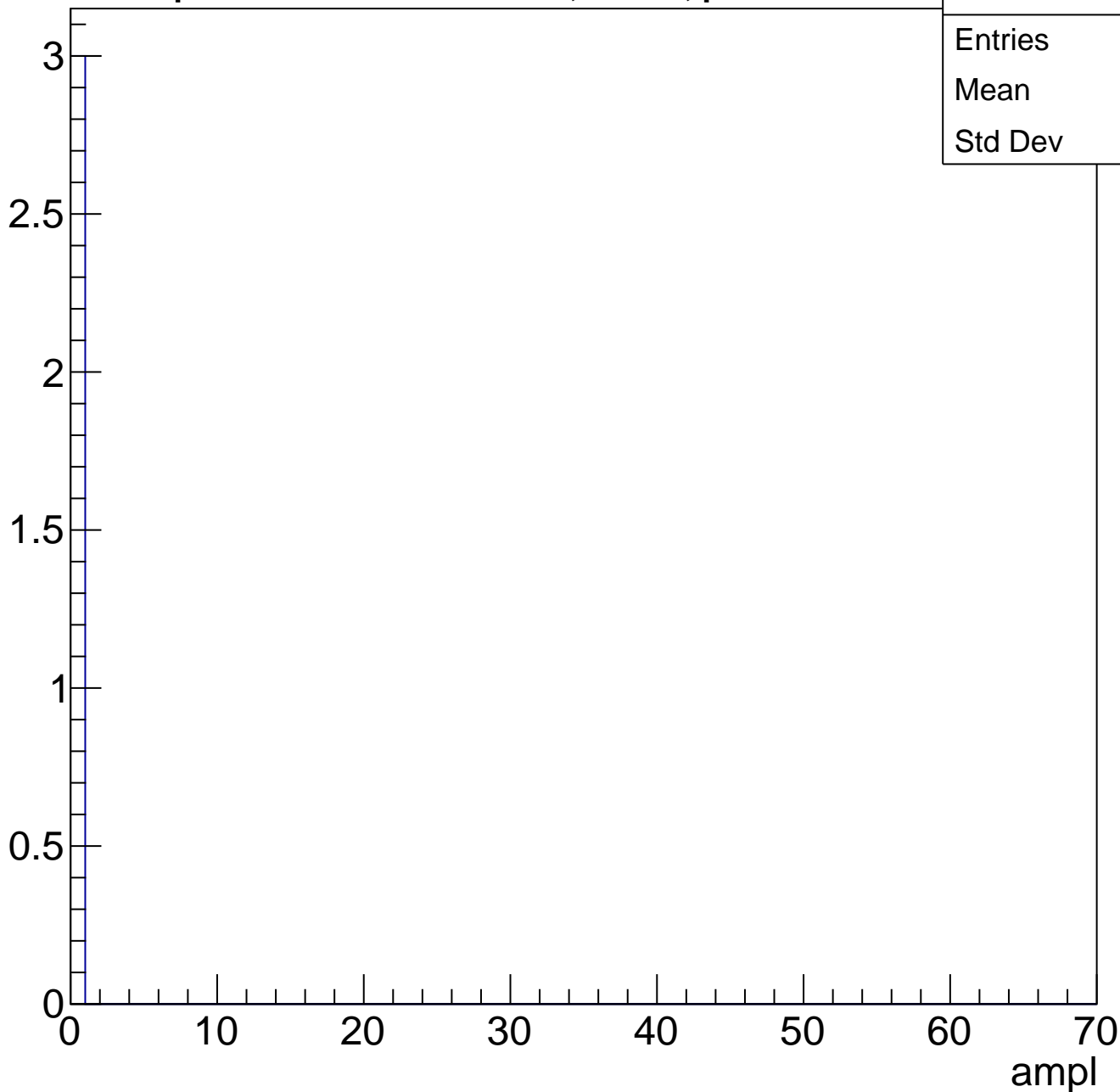
Entry



B0L102S, U12-ch57, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U12-ch58, adc0

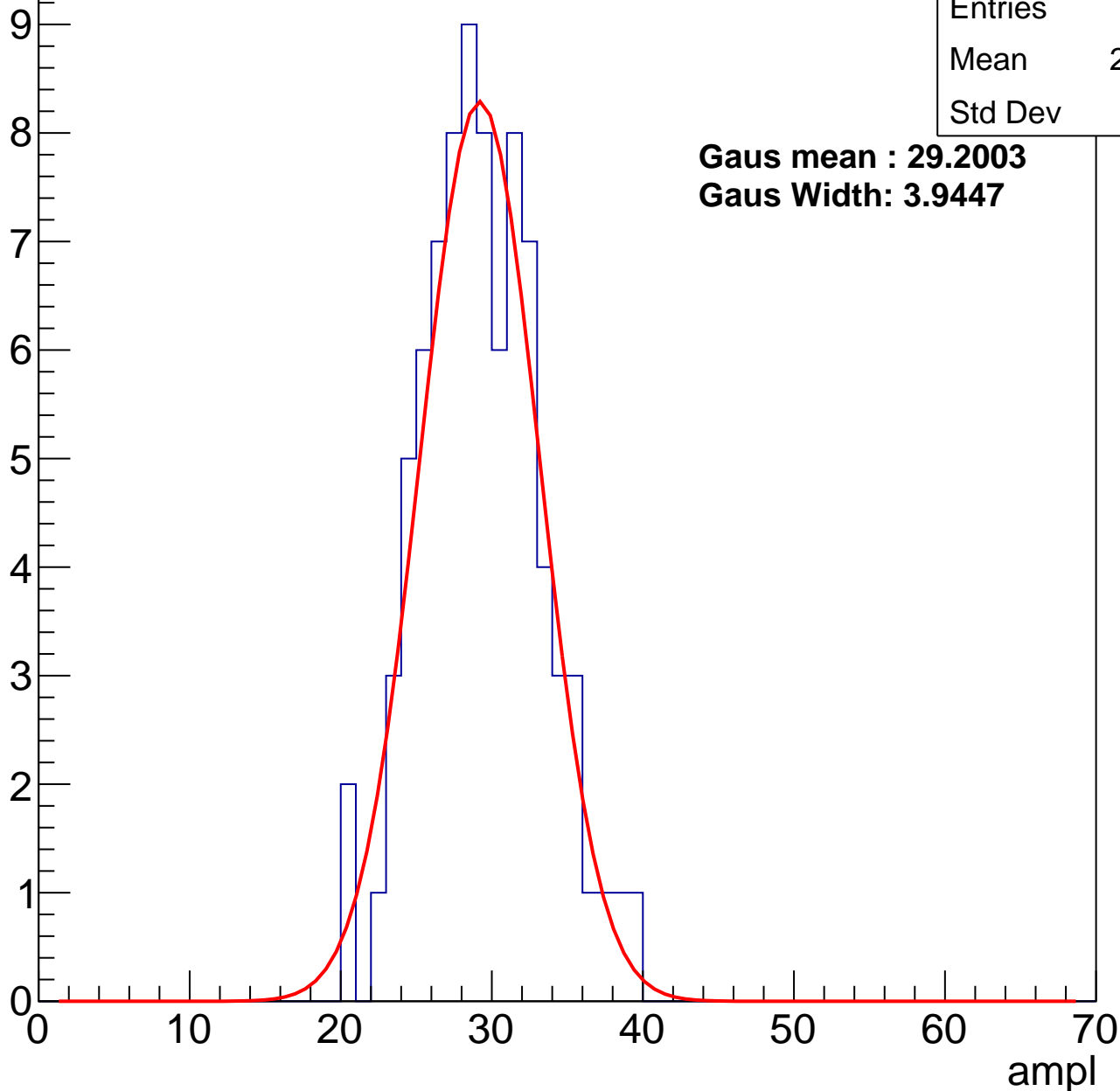
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	28.86
Std Dev	3.91

Gaus mean : 29.2003

Gaus Width: 3.9447



B0L102S, U12-ch58, adc1

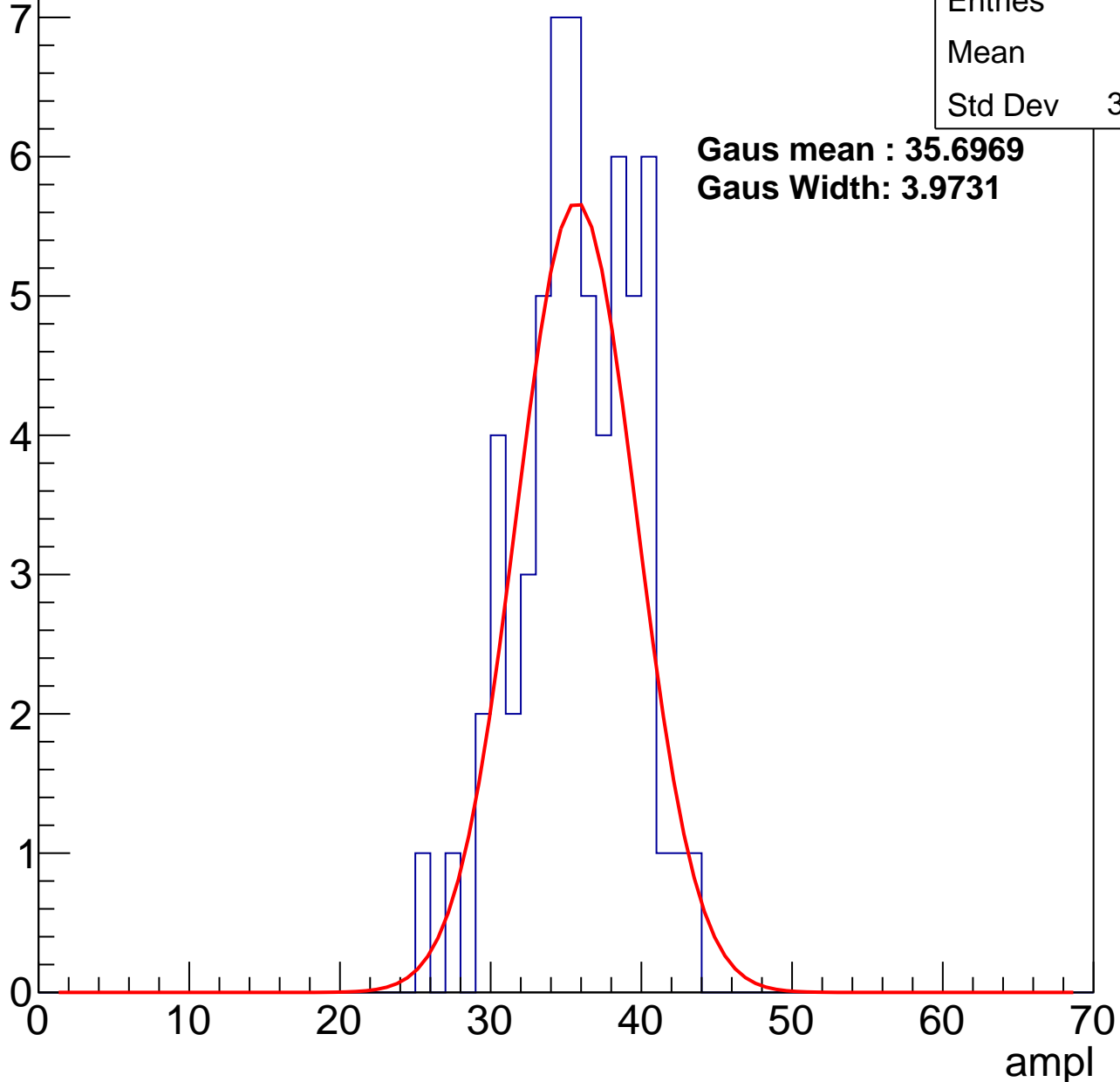
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	35.3
Std Dev	3.778

Gaus mean : 35.6969

Gaus Width: 3.9731



B0L102S, U12-ch58, adc2

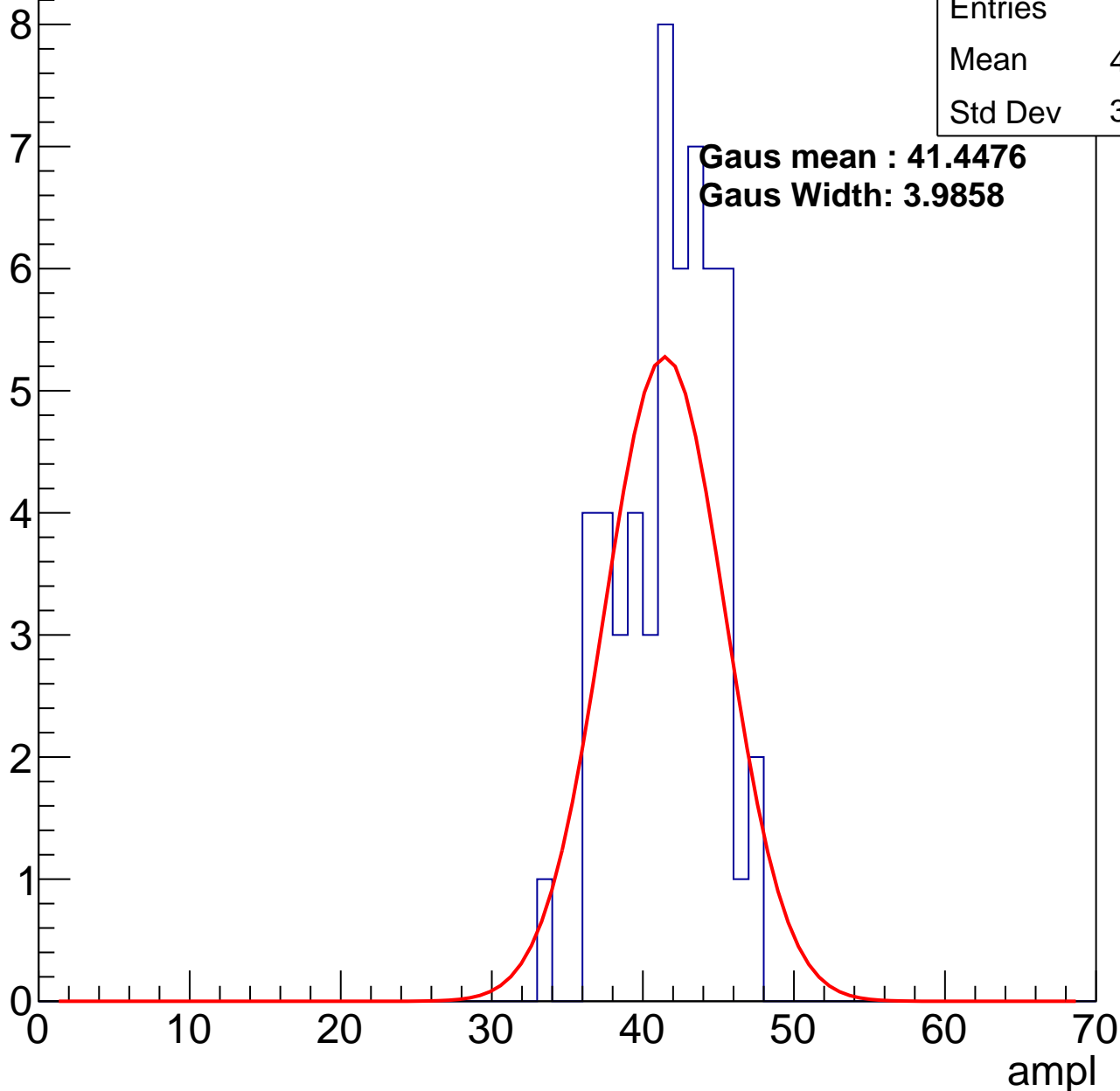
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.27
Std Dev	3.165

Gaus mean : 41.4476

Gaus Width: 3.9858

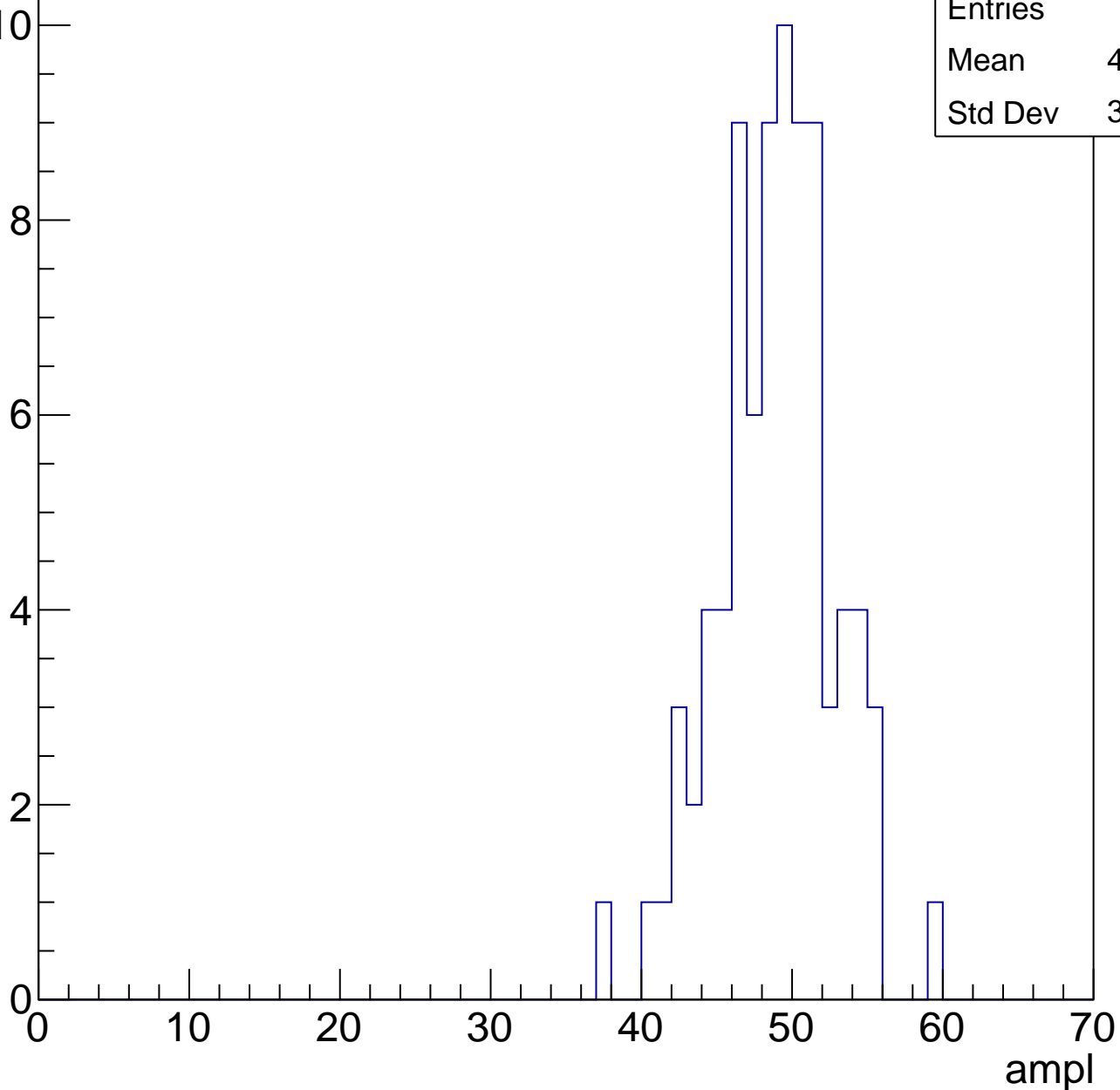


B0L102S, U12-ch58, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	48.45
Std Dev	3.809

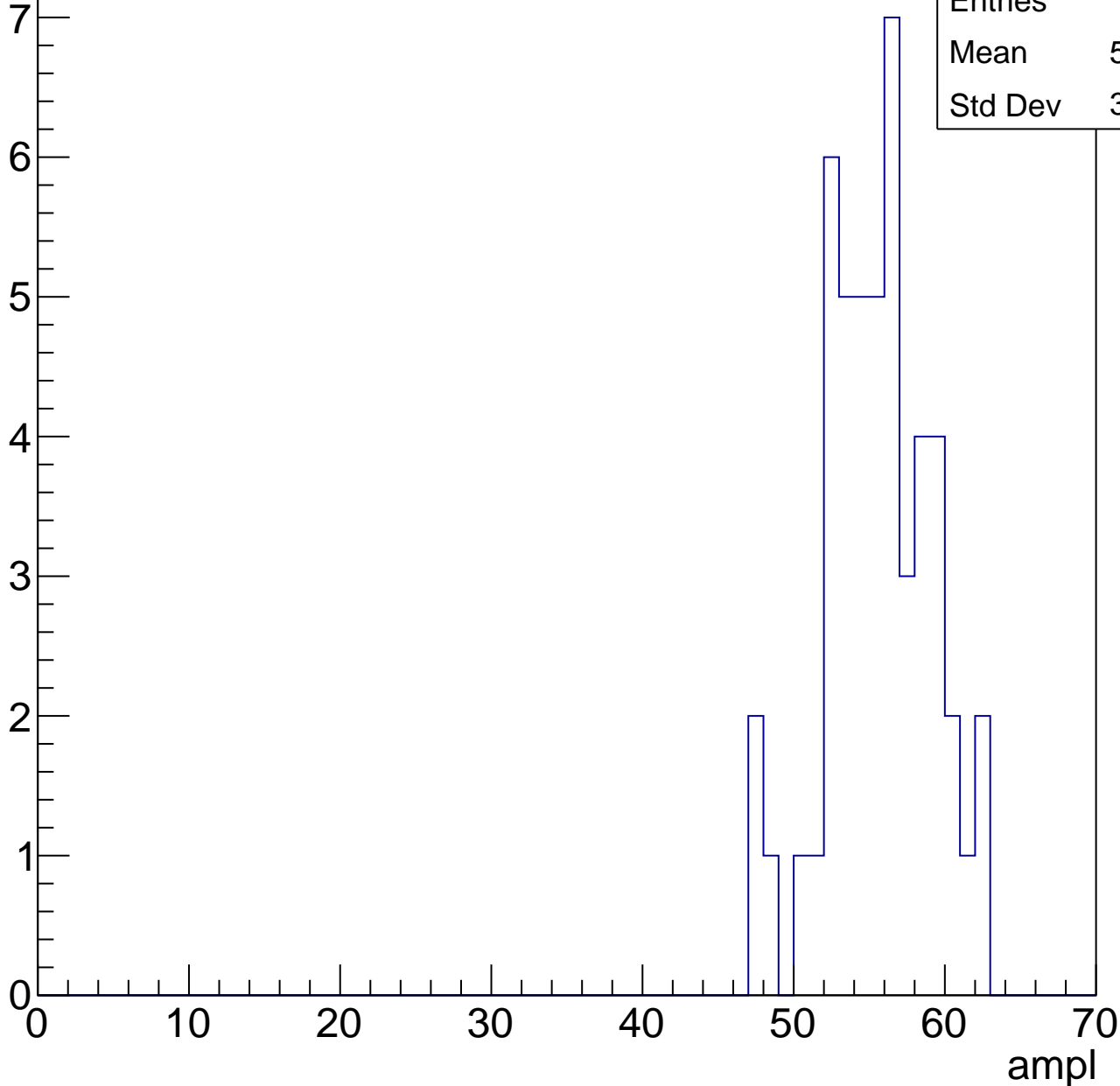


B0L102S, U12-ch58, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	55.12
Std Dev	3.497

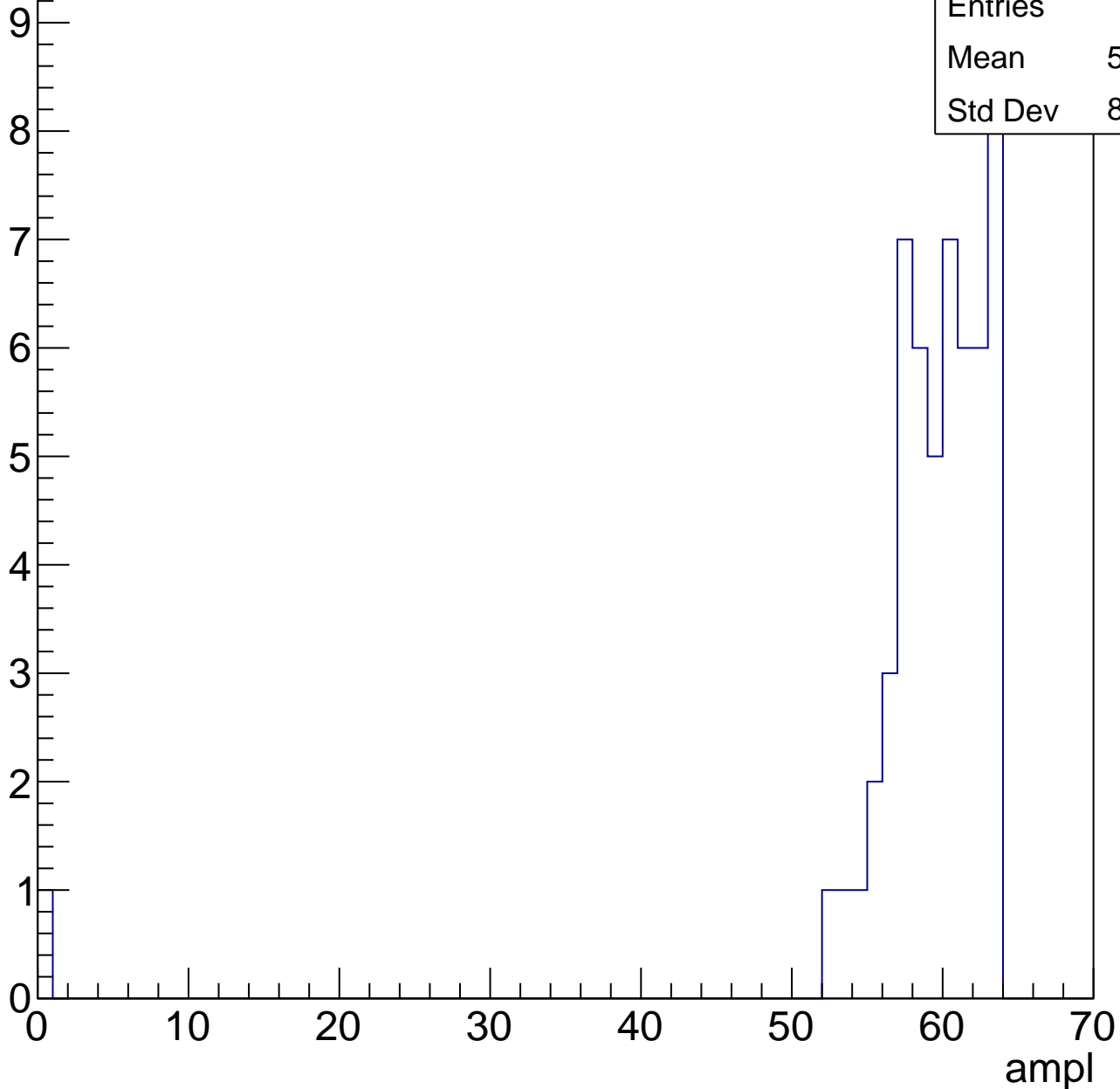


B0L102S, U12-ch58, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

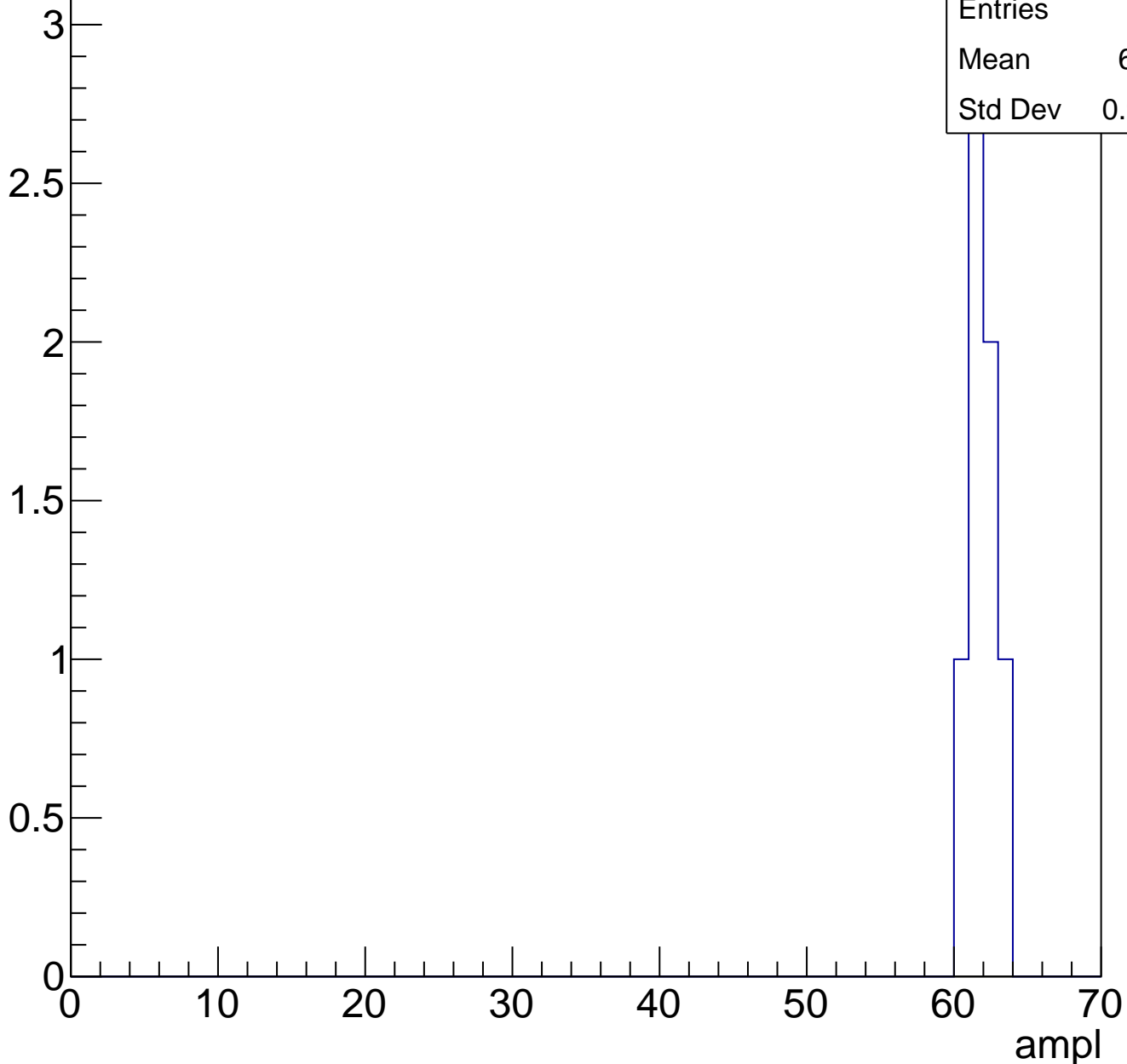
Entries	55
Mean	58.25
Std Dev	8.402



B0L102S, U12-ch58, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch58, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	9
Std Dev	9

B0L102S, U12-ch59, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	63
Mean	30.37
Std Dev	5.094

Gaus mean : 30.9949

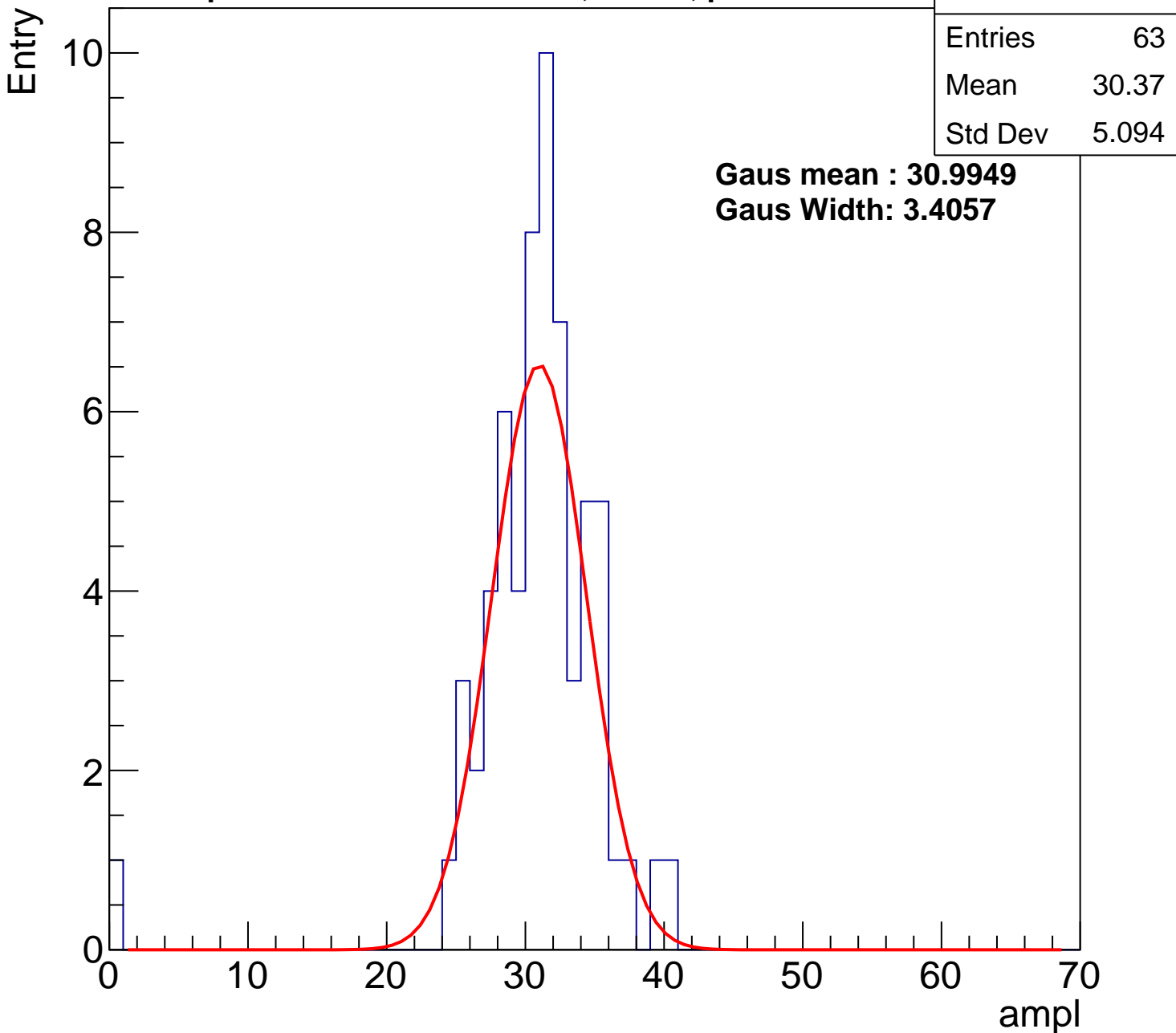
Gaus Width: 3.4057

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch59, adc1

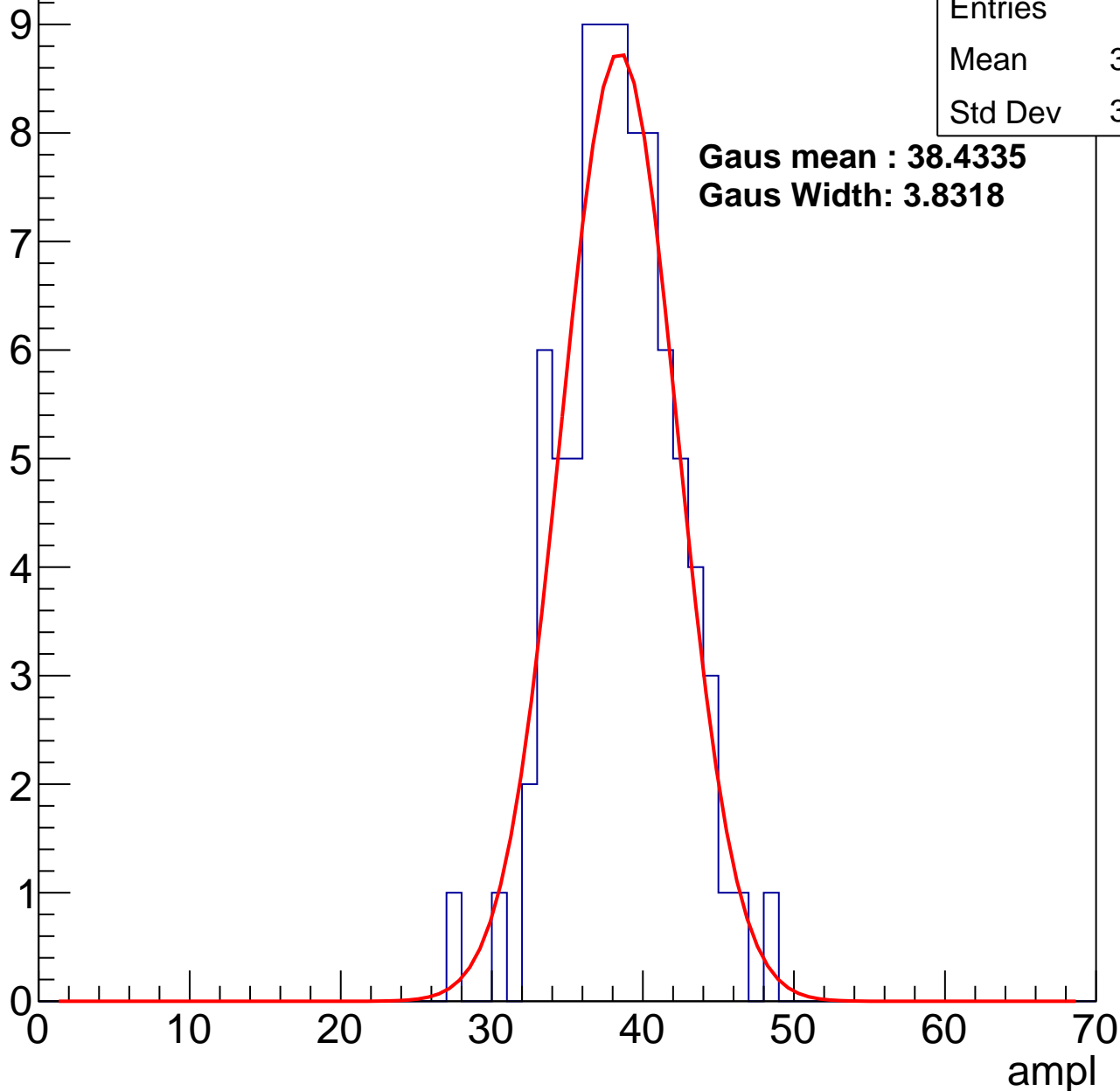
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	38.02
Std Dev	3.735

Gaus mean : 38.4335

Gaus Width: 3.8318



B0L102S, U12-ch59, adc2

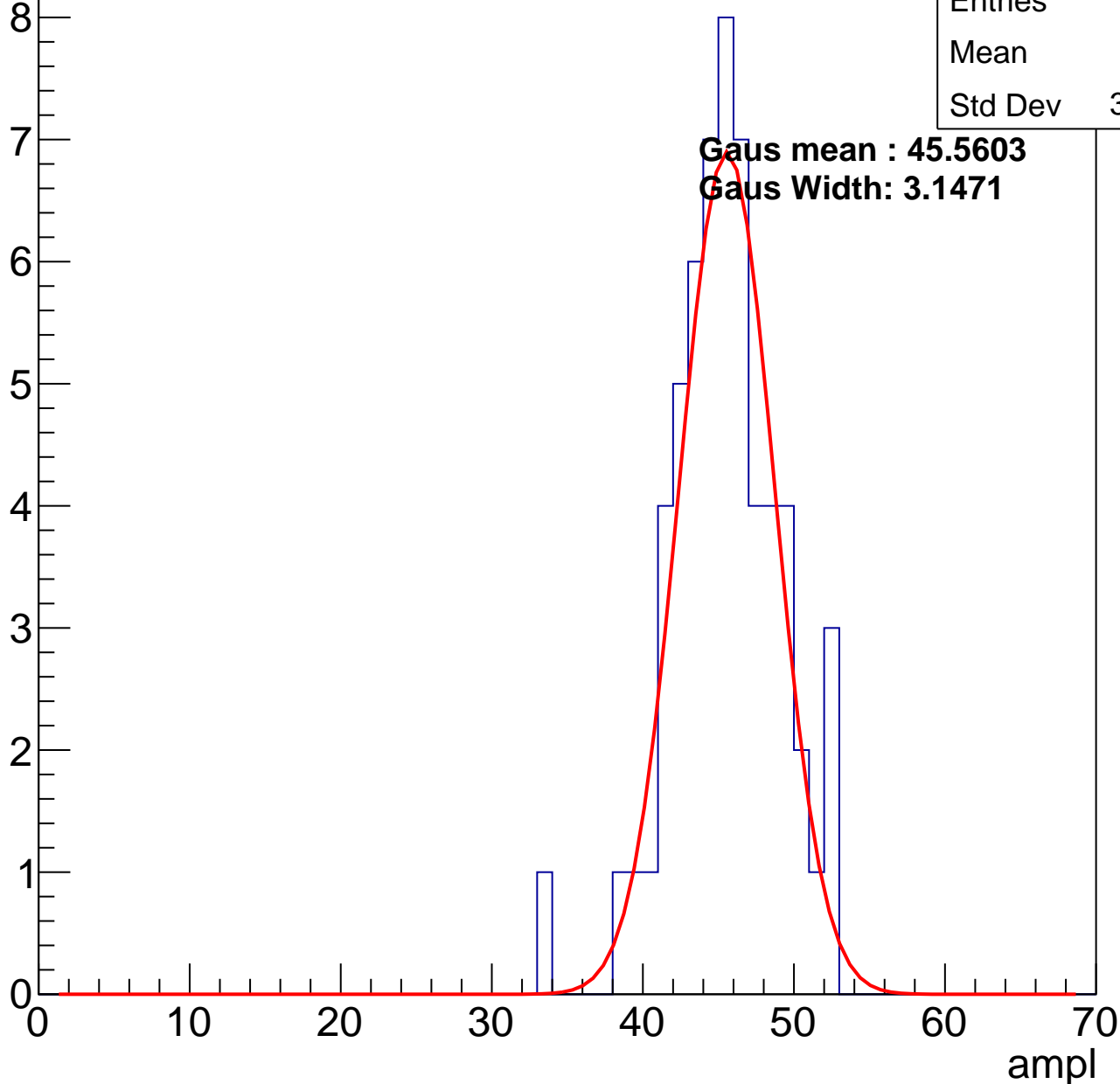
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	45
Std Dev	3.565

Gaus mean : 45.5603

Gaus Width: 3.1471

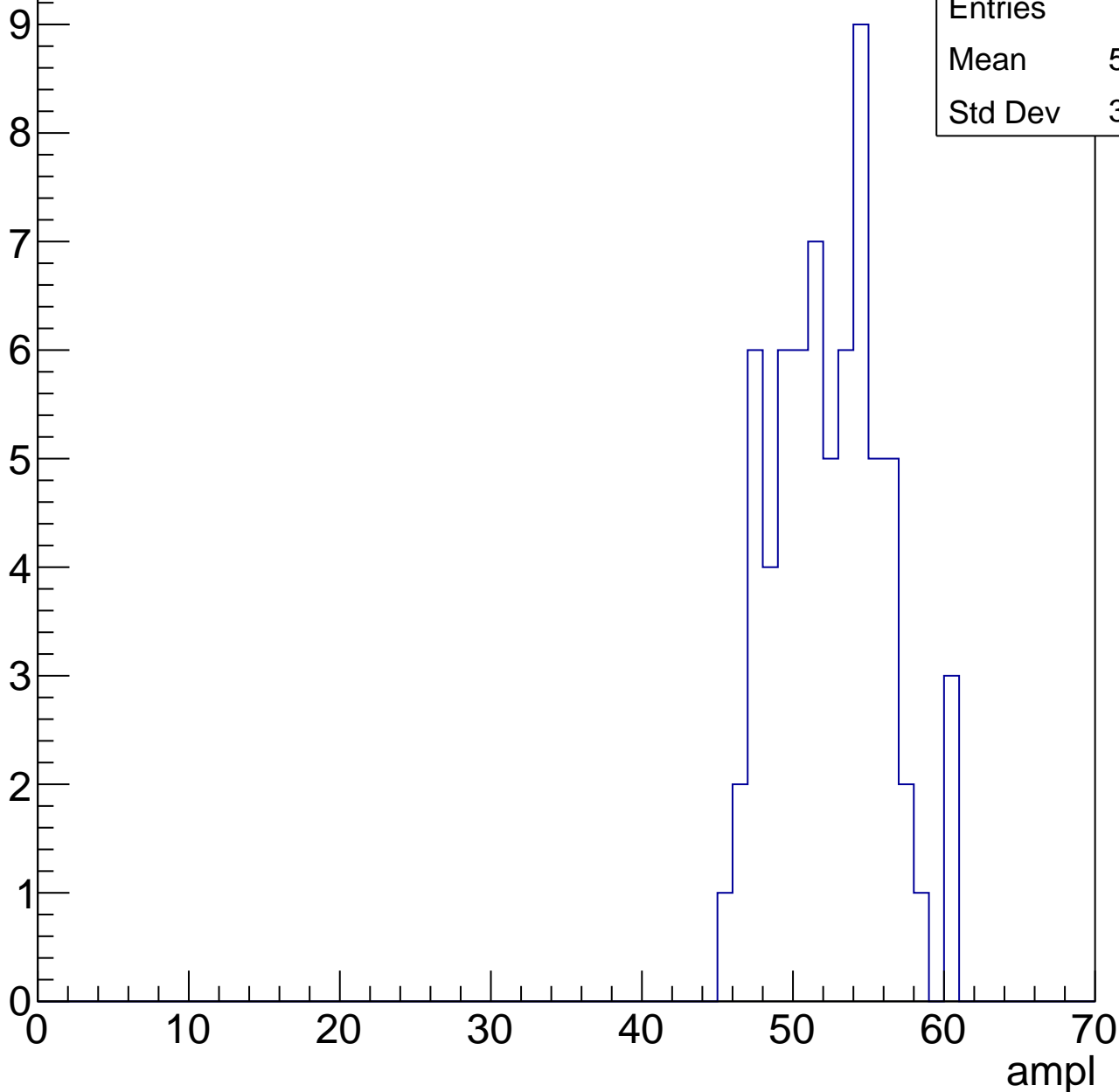


B0L102S, U12-ch59, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	51.96
Std Dev	3.566

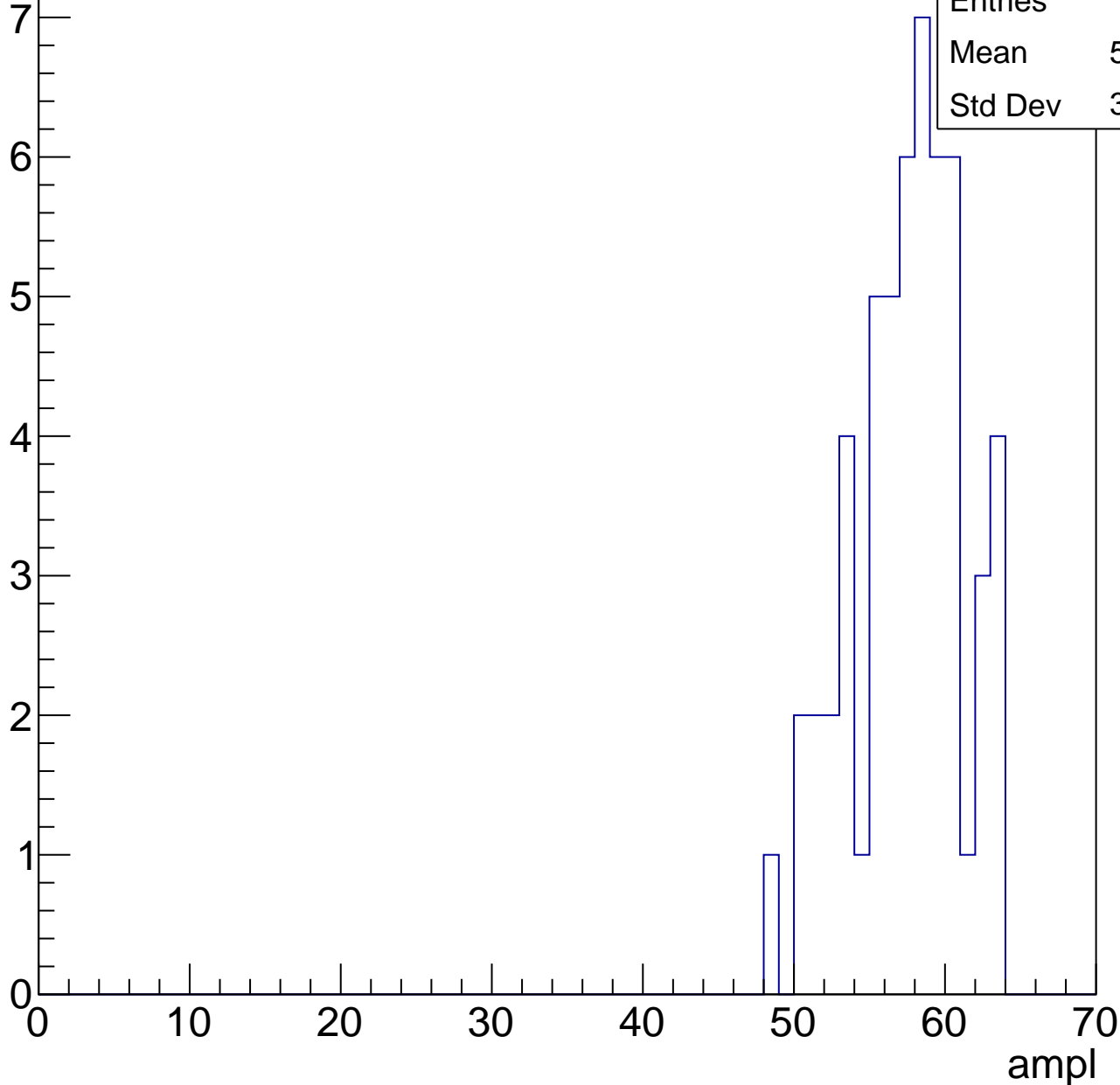


B0L102S, U12-ch59, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	57.02
Std Dev	3.616

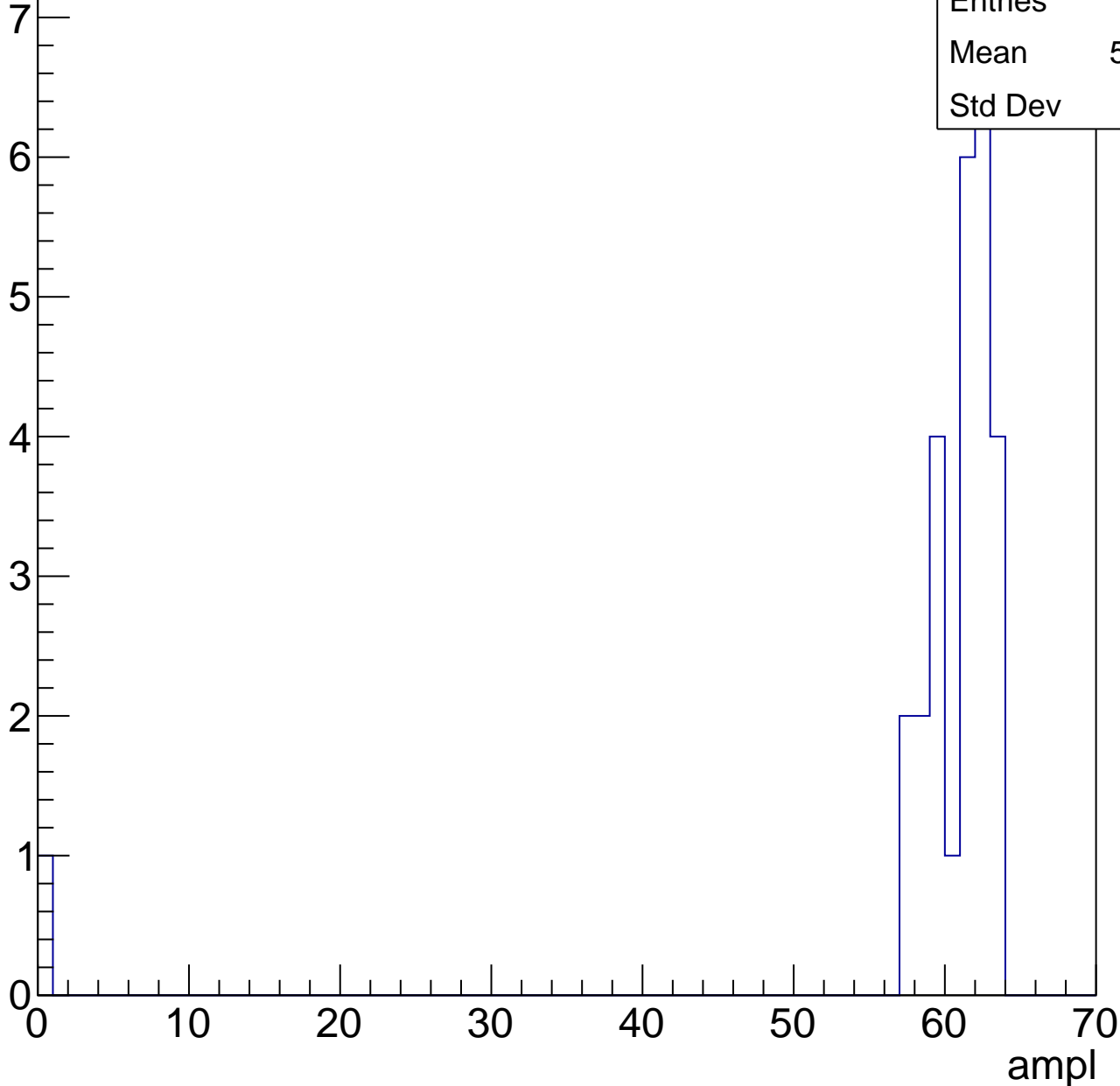


B0L102S, U12-ch59, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

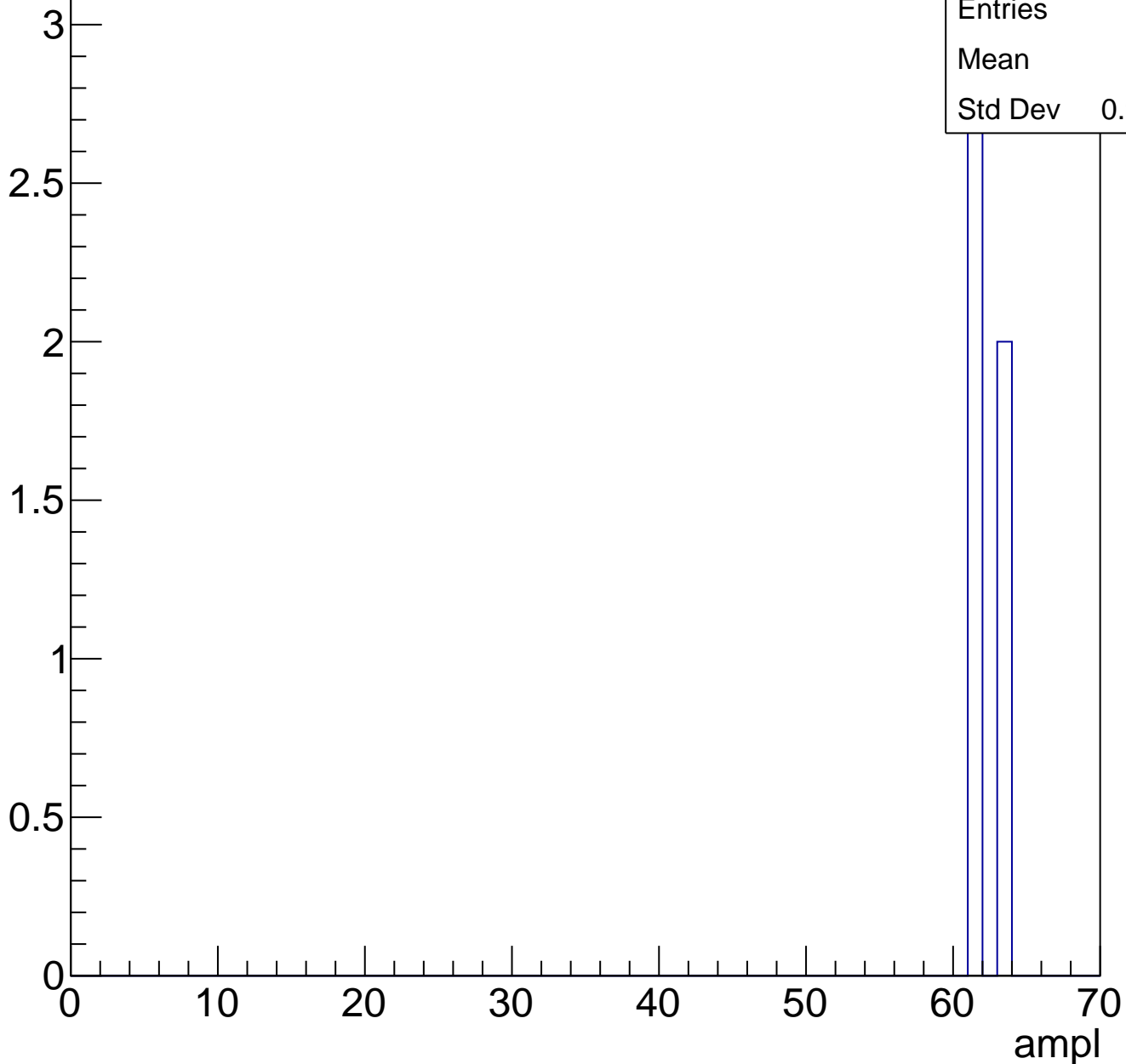
Entries	27
Mean	58.44
Std Dev	11.6



B0L102S, U12-ch59, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch59, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L102S, U12-ch60, adc0

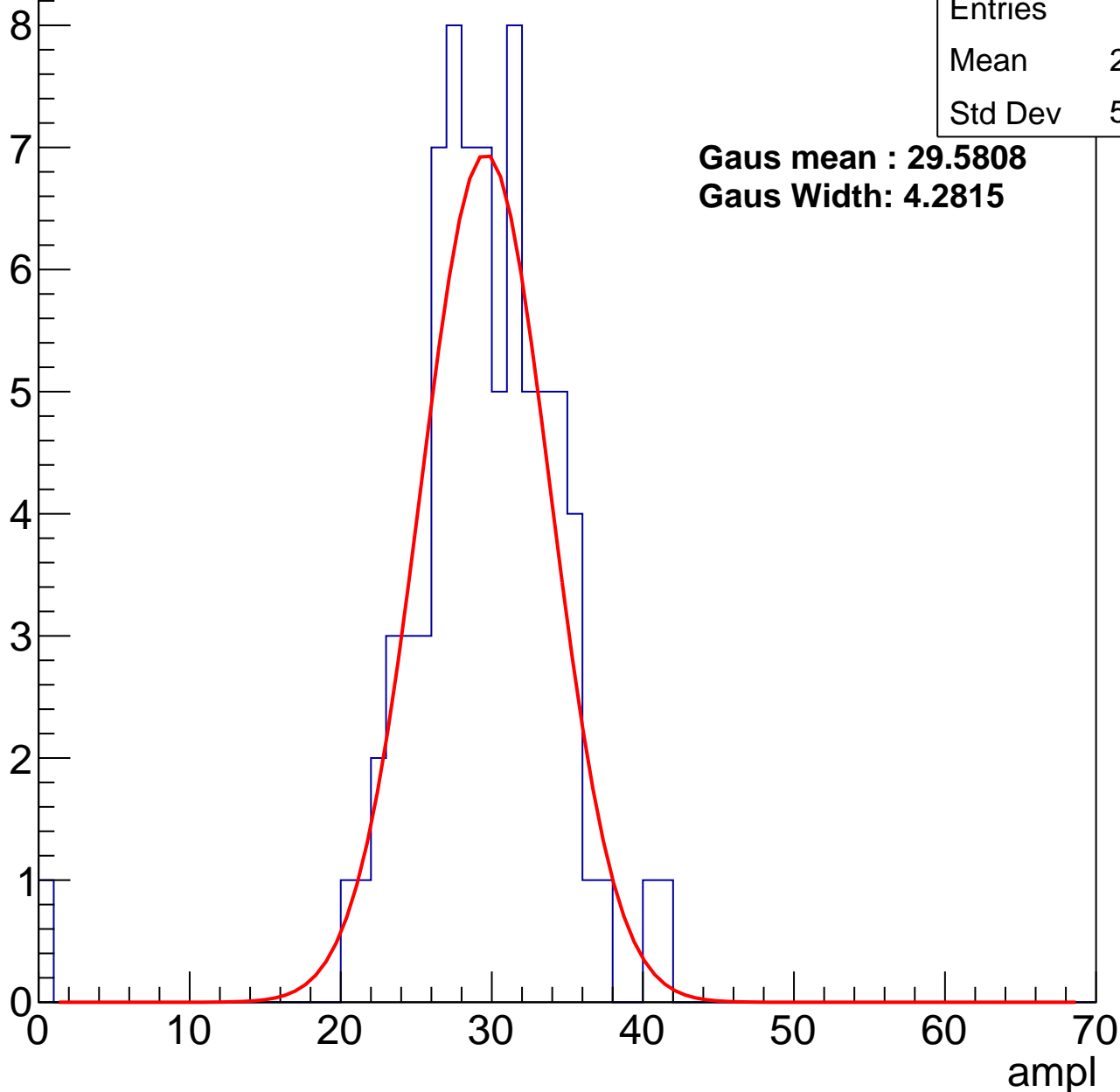
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	28.92
Std Dev	5.308

Gaus mean : 29.5808

Gaus Width: 4.2815



B0L102S, U12-ch60, adc1

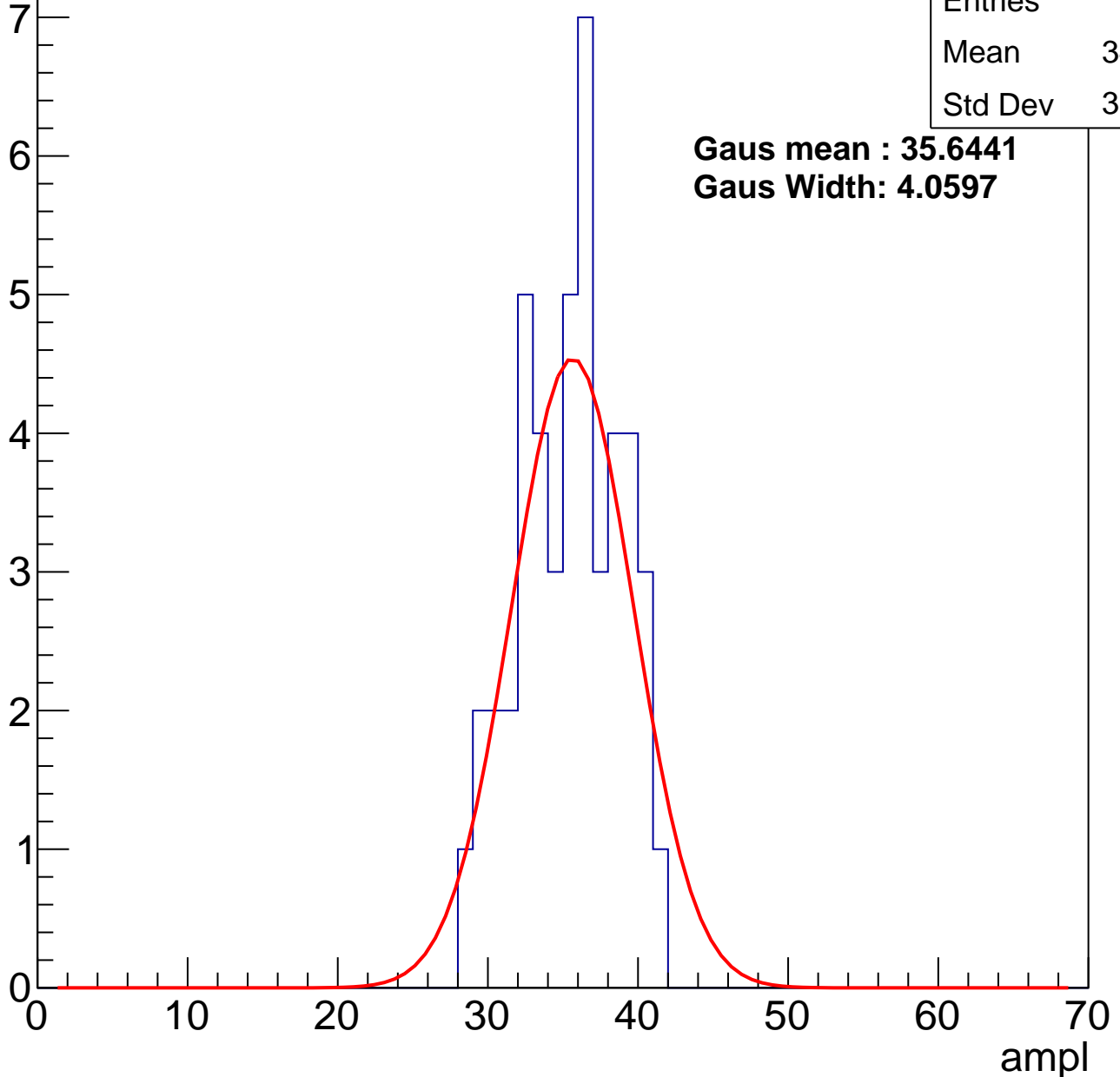
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	34.98
Std Dev	3.287

Gaus mean : 35.6441

Gaus Width: 4.0597



B0L102S, U12-ch60, adc2

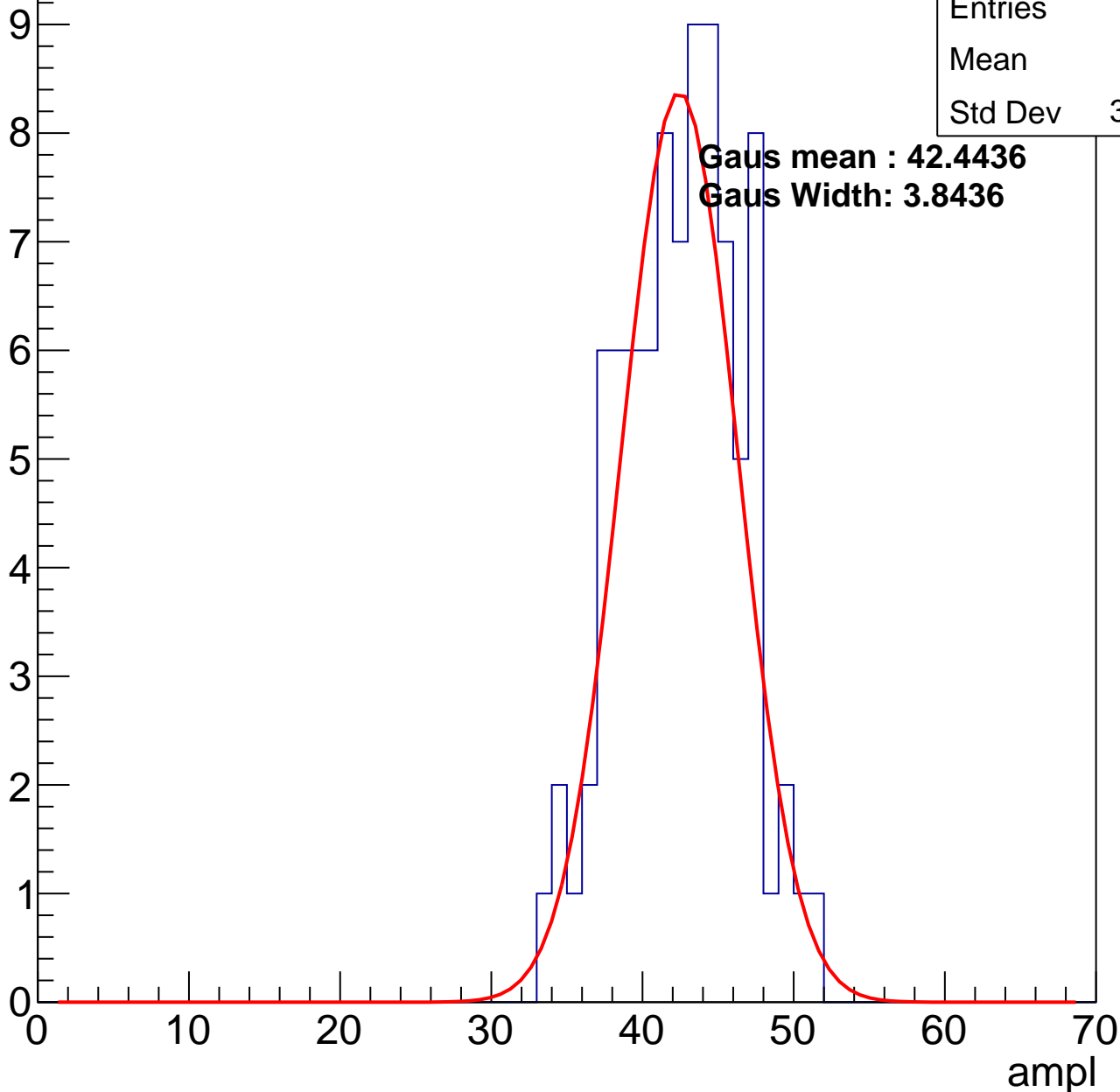
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	88
Mean	42.1
Std Dev	3.885

Gaus mean : 42.4436

Gaus Width: 3.8436

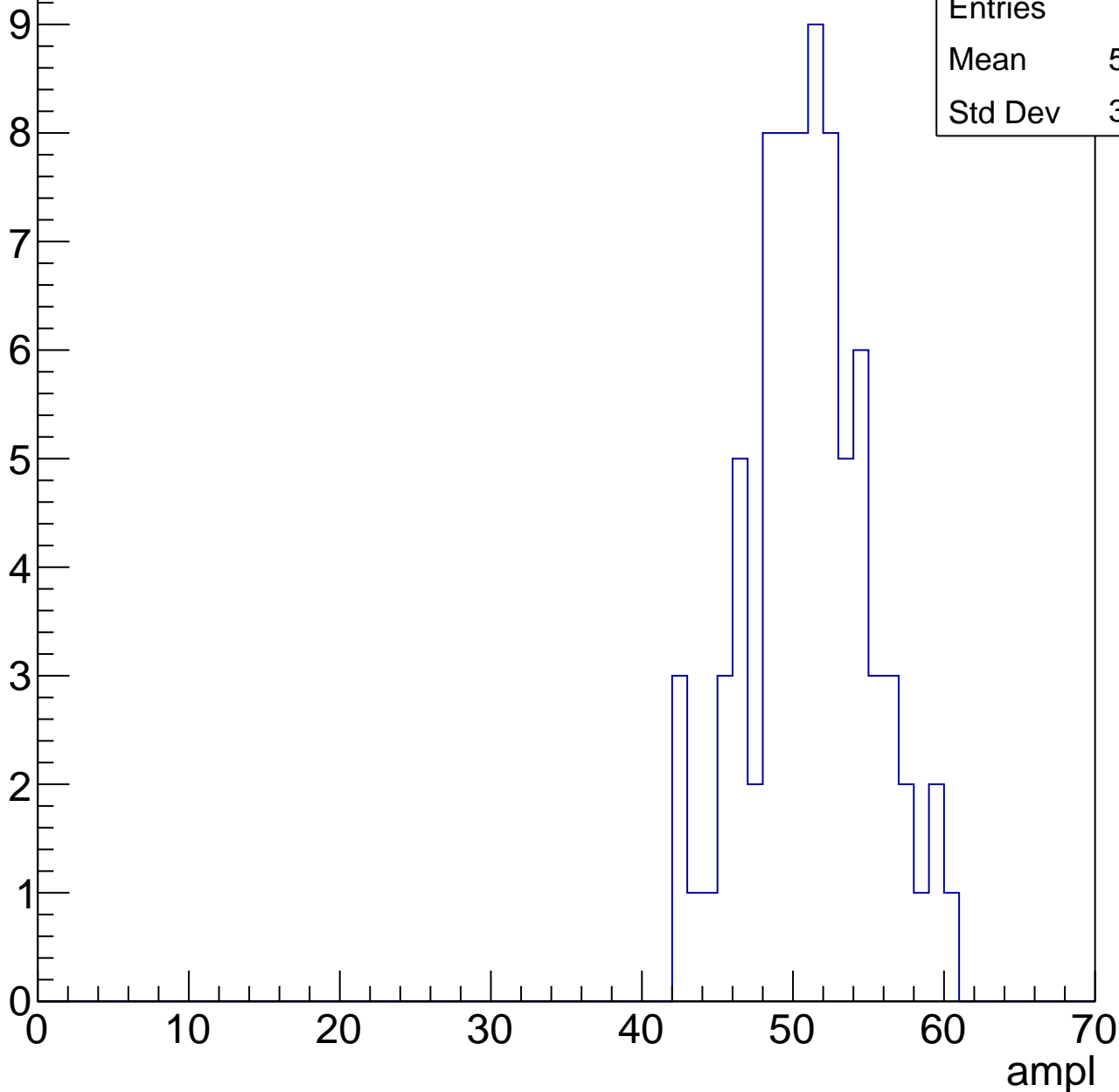


B0L102S, U12-ch60, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	50.57
Std Dev	3.983

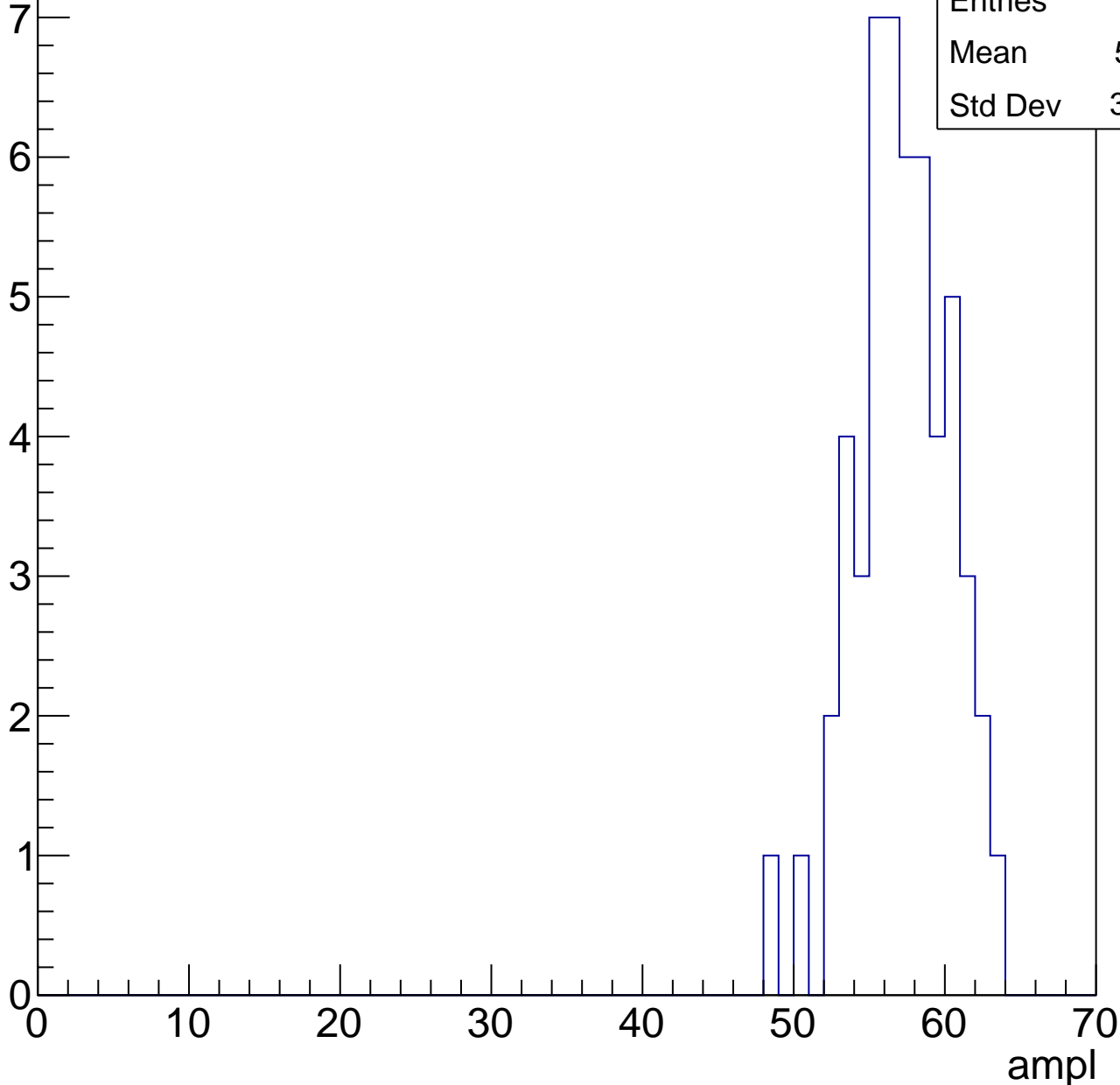


B0L102S, U12-ch60, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	56.71
Std Dev	3.109

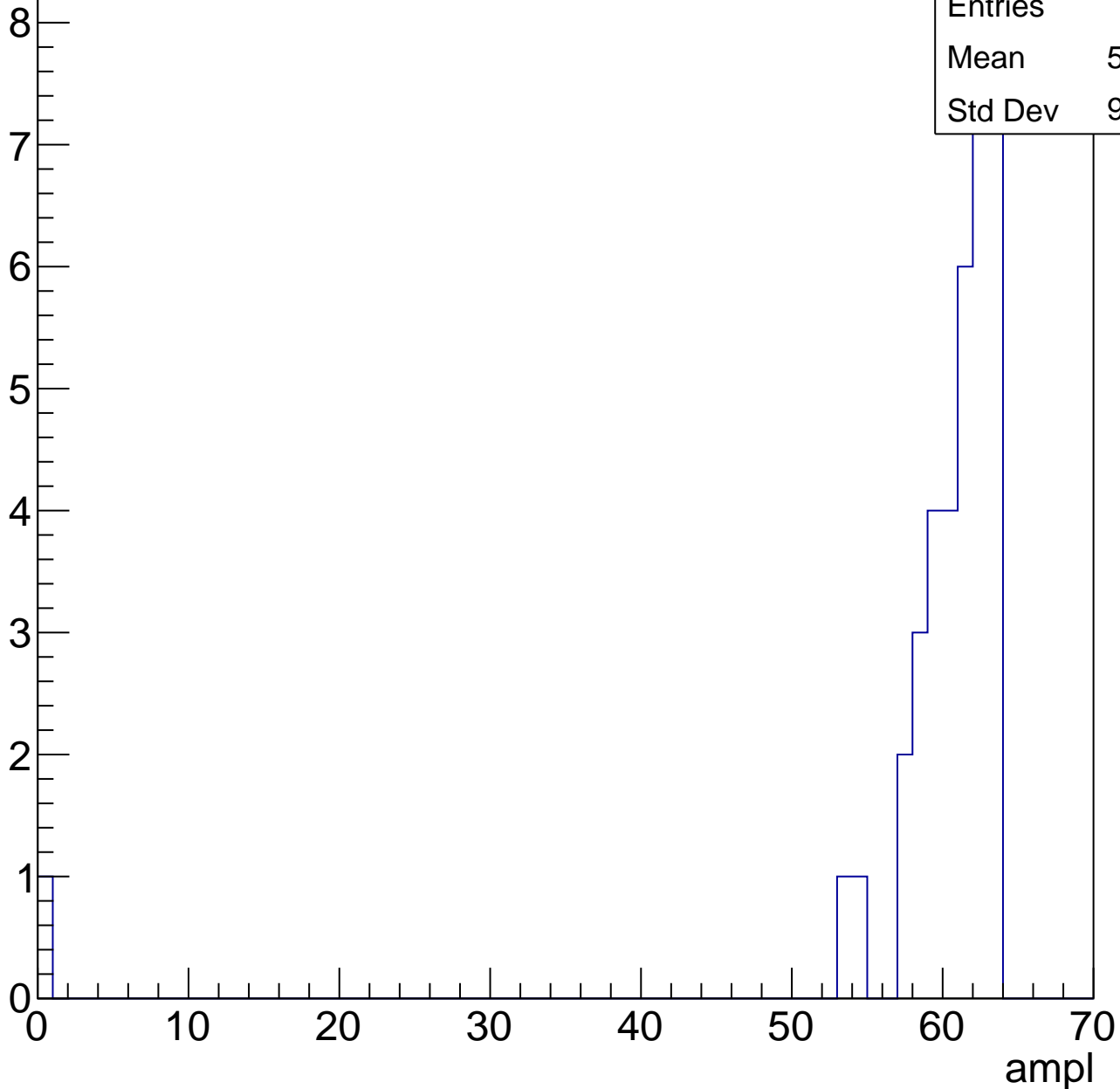


B0L102S, U12-ch60, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	38
Mean	58.87
Std Dev	9.974



B0L102S, U12-ch60, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch60, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B0L102S, U12-ch61, adc0

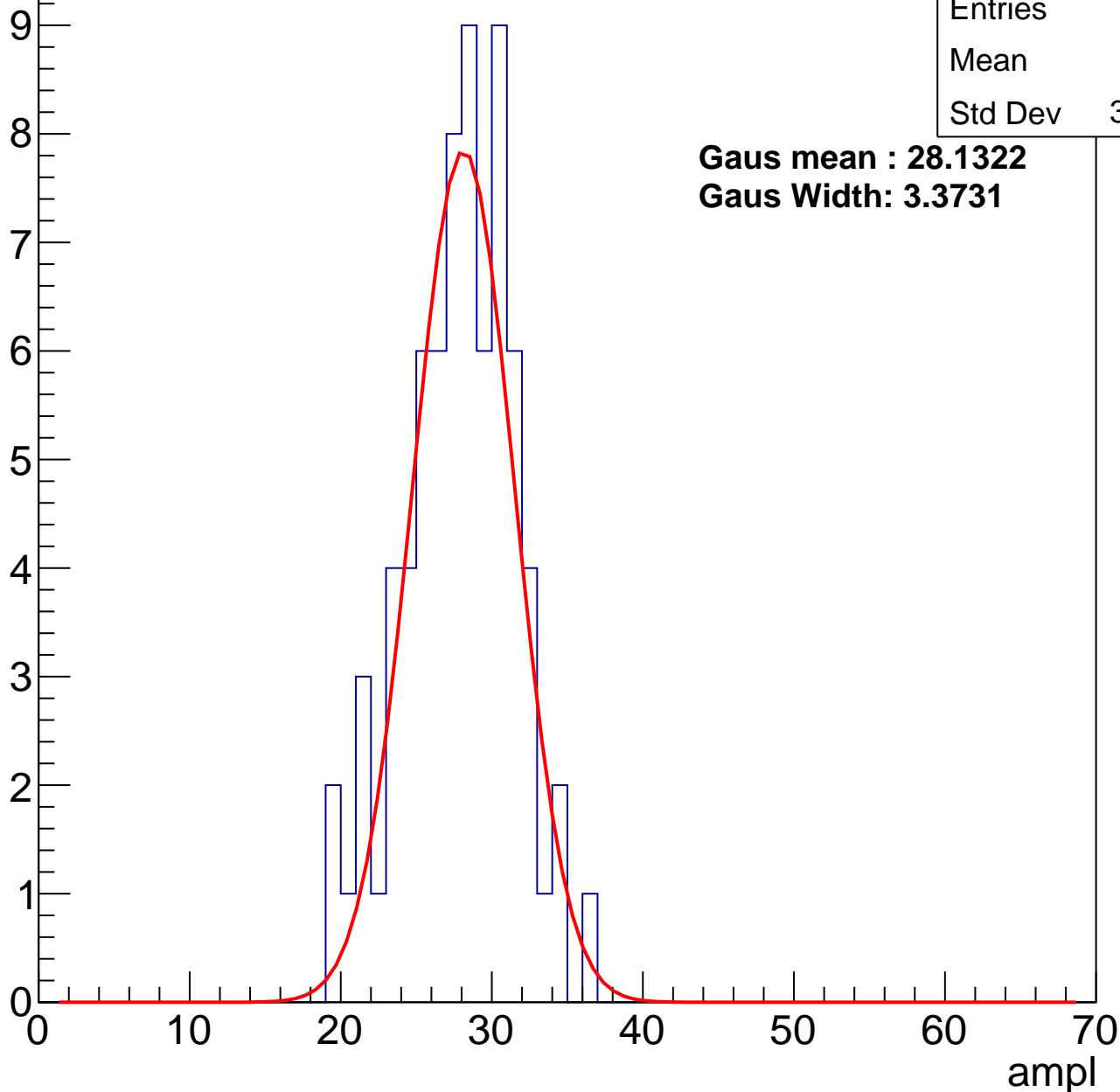
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	27.4
Std Dev	3.614

Gaus mean : 28.1322

Gaus Width: 3.3731



B0L102S, U12-ch61, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	35.19
Std Dev	3.428

Gaus mean : 35.8704

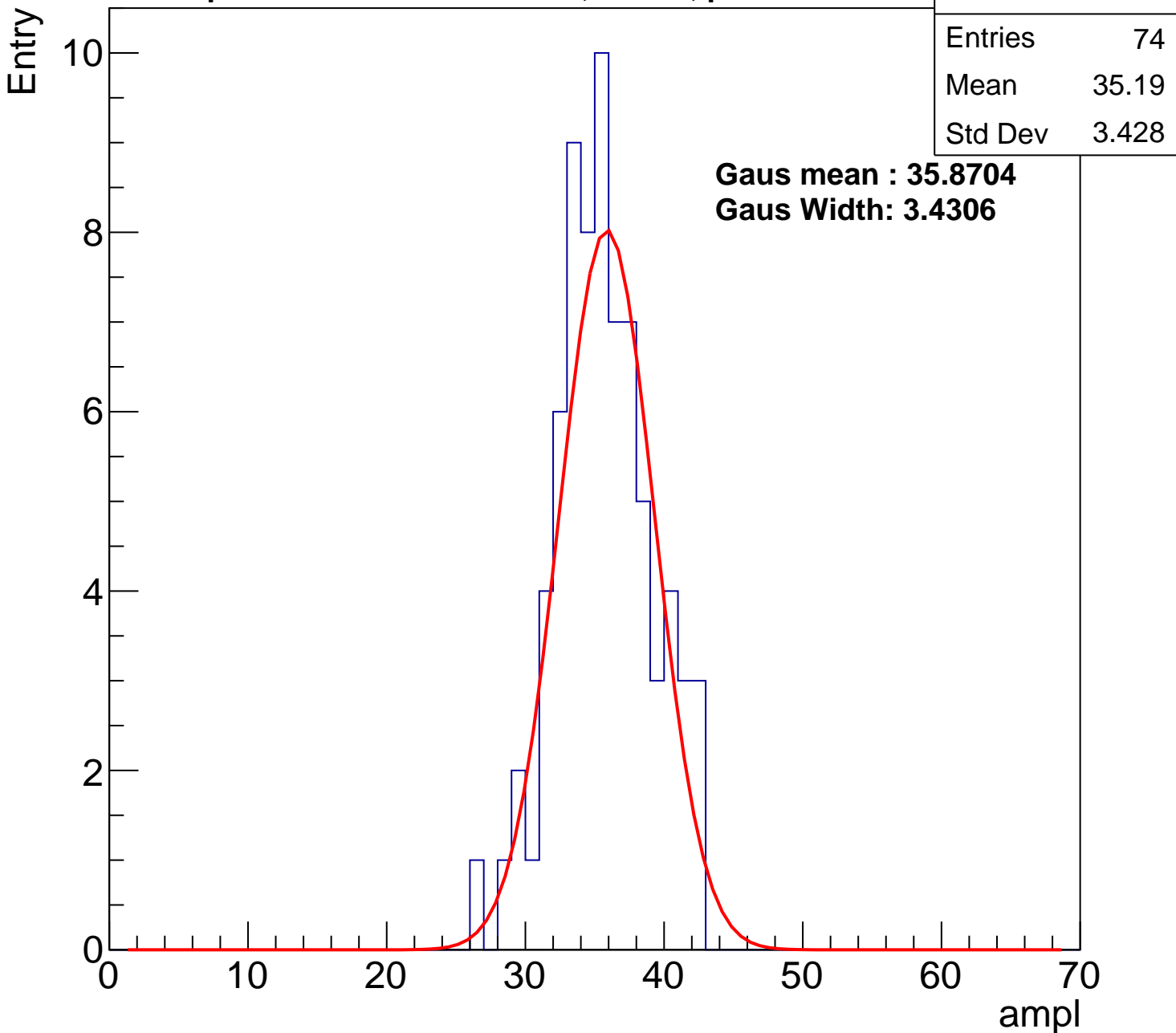
Gaus Width: 3.4306

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch61, adc2

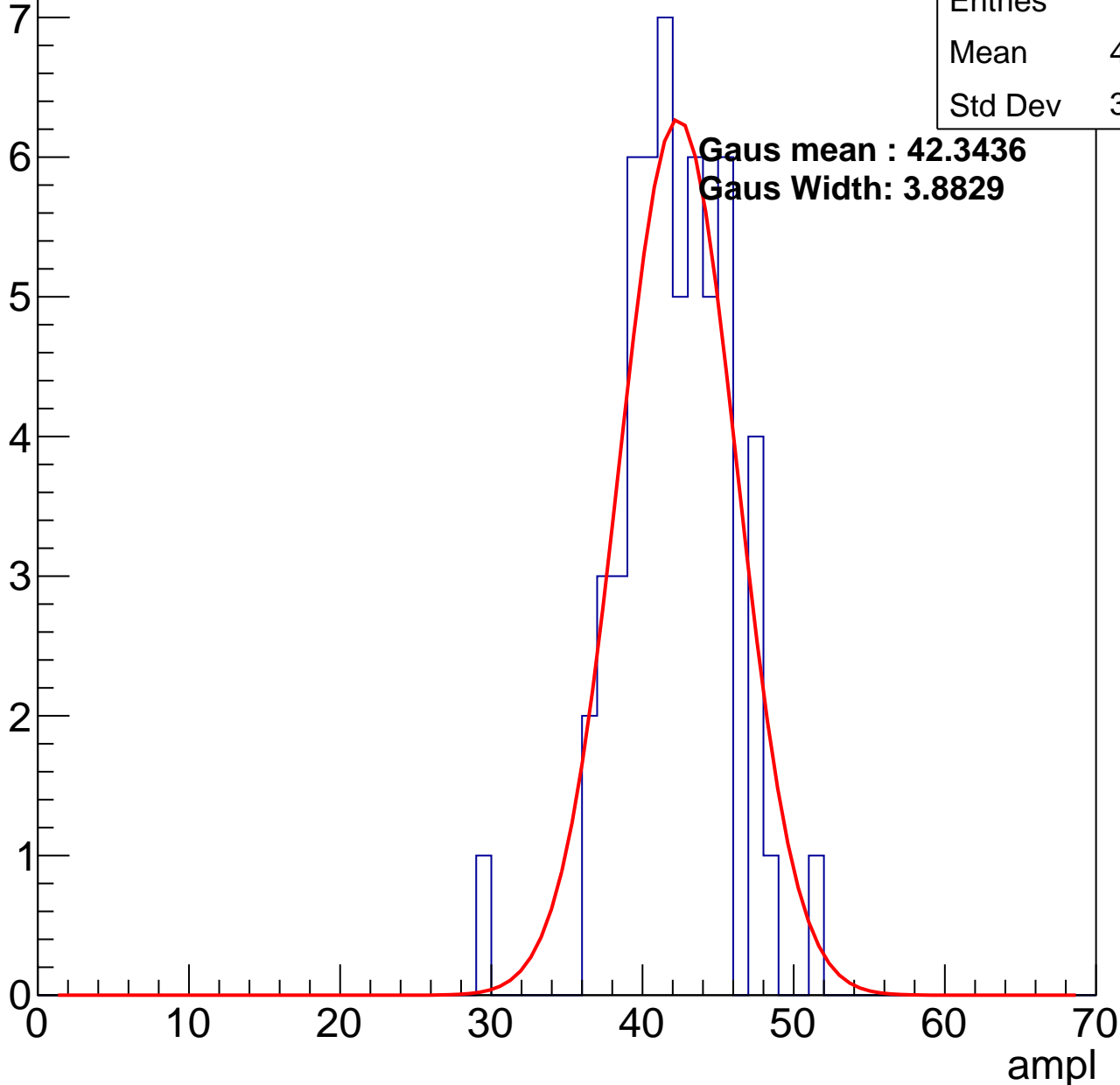
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	41.64
Std Dev	3.632

Gaus mean : 42.3436

Gaus Width: 3.8829

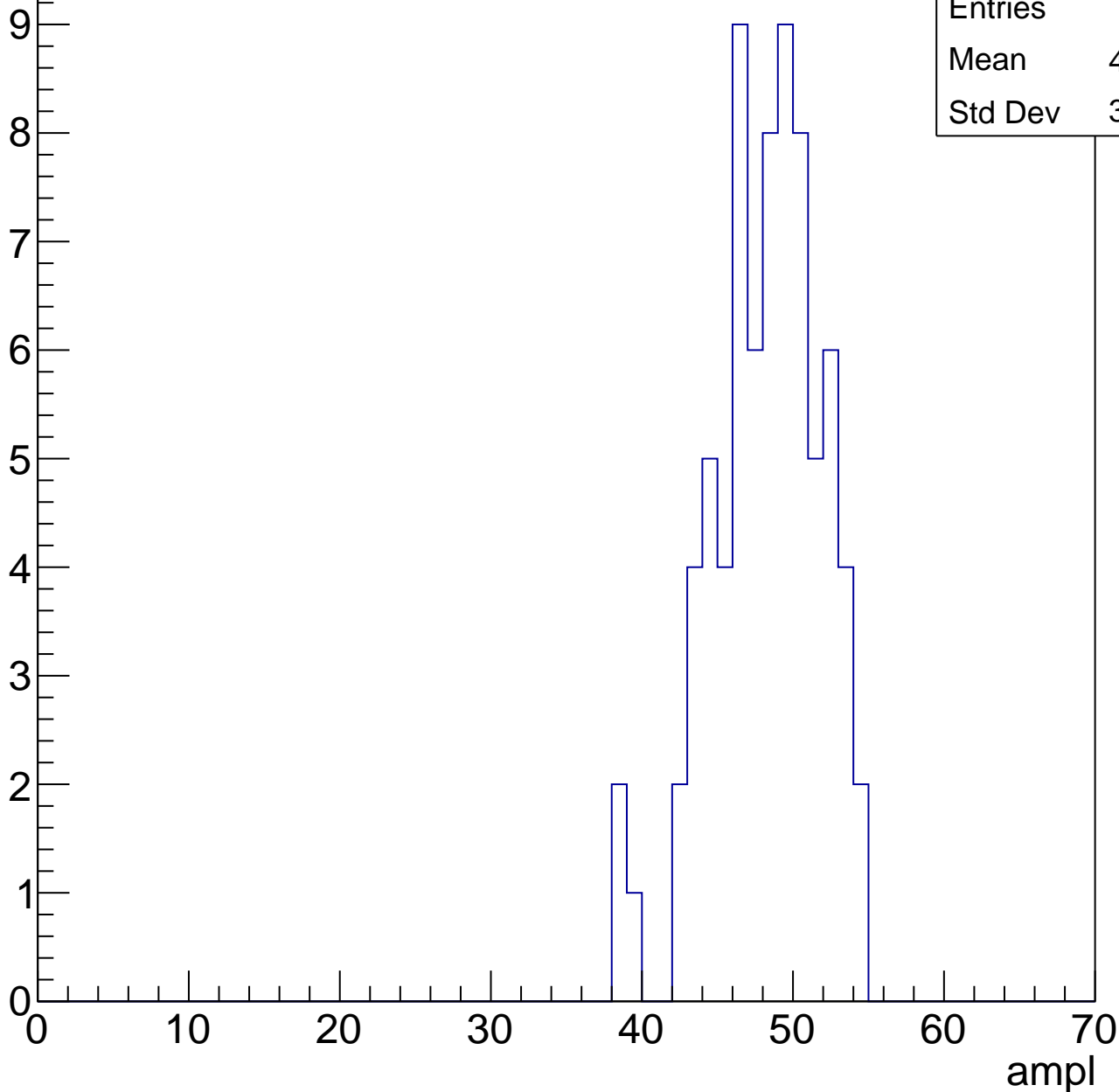


B0L102S, U12-ch61, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	47.72
Std Dev	3.576

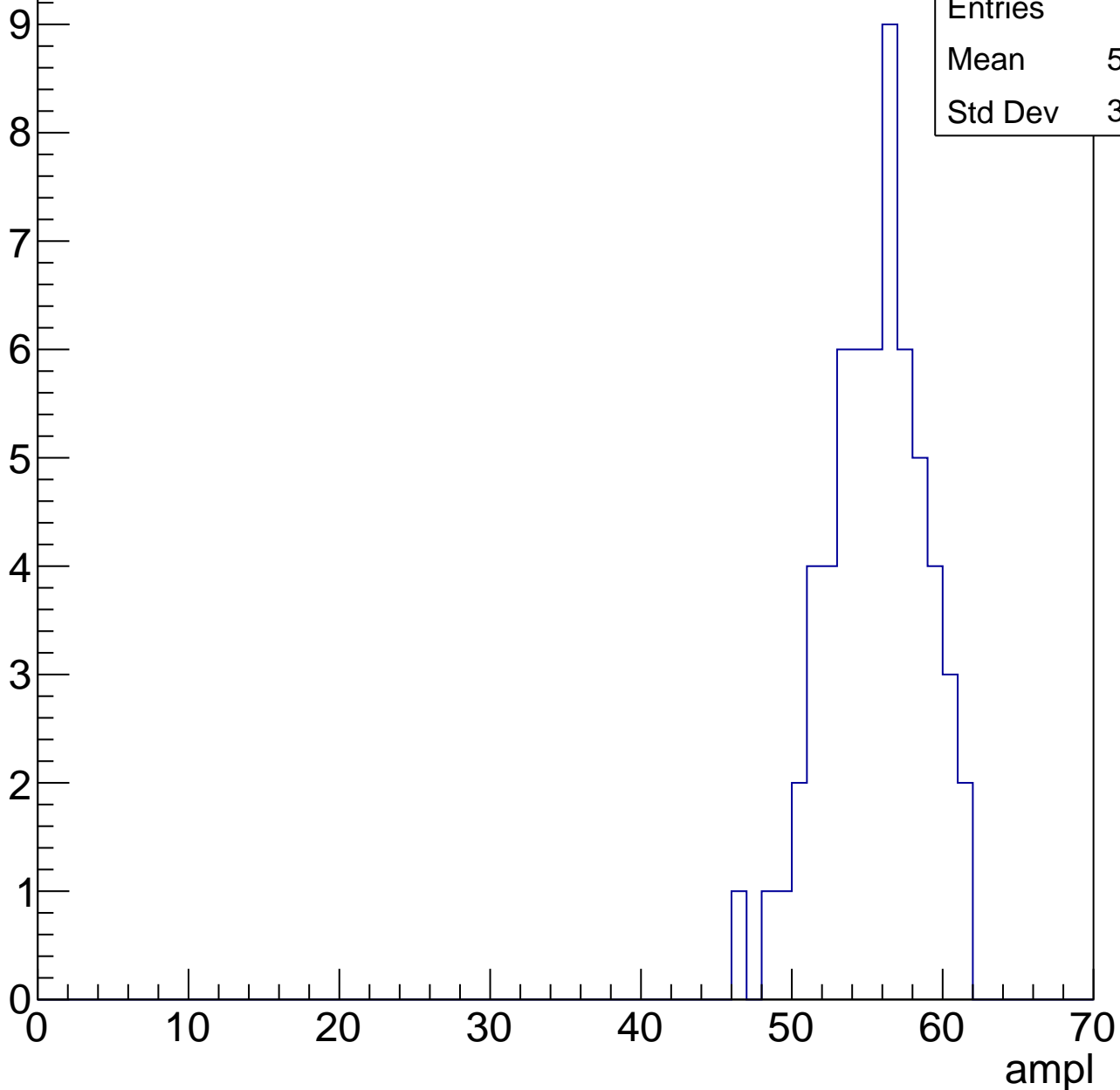


B0L102S, U12-ch61, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	55.02
Std Dev	3.253



B0L102S, U12-ch61, adc5

calib_packv5_042523_0143.root, FC#12, port B1

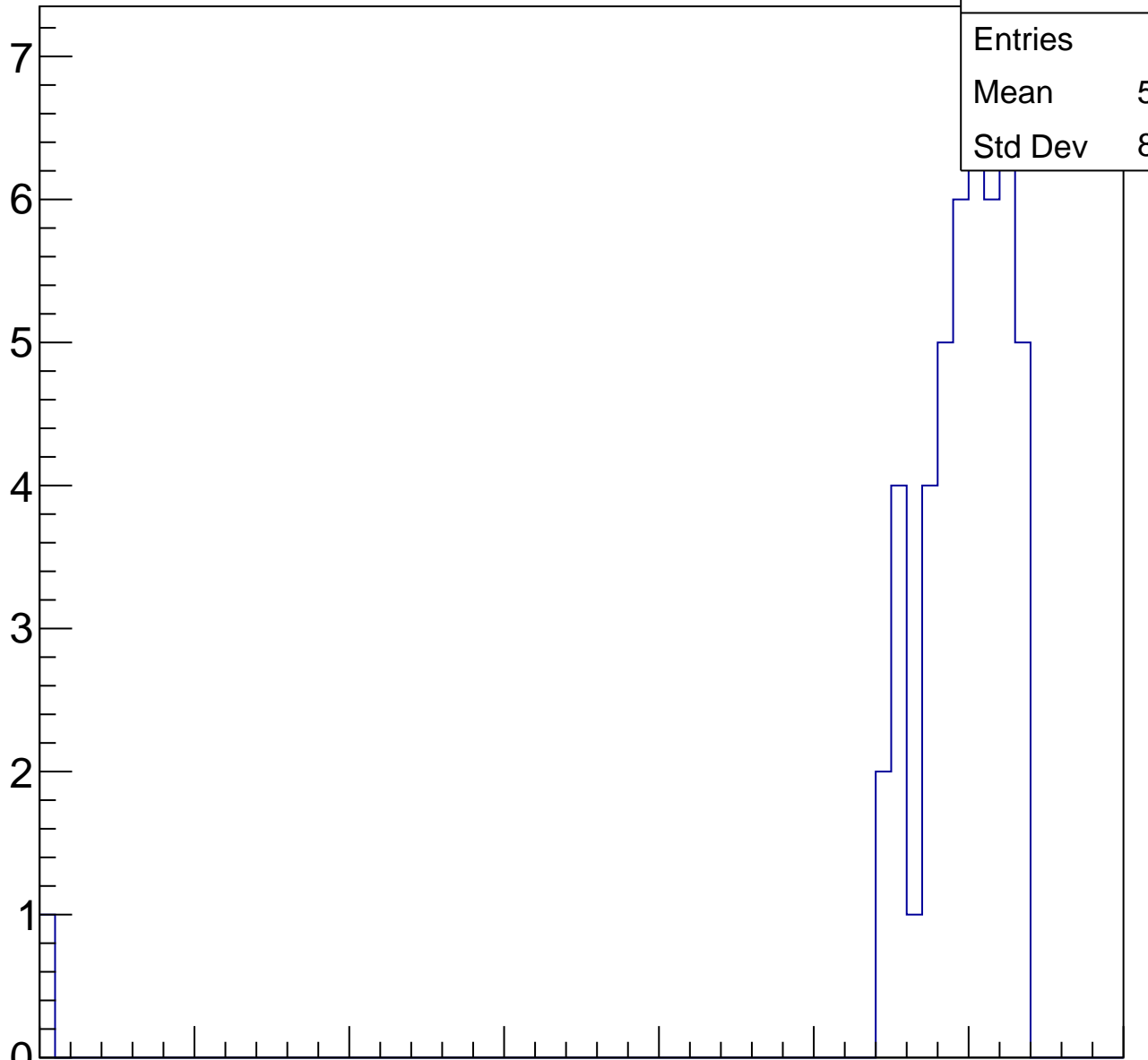
Entry

7
6
5
4
3
2
1
0

Entries	48
Mean	58.15
Std Dev	8.855

ampl

0 10 20 30 40 50 60 70

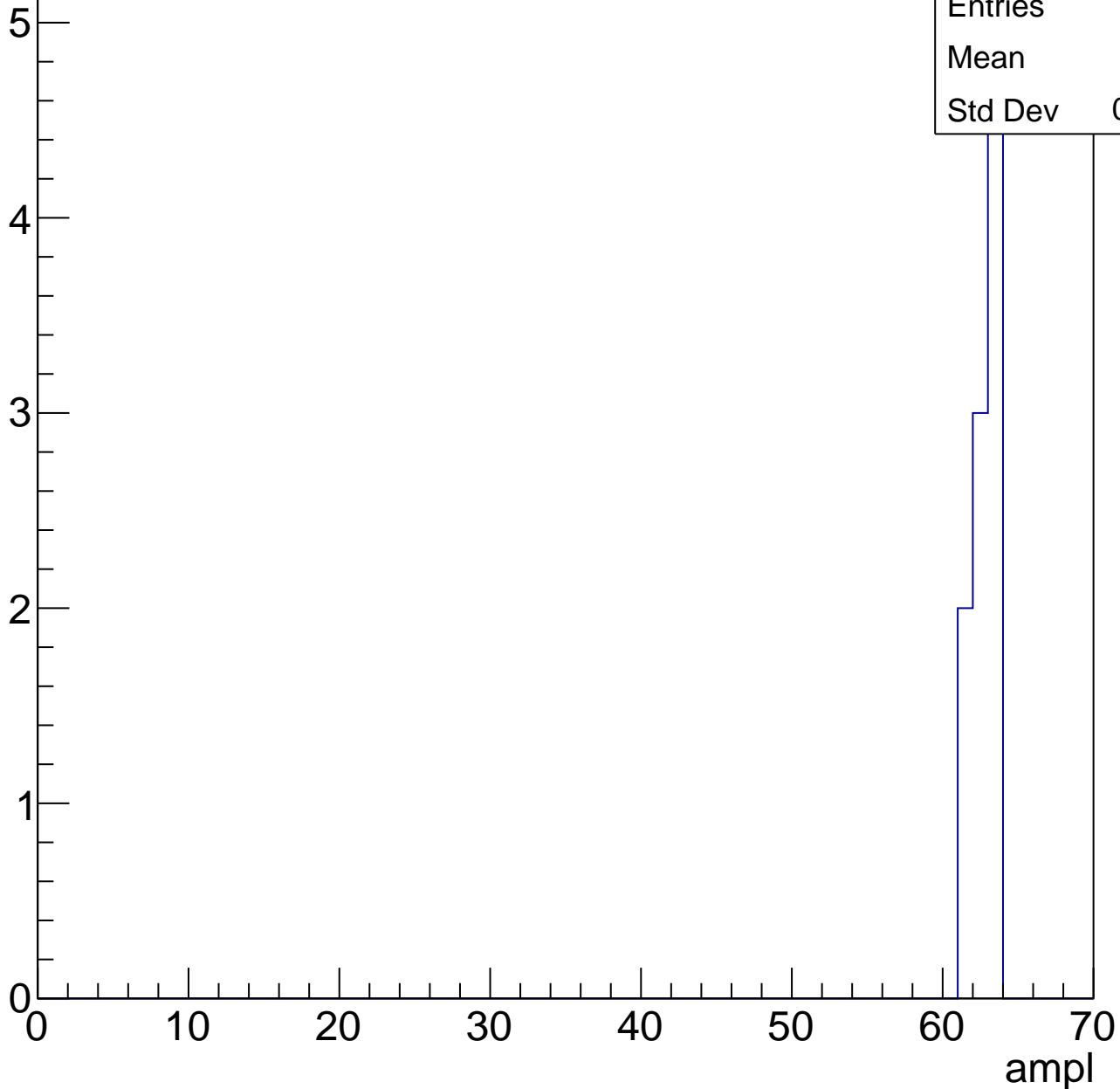


B0L102S, U12-ch61, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	10
Mean	62.3
Std Dev	0.781



B0L102S, U12-ch61, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch62, adc0

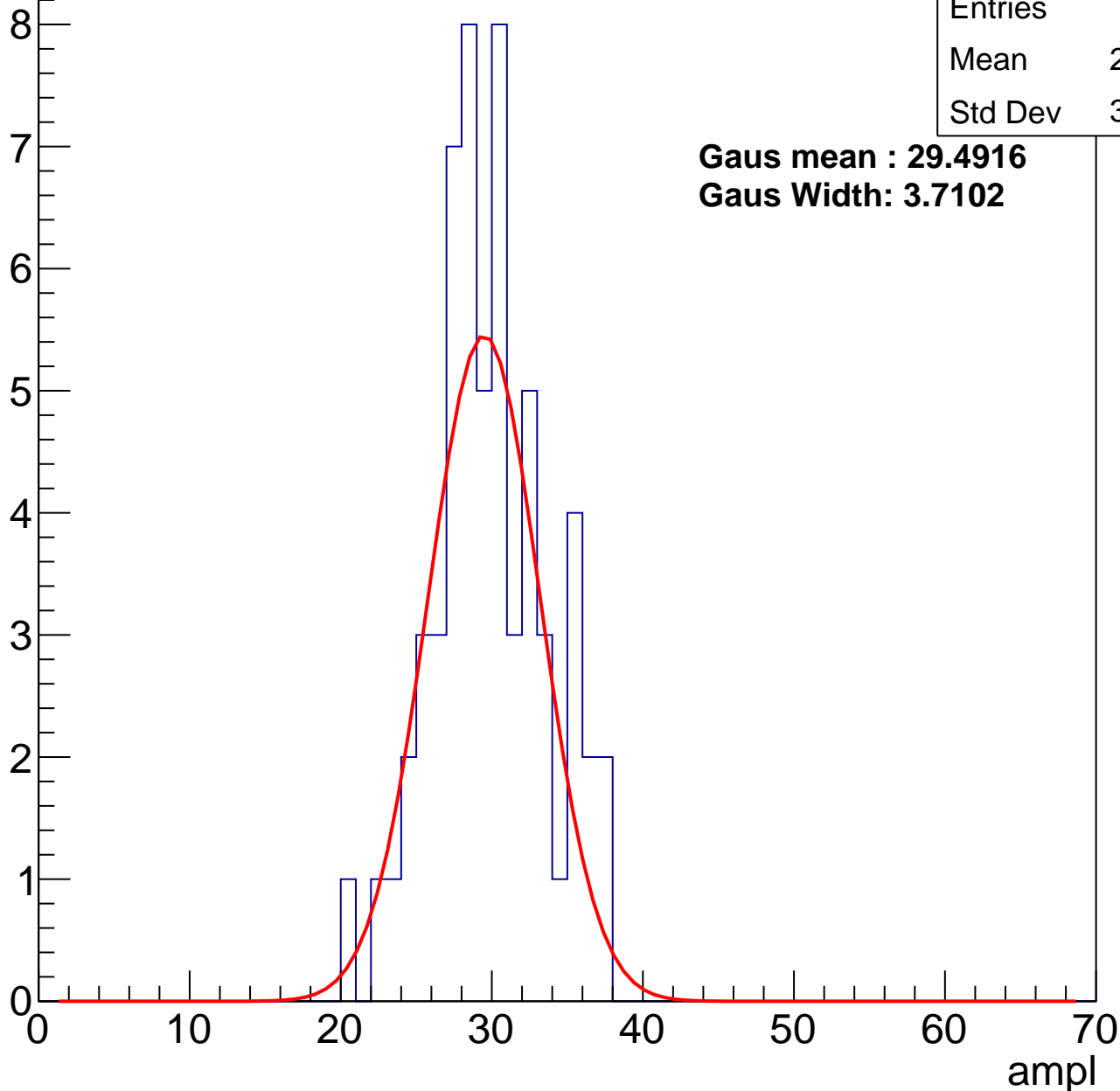
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	29.42
Std Dev	3.738

Gaus mean : 29.4916

Gaus Width: 3.7102



B0L102S, U12-ch62, adc1

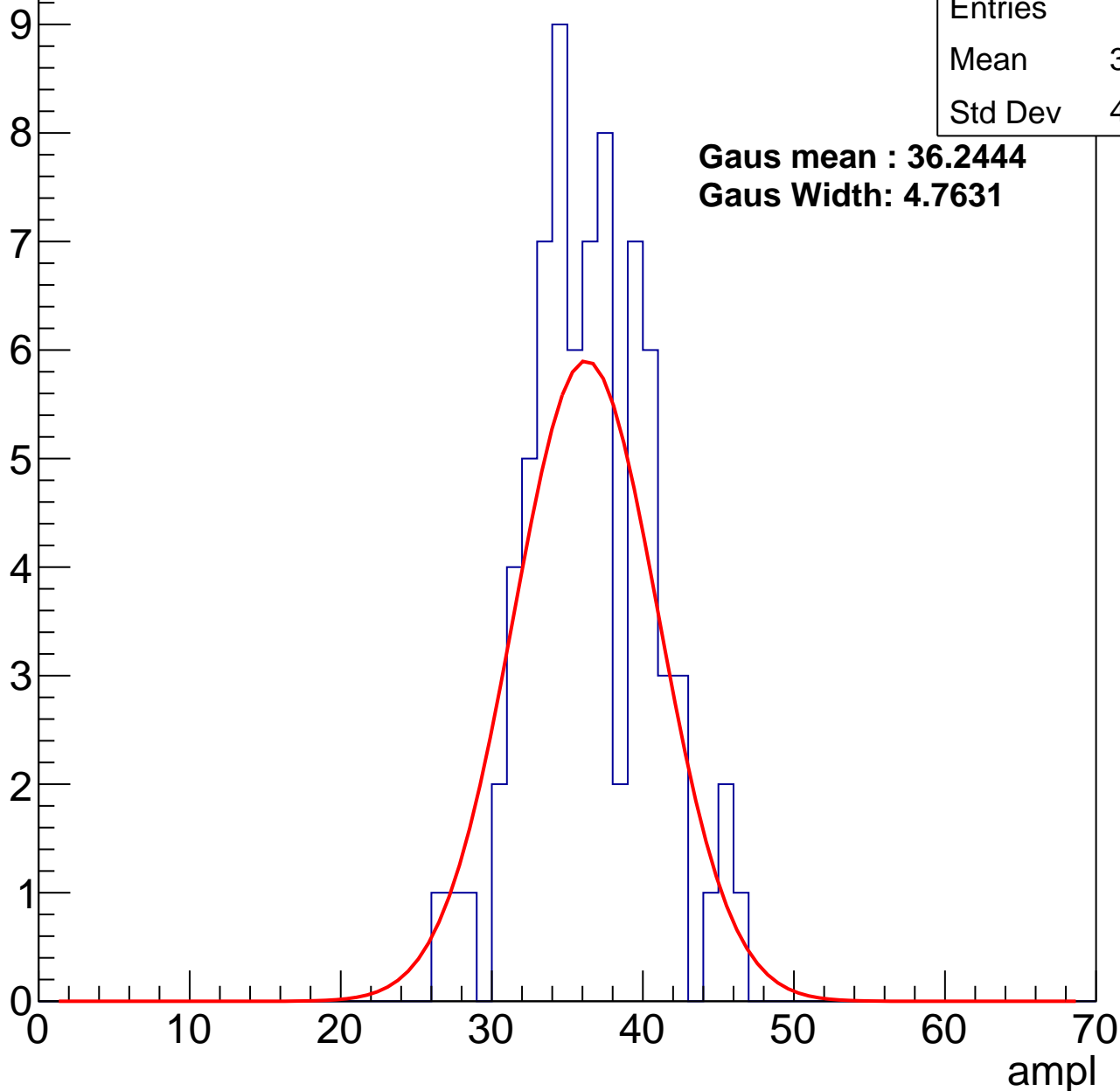
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	36.03
Std Dev	4.113

Gaus mean : 36.2444

Gaus Width: 4.7631



B0L102S, U12-ch62, adc2

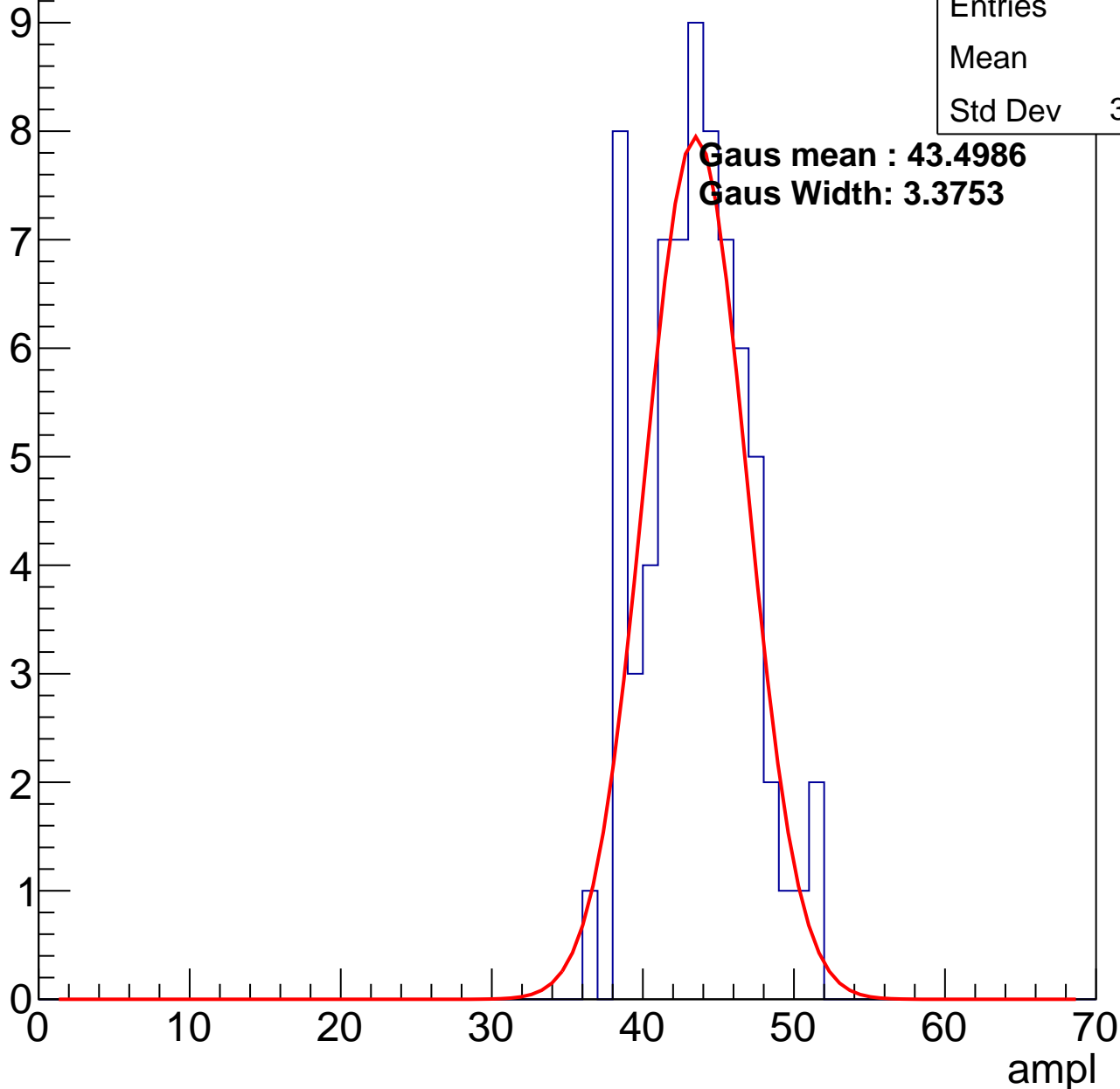
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	43.1
Std Dev	3.357

Gaus mean : 43.4986

Gaus Width: 3.3753

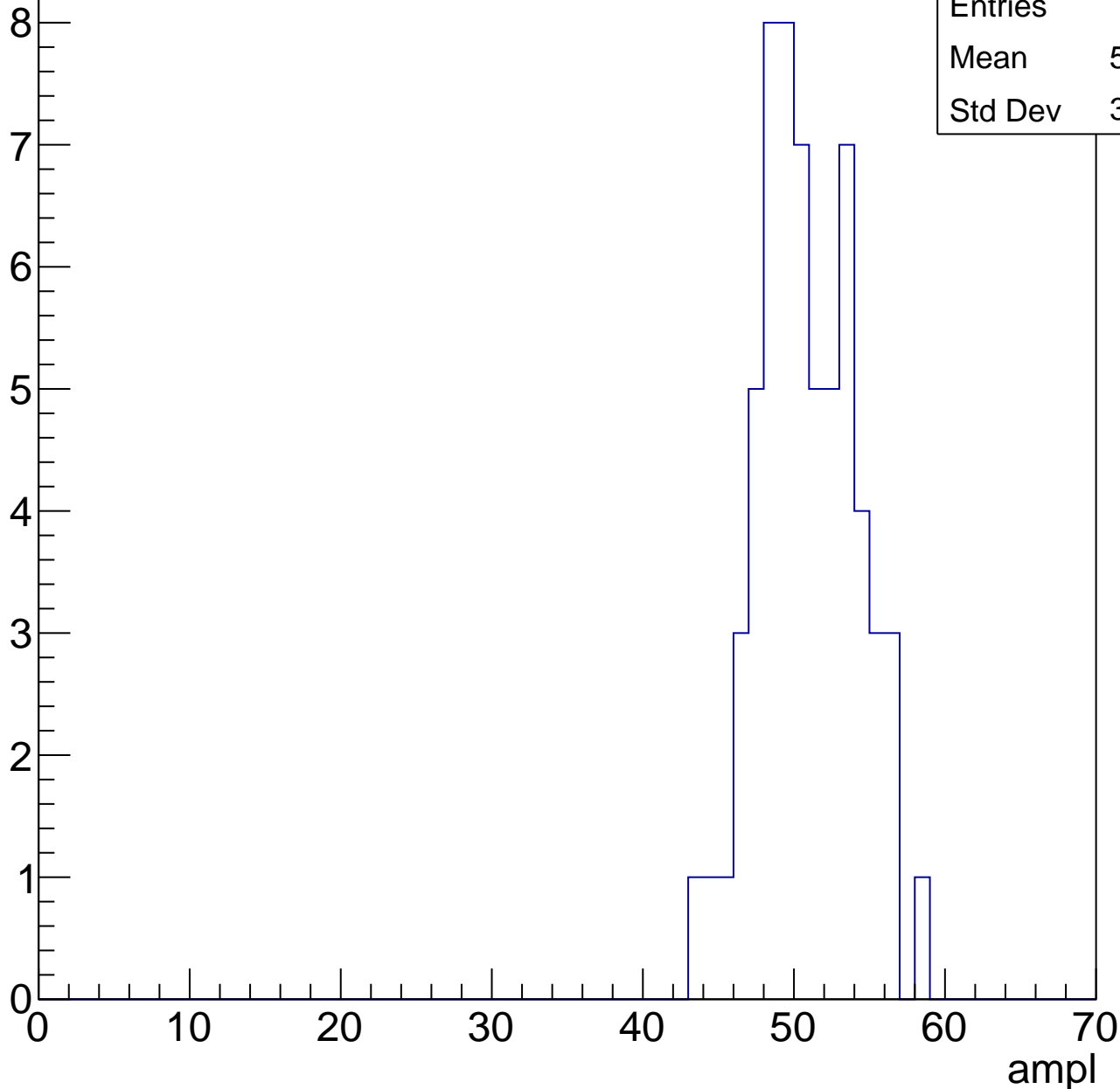


B0L102S, U12-ch62, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	50.39
Std Dev	3.195

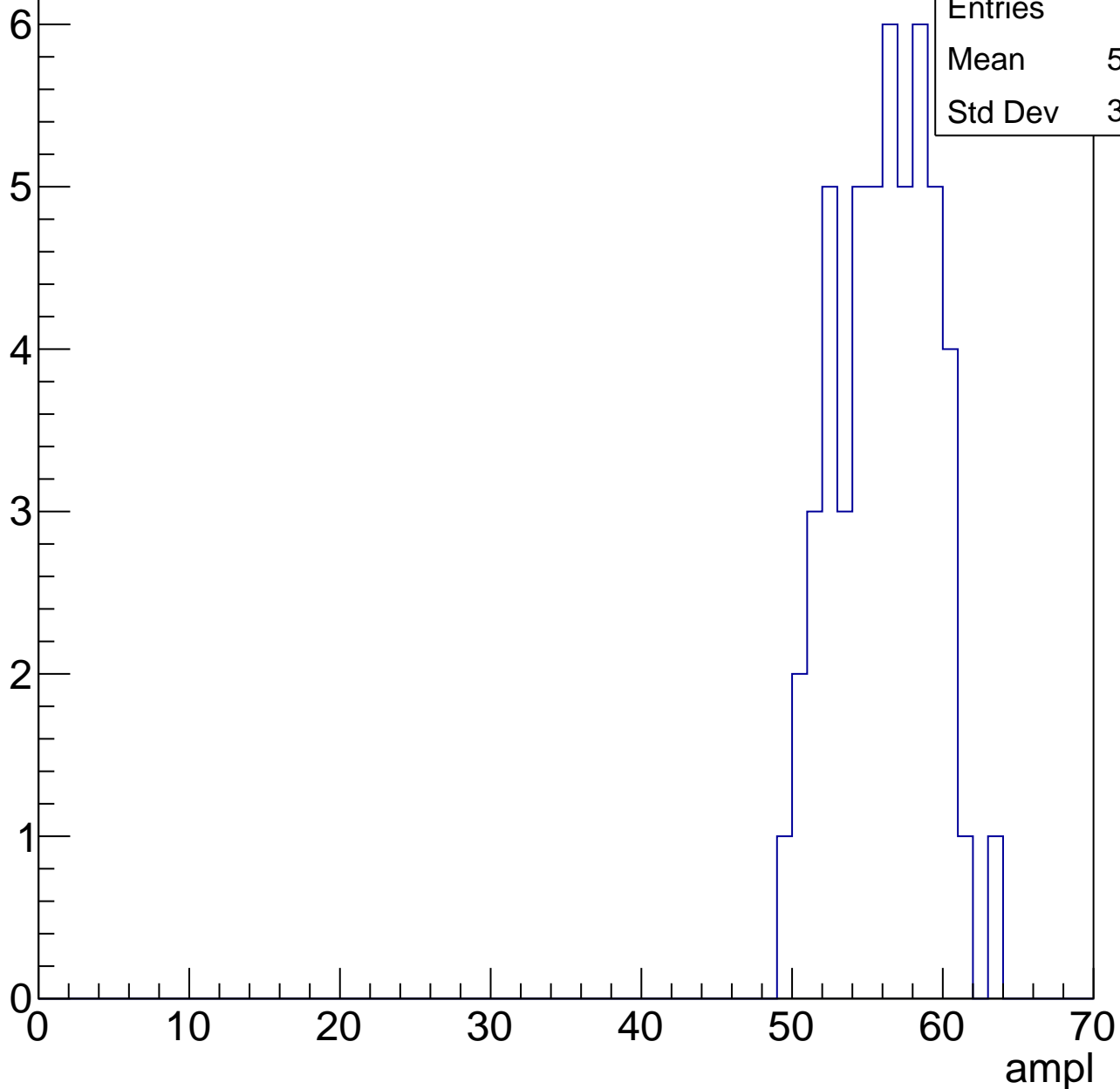


B0L102S, U12-ch62, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55.65
Std Dev	3.204

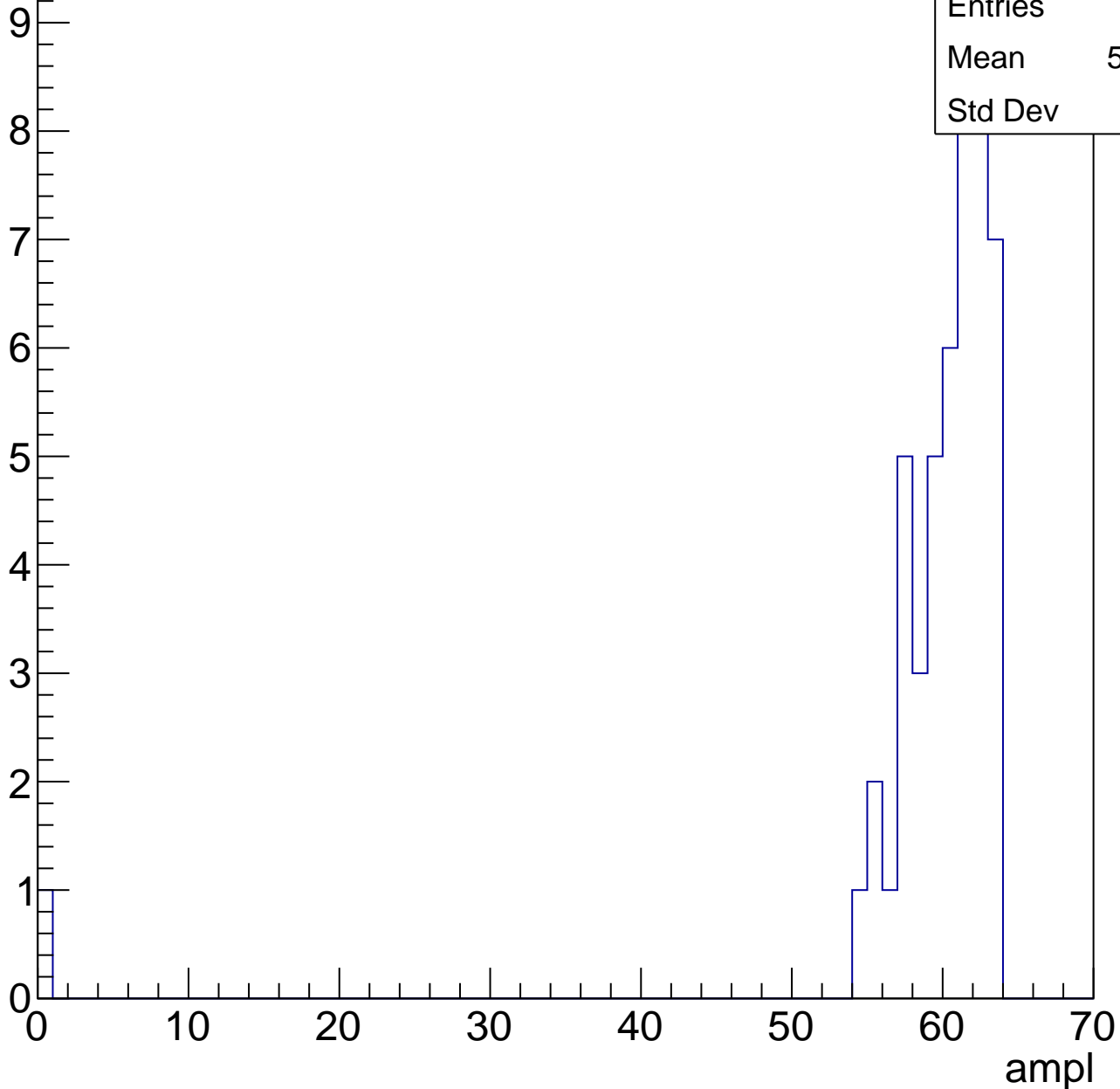


B0L102S, U12-ch62, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	58.82
Std Dev	8.81



B0L102S, U12-ch62, adc6

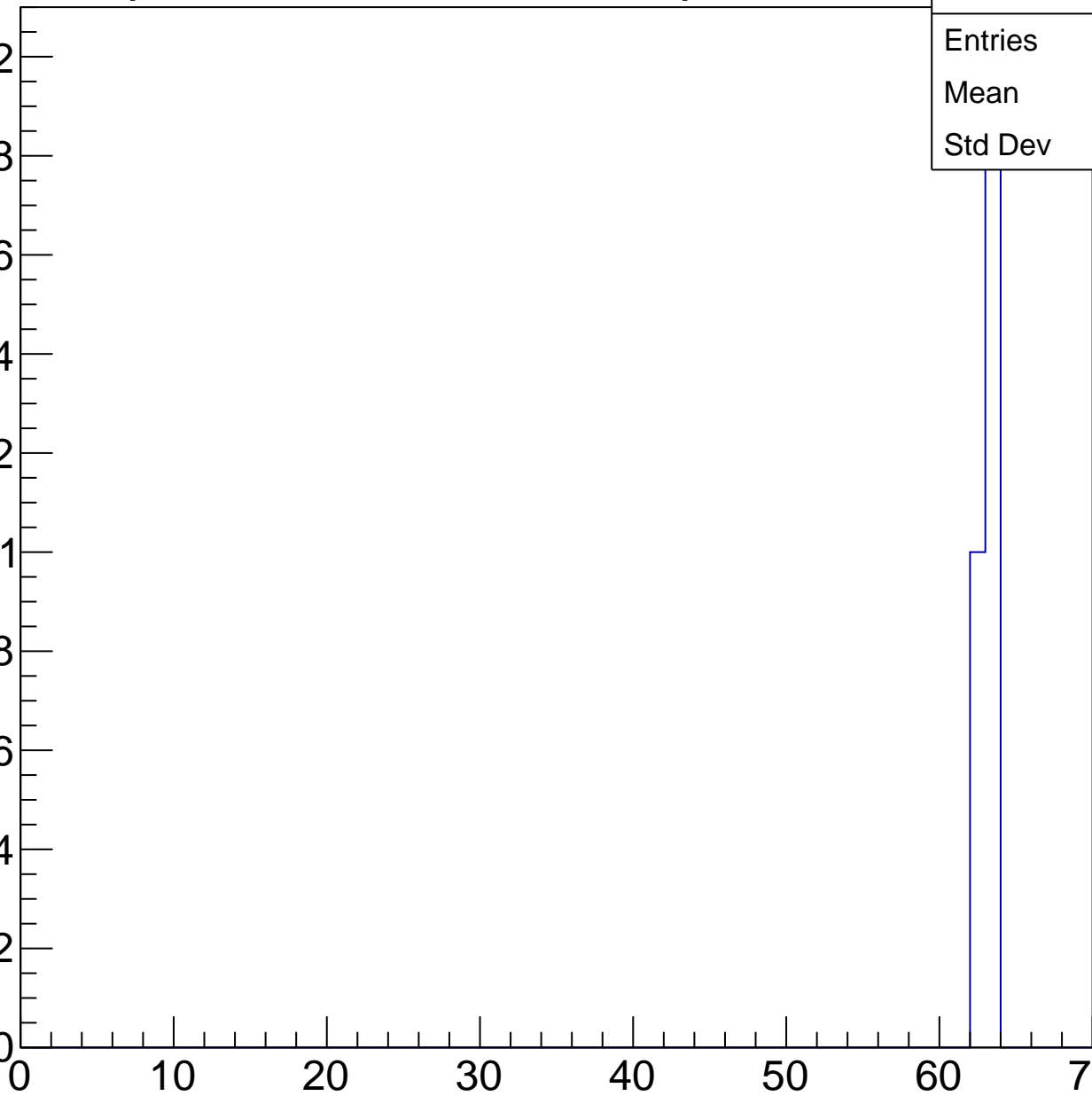
calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B0L102S, U12-ch62, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch63, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	29.37
Std Dev	4.965

Gaus mean : 29.6783

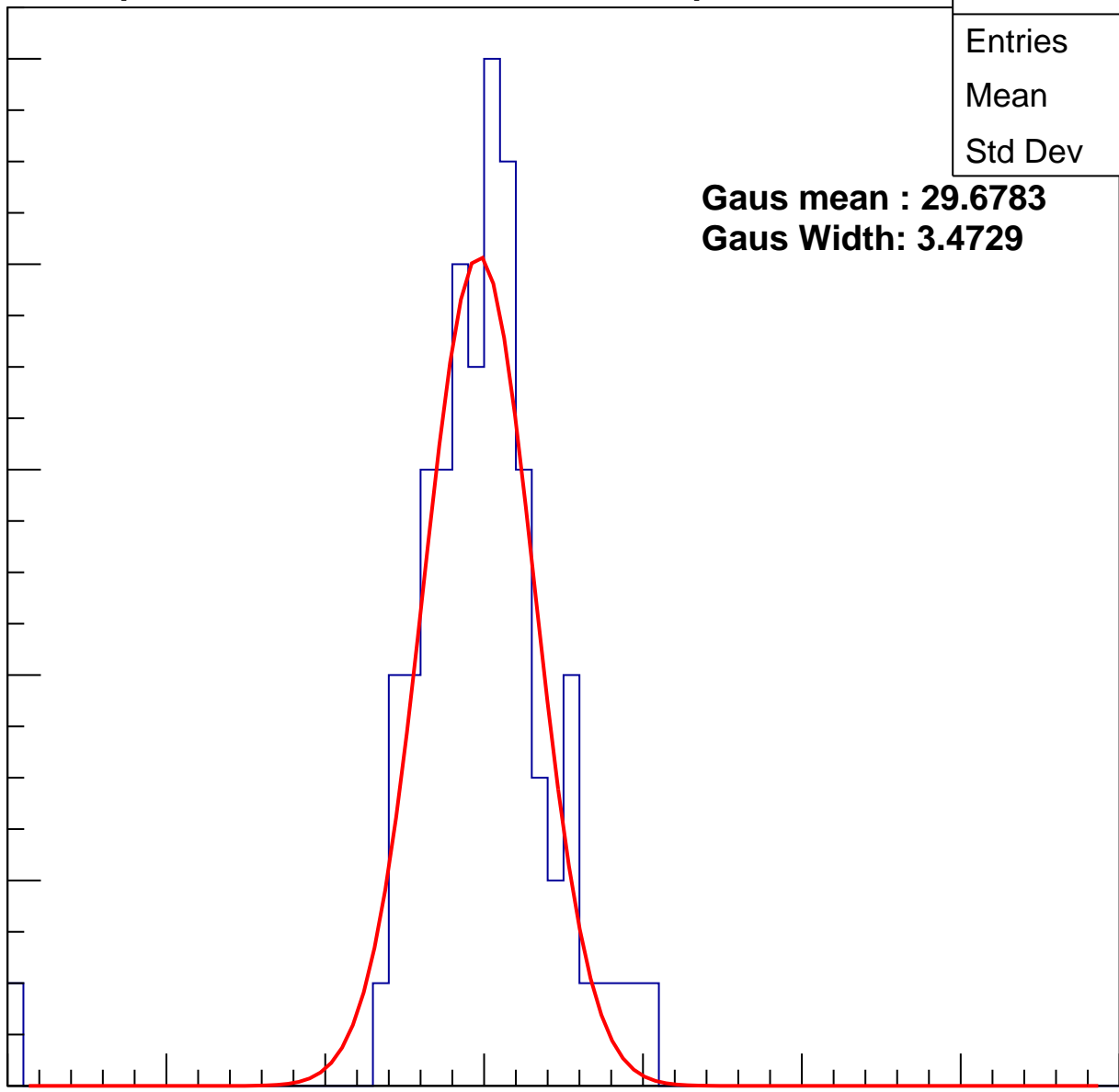
Gaus Width: 3.4729

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



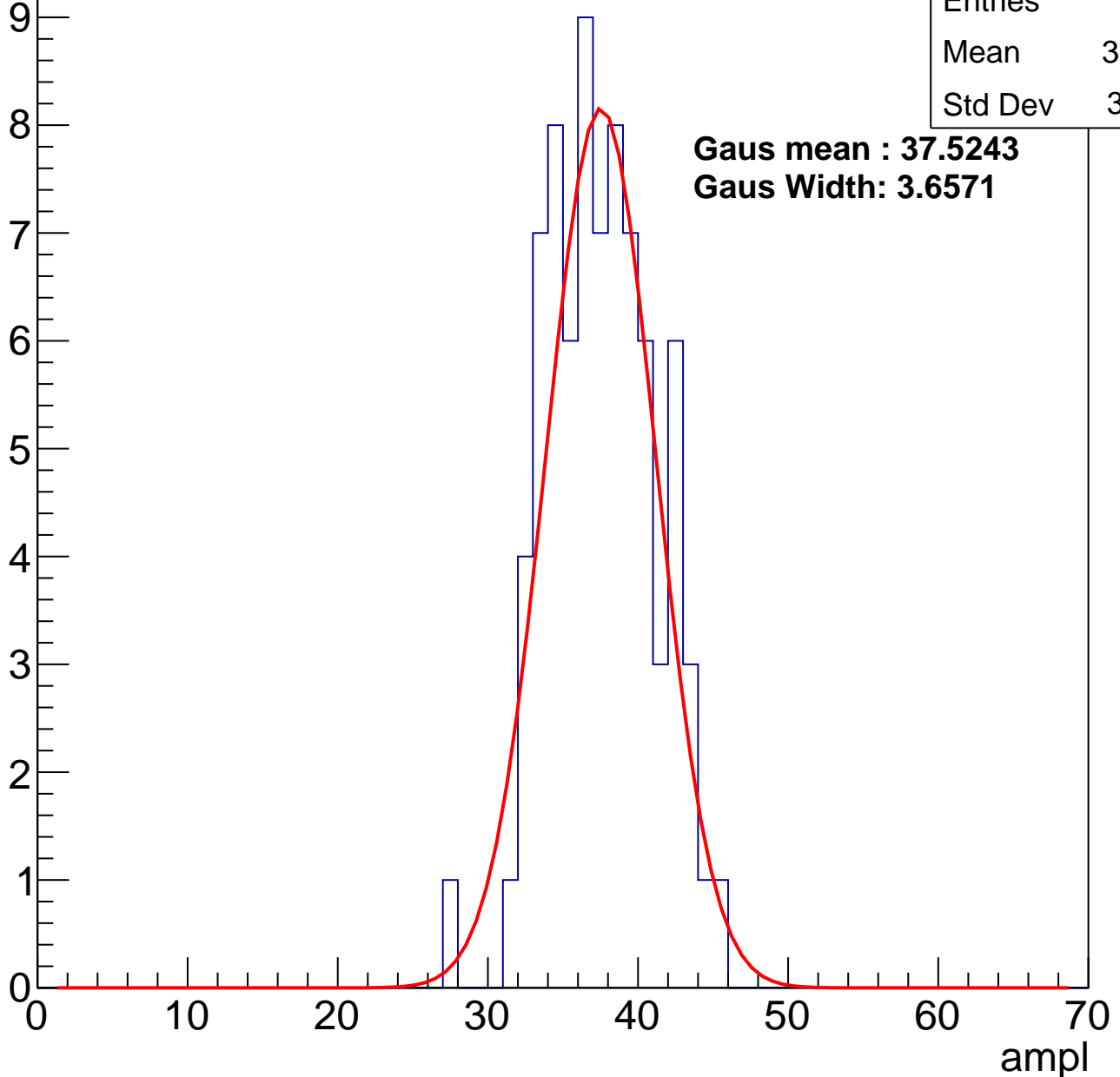
B0L102S, U12-ch63, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	37.08
Std Dev	3.511

Gaus mean : 37.5243
Gaus Width: 3.6571



B0L102S, U12-ch63, adc2

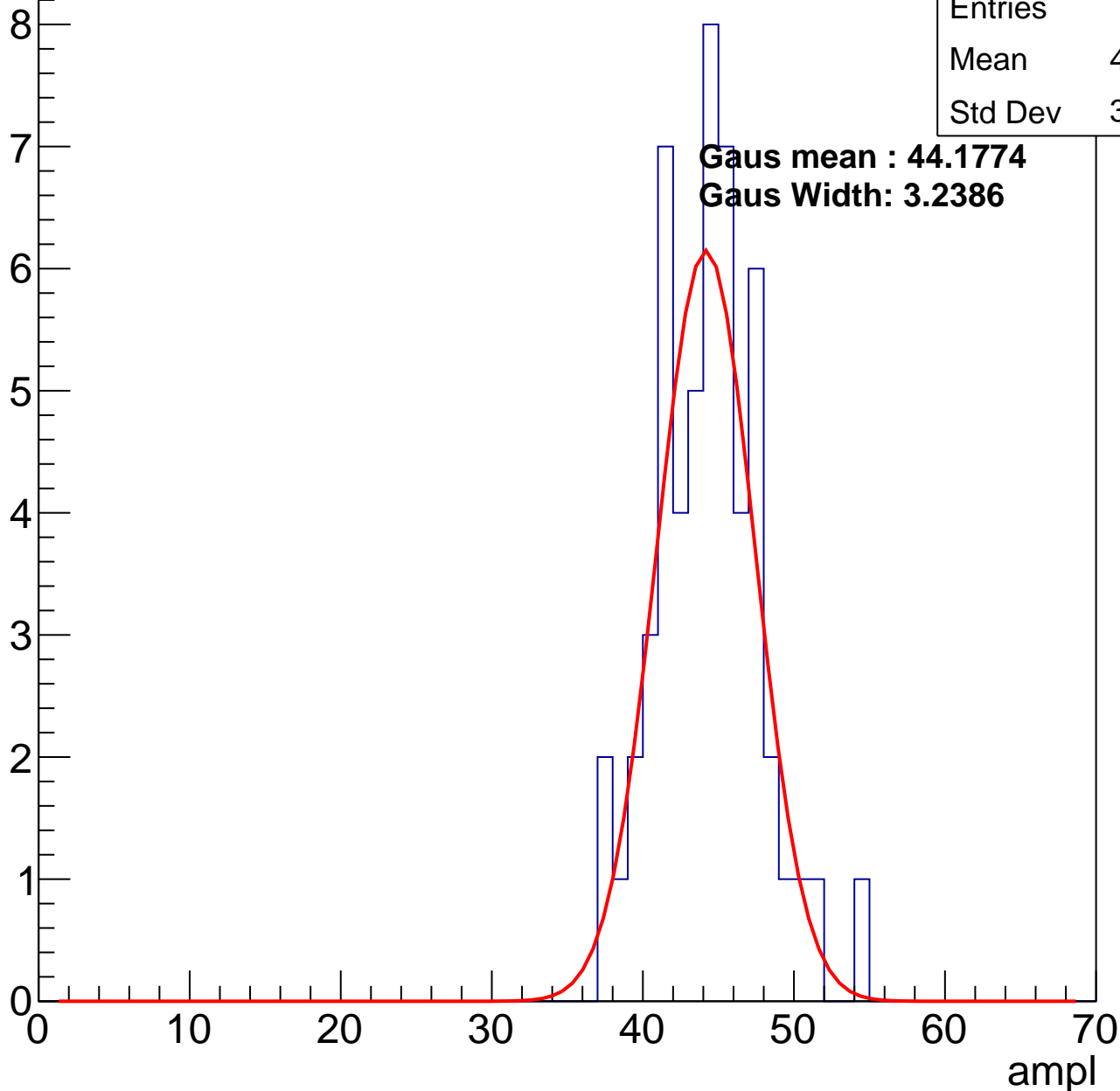
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	43.87
Std Dev	3.395

Gaus mean : 44.1774

Gaus Width: 3.2386

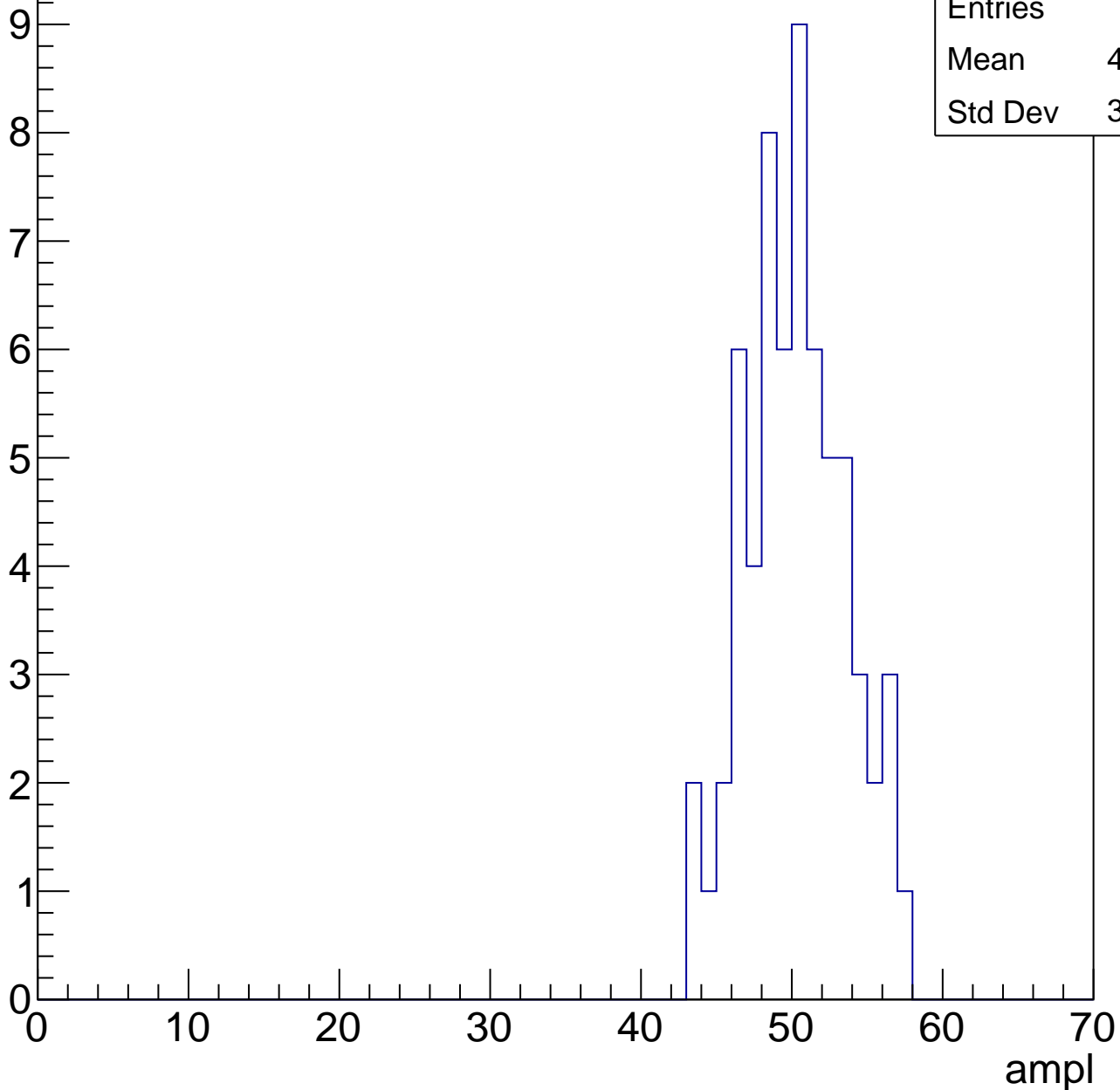


B0L102S, U12-ch63, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.84
Std Dev	3.282

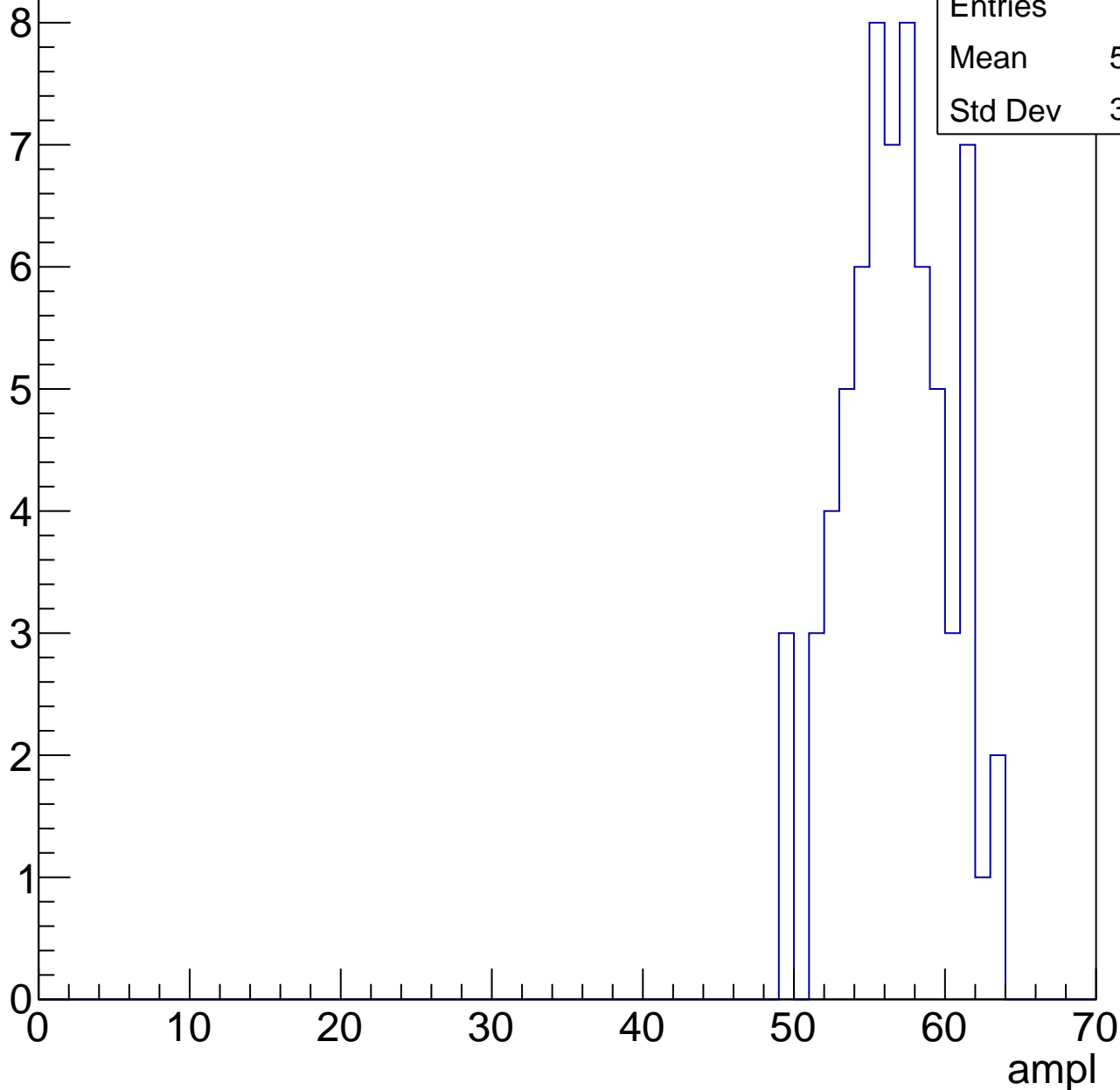


B0L102S, U12-ch63, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	56.22
Std Dev	3.416

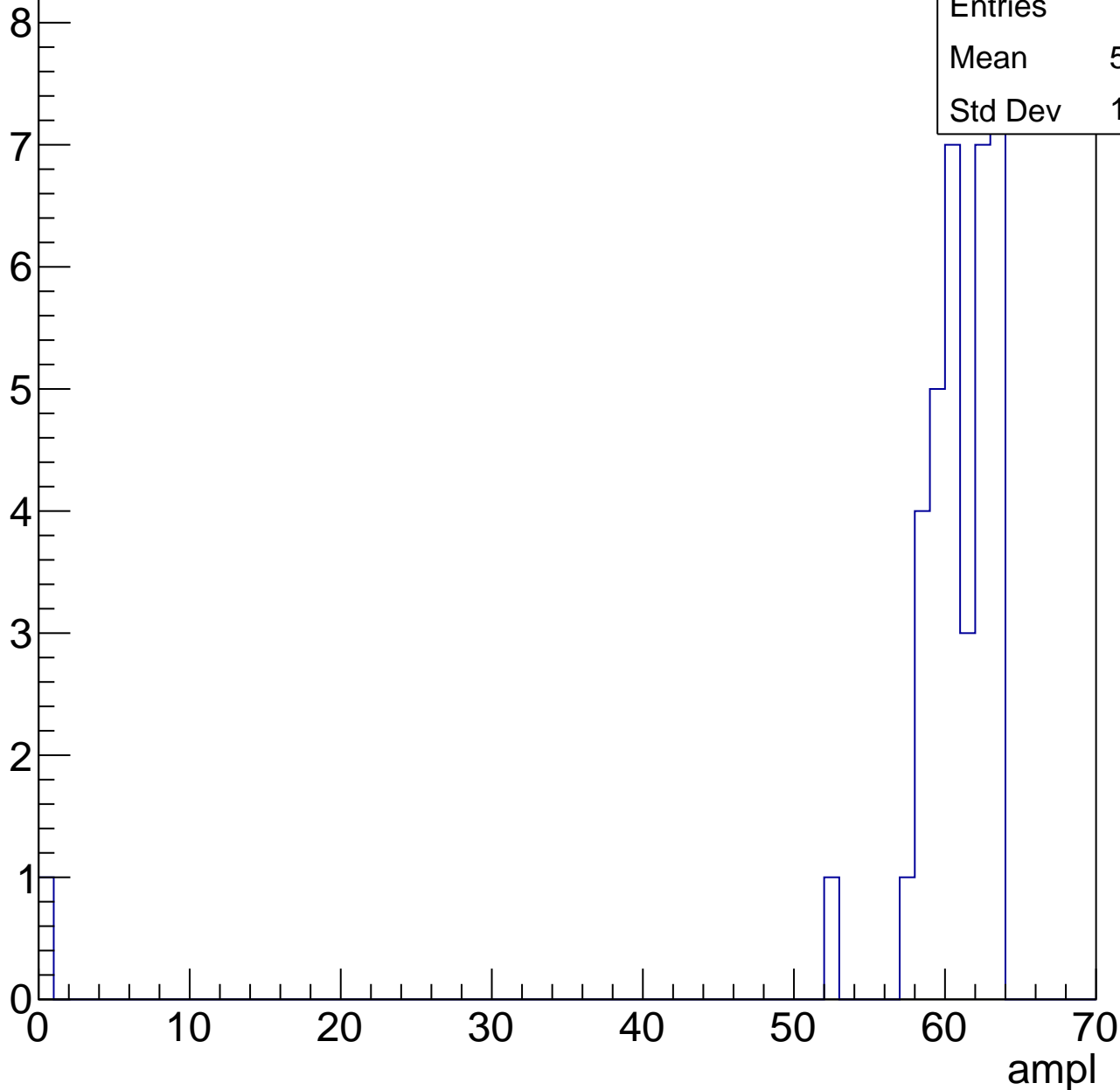


B0L102S, U12-ch63, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	37
Mean	58.84
Std Dev	10.06



B0L102S, U12-ch63, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch63, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch64, adc0

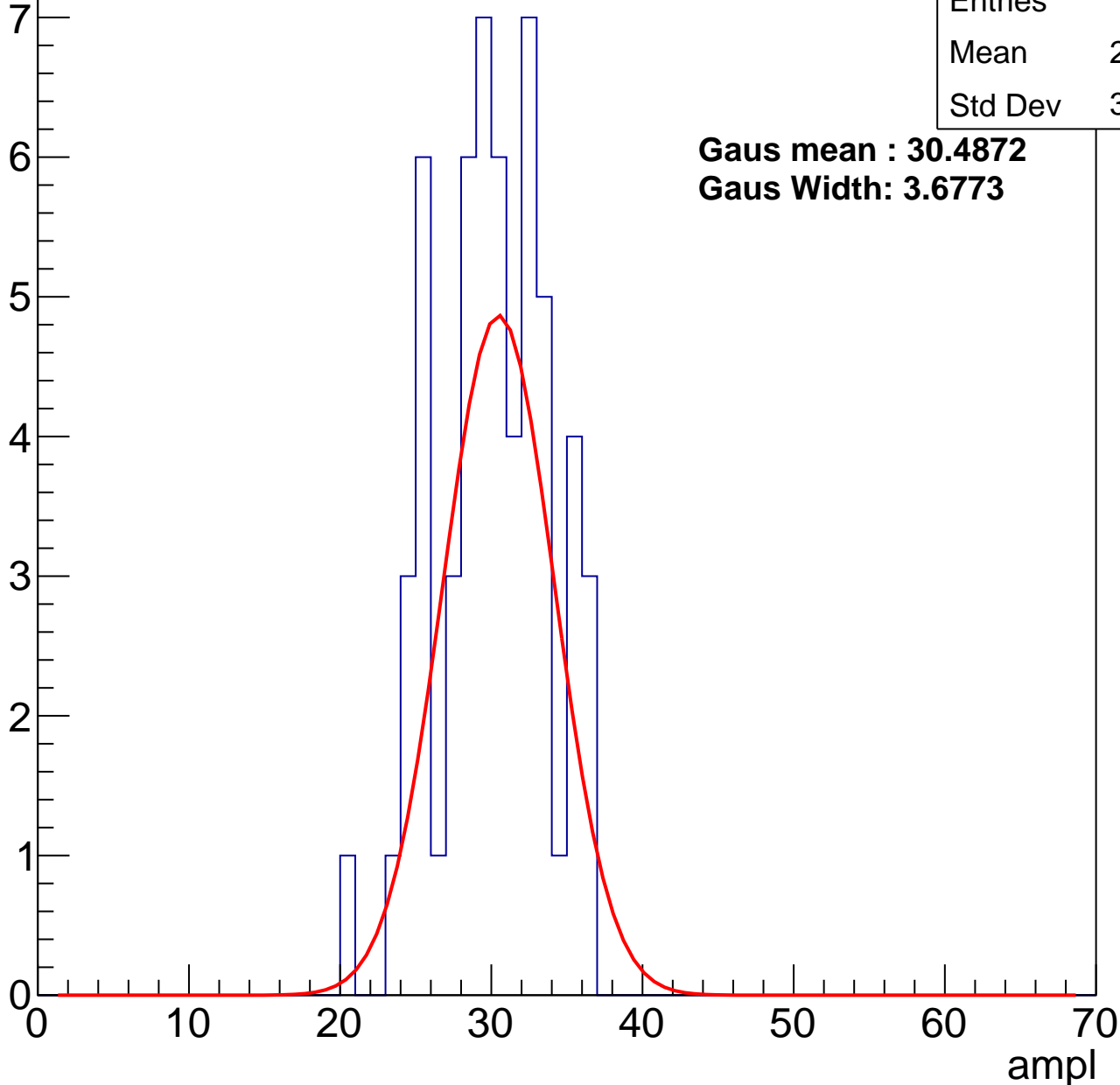
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	29.62
Std Dev	3.652

Gaus mean : 30.4872

Gaus Width: 3.6773



B0L102S, U12-ch64, adc1

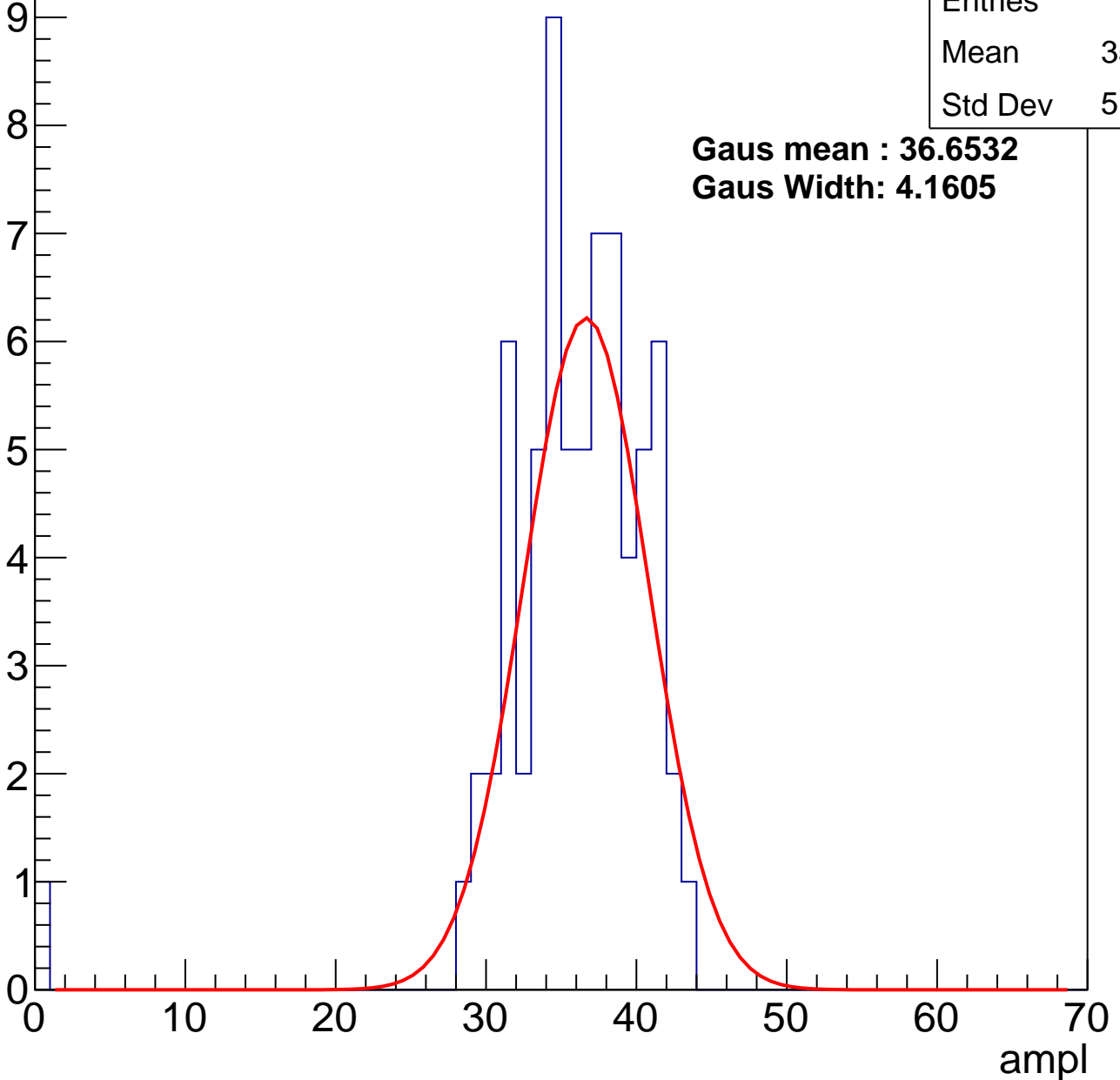
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	35.37
Std Dev	5.596

Gaus mean : 36.6532

Gaus Width: 4.1605



B0L102S, U12-ch64, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	43.5
Std Dev	3.832

Gaus mean : 42.8184

Gaus Width: 4.3398

10

8

6

4

2

0

0

10

20

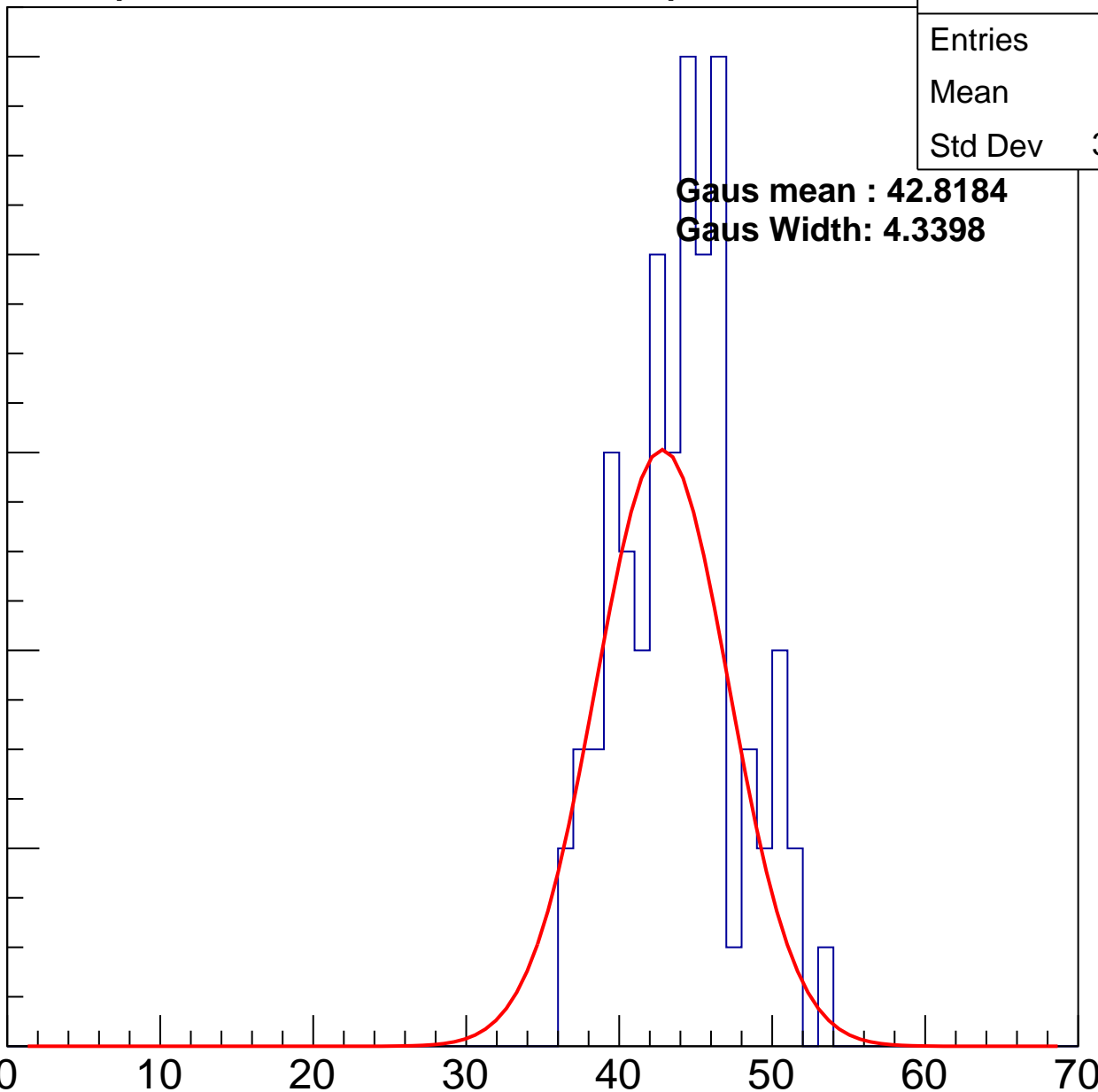
30

40

50

60

ampl

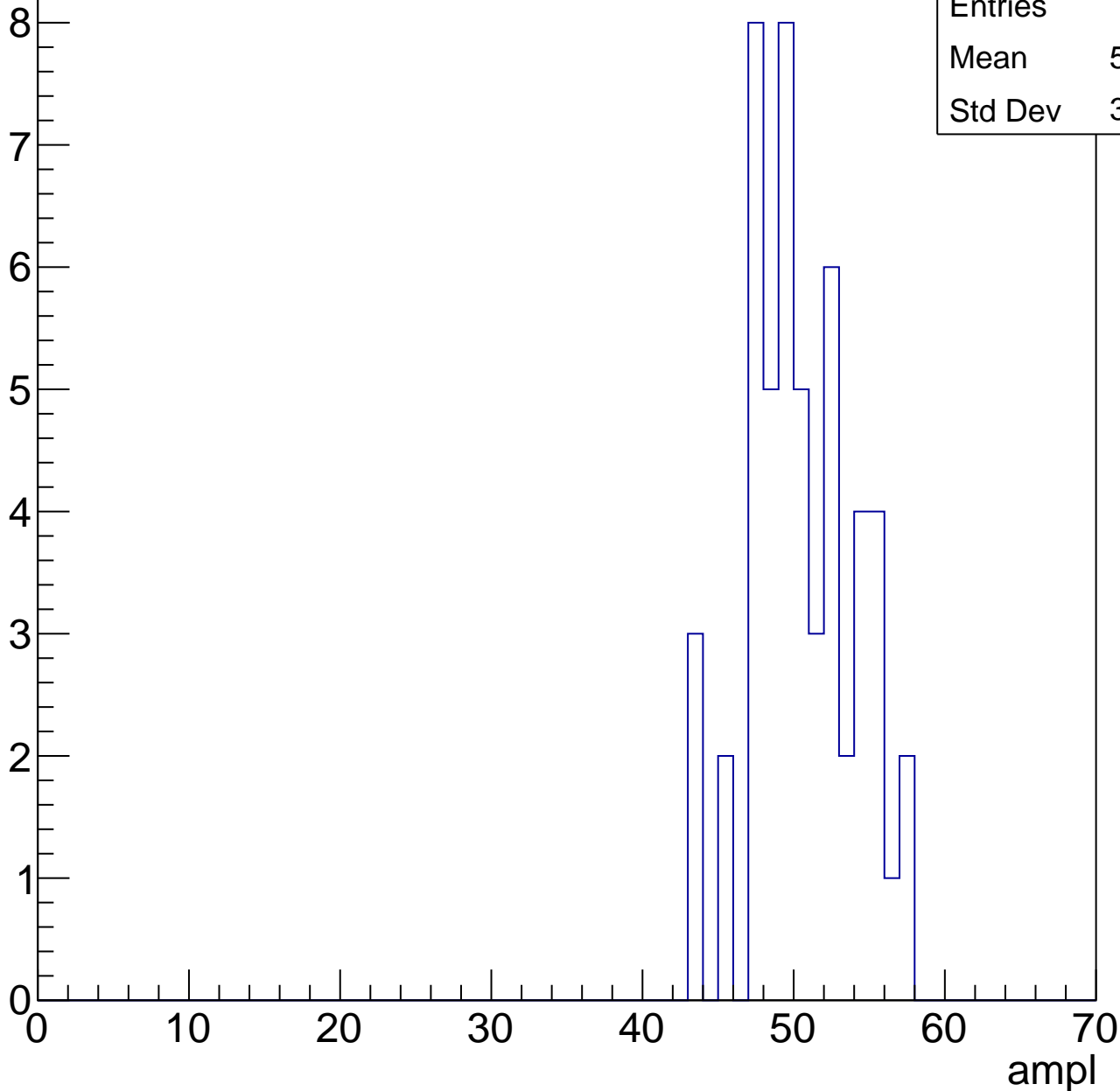


B0L102S, U12-ch64, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	50.08
Std Dev	3.474

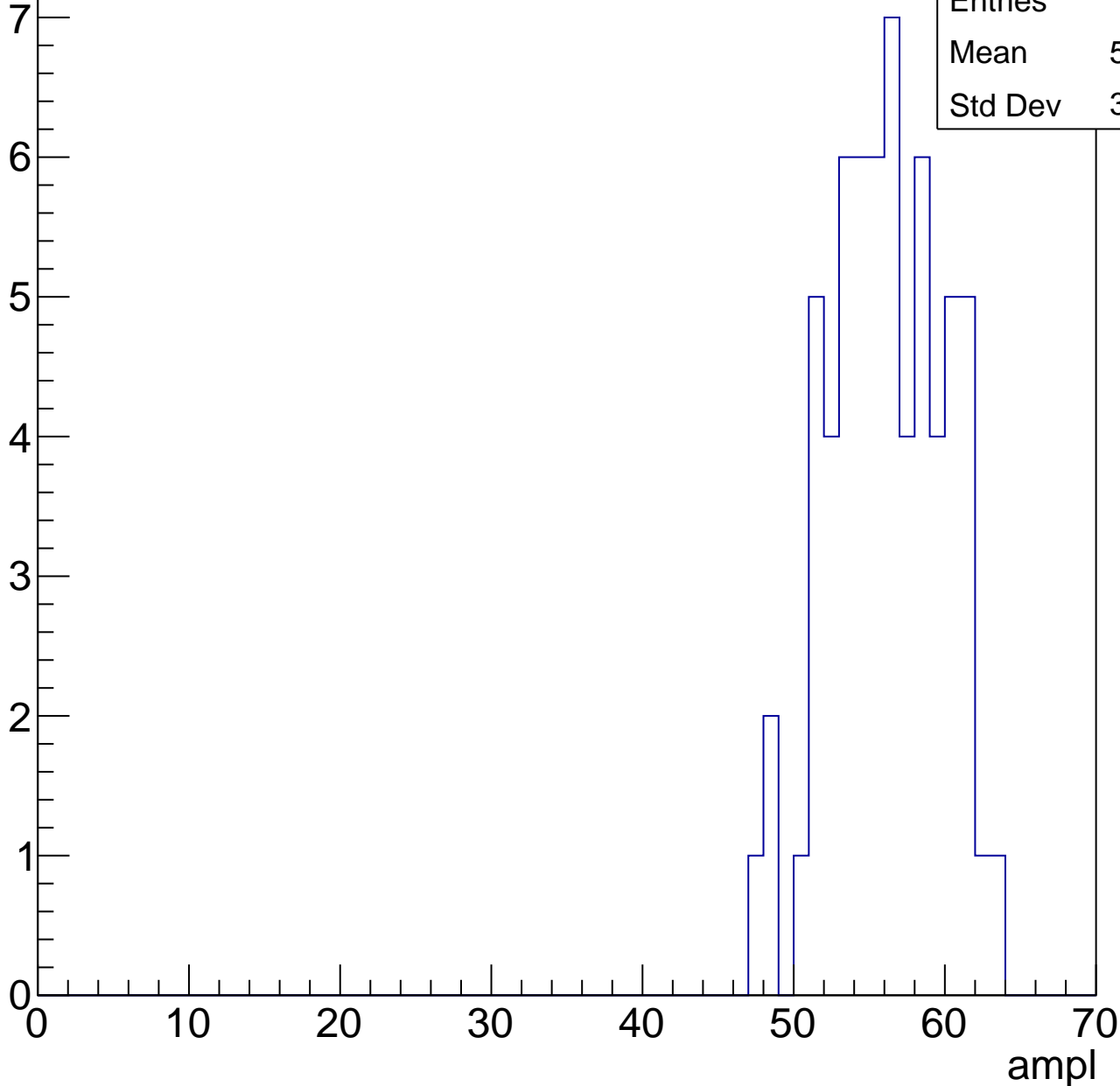


B0L102S, U12-ch64, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	55.66
Std Dev	3.675

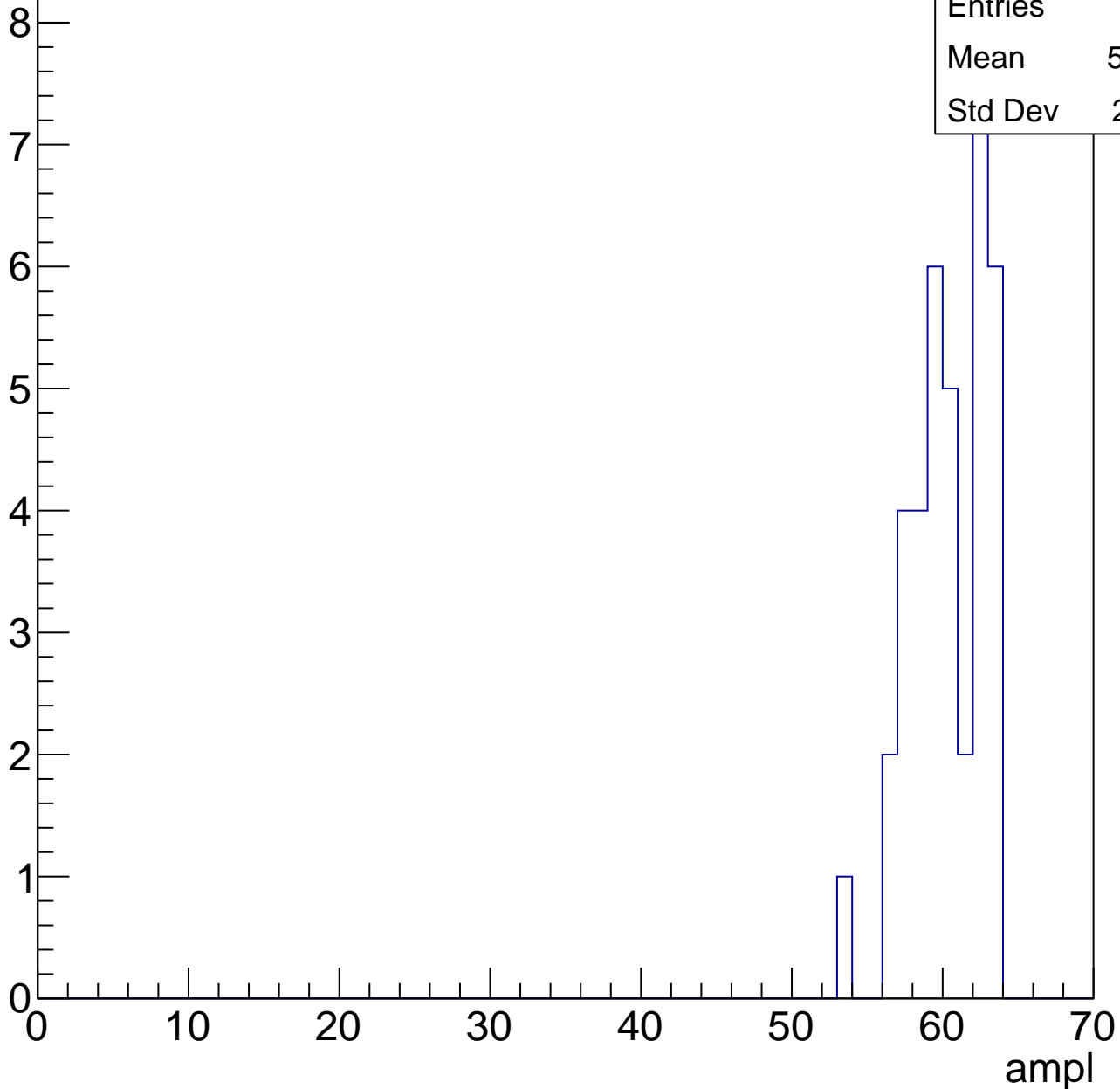


B0L102S, U12-ch64, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

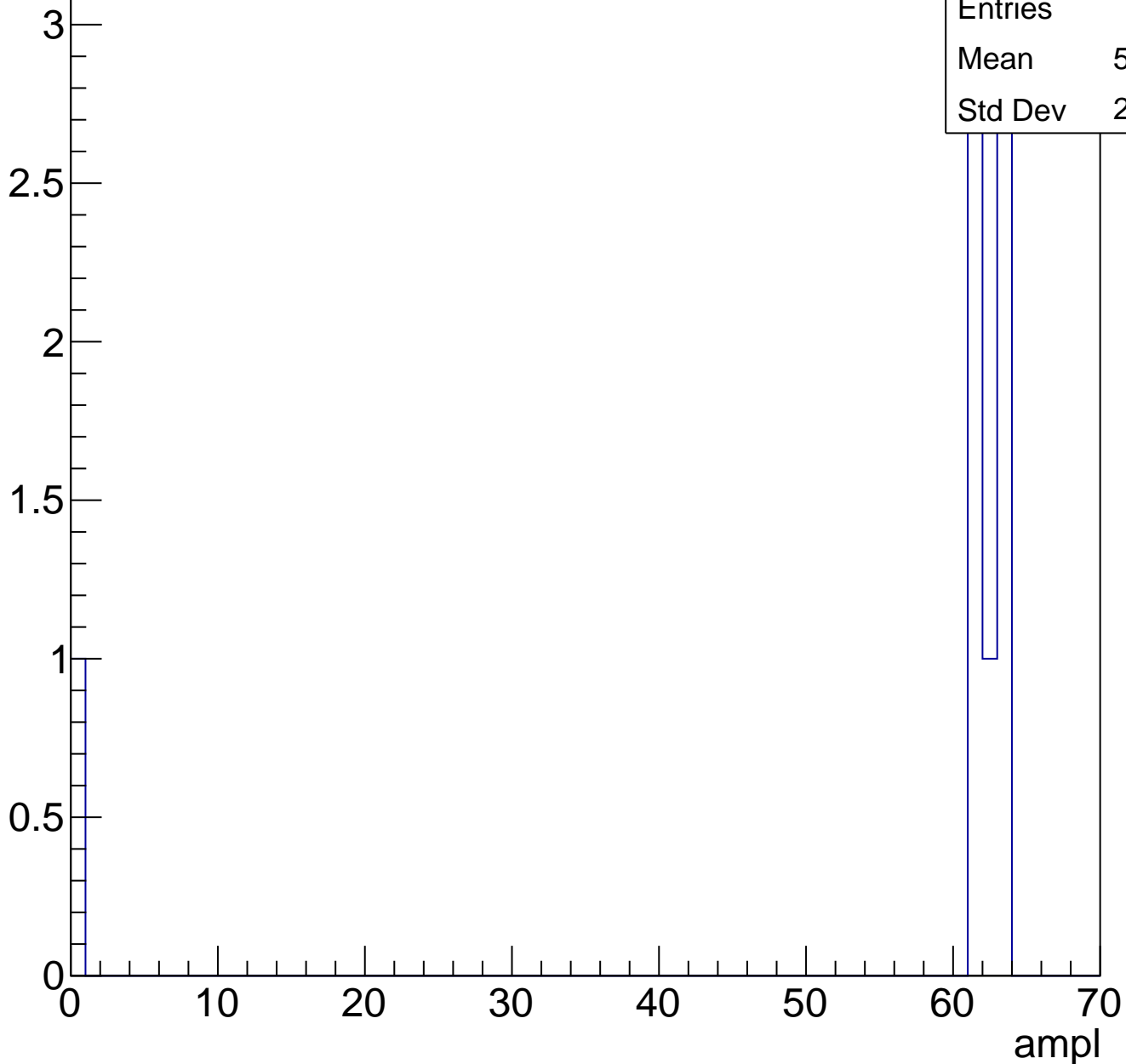
Entries	38
Mean	59.87
Std Dev	2.441



B0L102S, U12-ch64, adc6

calib_packv5_042523_0143.root, FC#12, port B1

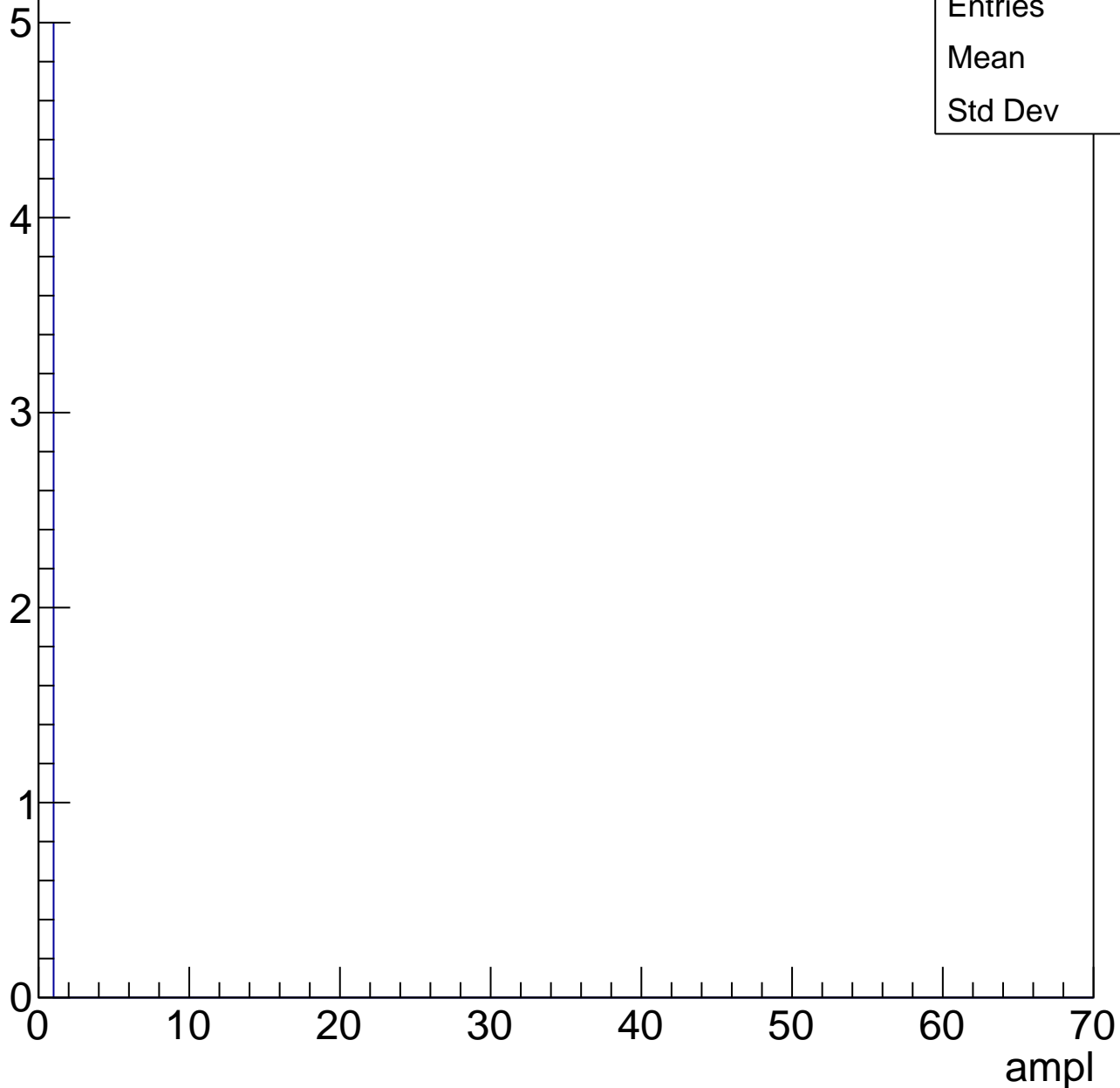
Entry



B0L102S, U12-ch64, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	0
Std Dev	0

B0L102S, U12-ch65, adc0

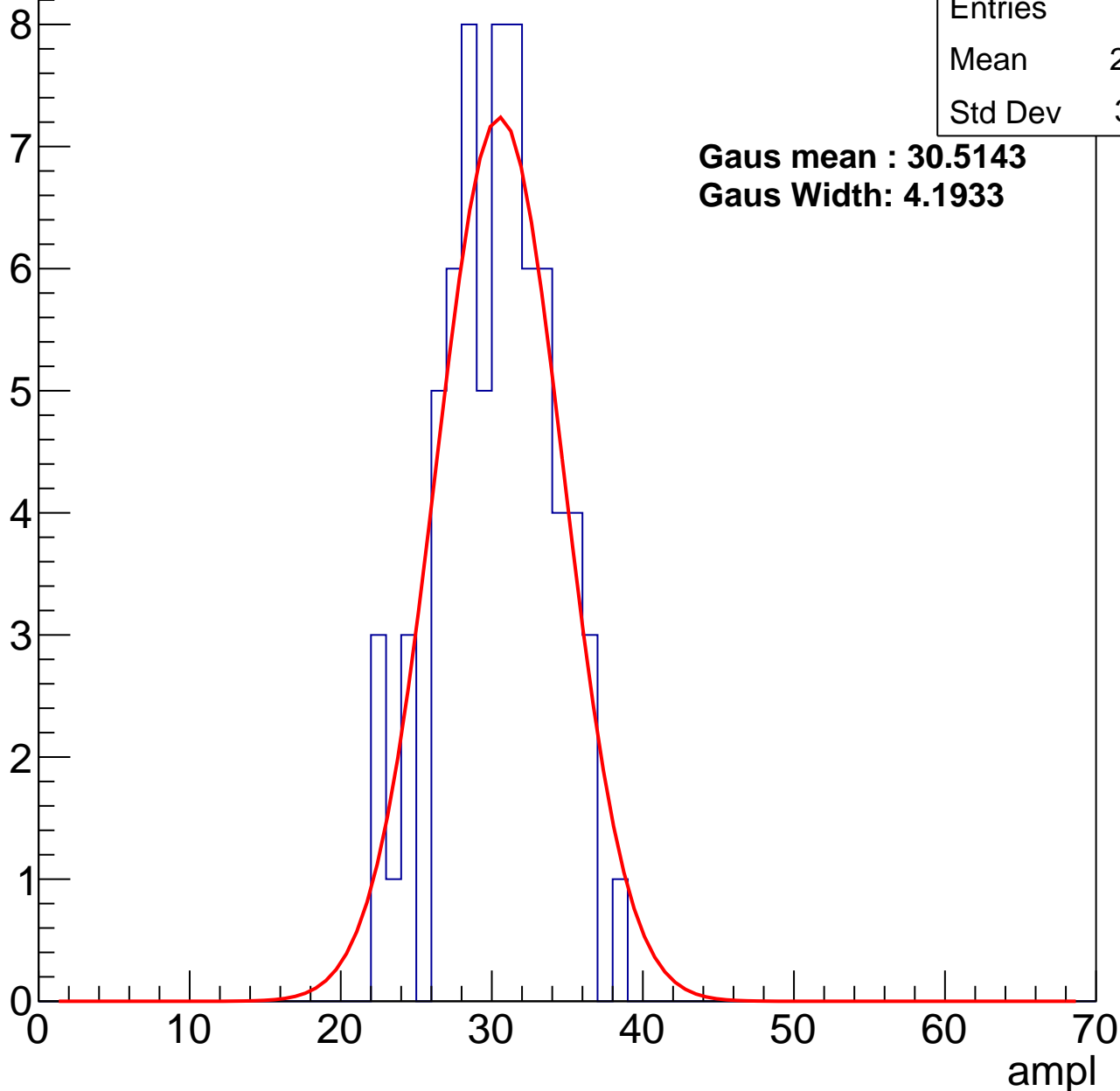
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.89
Std Dev	3.641

Gaus mean : 30.5143

Gaus Width: 4.1933



B0L102S, U12-ch65, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	36.92
Std Dev	3.498

Gaus mean : 37.4182

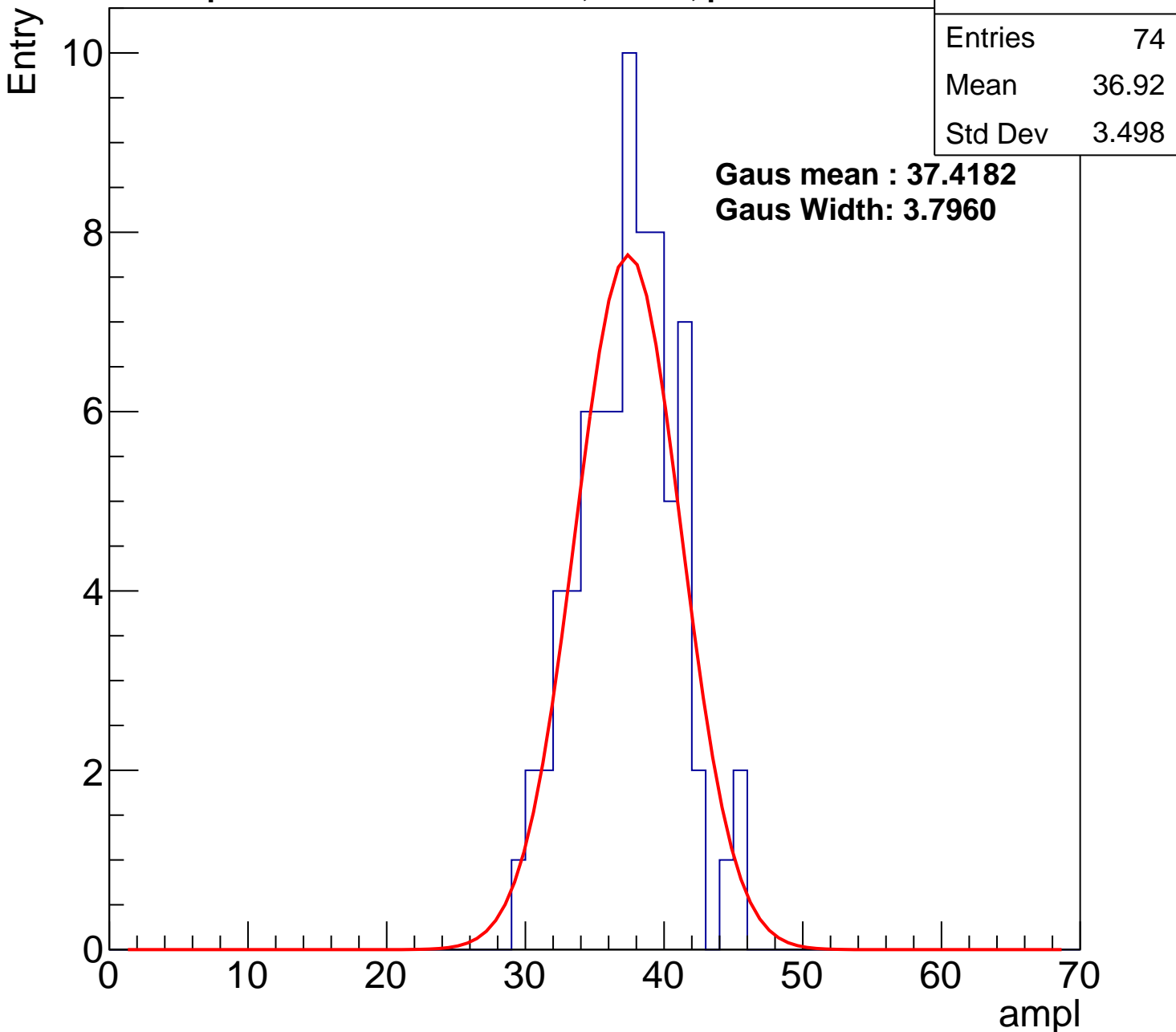
Gaus Width: 3.7960

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch65, adc2

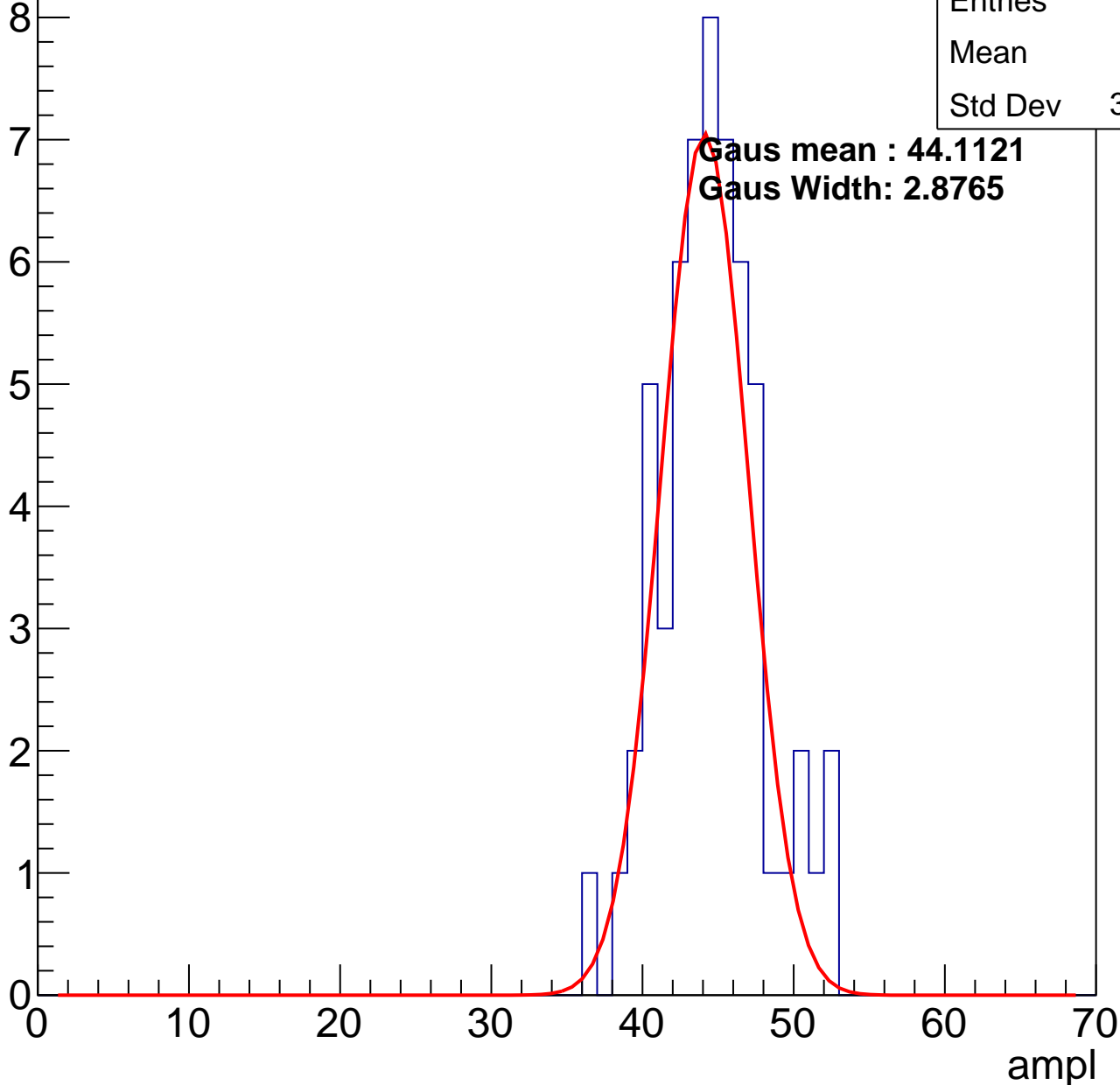
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	44.1
Std Dev	3.356

Gaus mean : 44.1121

Gaus Width: 2.8765

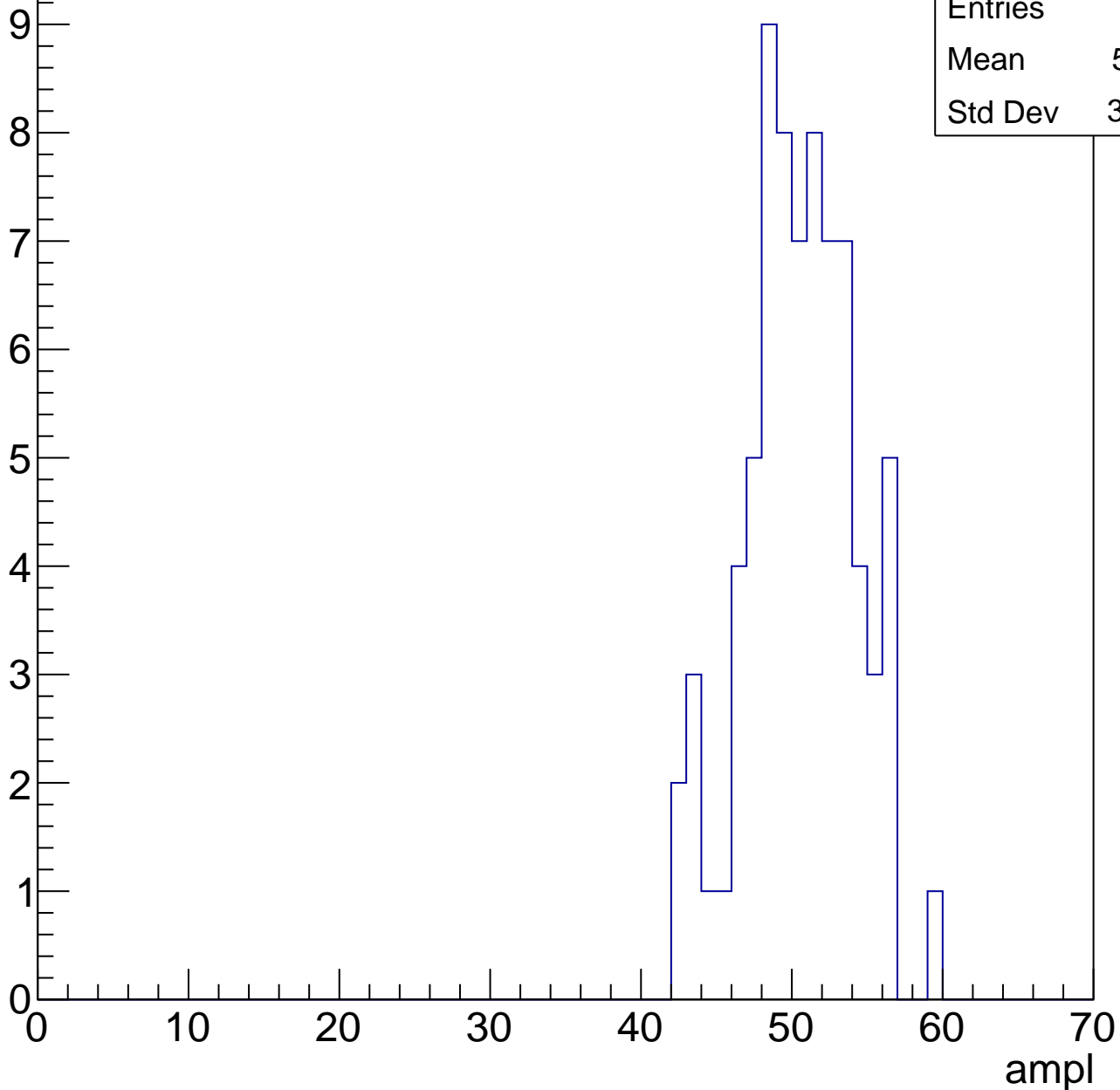


B0L102S, U12-ch65, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	50.11
Std Dev	3.628

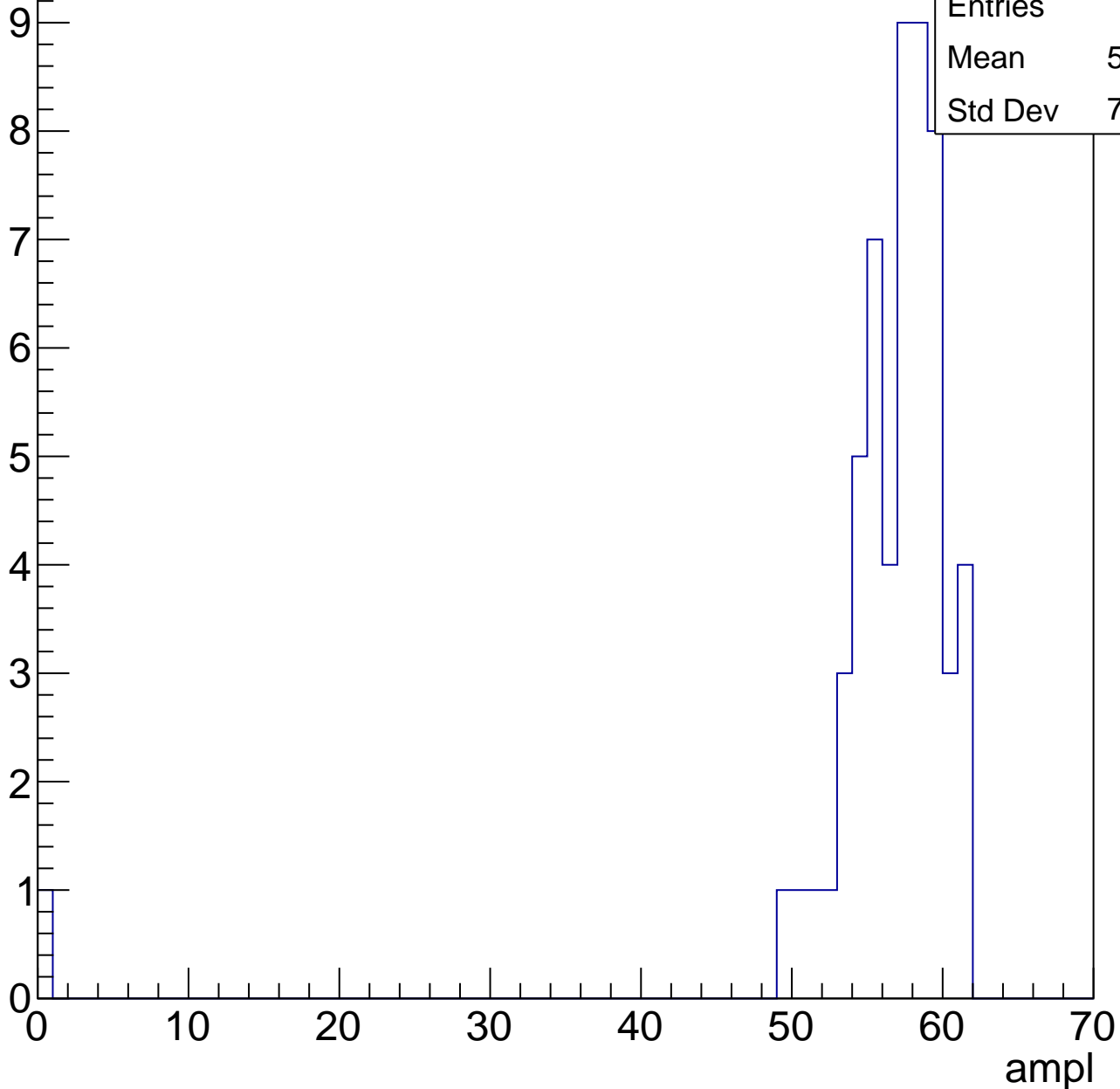


B0L102S, U12-ch65, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55.63
Std Dev	7.918

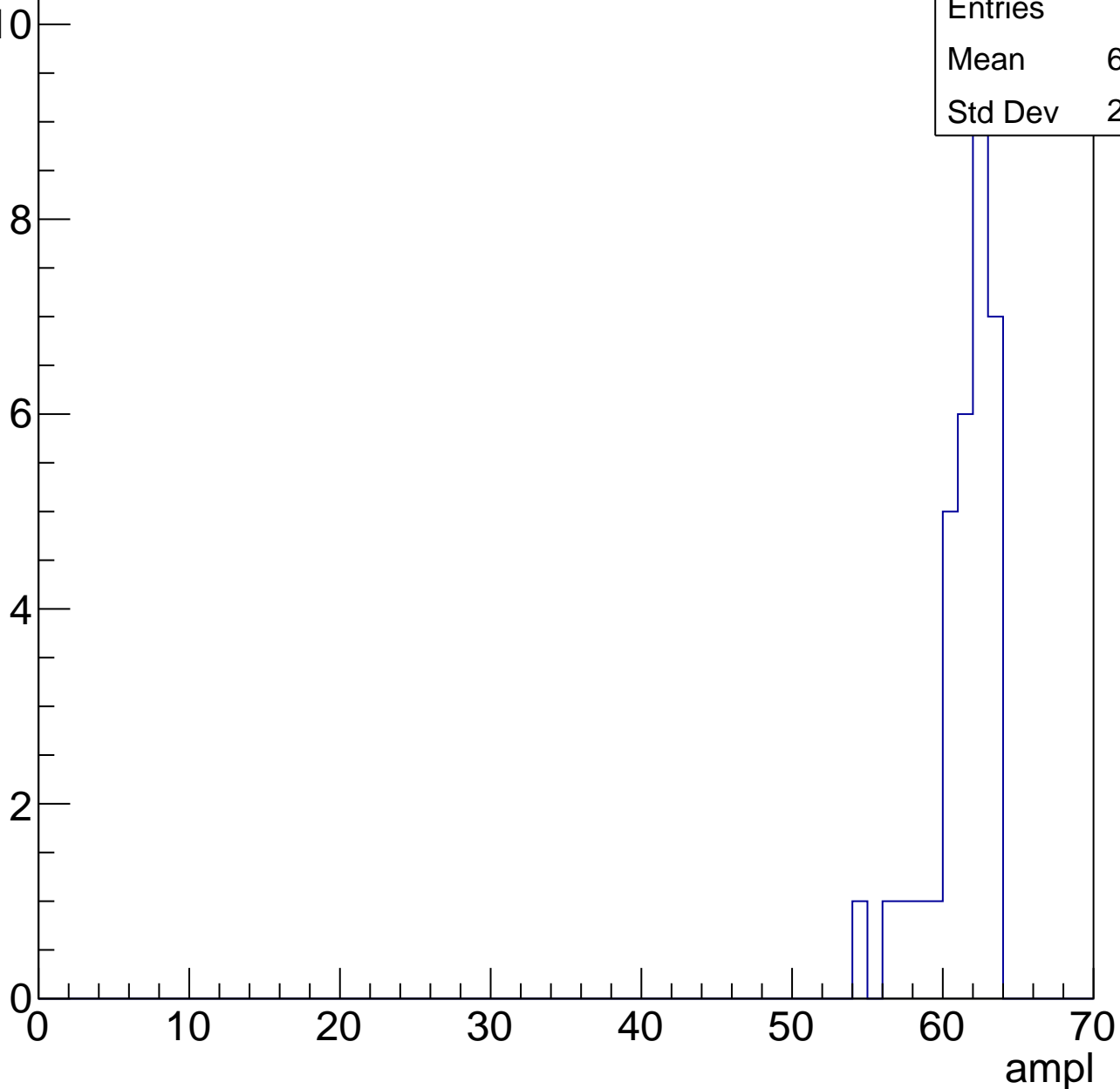


B0L102S, U12-ch65, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

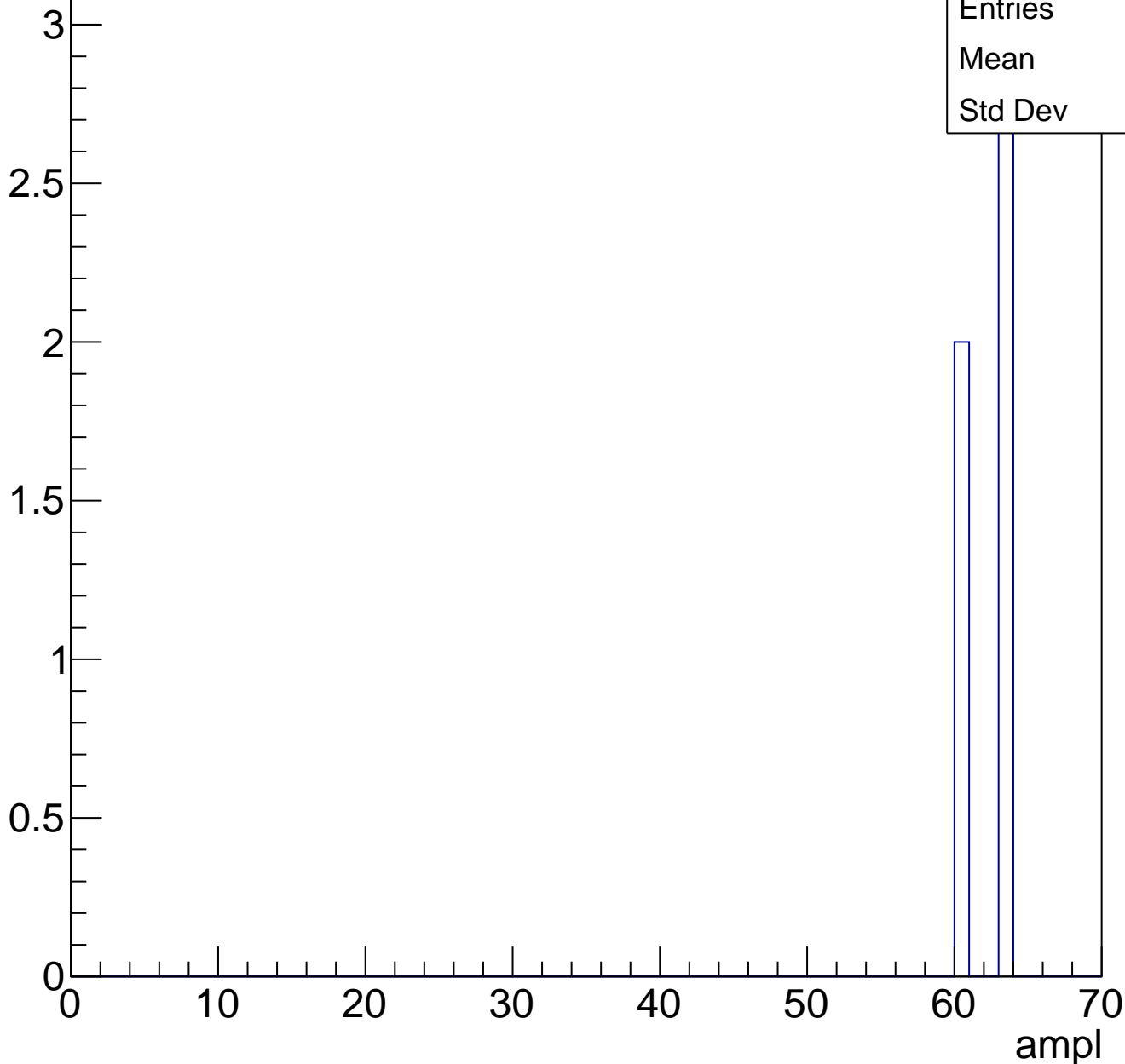
Entries	33
Mean	60.94
Std Dev	2.103



B0L102S, U12-ch65, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch65, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch66, adc0

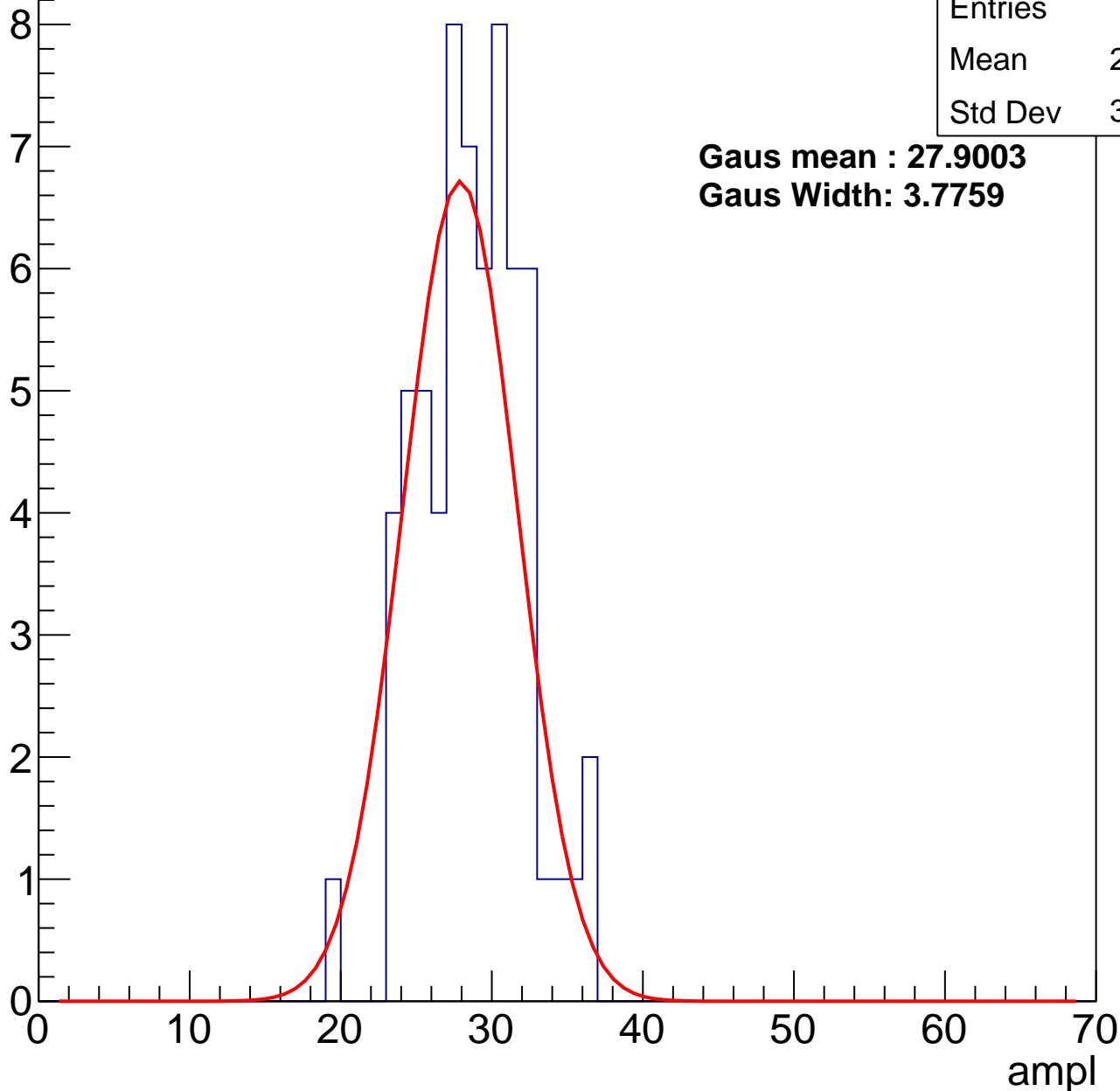
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	28.28
Std Dev	3.399

Gaus mean : 27.9003

Gaus Width: 3.7759



B0L102S, U12-ch66, adc1

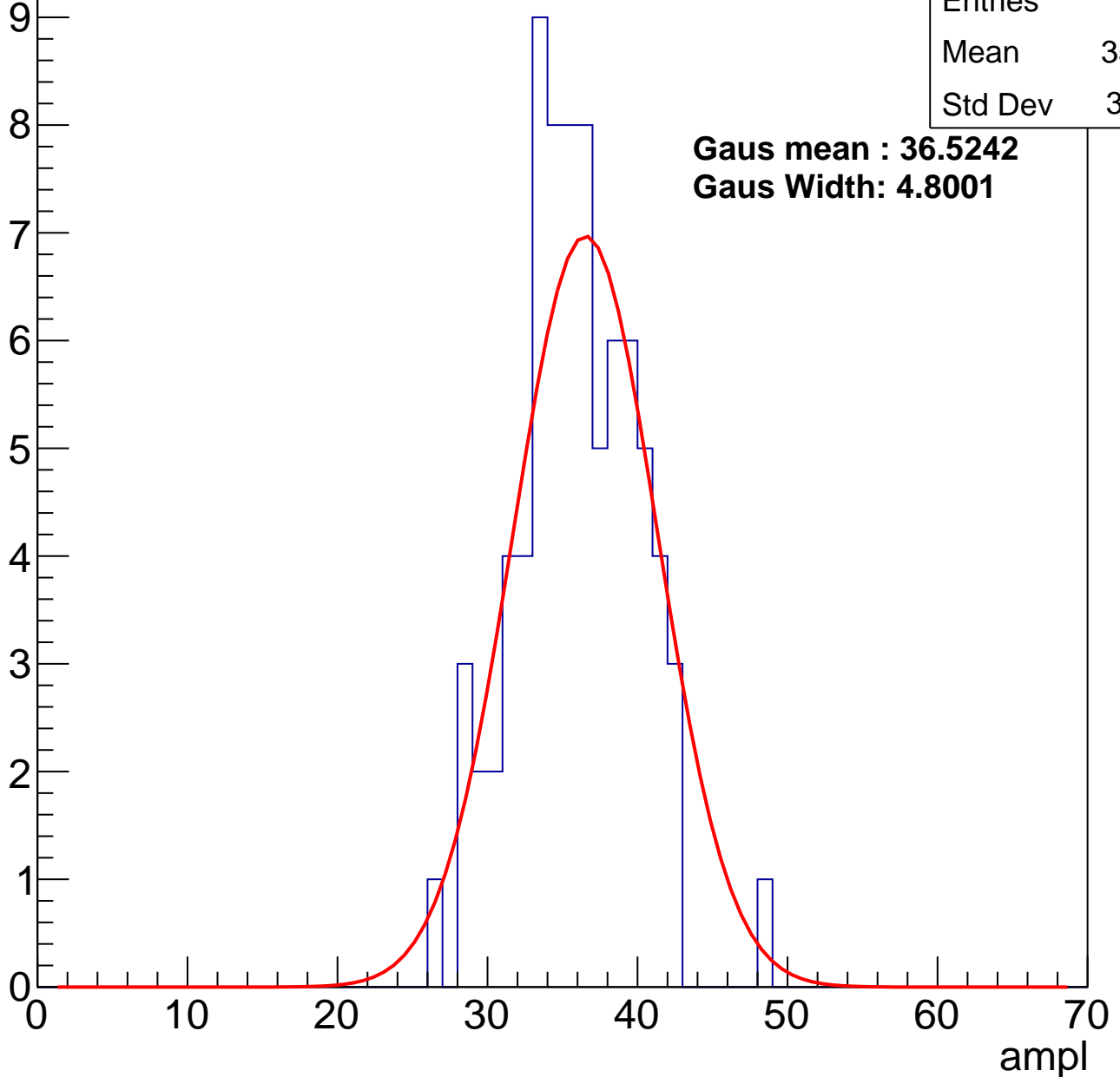
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.47
Std Dev	3.961

Gaus mean : 36.5242

Gaus Width: 4.8001



B0L102S, U12-ch66, adc2

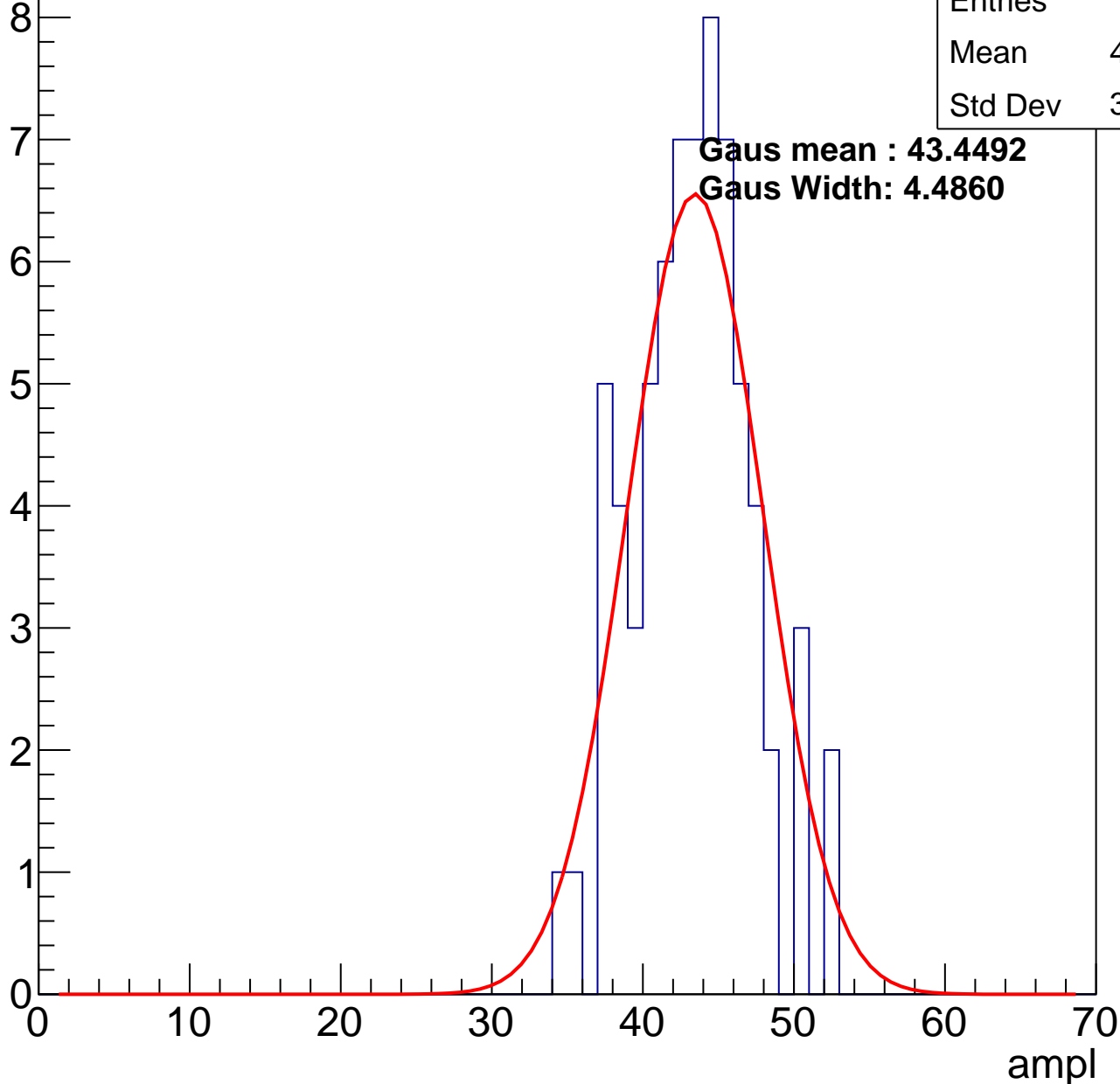
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	42.84
Std Dev	3.875

Gaus mean : 43.4492

Gaus Width: 4.4860

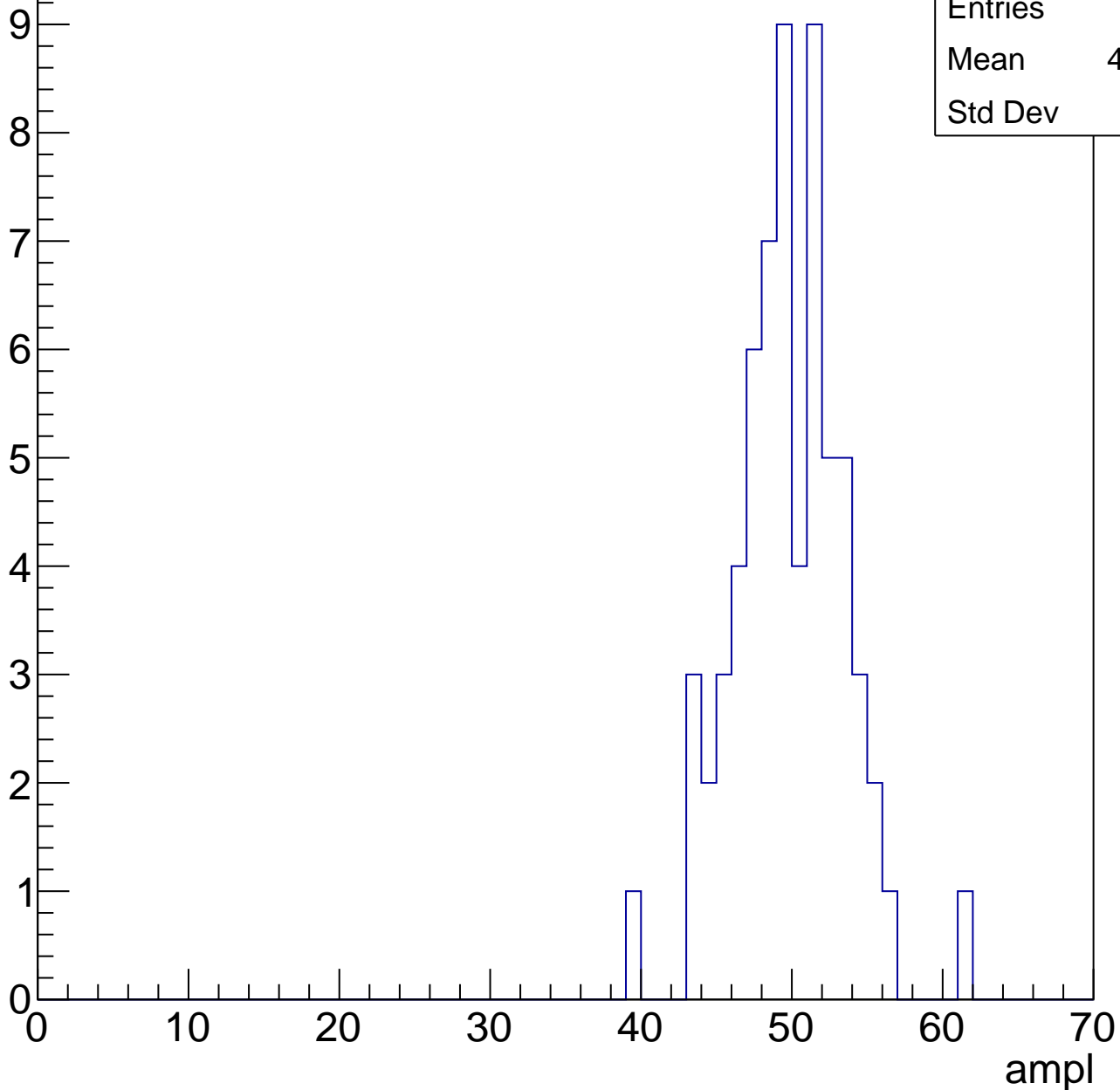


B0L102S, U12-ch66, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.34
Std Dev	3.66

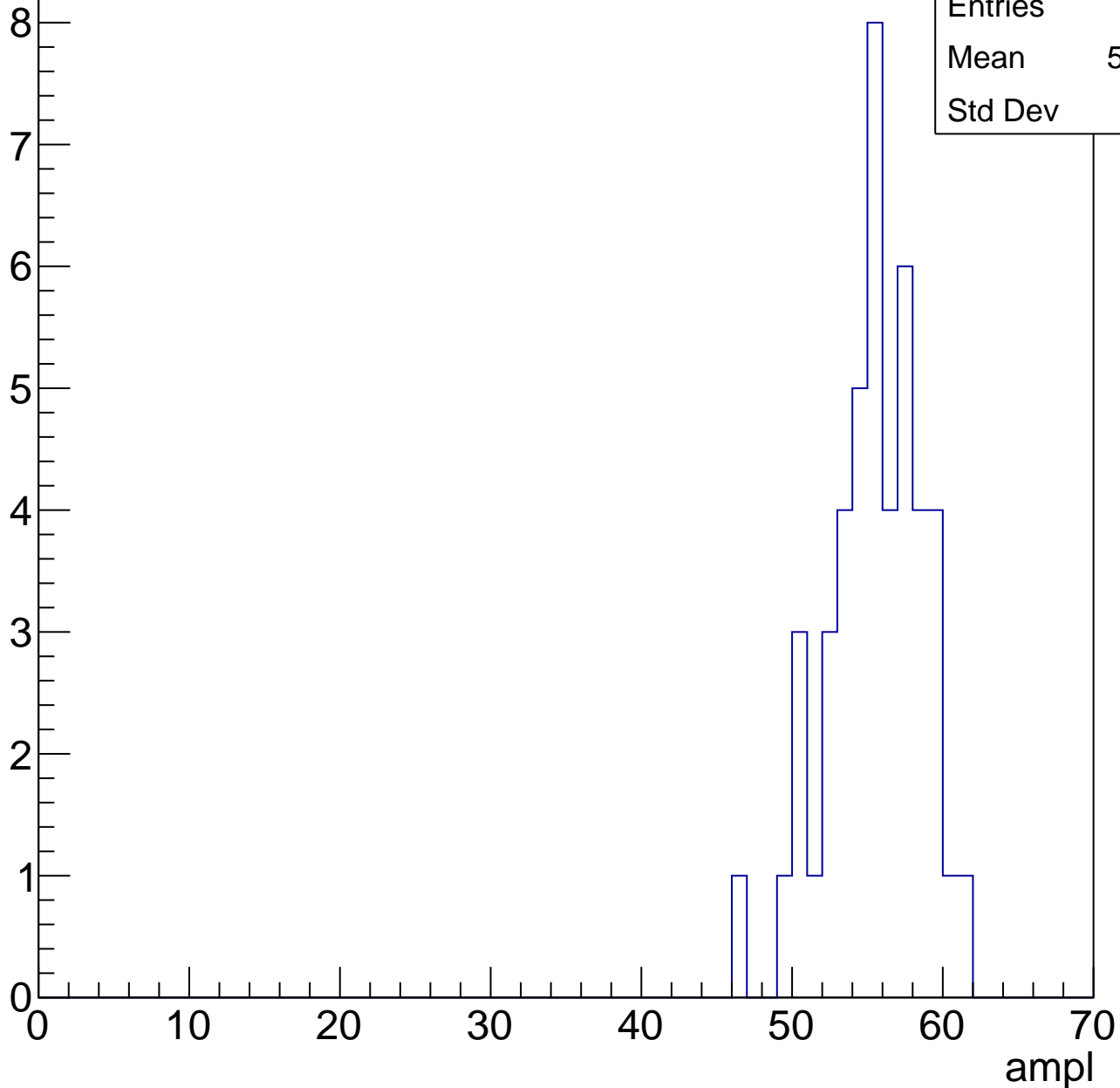


B0L102S, U12-ch66, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	54.98
Std Dev	3.11

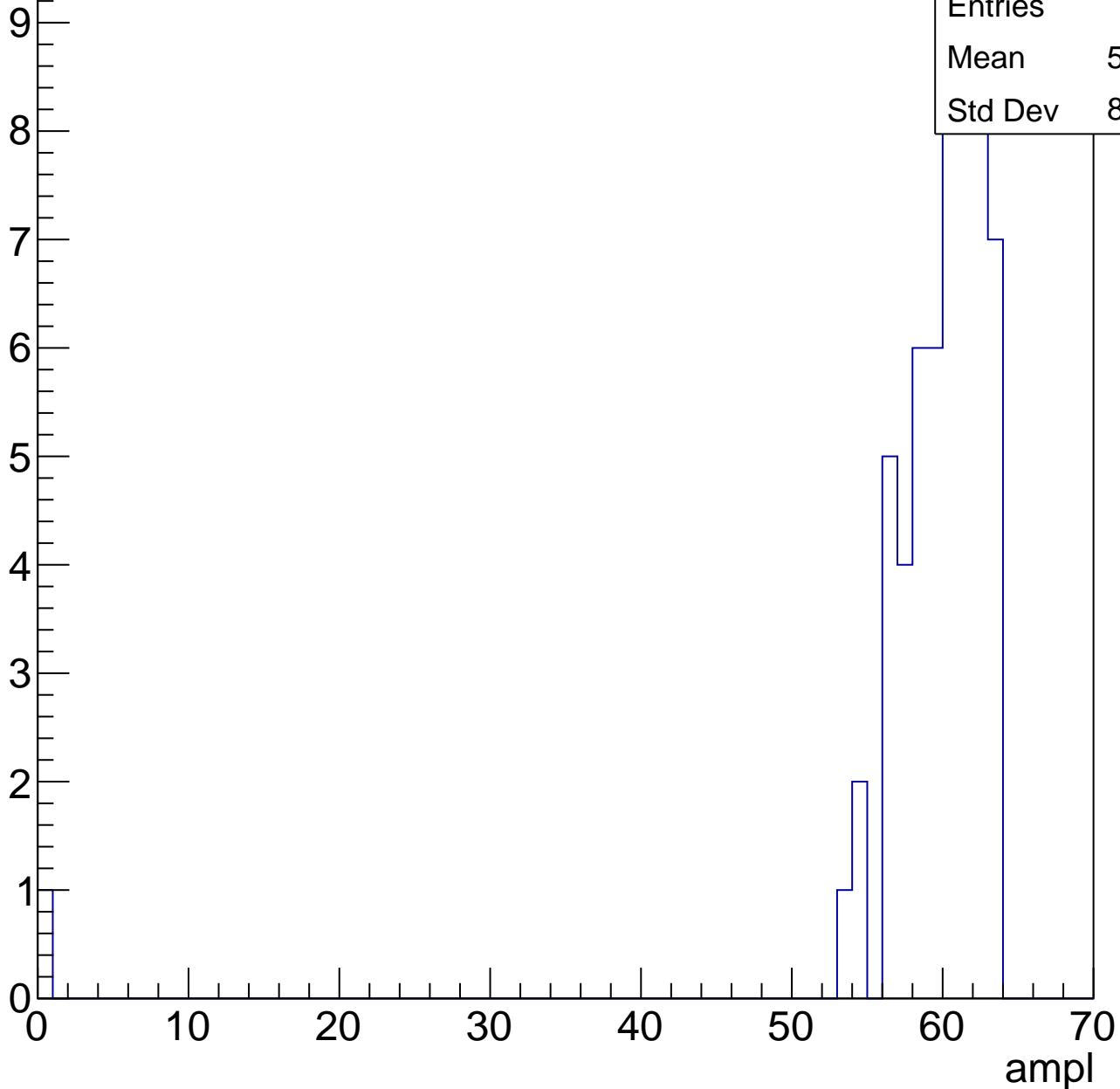


B0L102S, U12-ch66, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	58.59
Std Dev	8.156



B0L102S, U12-ch66, adc6

calib_packv5_042523_0143.root, FC#12, port B1

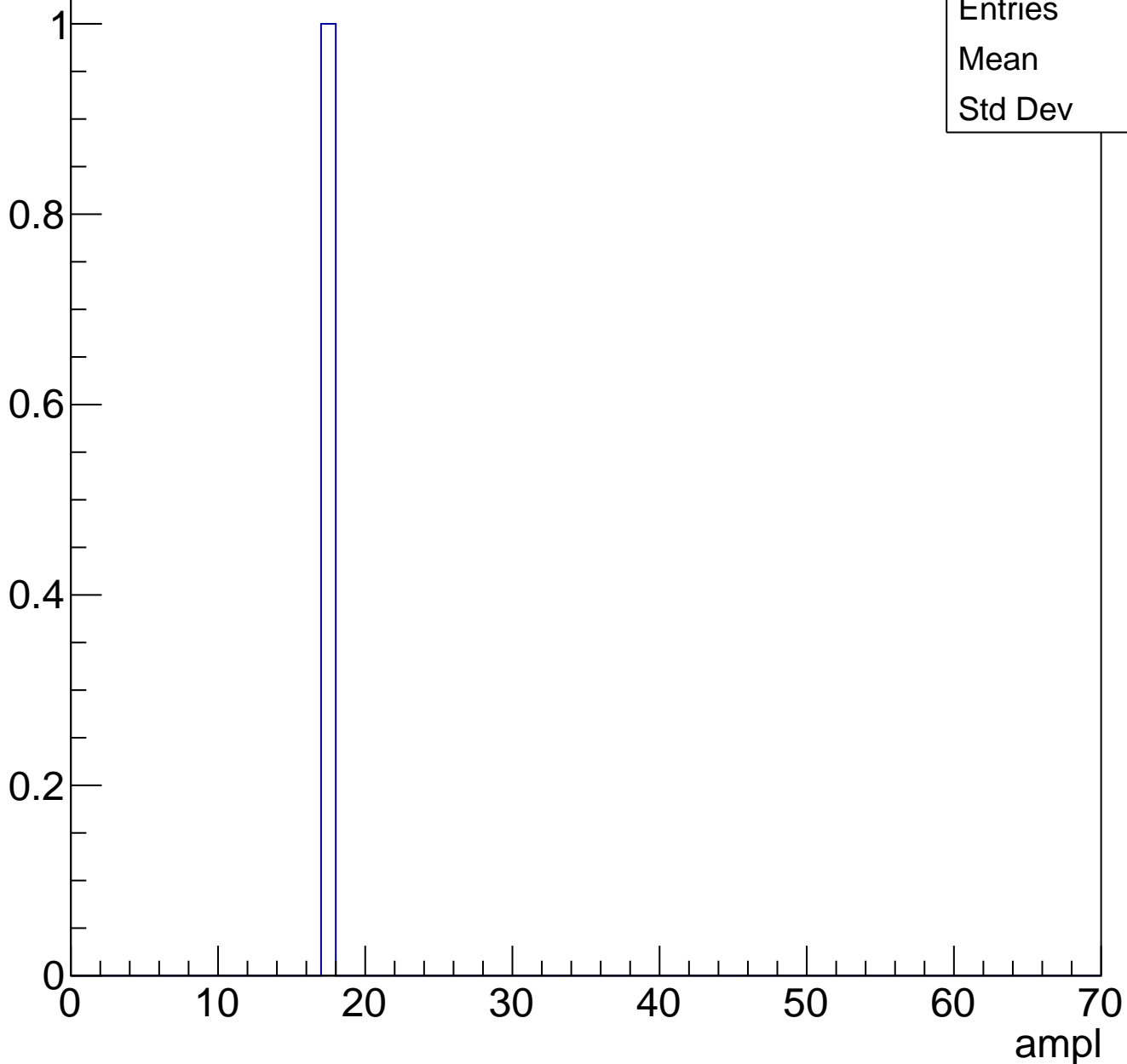
Entry



B0L102S, U12-ch66, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch67, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	85
Mean	29.56
Std Dev	5.822

Gaus mean : 30.9312

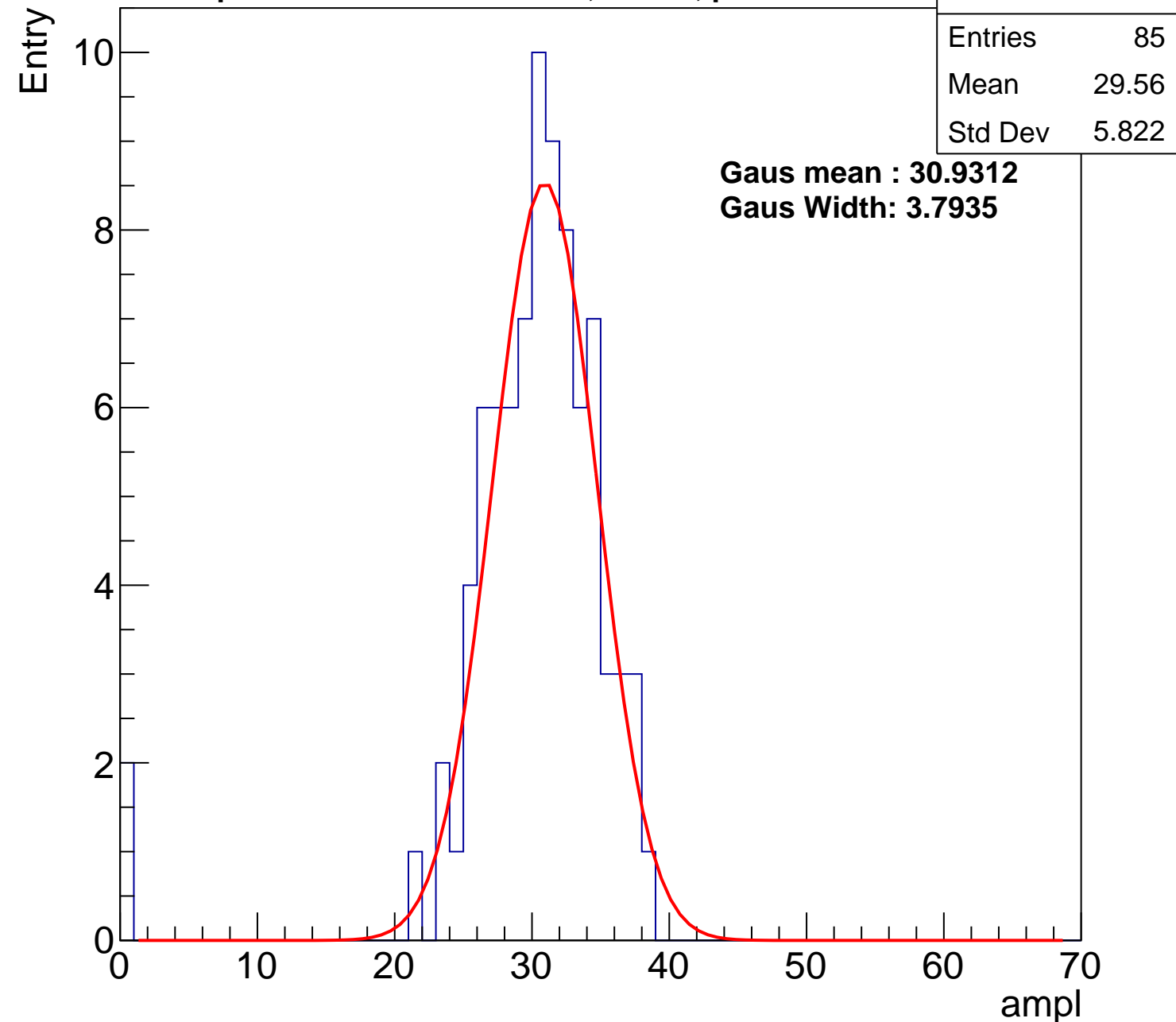
Gaus Width: 3.7935

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



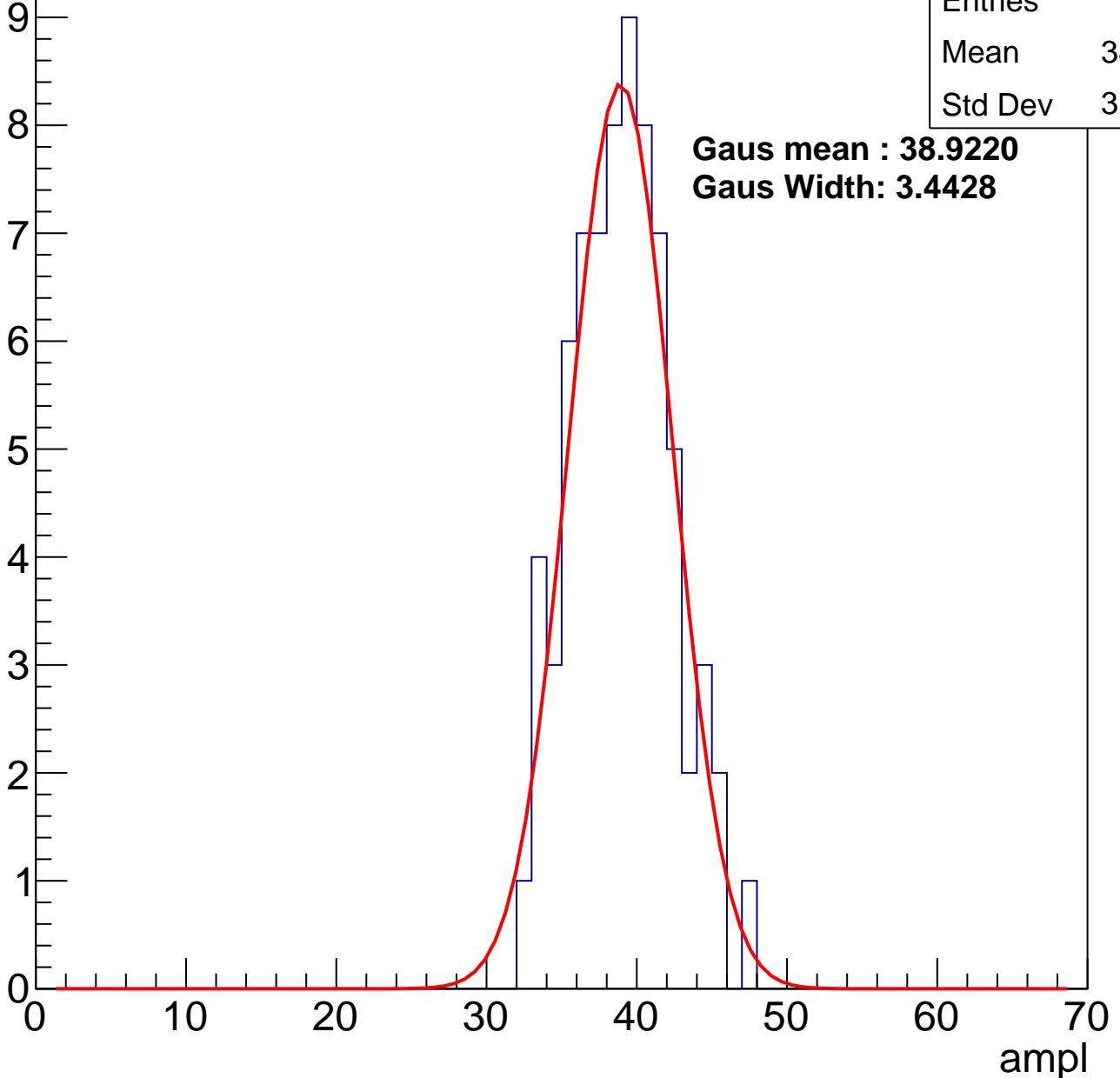
B0L102S, U12-ch67, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	38.55
Std Dev	3.252

Gaus mean : 38.9220
Gaus Width: 3.4428



B0L102S, U12-ch67, adc2

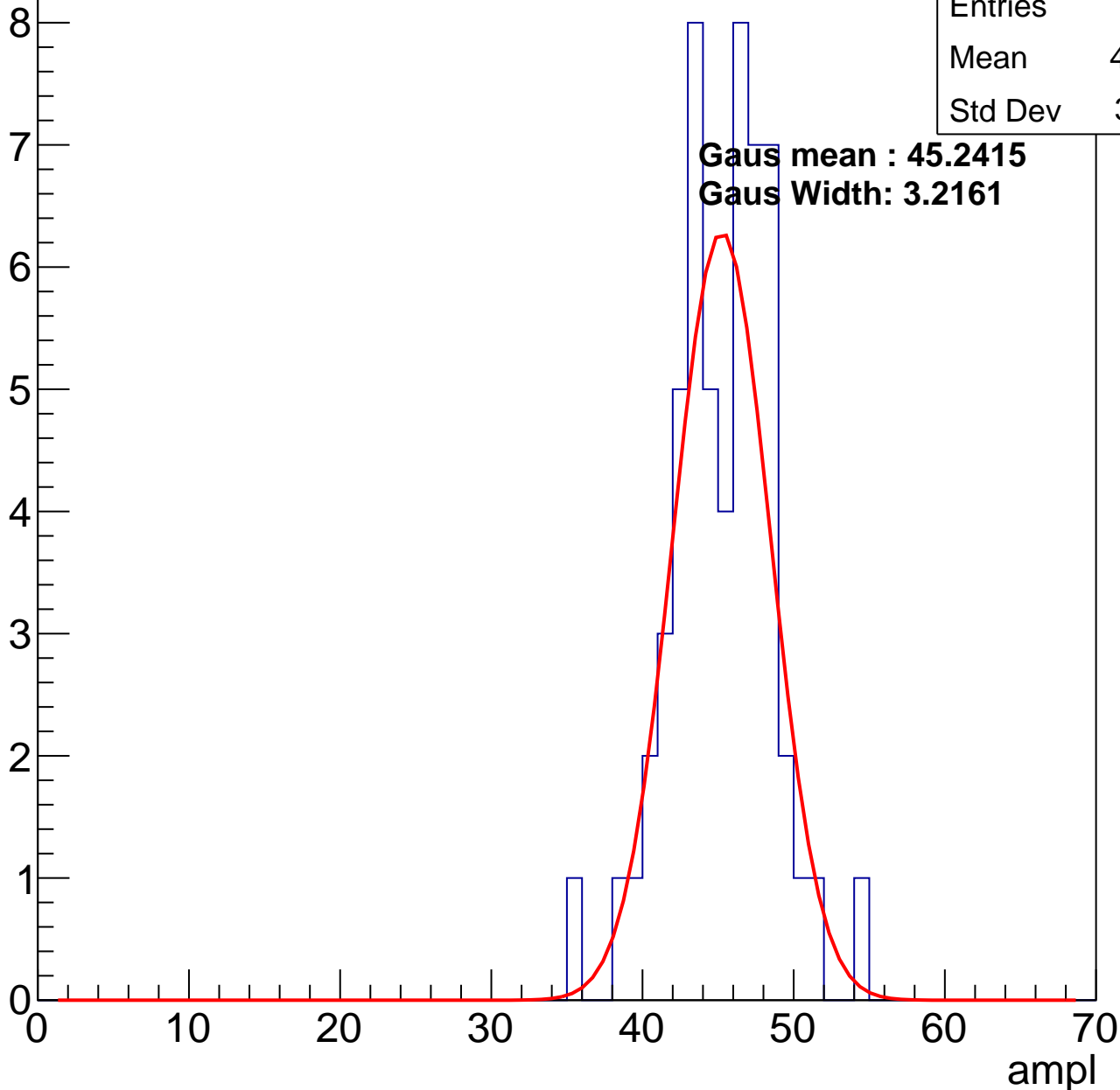
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	44.82
Std Dev	3.341

Gaus mean : 45.2415

Gaus Width: 3.2161



B0L102S, U12-ch67, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	78
Mean	51.64
Std Dev	3.493

Entry

10

8

6

4

2

0

0

10

20

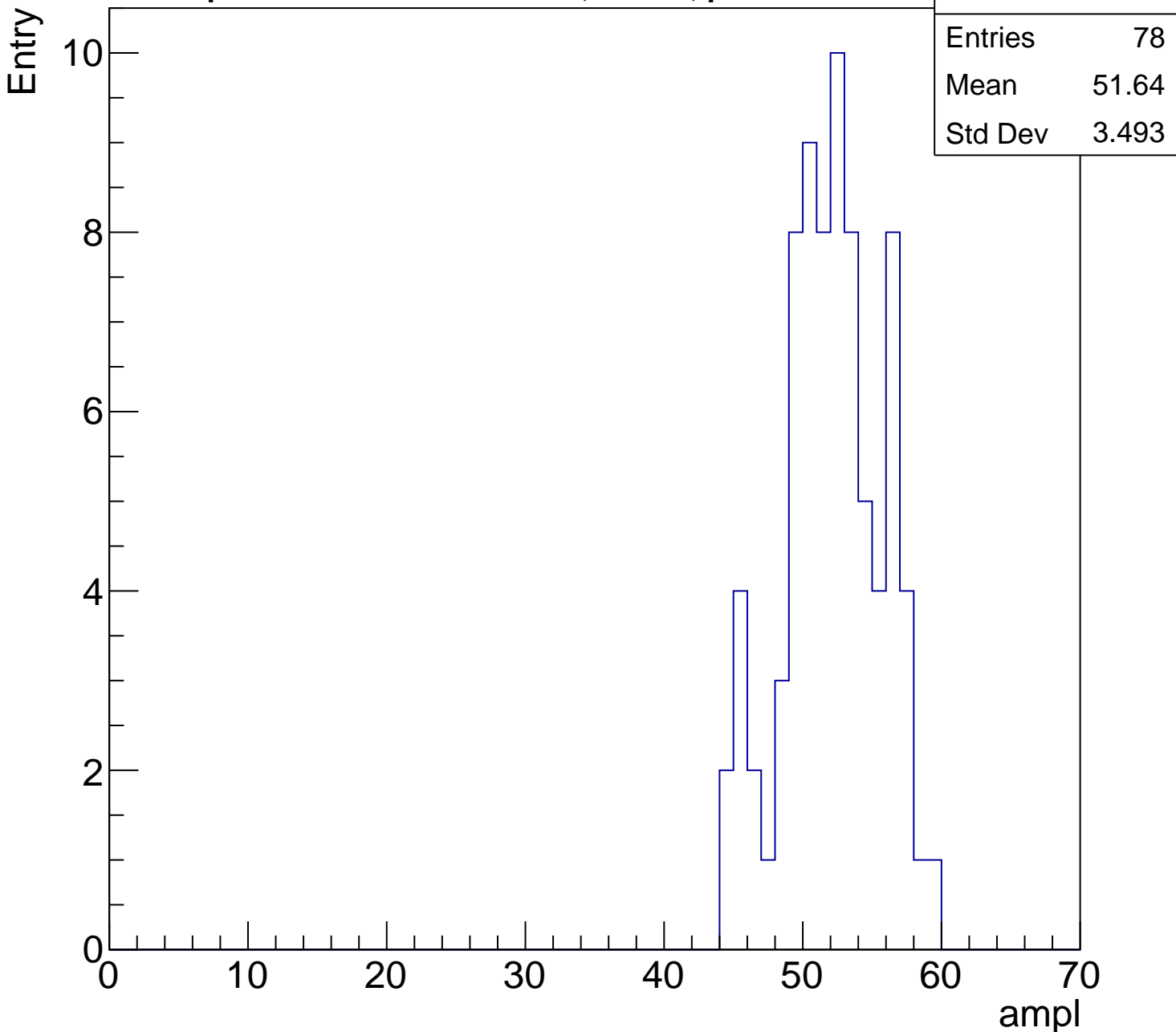
30

40

50

60

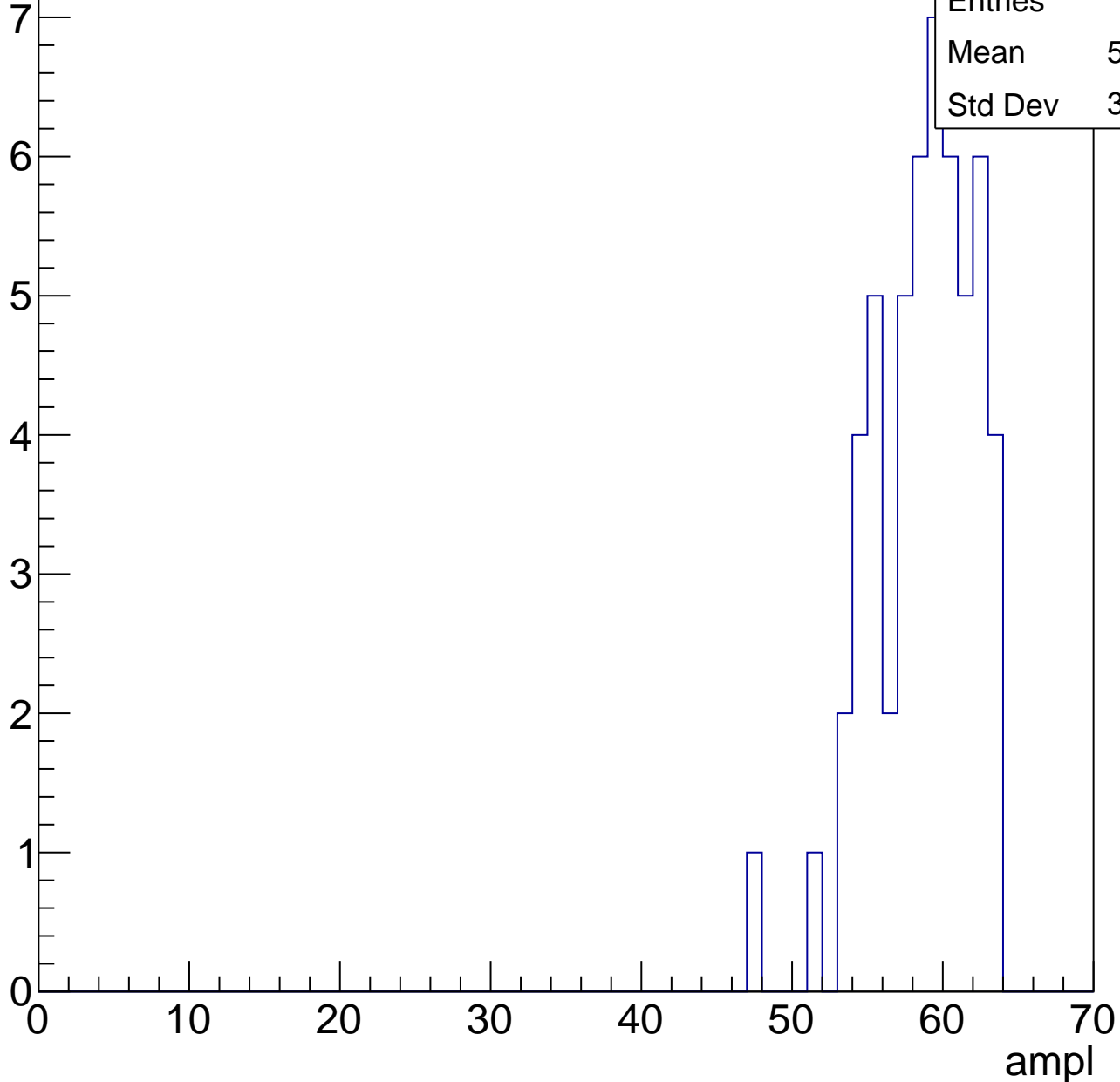
ampl



B0L102S, U12-ch67, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

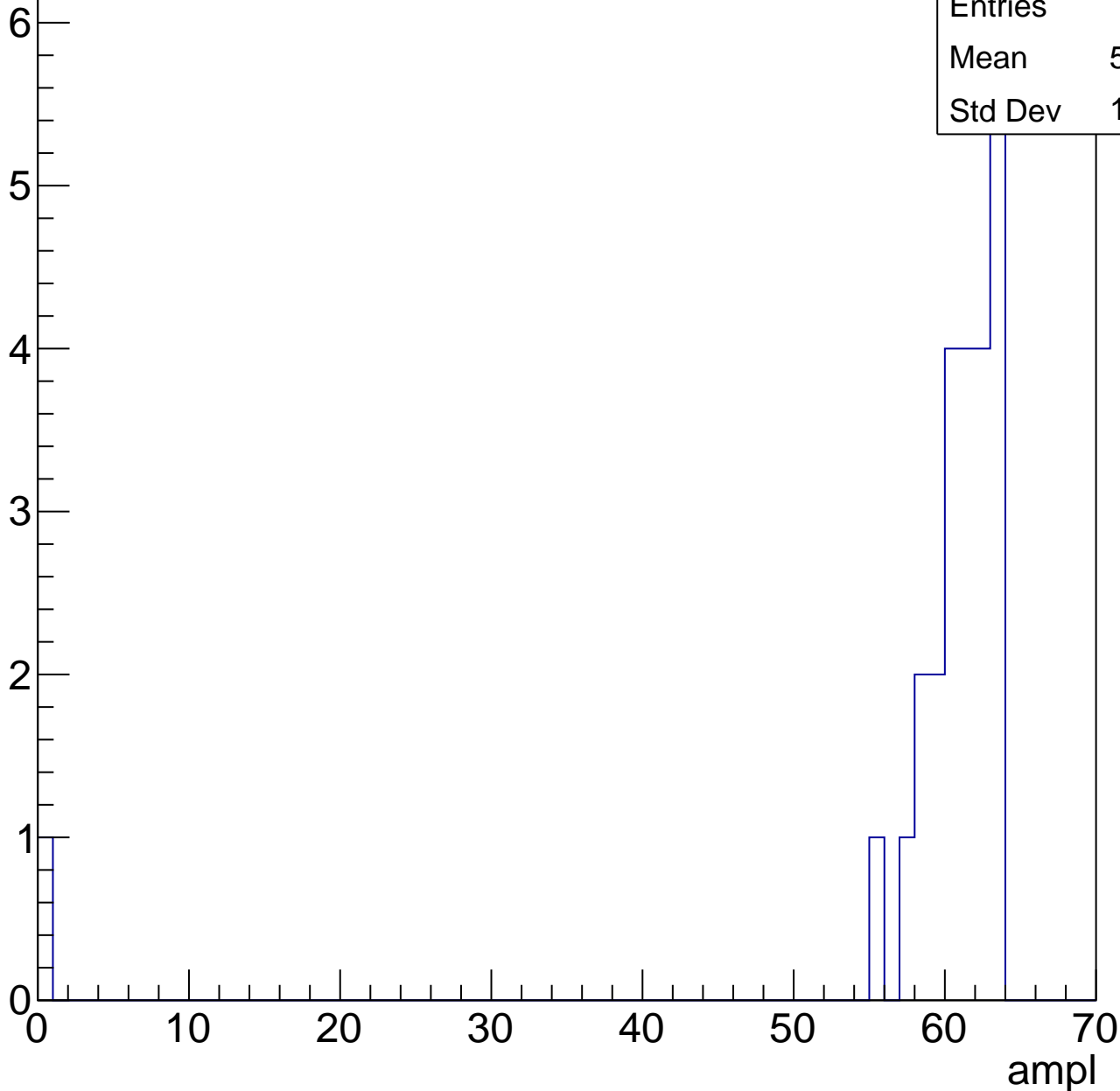


B0L102S, U12-ch67, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	25
Mean	58.24
Std Dev	12.07



B0L102S, U12-ch67, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	2
Mean	59.5
Std Dev	1.5

B0L102S, U12-ch67, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch68, adc0

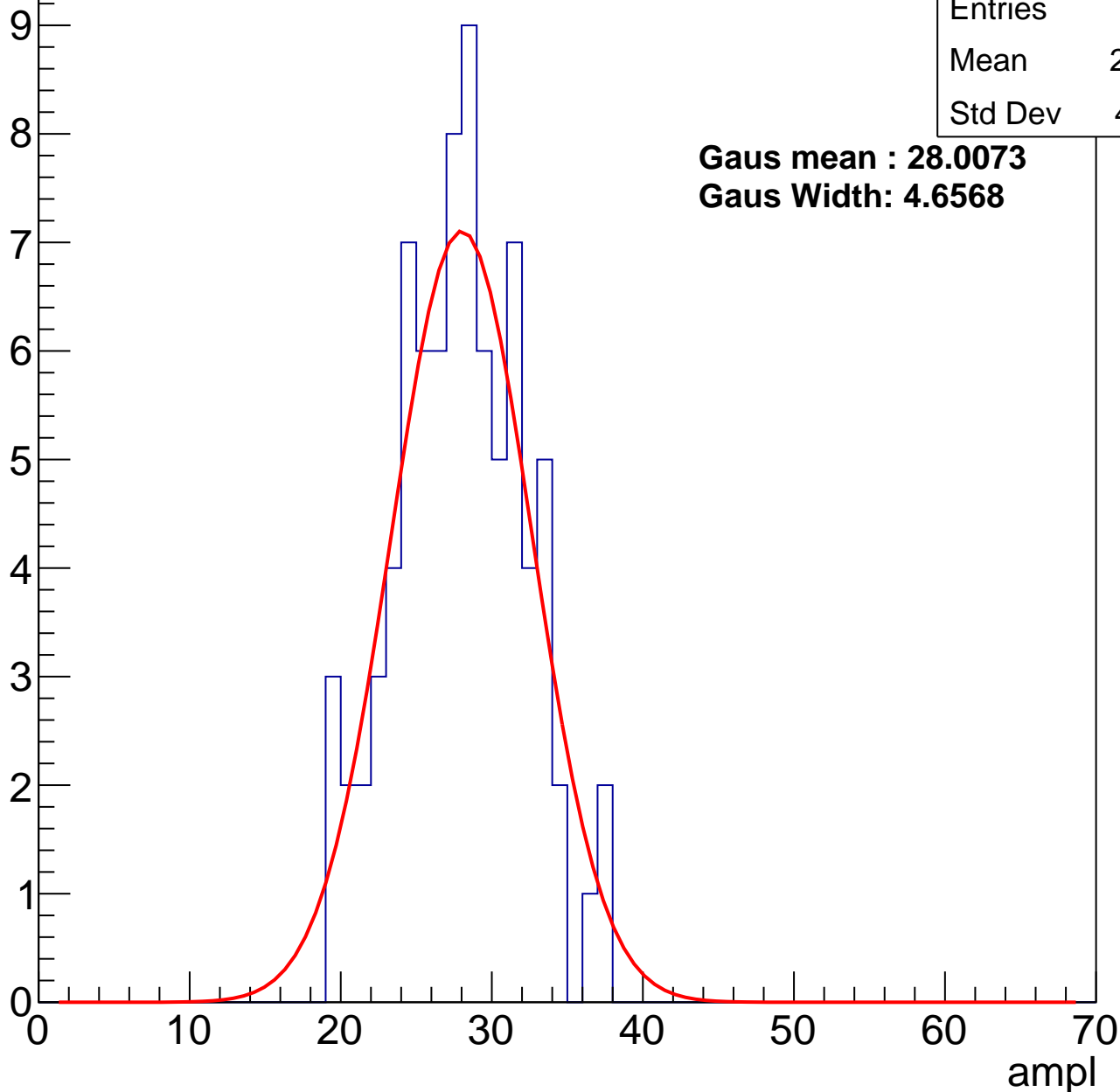
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	27.45
Std Dev	4.171

Gaus mean : 28.0073

Gaus Width: 4.6568



B0L102S, U12-ch68, adc1

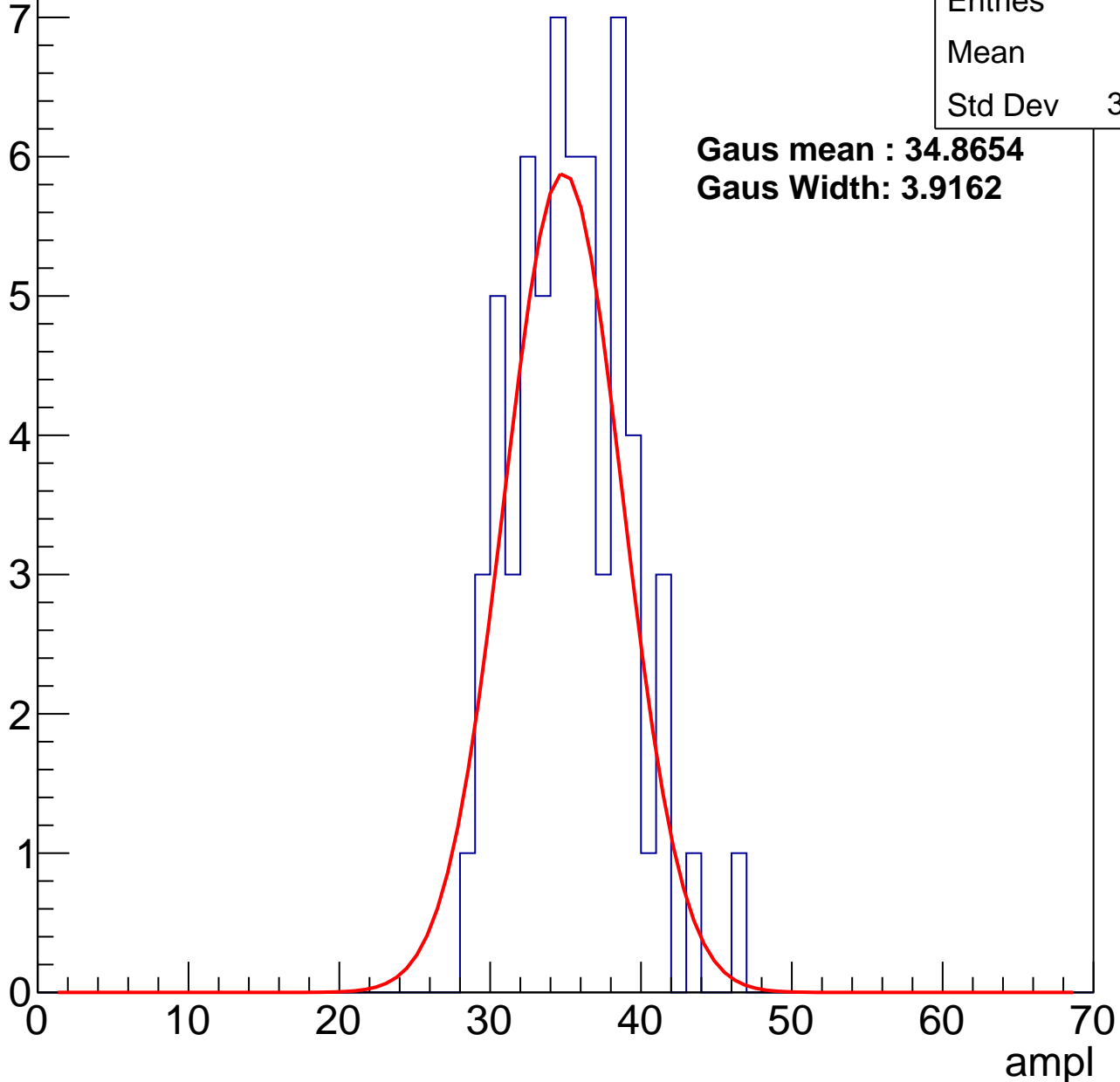
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	34.9
Std Dev	3.753

Gaus mean : 34.8654

Gaus Width: 3.9162



B0L102S, U12-ch68, adc2

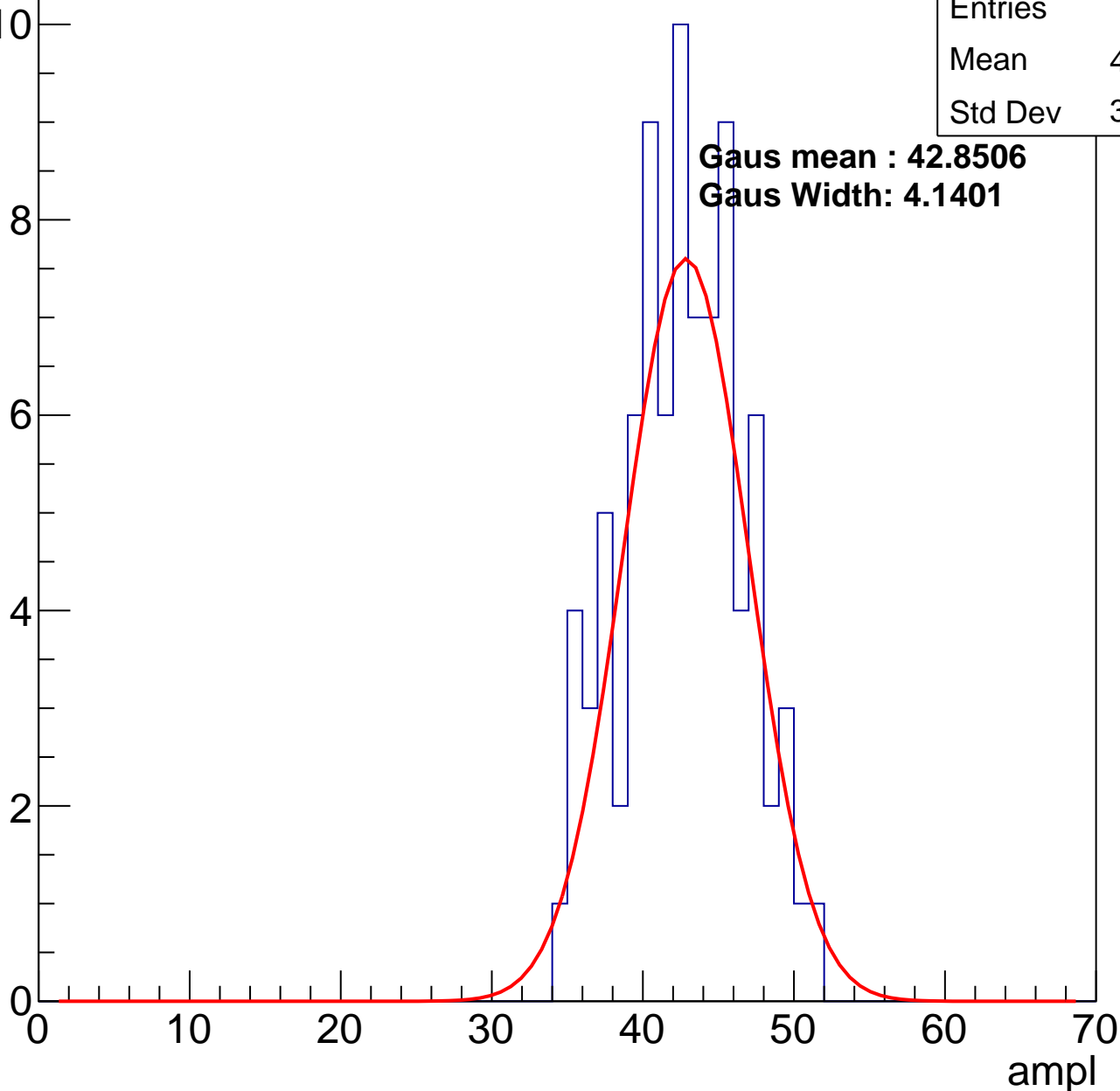
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	42.17
Std Dev	3.906

Gaus mean : 42.8506

Gaus Width: 4.1401

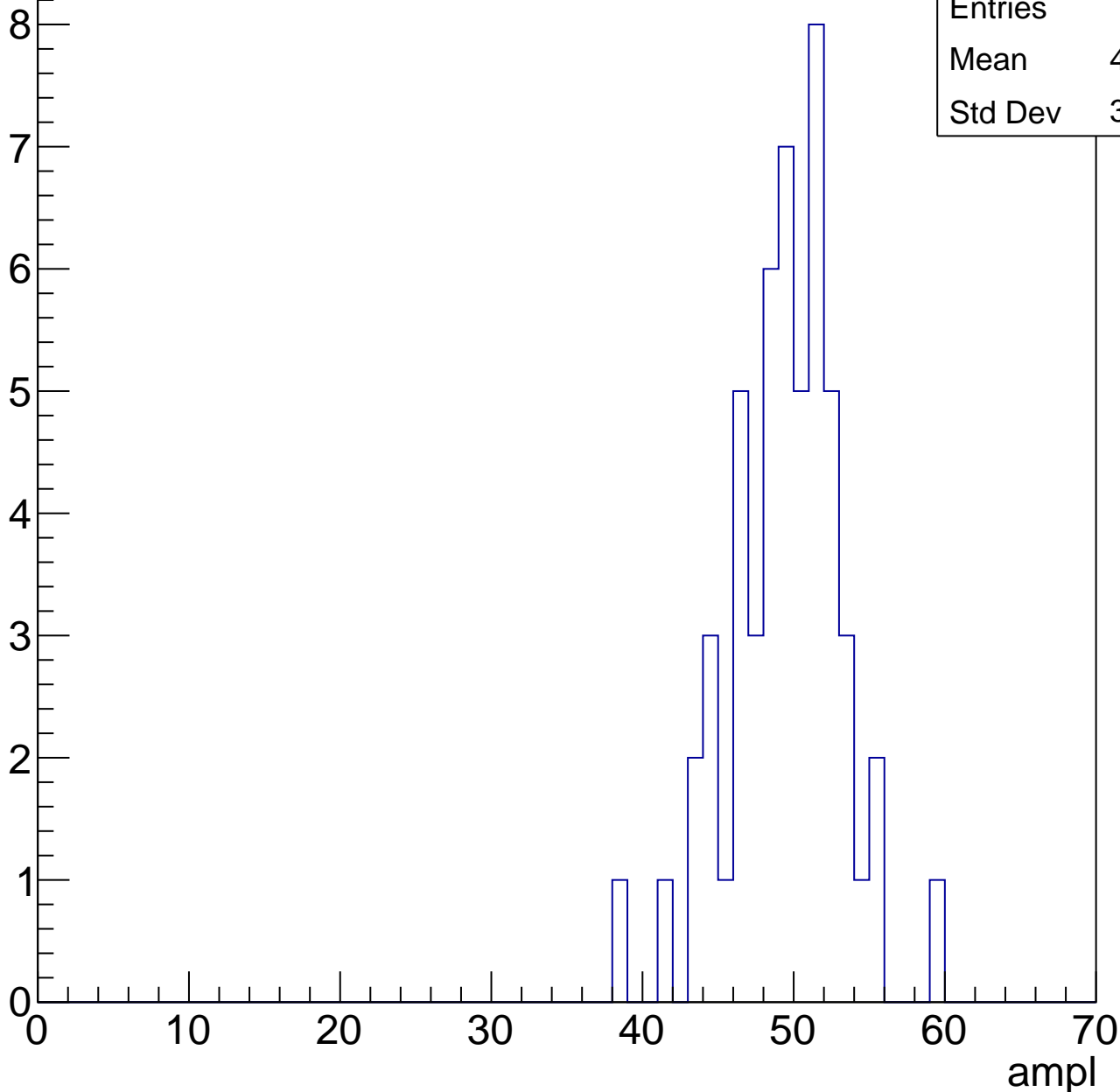


B0L102S, U12-ch68, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	48.96
Std Dev	3.697

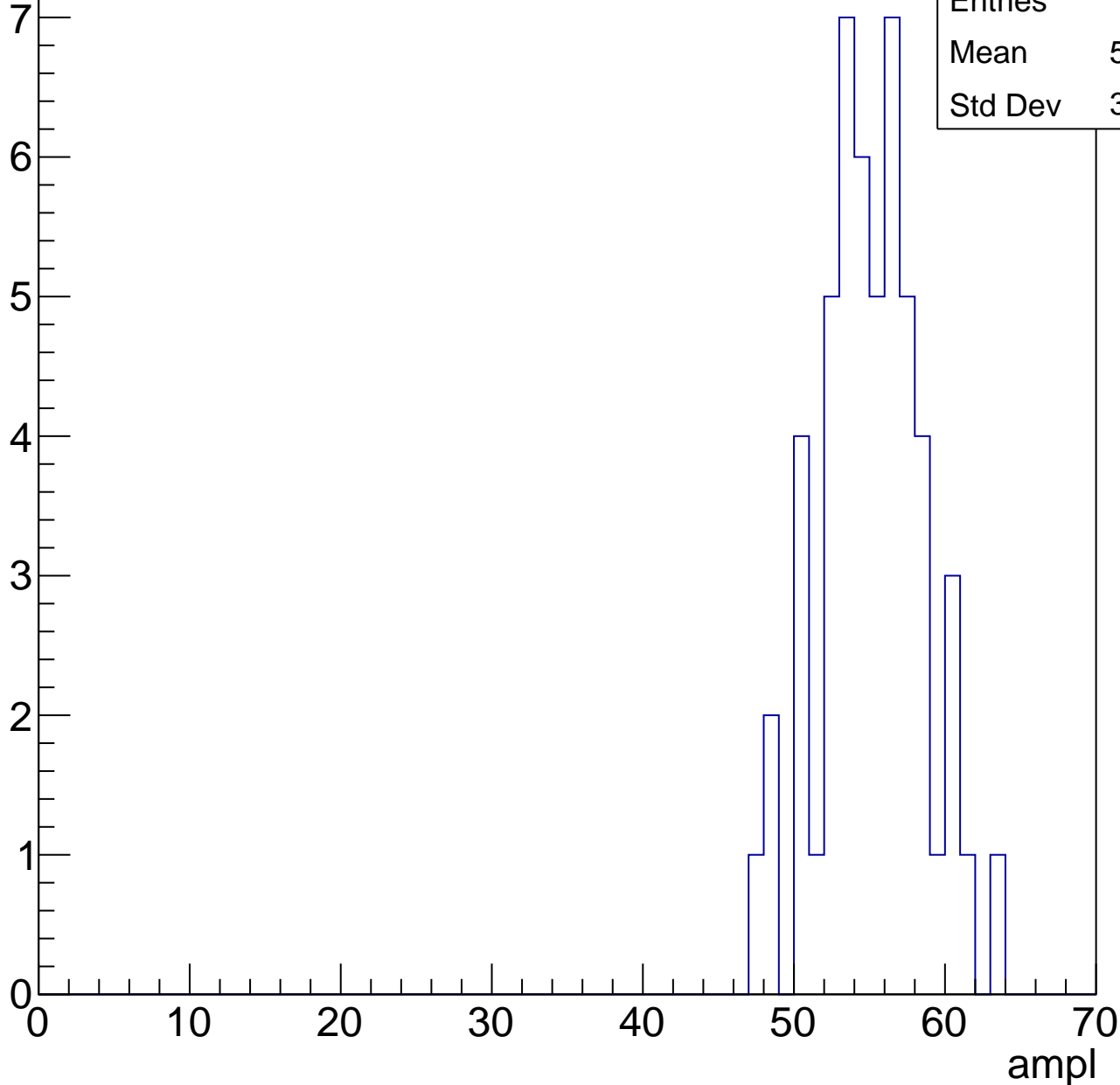


B0L102S, U12-ch68, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

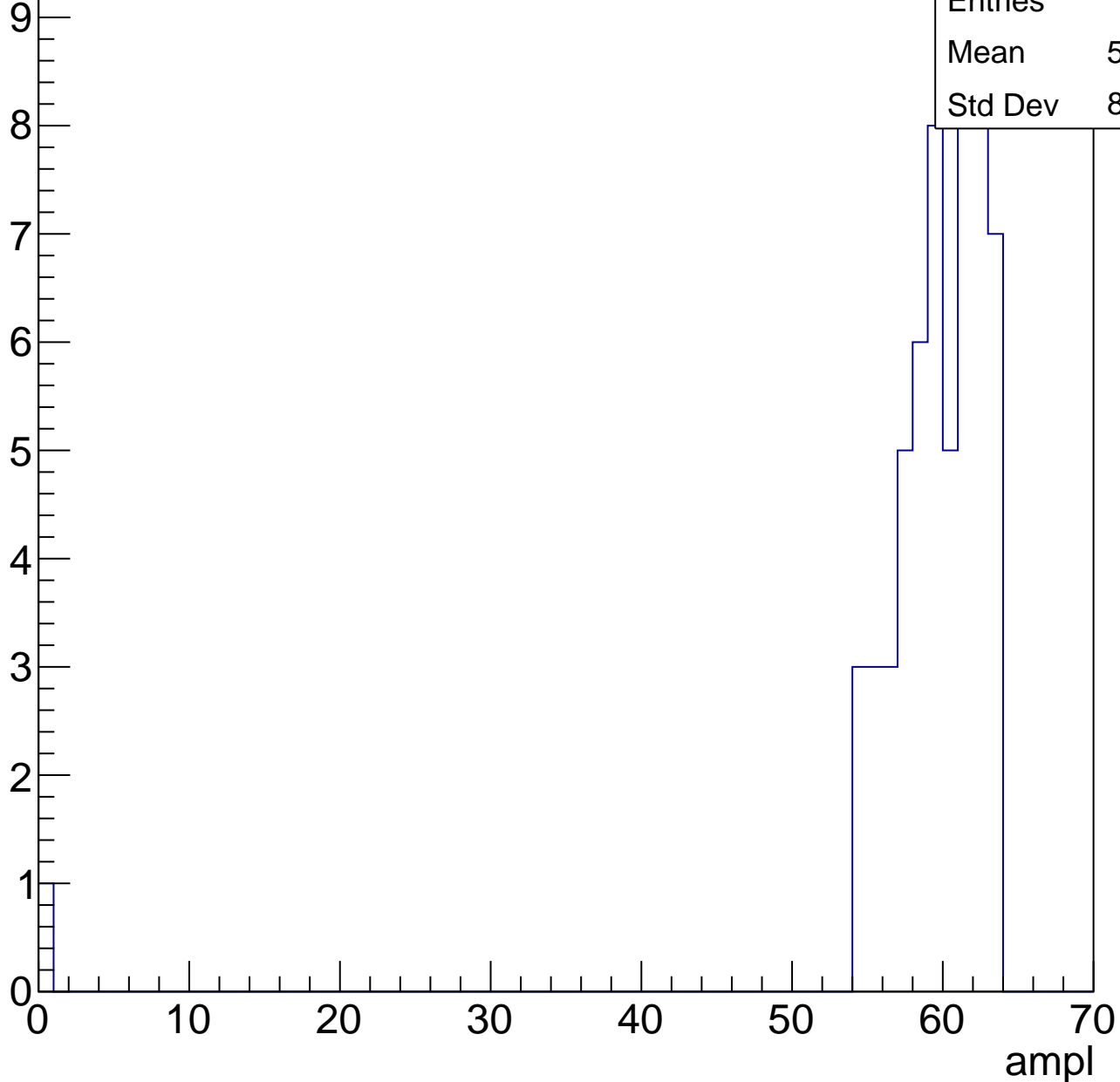
Entries	53
Mean	54.64
Std Dev	3.376



B0L102S, U12-ch68, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch68, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch68, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch69, adc0

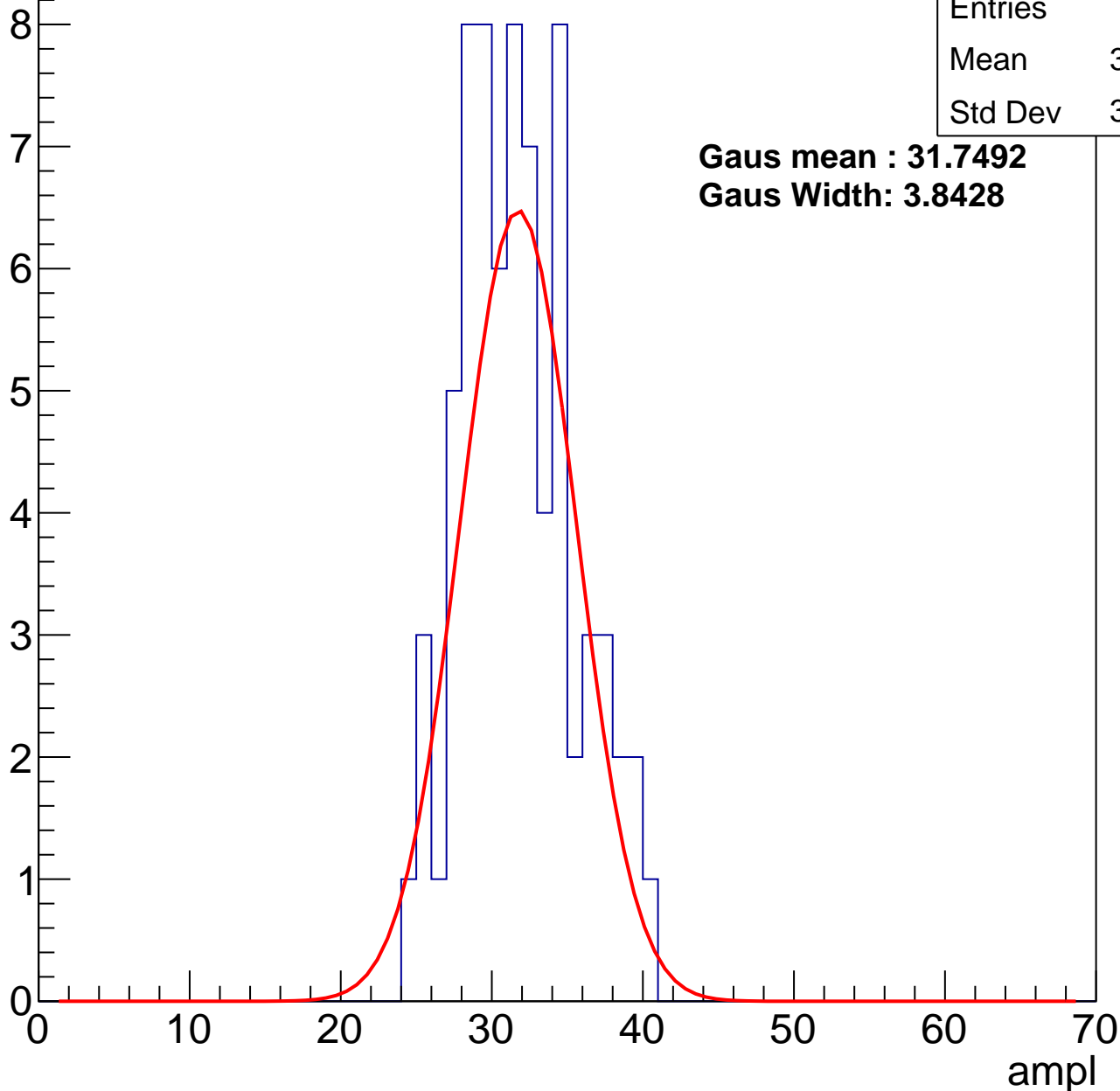
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	31.32
Std Dev	3.692

Gaus mean : 31.7492

Gaus Width: 3.8428



B0L102S, U12-ch69, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	88
Mean	38.78
Std Dev	3.967

Gaus mean : 39.2463

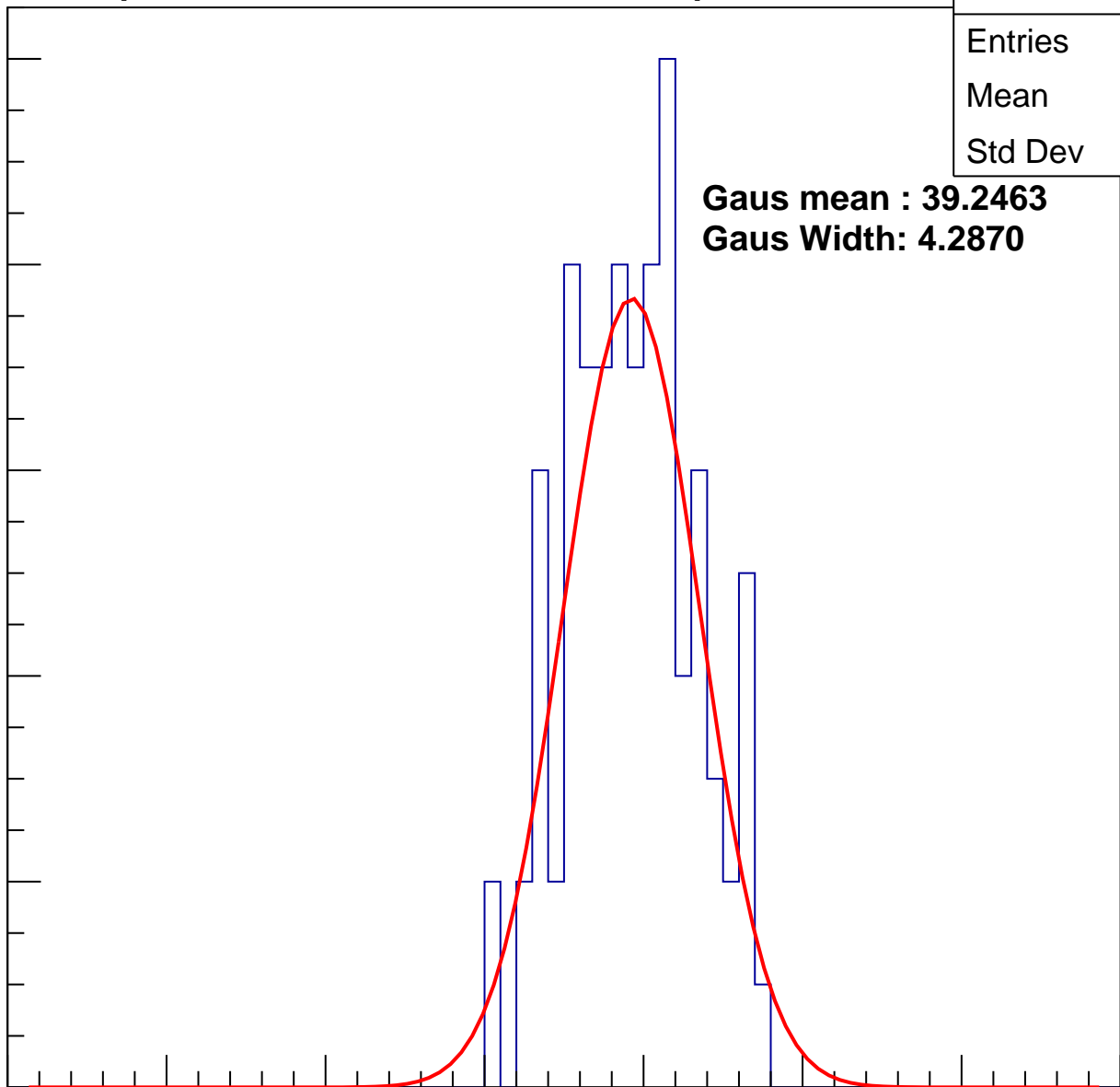
Gaus Width: 4.2870

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch69, adc2

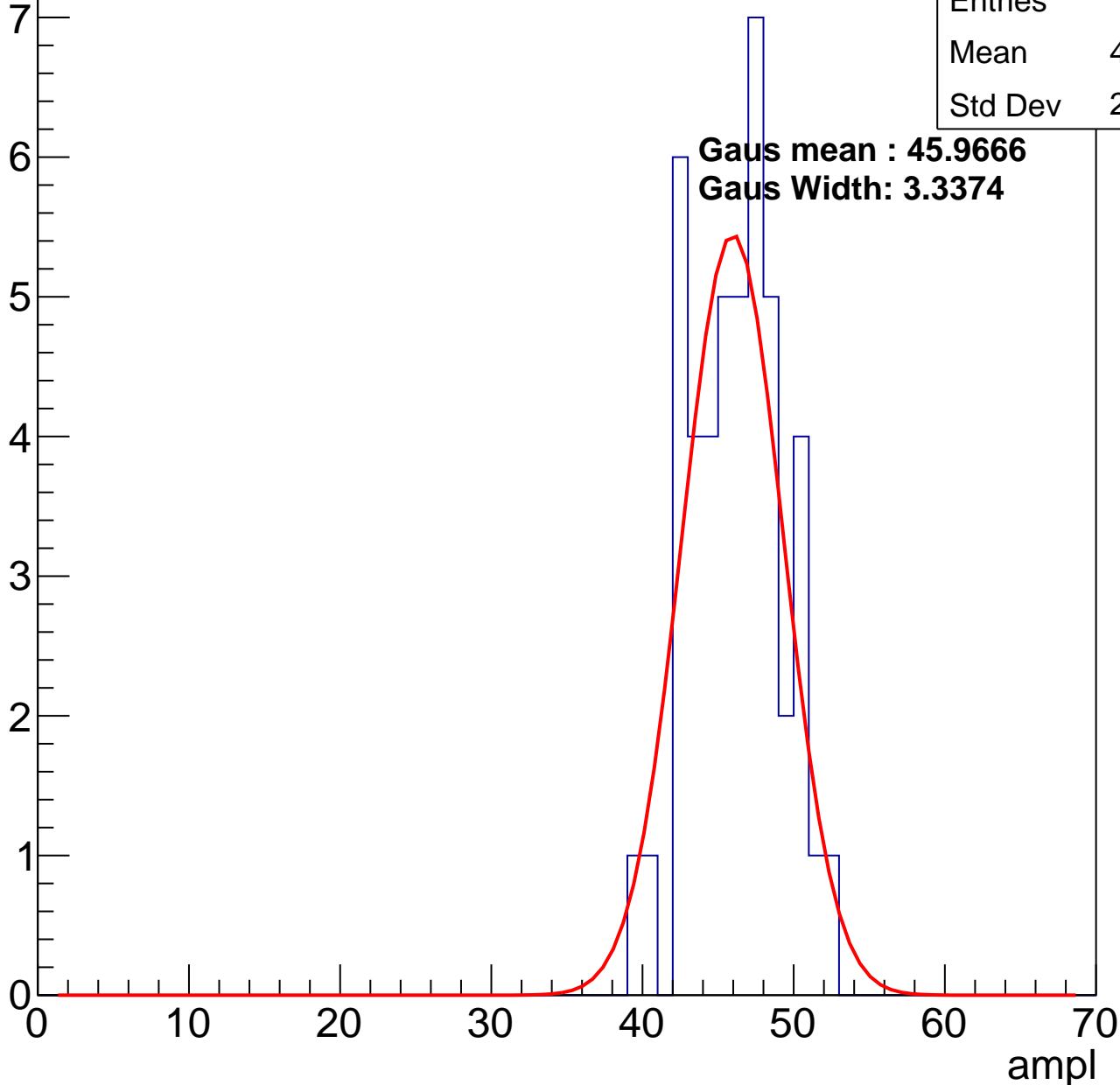
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	45.74
Std Dev	2.967

Gaus mean : 45.9666

Gaus Width: 3.3374

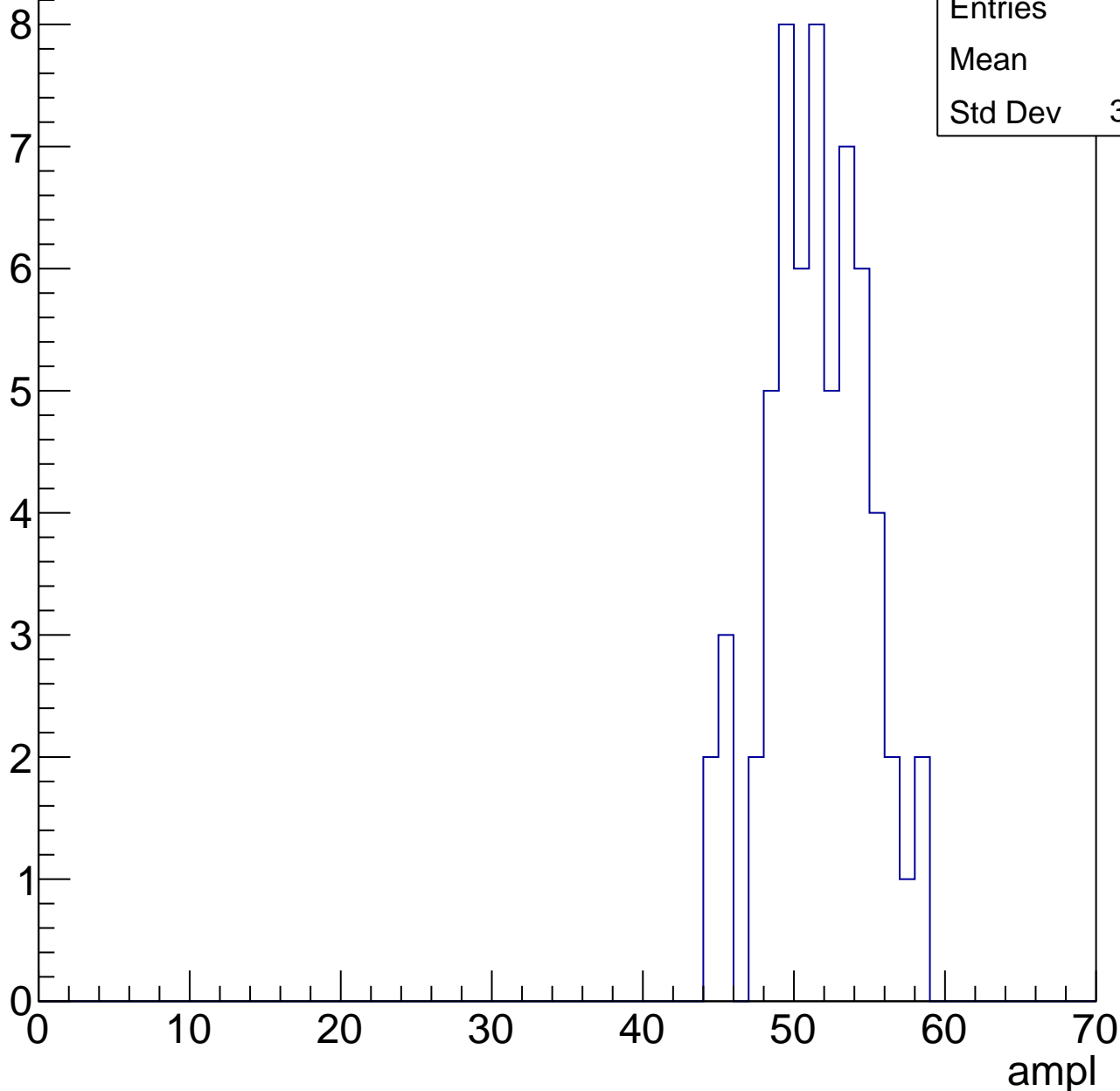


B0L102S, U12-ch69, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	51.1
Std Dev	3.278

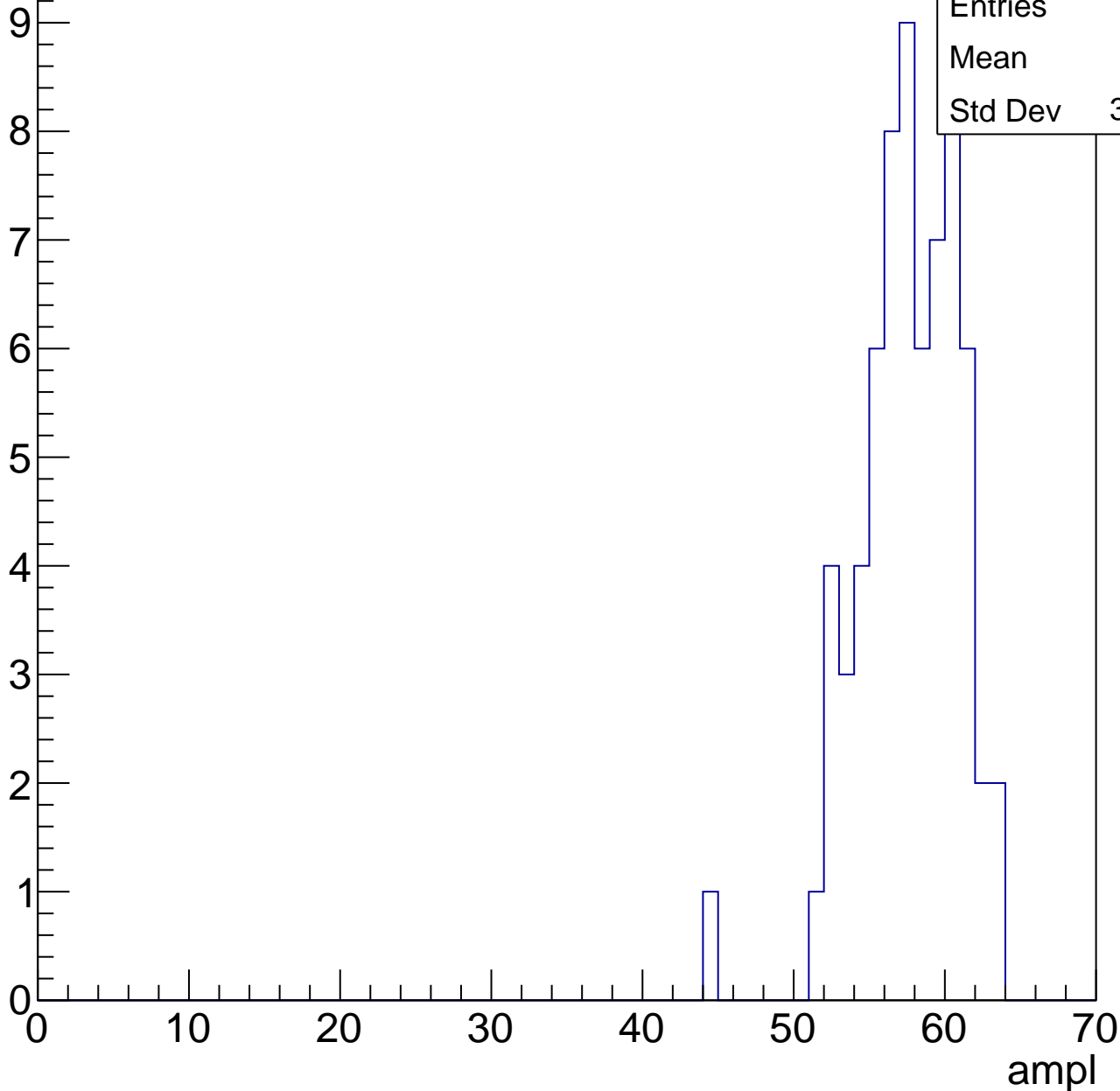


B0L102S, U12-ch69, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	57.1
Std Dev	3.333

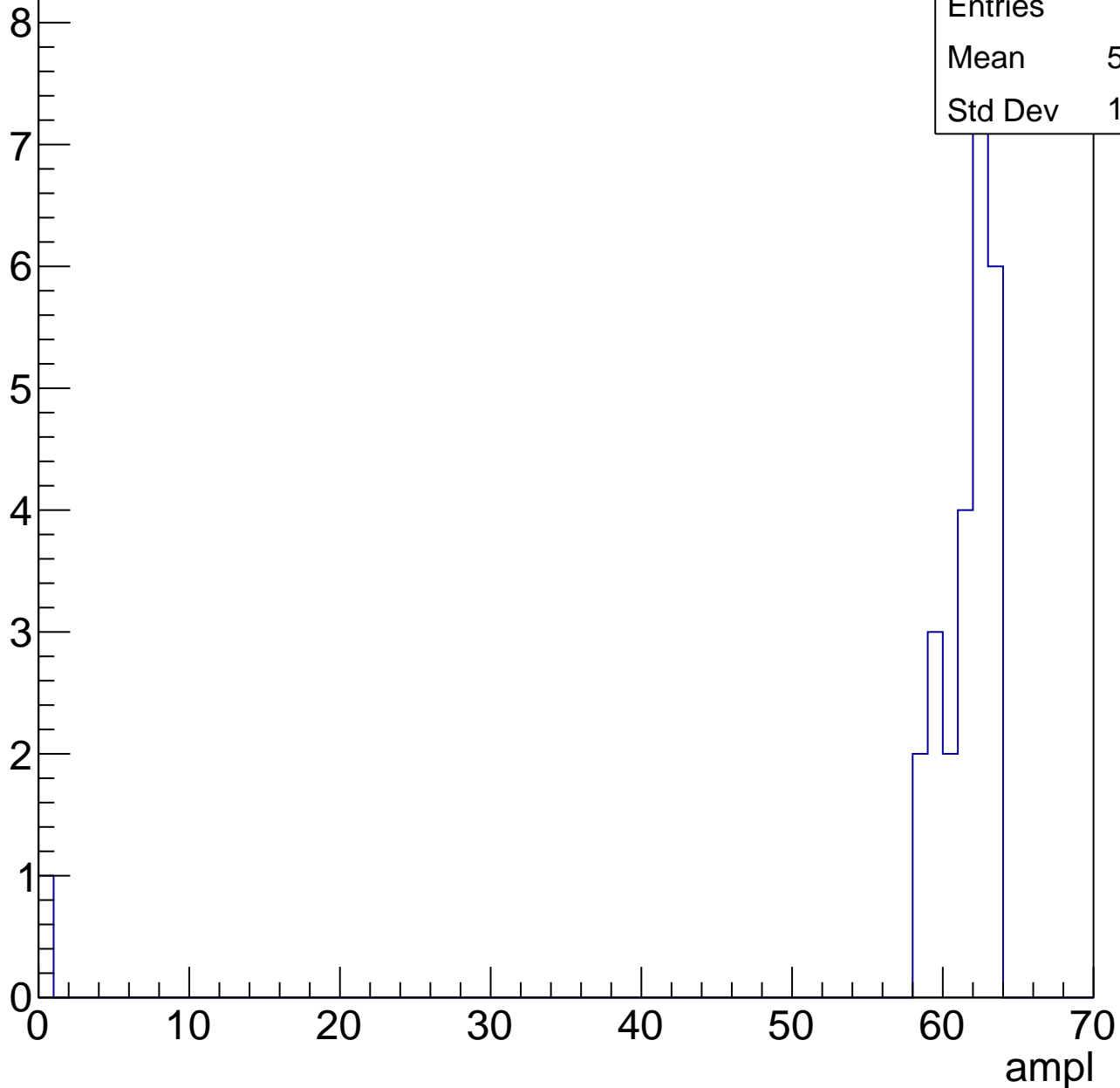


B0L102S, U12-ch69, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	26
Mean	58.88
Std Dev	11.88



B0L102S, U12-ch69, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch69, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch70, adc0

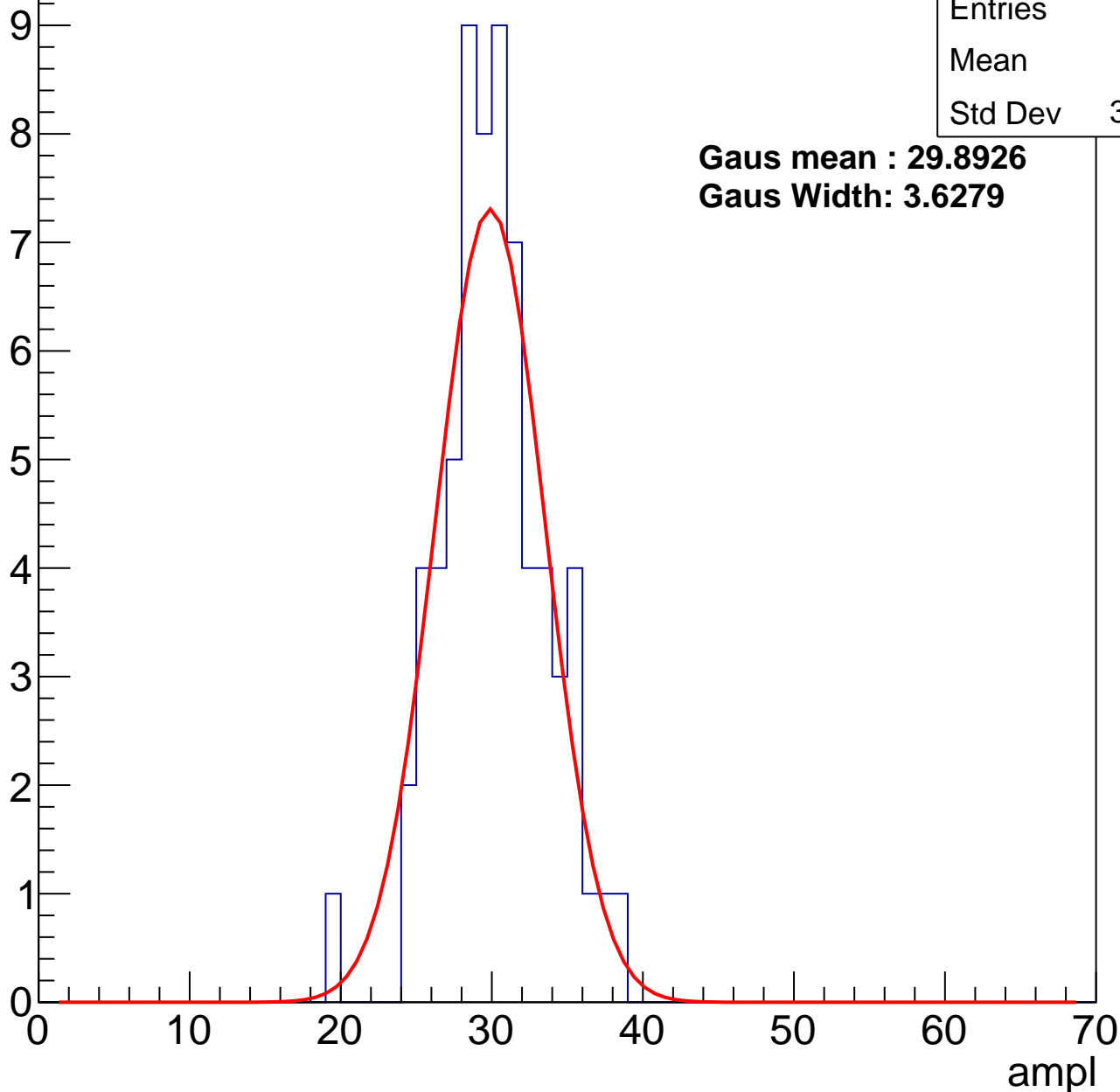
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	29.7
Std Dev	3.447

Gaus mean : 29.8926

Gaus Width: 3.6279



B0L102S, U12-ch70, adc1

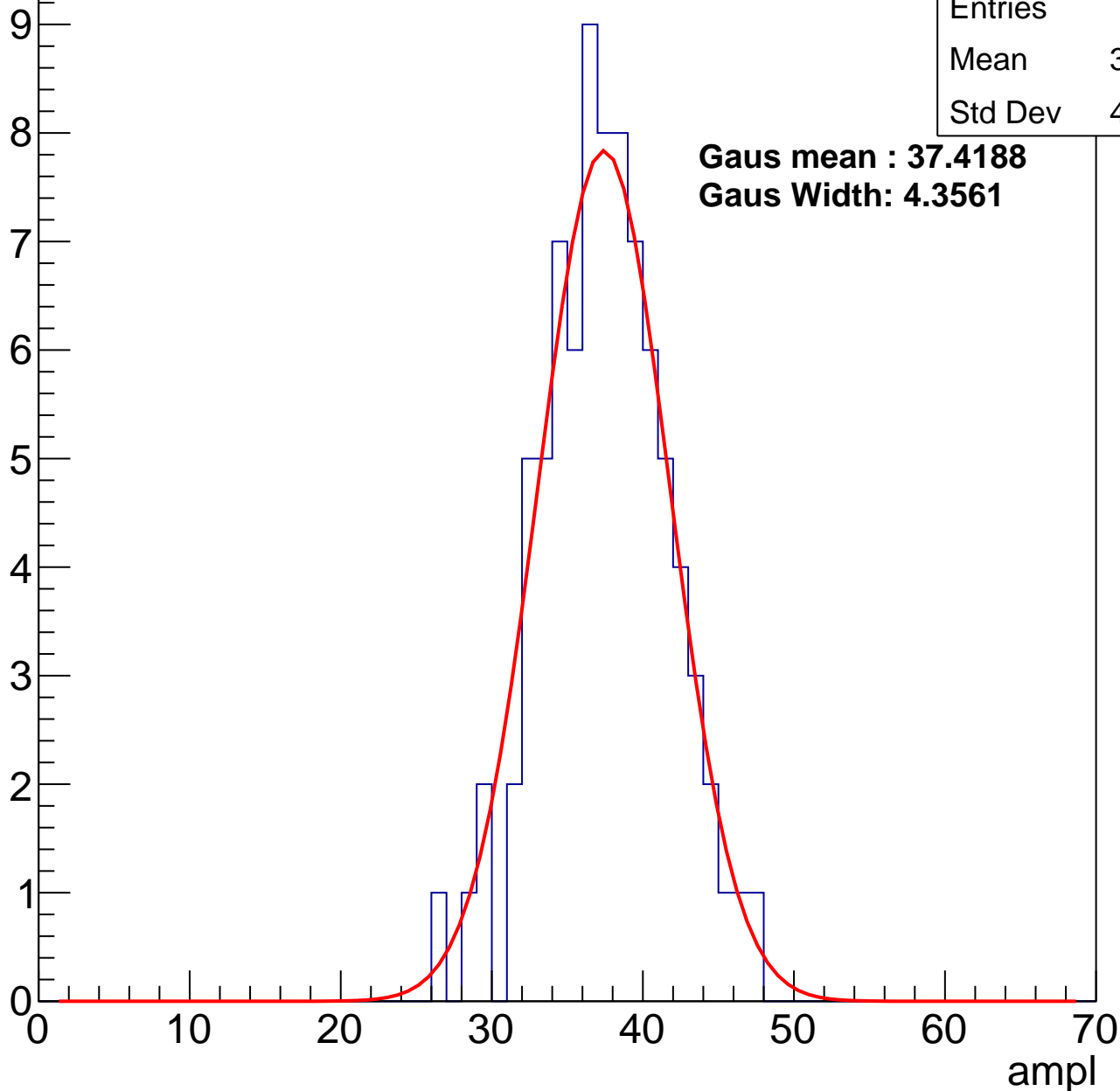
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	37.05
Std Dev	4.117

Gaus mean : 37.4188

Gaus Width: 4.3561



B0L102S, U12-ch70, adc2

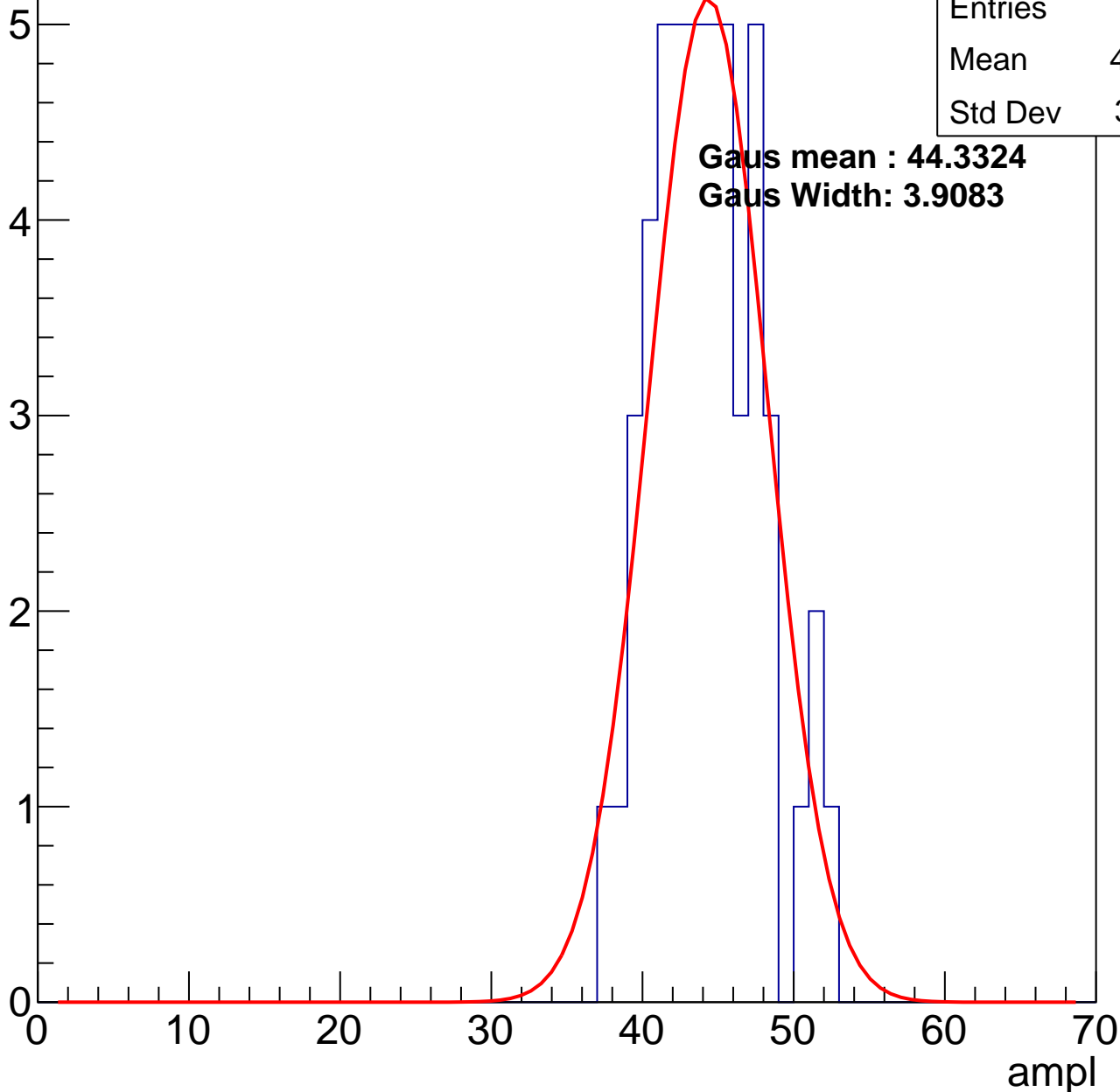
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	43.84
Std Dev	3.501

Gaus mean : 44.3324

Gaus Width: 3.9083

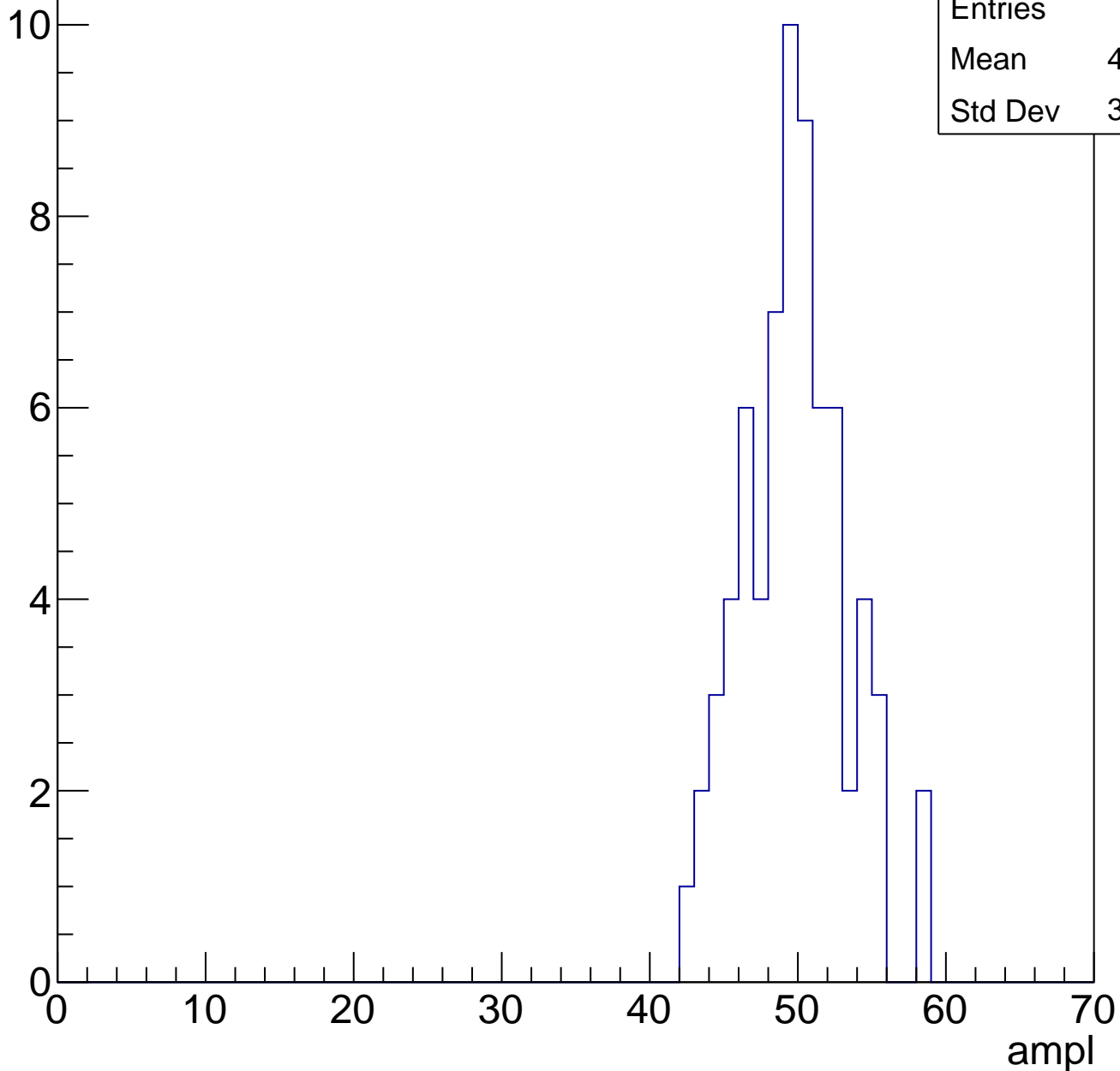


B0L102S, U12-ch70, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	49.29
Std Dev	3.448

Entry

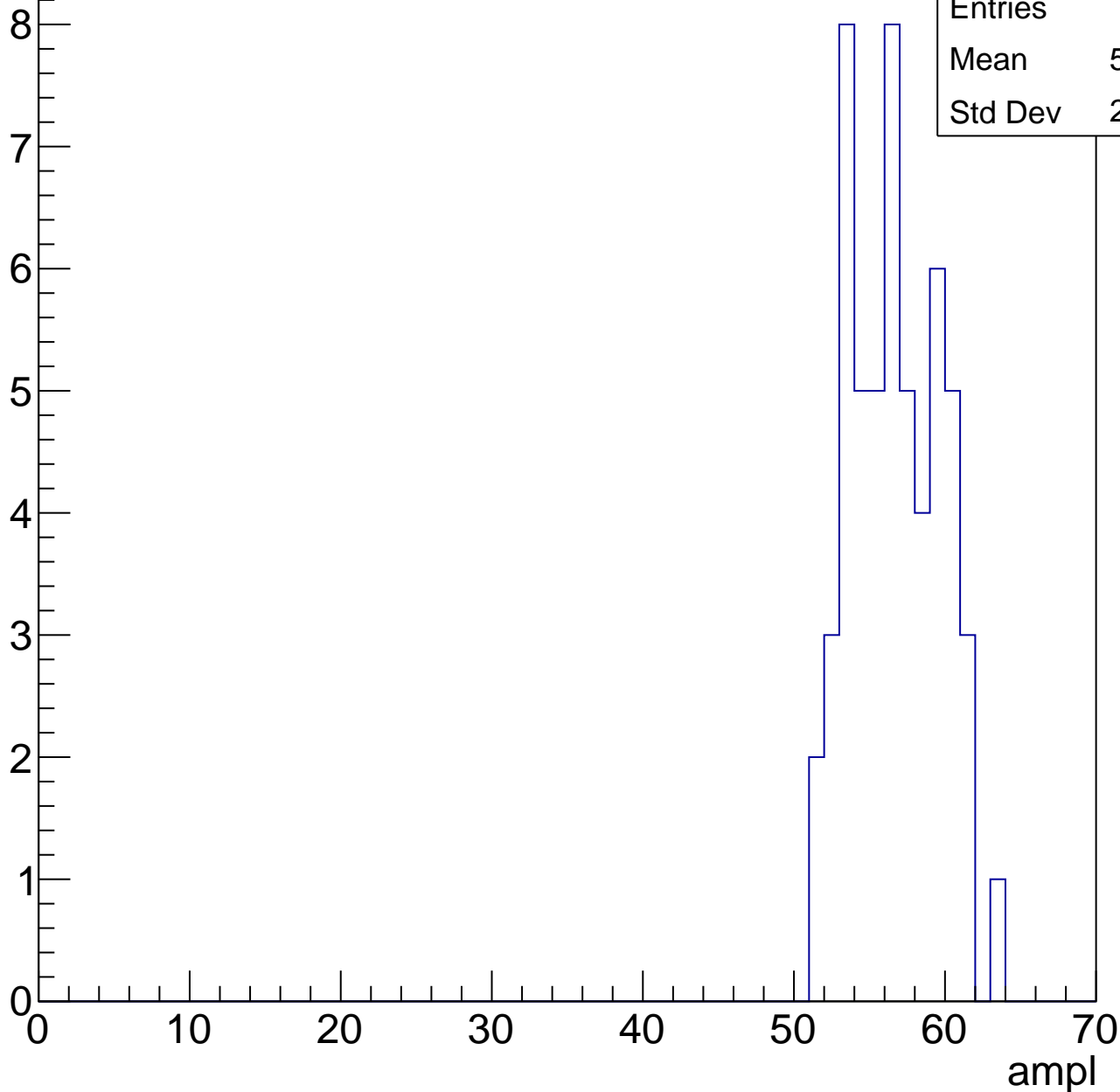


B0L102S, U12-ch70, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	56.22
Std Dev	2.928

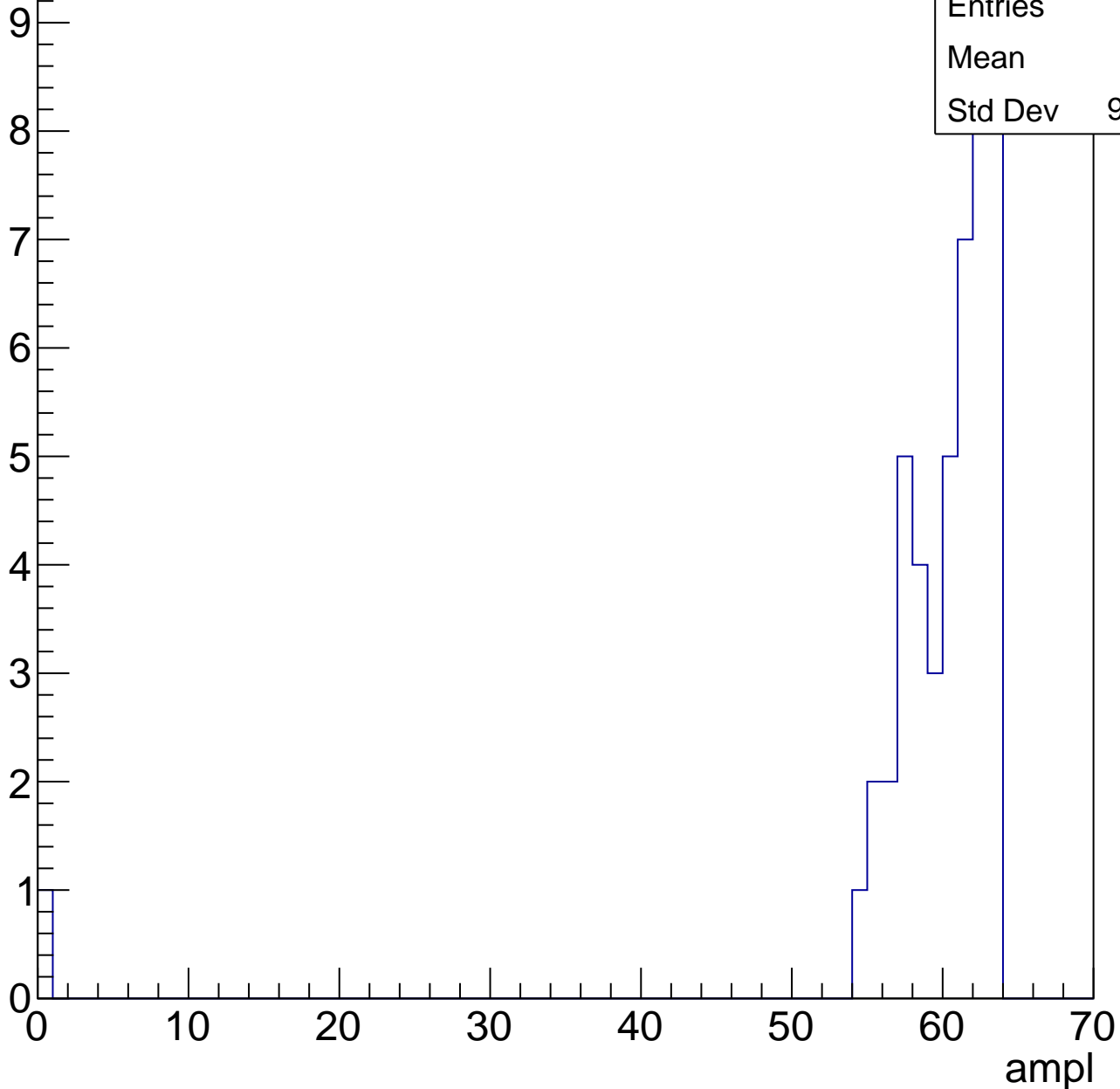


B0L102S, U12-ch70, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	58.7
Std Dev	9.013



B0L102S, U12-ch70, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch70, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch71, adc0

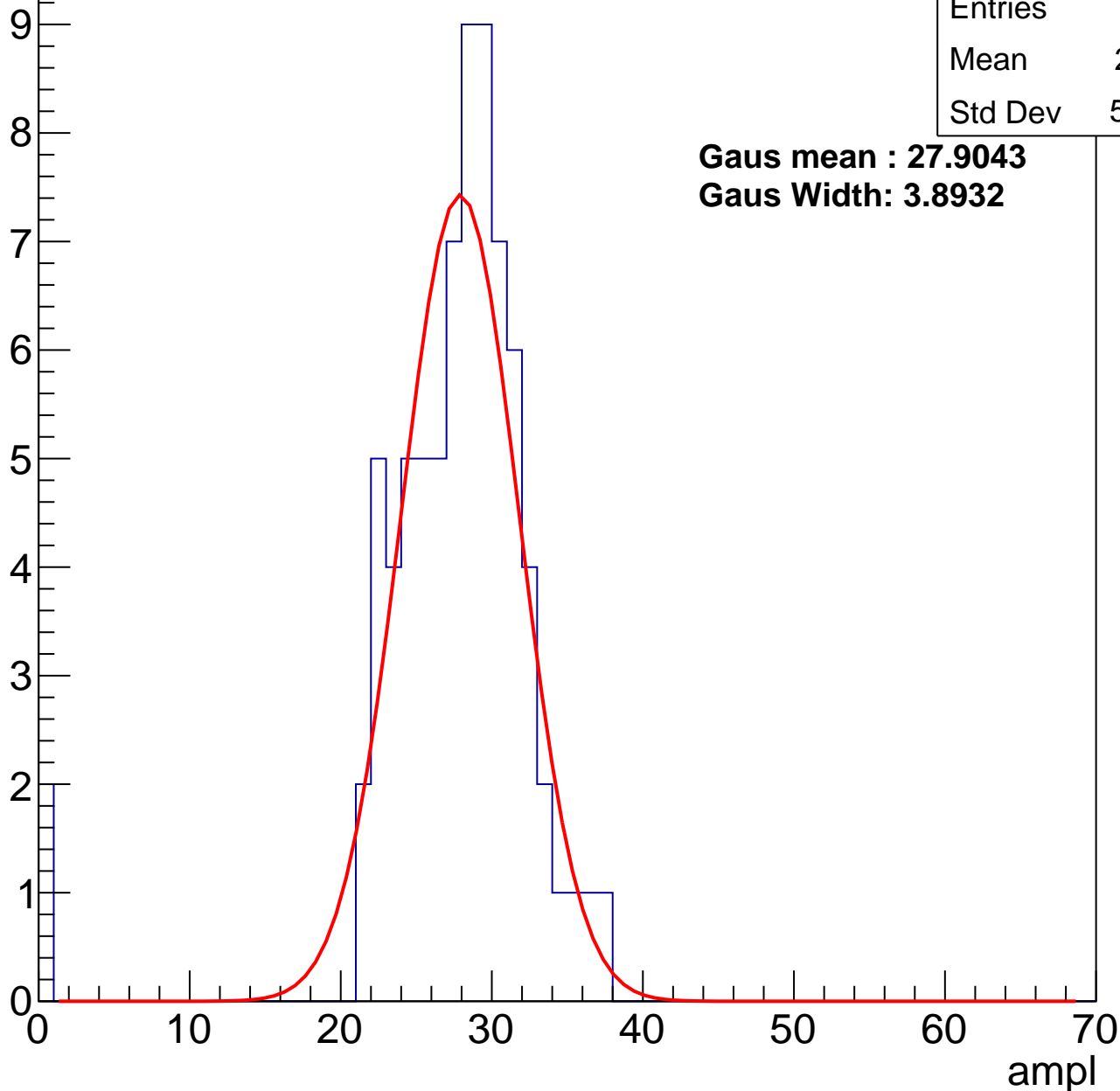
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	27.01
Std Dev	5.688

Gaus mean : 27.9043

Gaus Width: 3.8932



B0L102S, U12-ch71, adc1

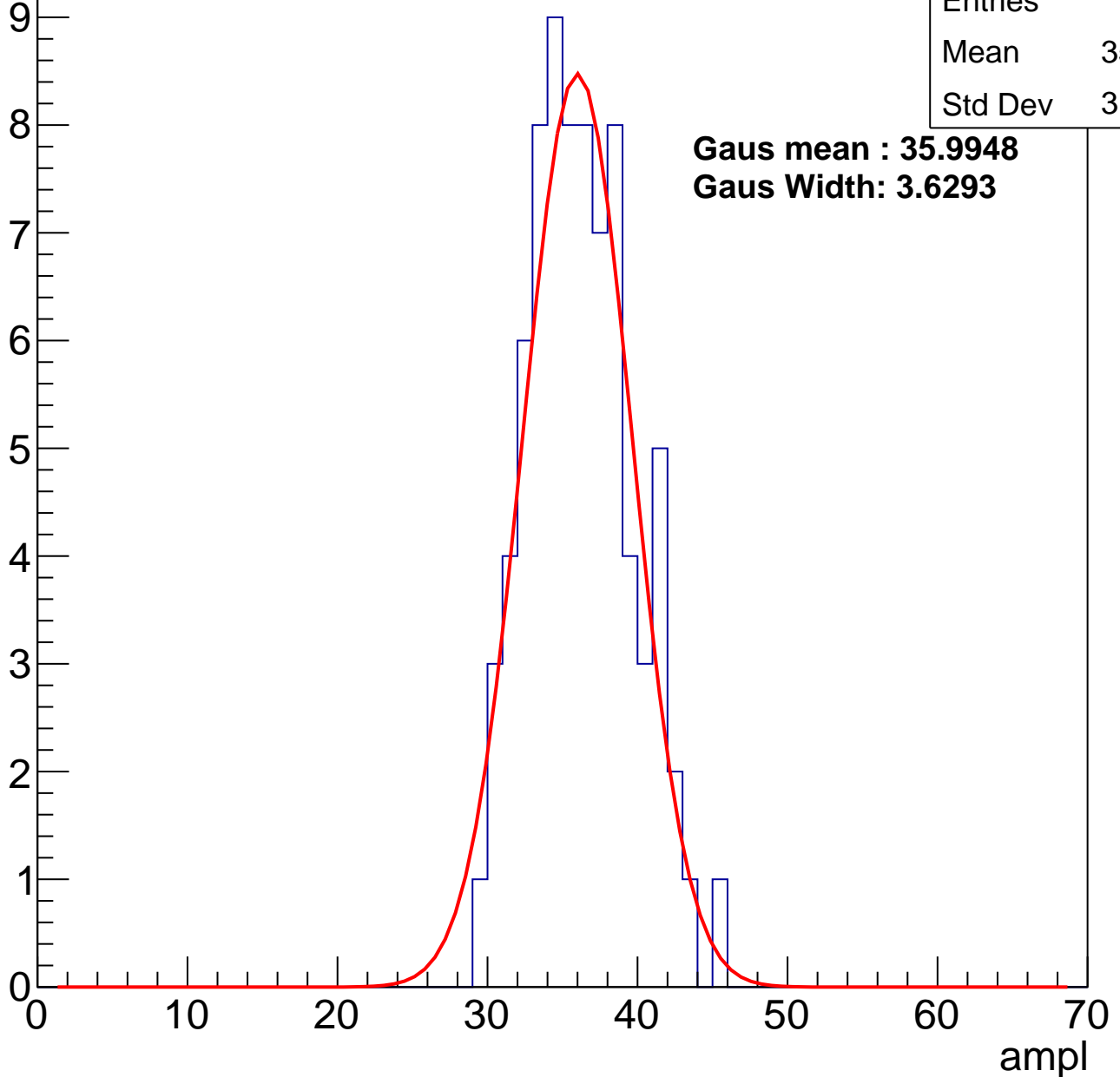
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	35.76
Std Dev	3.424

Gaus mean : 35.9948

Gaus Width: 3.6293



B0L102S, U12-ch71, adc2

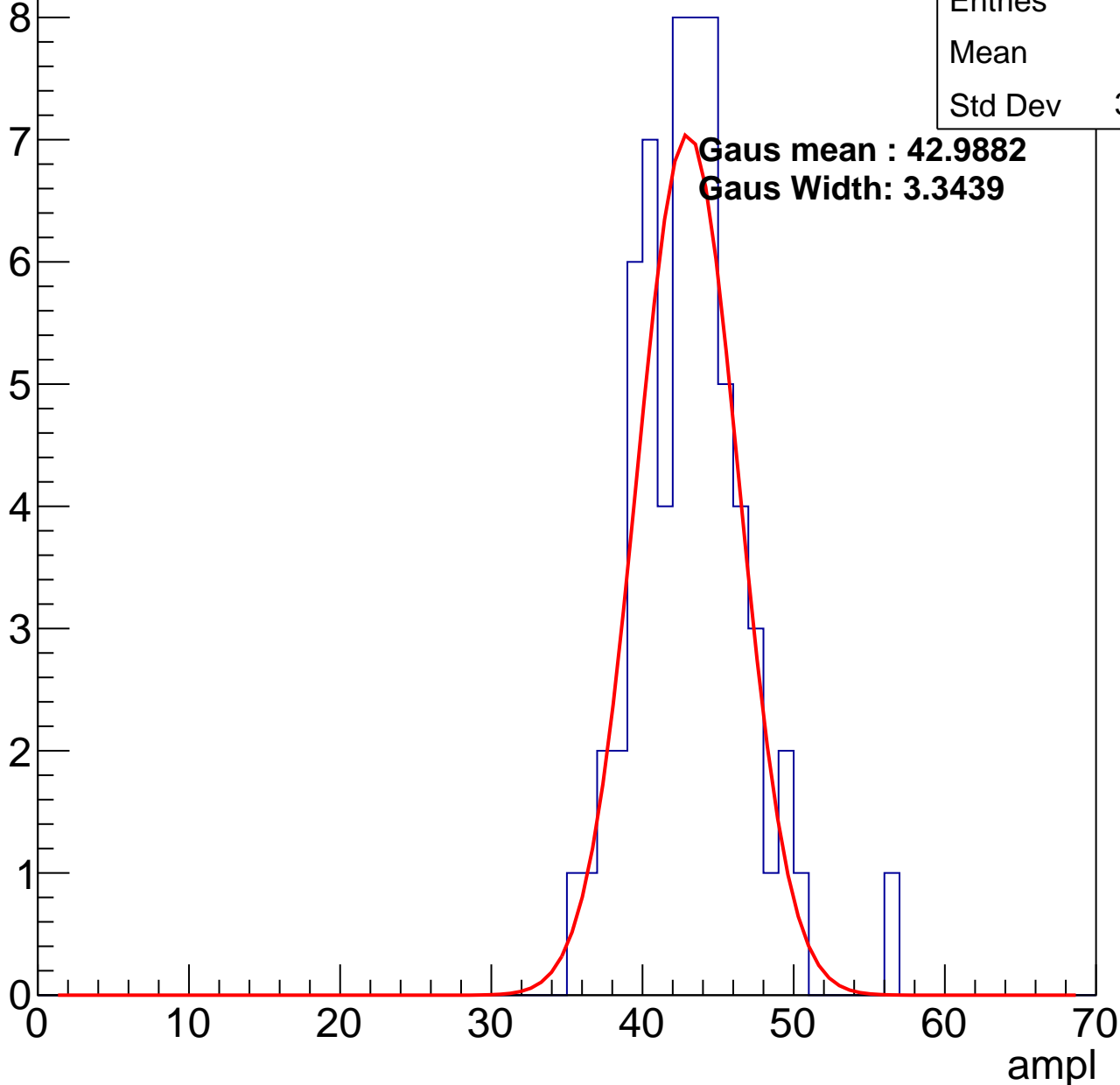
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.7
Std Dev	3.621

Gaus mean : 42.9882

Gaus Width: 3.3439

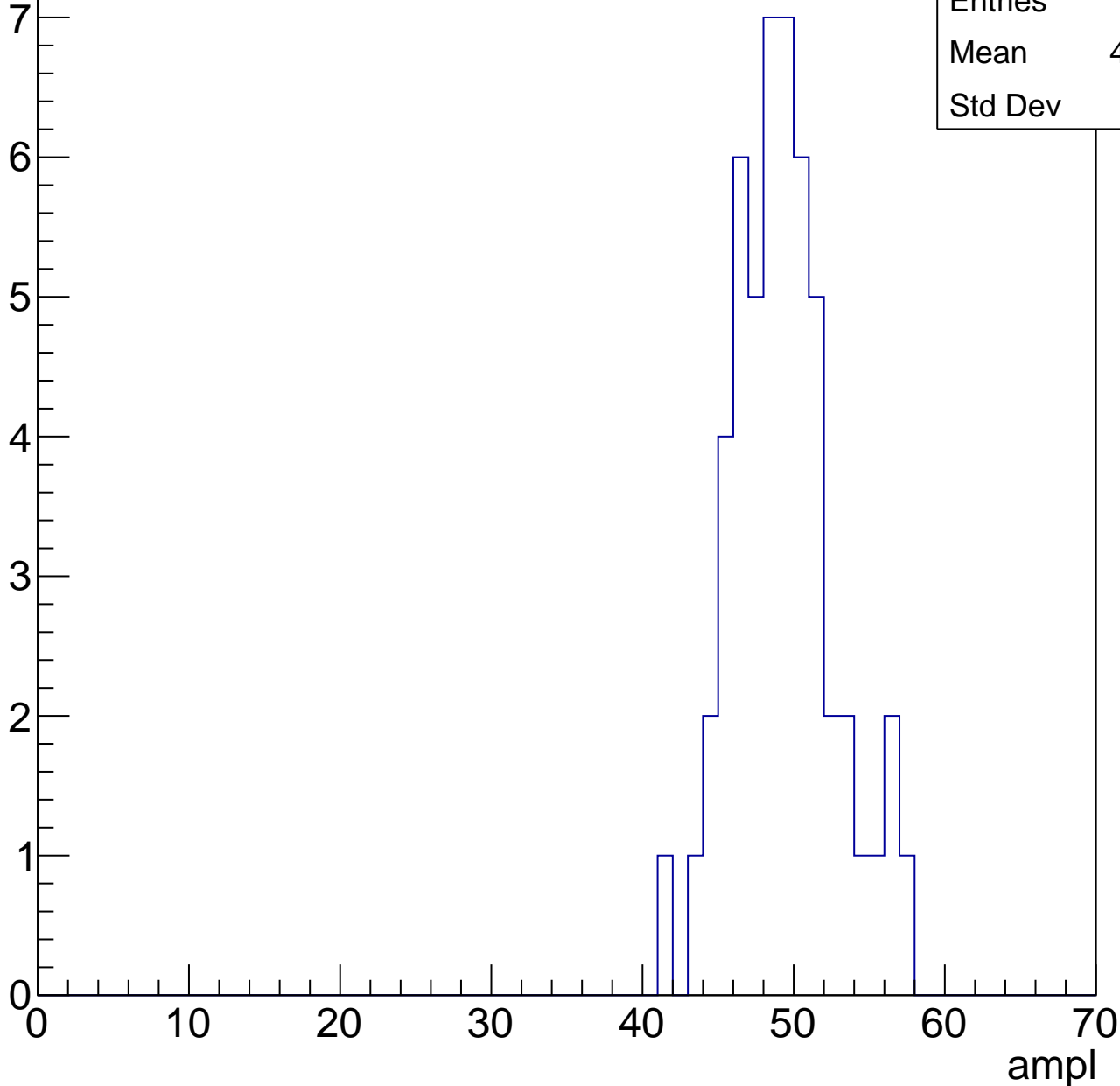


B0L102S, U12-ch71, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	48.77
Std Dev	3.34

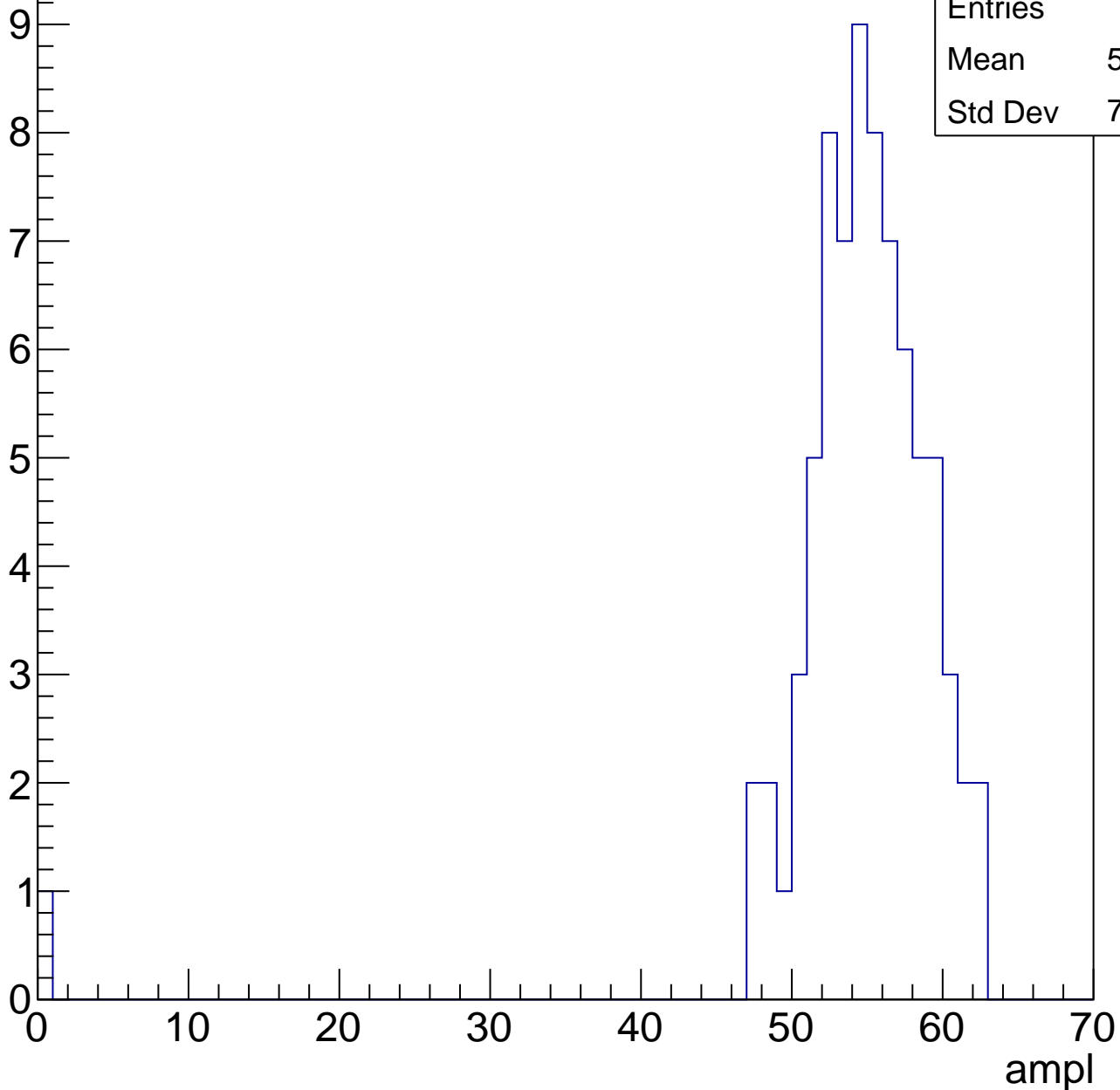


B0L102S, U12-ch71, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	53.97
Std Dev	7.138

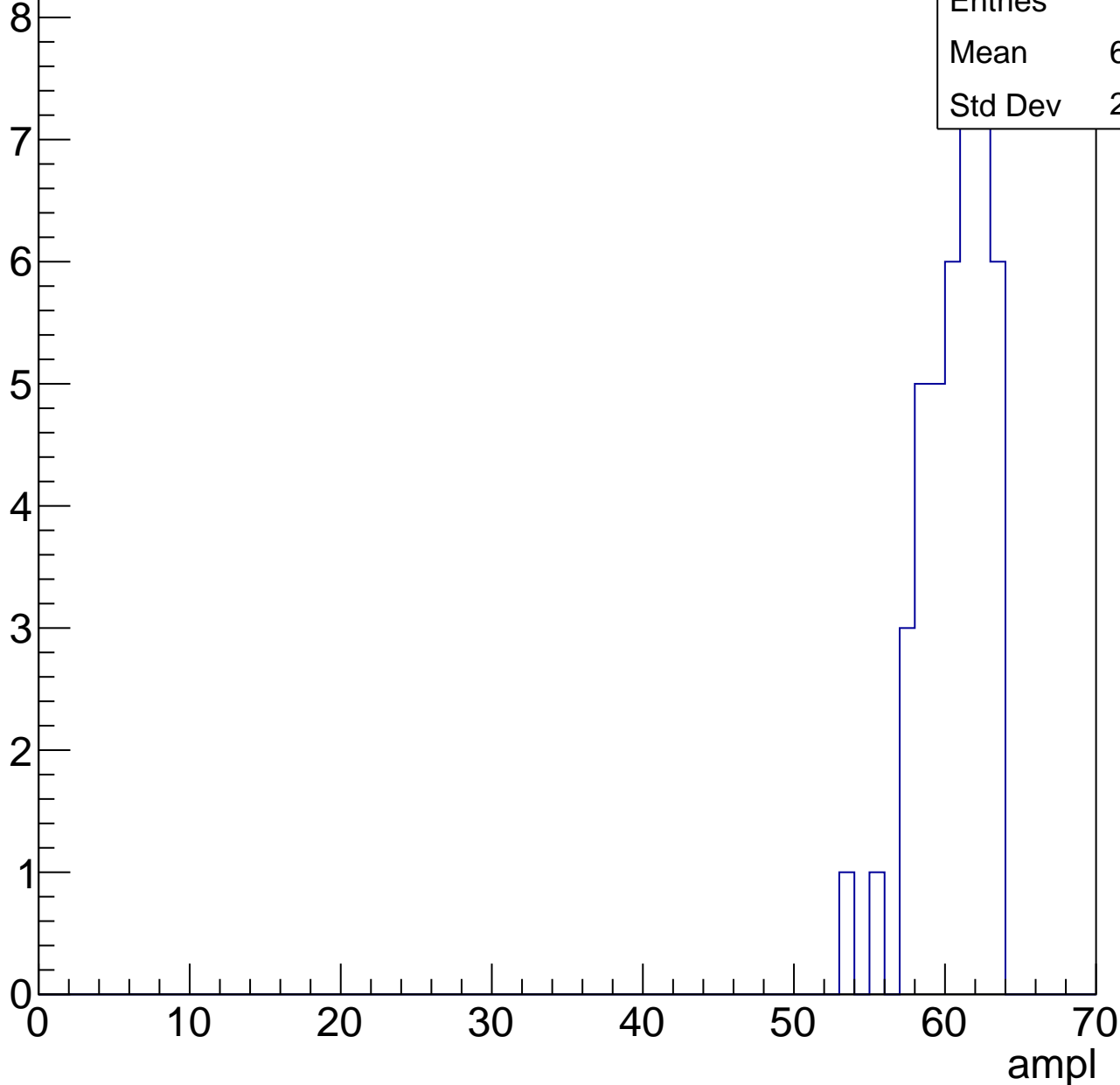


B0L102S, U12-ch71, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	60.14
Std Dev	2.258



B0L102S, U12-ch71, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch71, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch72, adc0

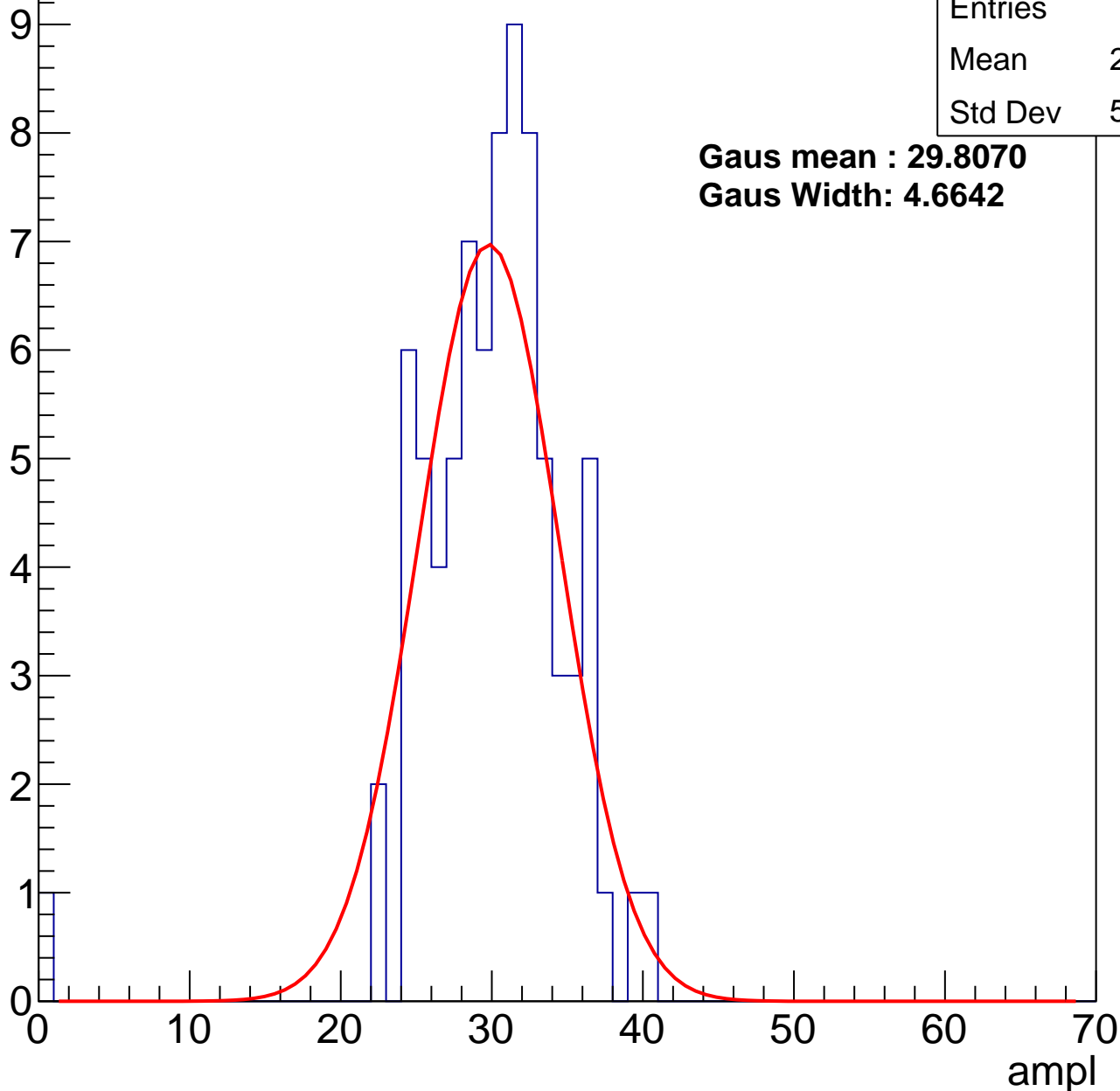
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	29.56
Std Dev	5.143

Gaus mean : 29.8070

Gaus Width: 4.6642



B0L102S, U12-ch72, adc1

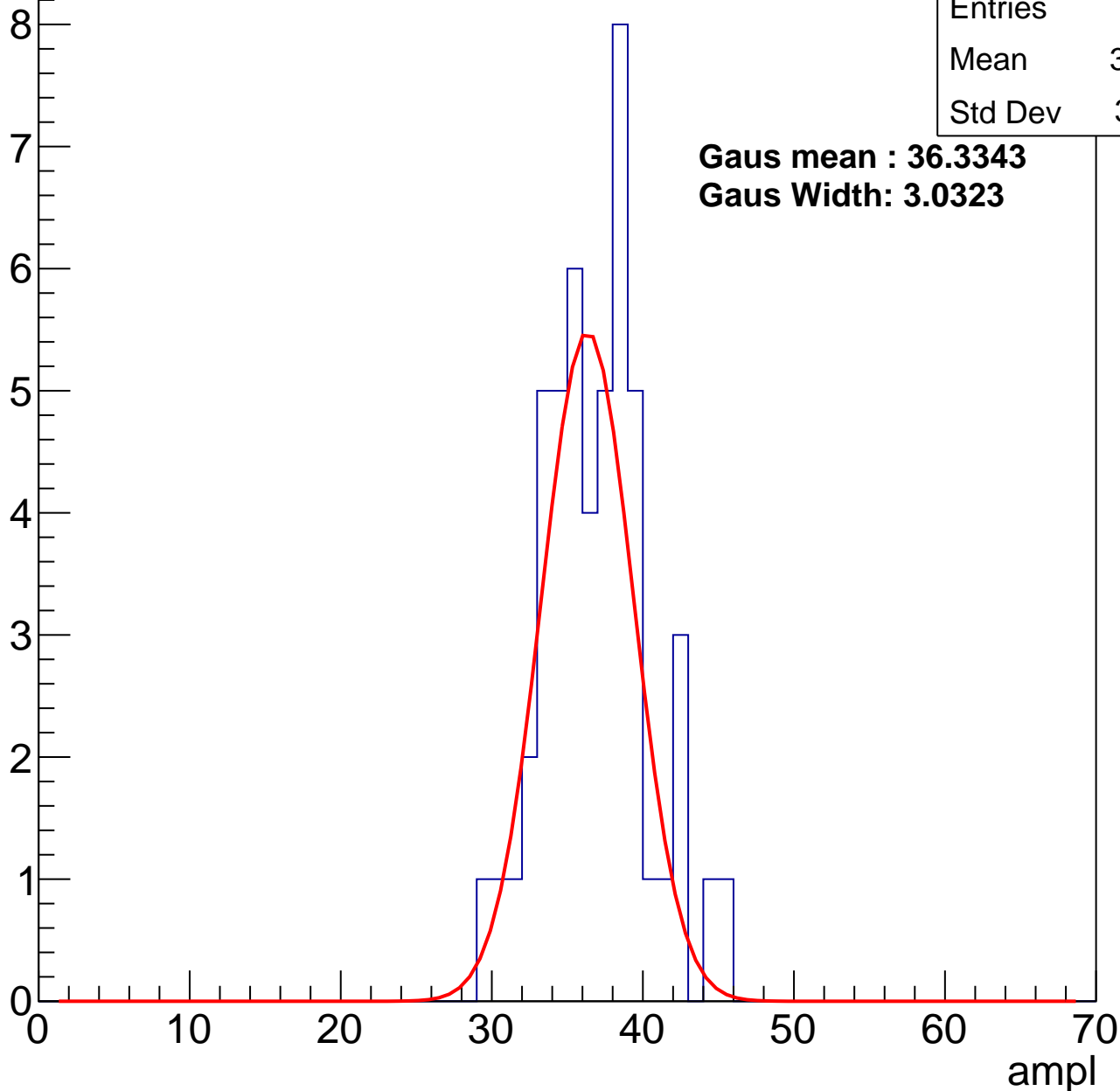
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	36.46
Std Dev	3.401

Gaus mean : 36.3343

Gaus Width: 3.0323



B0L102S, U12-ch72, adc2

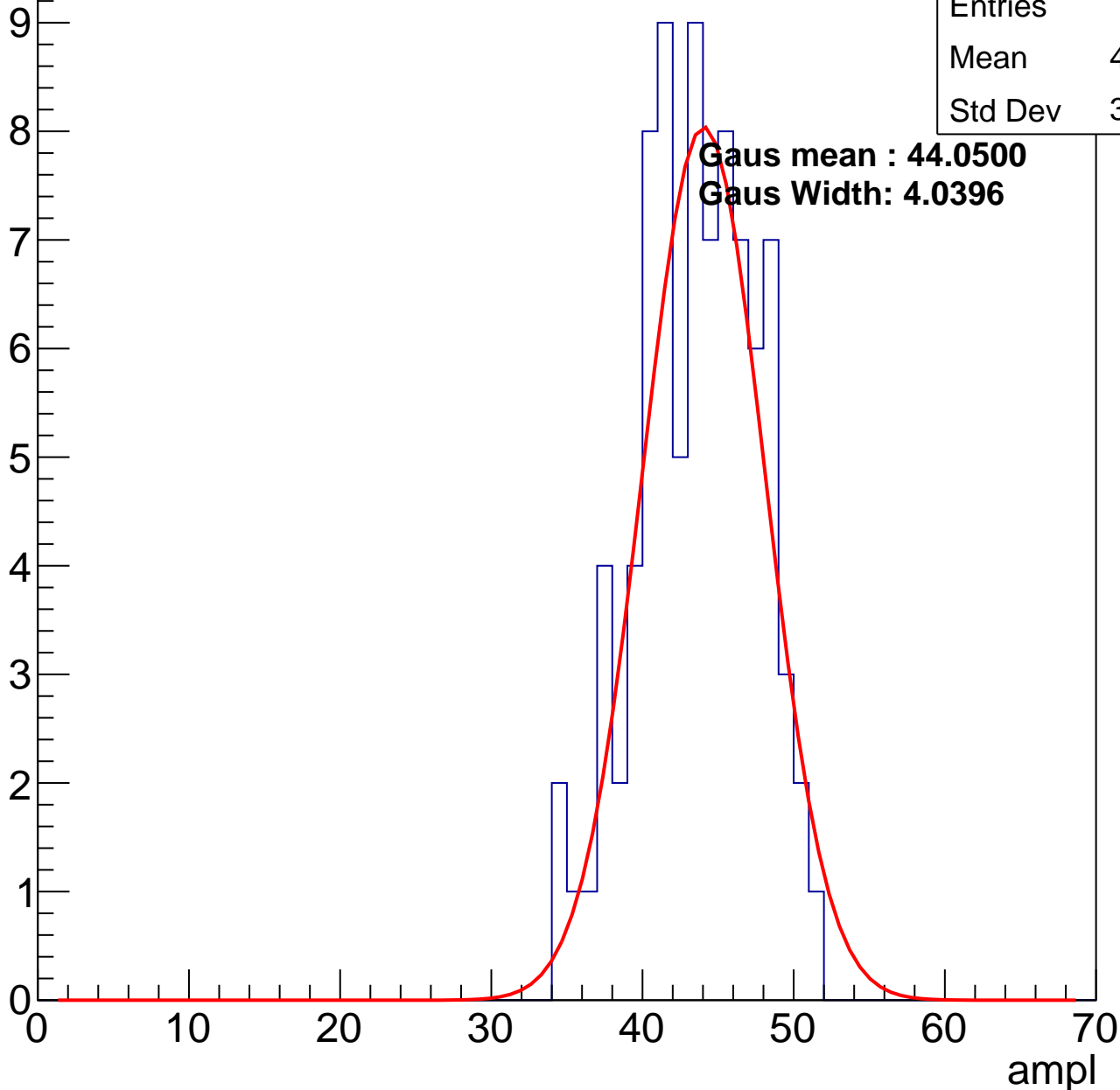
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	43.15
Std Dev	3.869

Gaus mean : 44.0500

Gaus Width: 4.0396

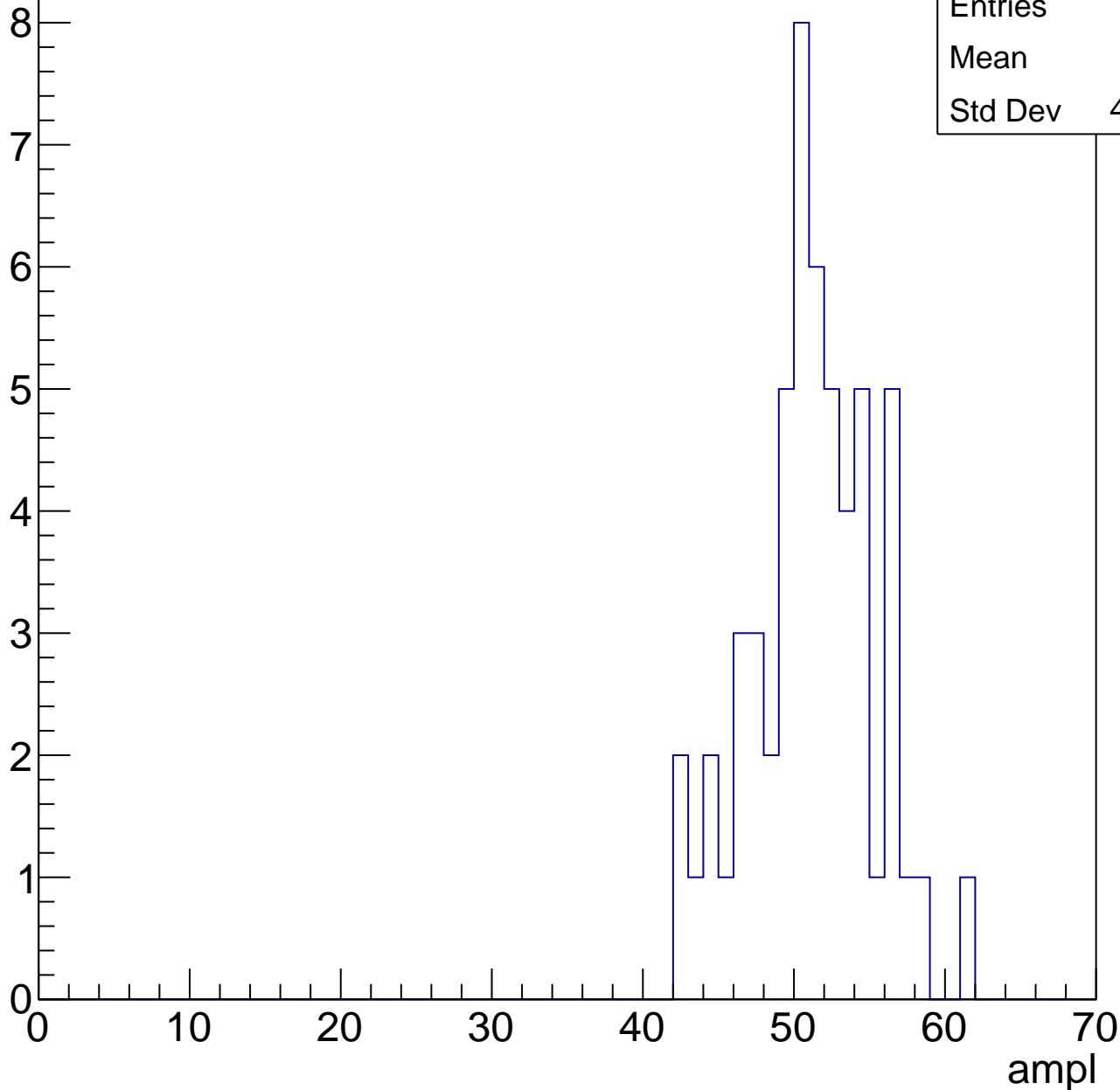


B0L102S, U12-ch72, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	50.7
Std Dev	4.044

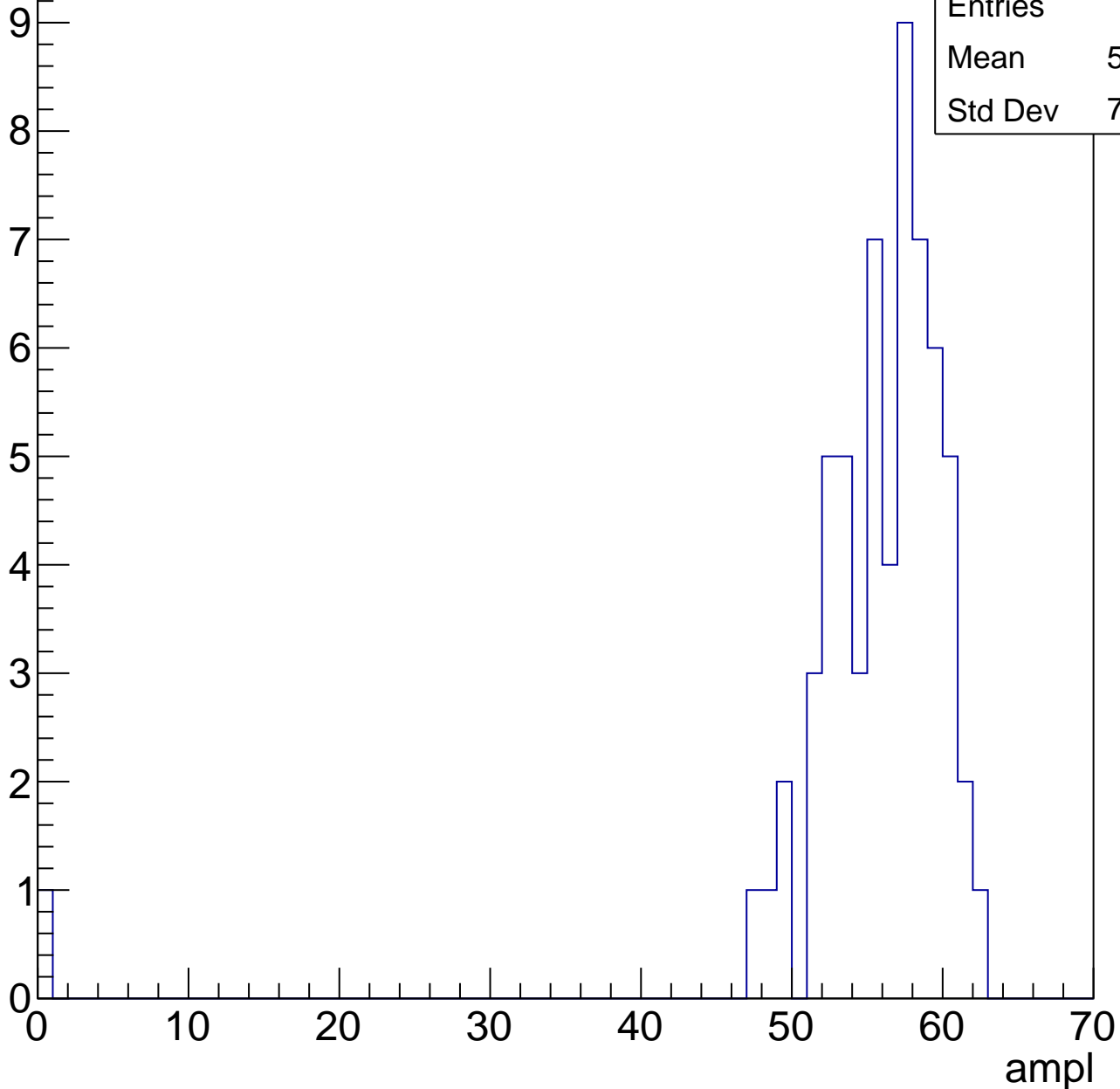


B0L102S, U12-ch72, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	54.82
Std Dev	7.797

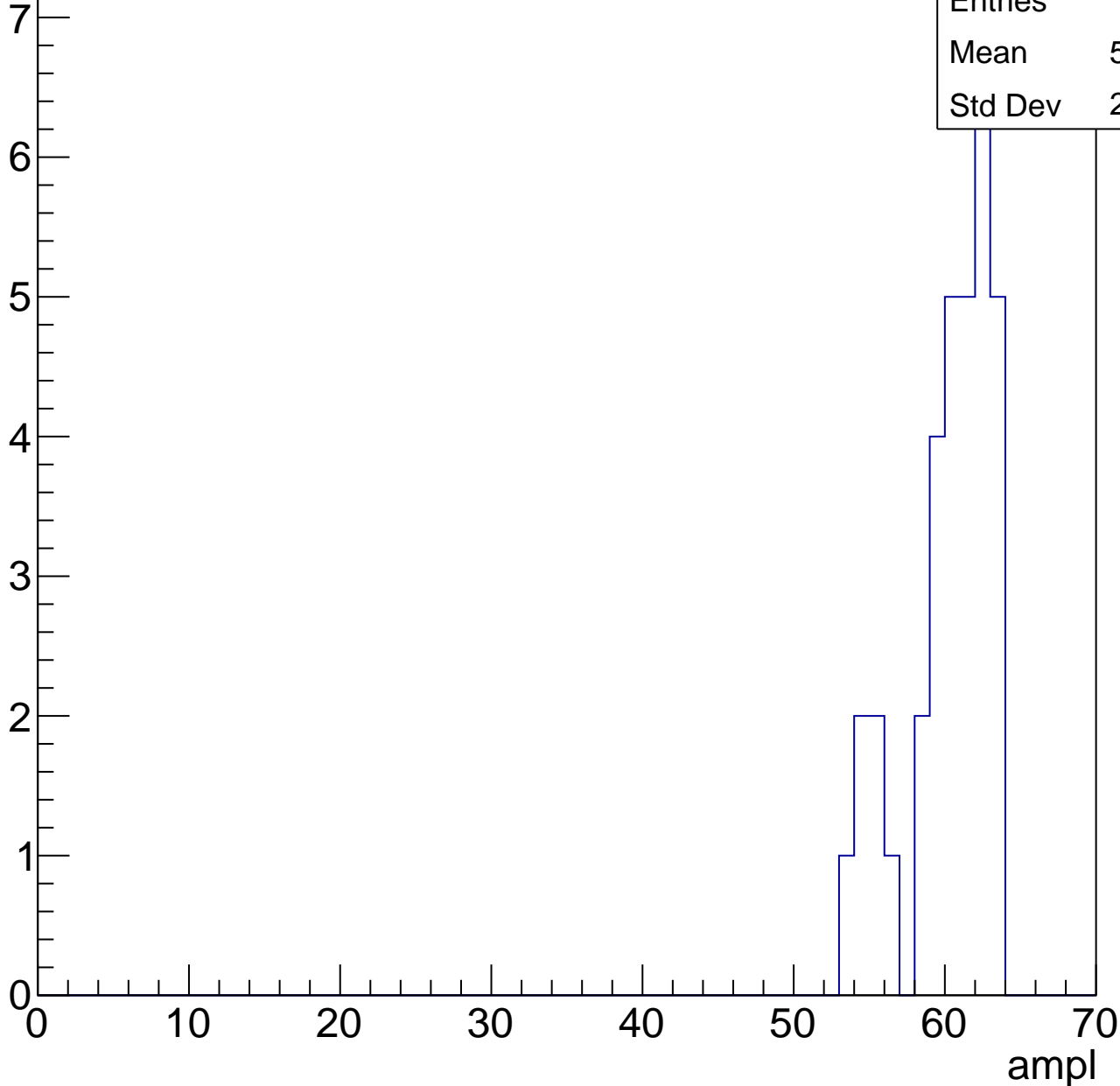


B0L102S, U12-ch72, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	34
Mean	59.79
Std Dev	2.847

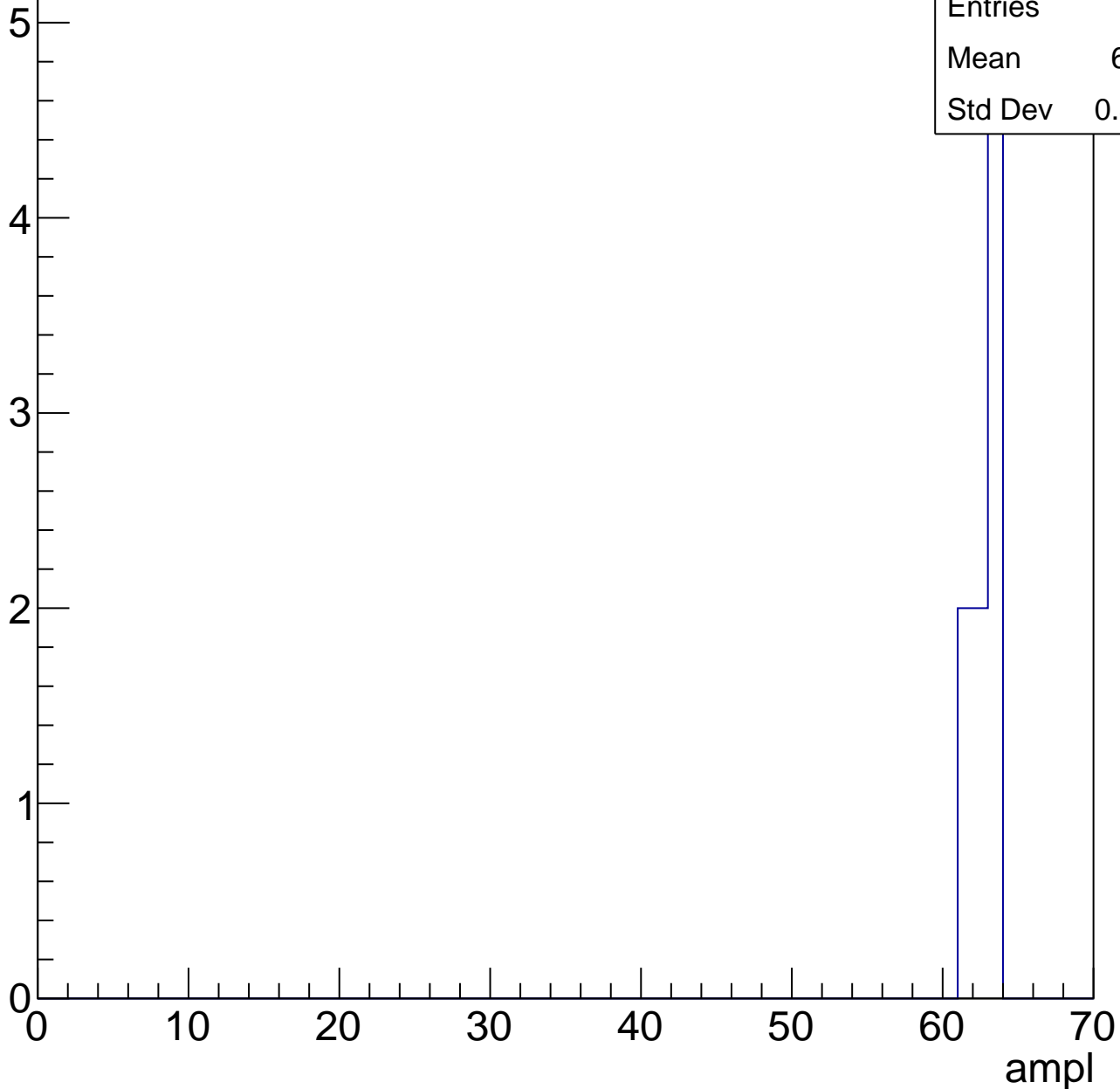


B0L102S, U12-ch72, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	9
Mean	62.33
Std Dev	0.8165



B0L102S, U12-ch72, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch73, adc0

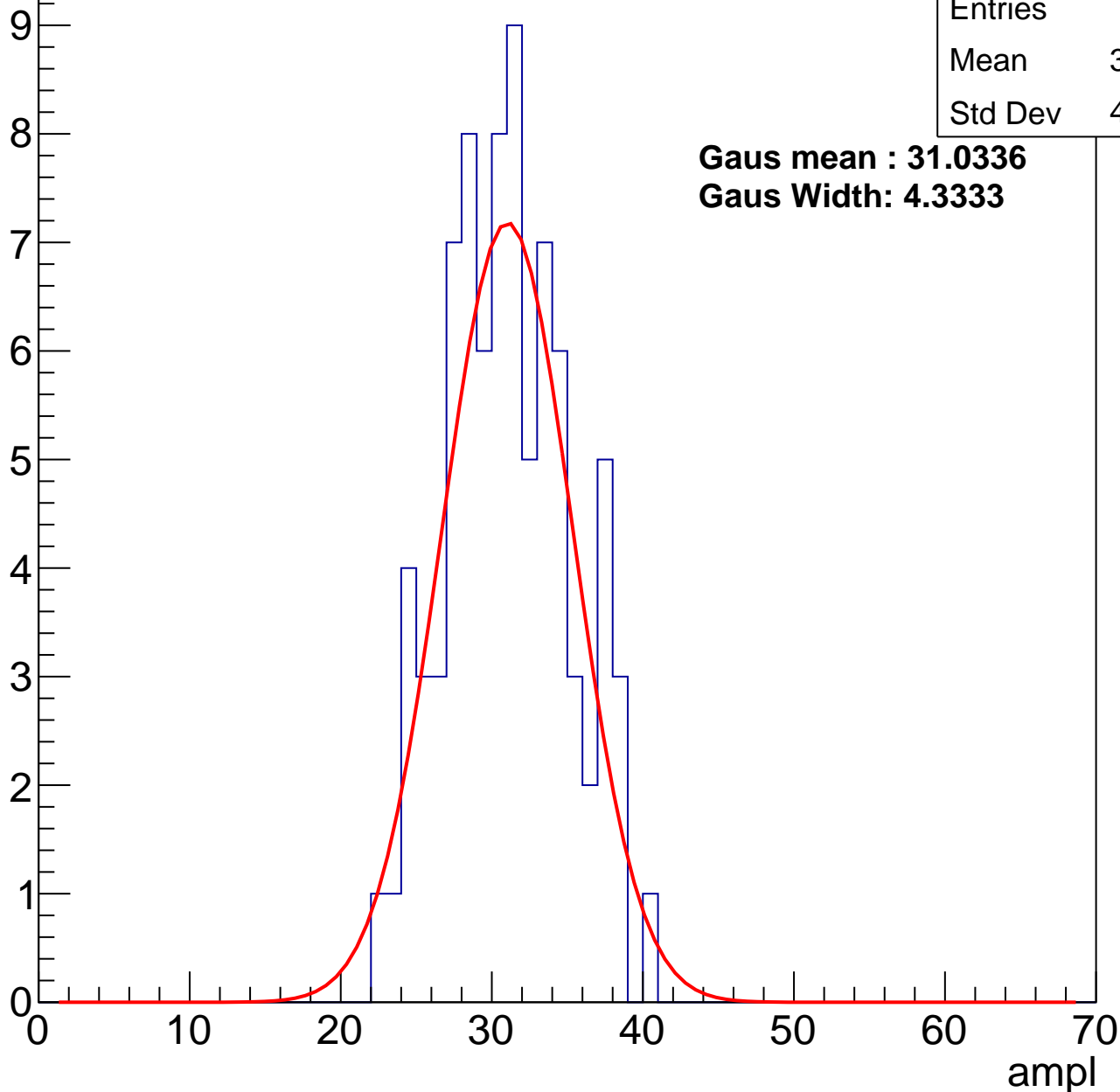
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	30.62
Std Dev	4.014

Gaus mean : 31.0336

Gaus Width: 4.3333



B0L102S, U12-ch73, adc1

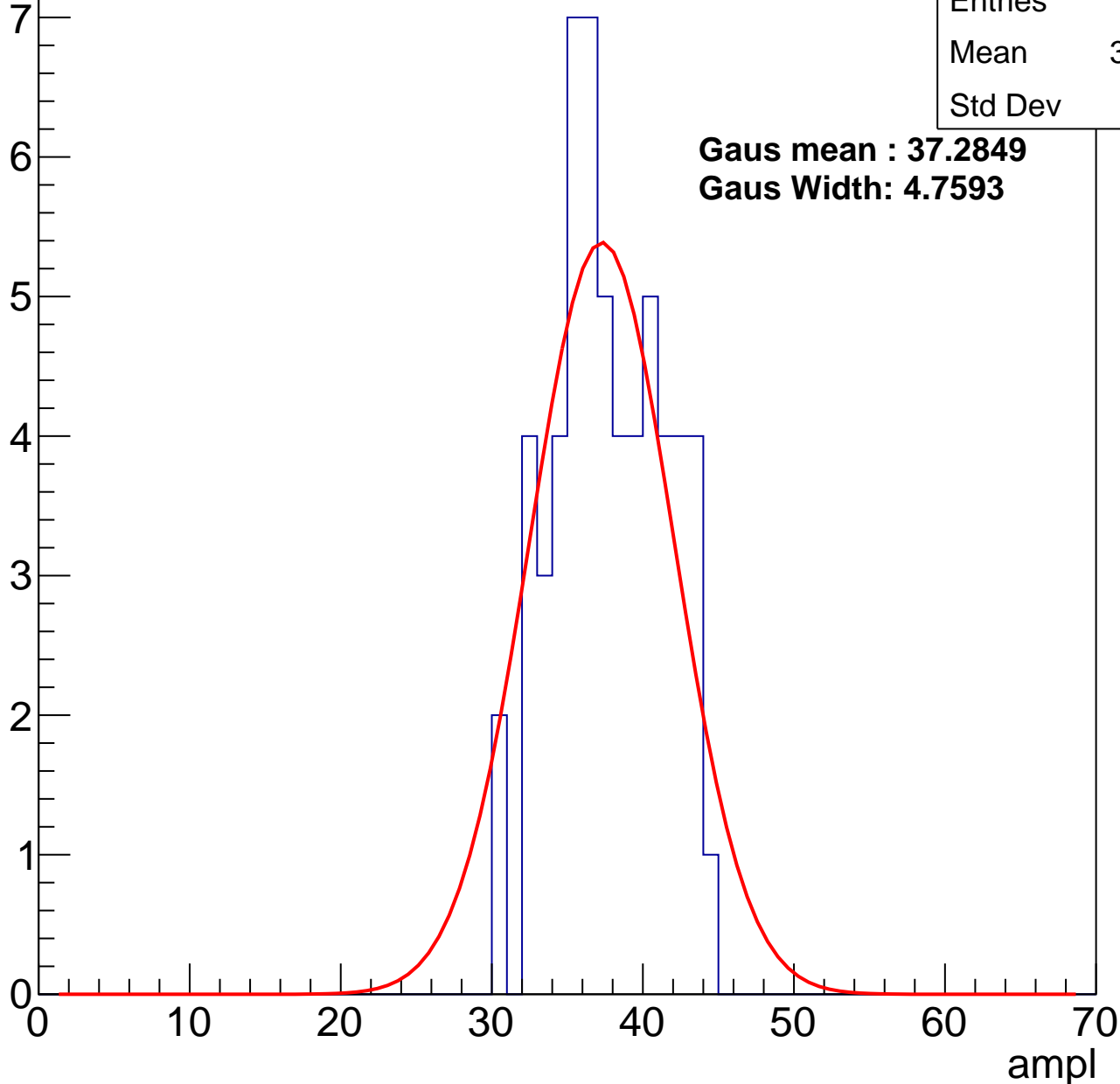
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	37.26
Std Dev	3.56

Gaus mean : 37.2849

Gaus Width: 4.7593



B0L102S, U12-ch73, adc2

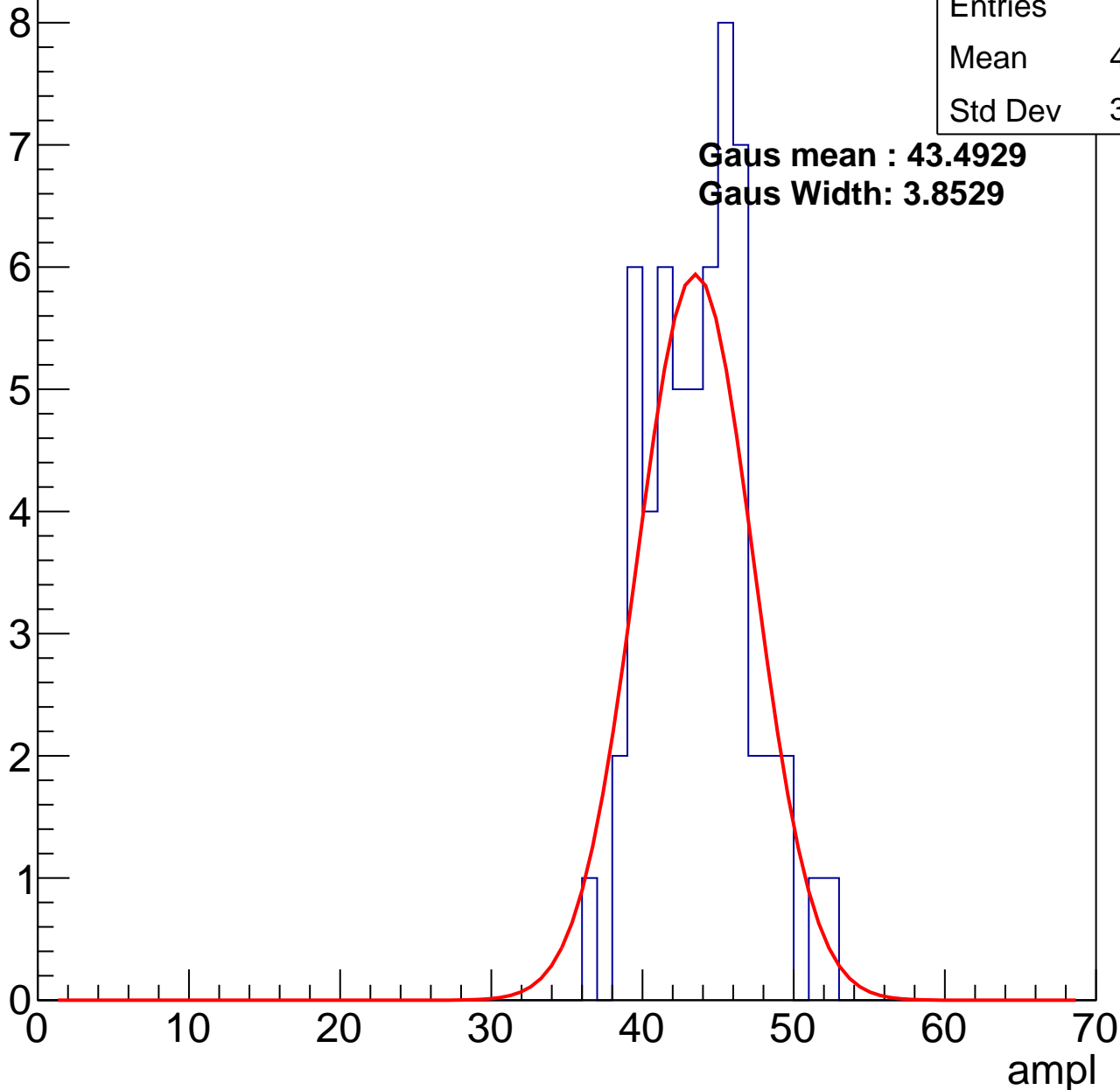
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	43.34
Std Dev	3.356

Gaus mean : 43.4929

Gaus Width: 3.8529

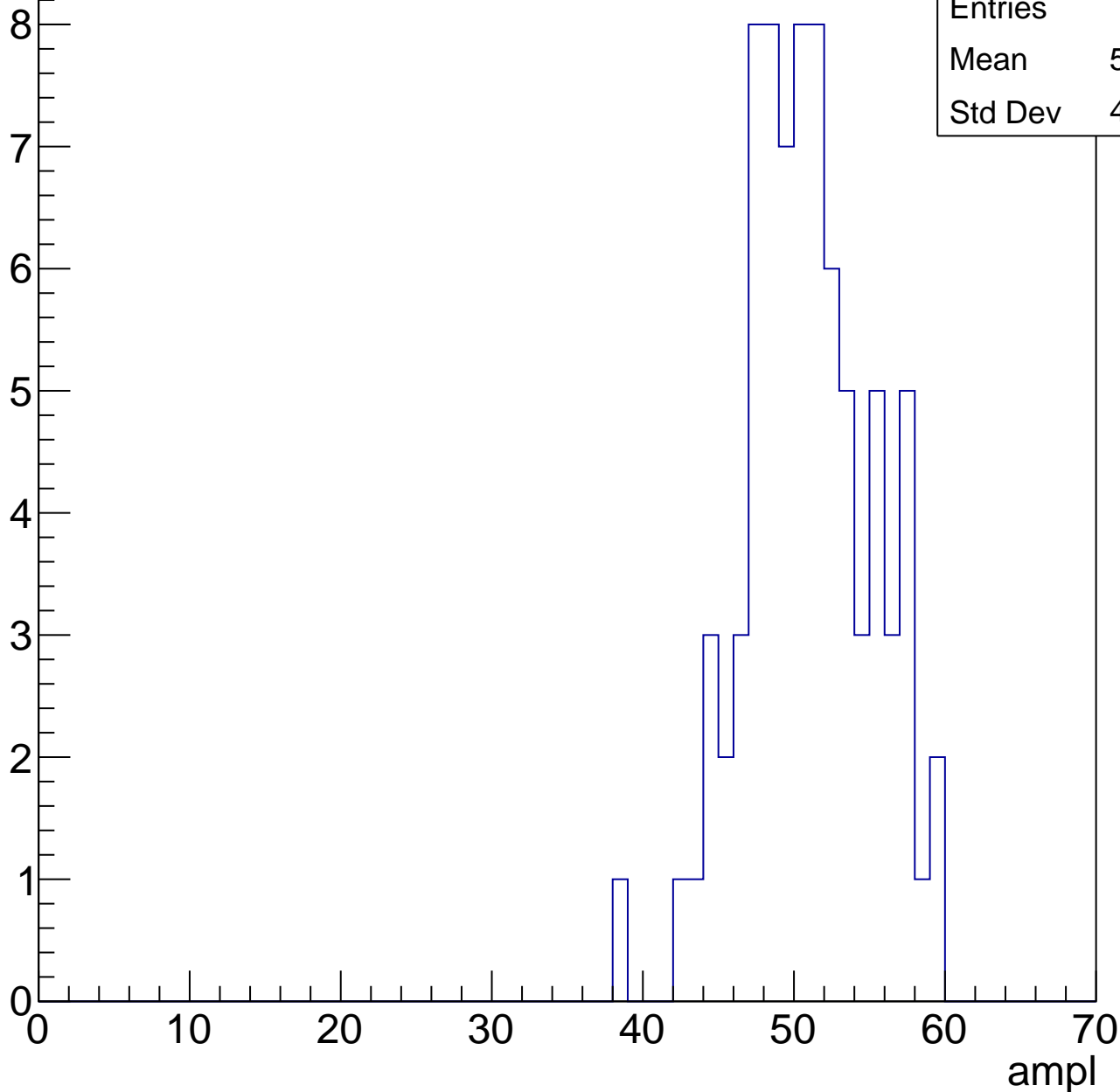


B0L102S, U12-ch73, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	50.46
Std Dev	4.162

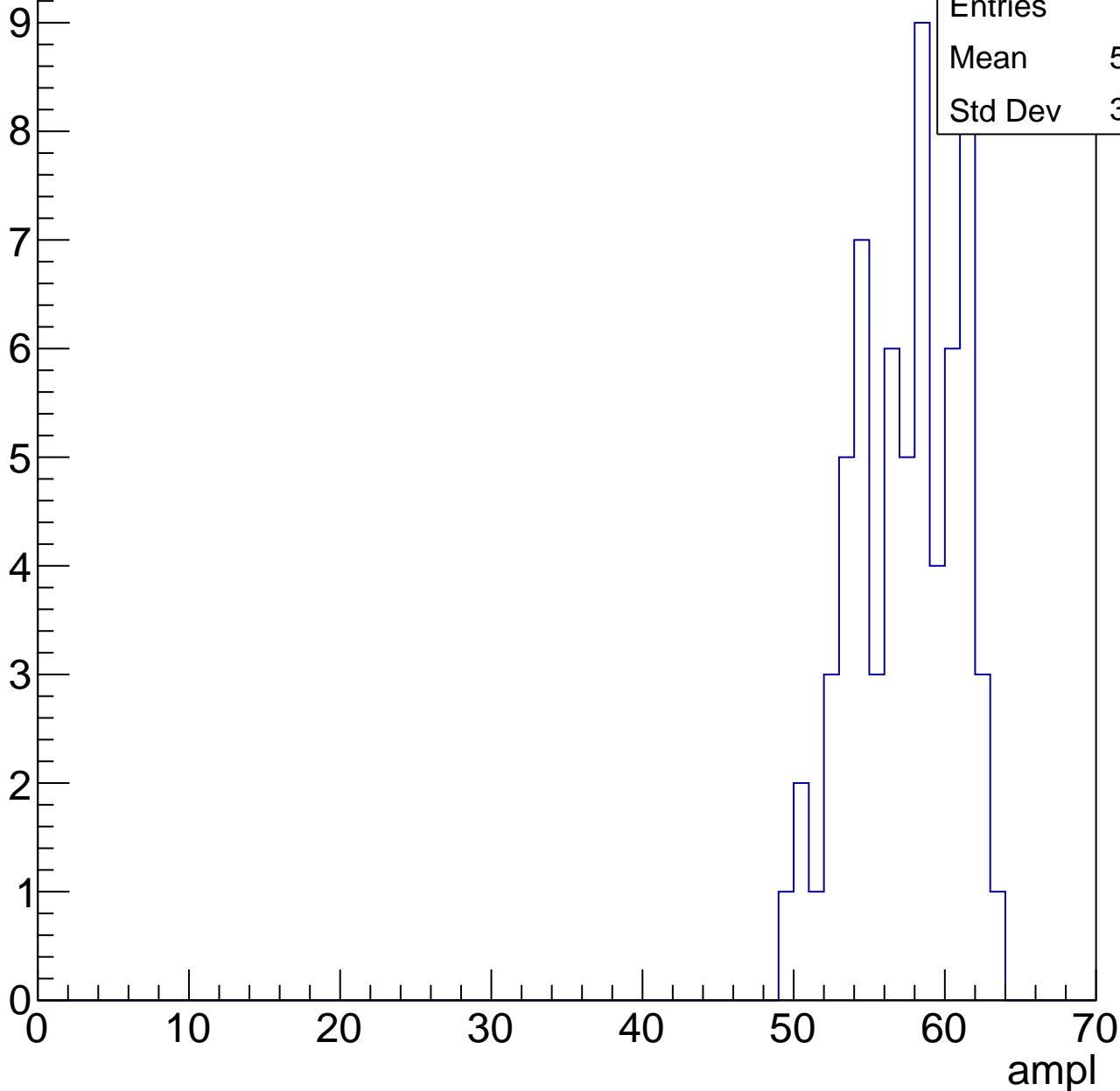


B0L102S, U12-ch73, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	56.88
Std Dev	3.426

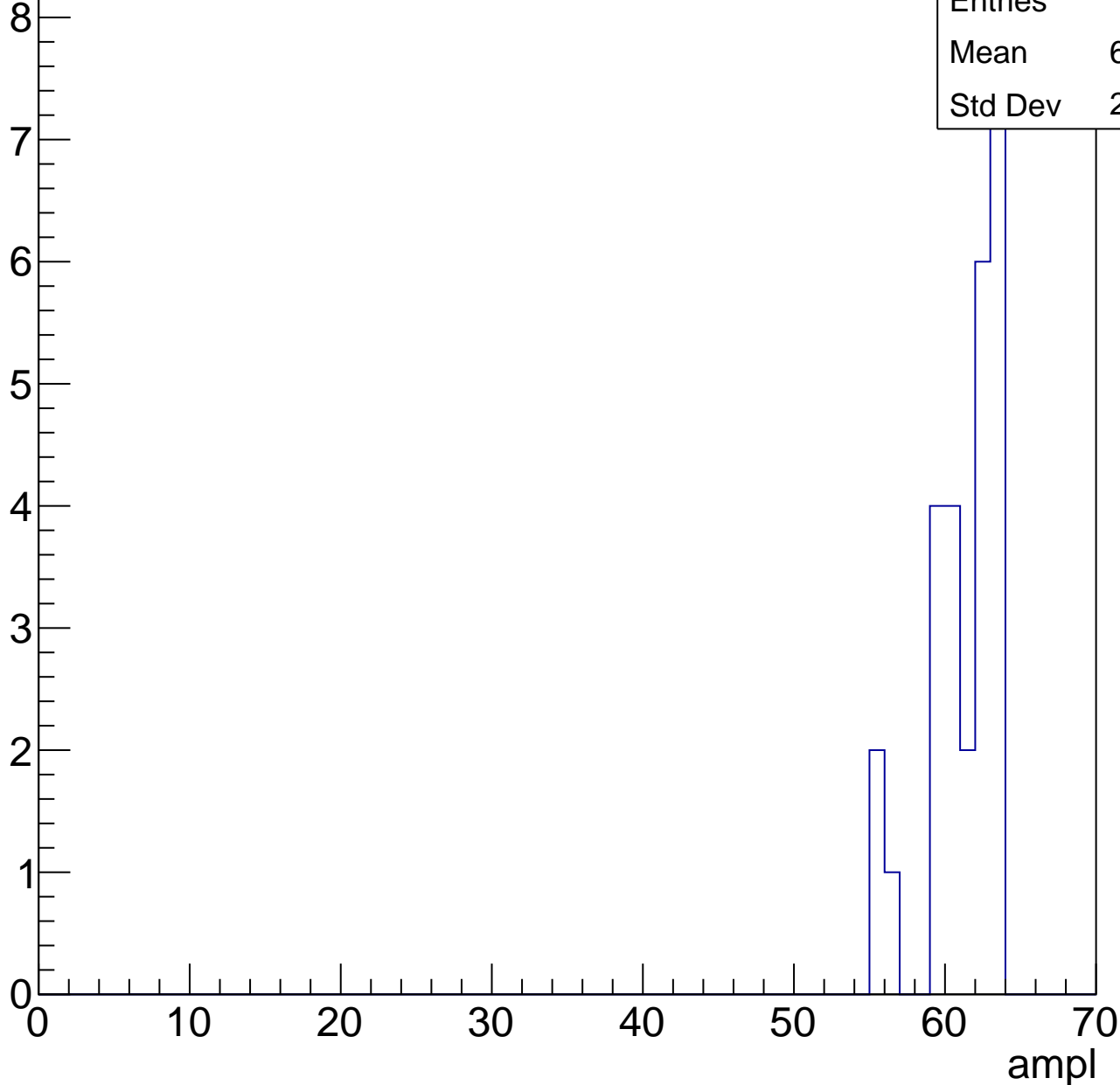


B0L102S, U12-ch73, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	27
Mean	60.74
Std Dev	2.382



B0L102S, U12-ch73, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch73, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch74, adc0

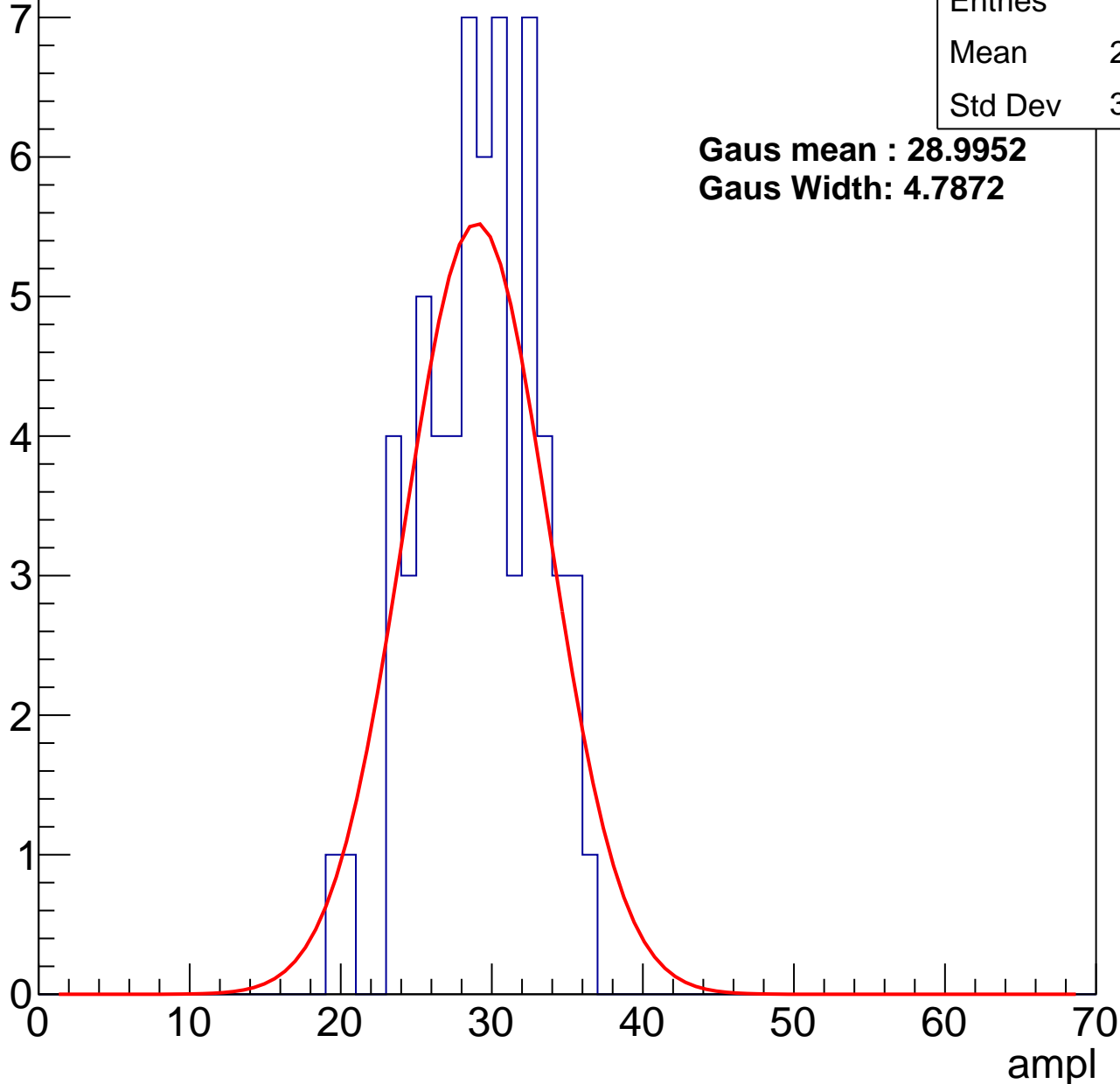
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.76
Std Dev	3.808

Gaus mean : 28.9952

Gaus Width: 4.7872



B0L102S, U12-ch74, adc1

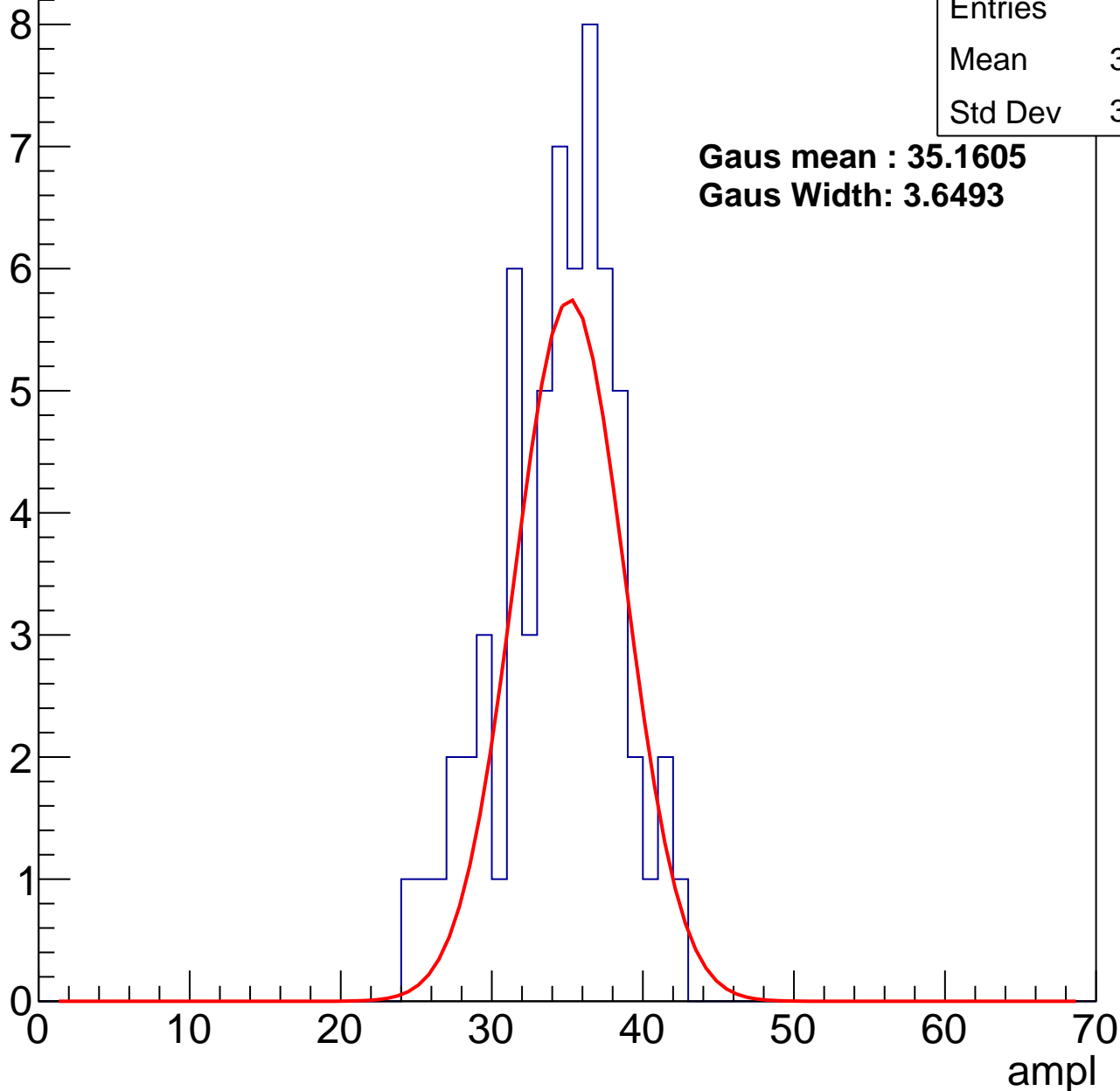
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	33.95
Std Dev	3.978

Gaus mean : 35.1605

Gaus Width: 3.6493



B0L102S, U12-ch74, adc2

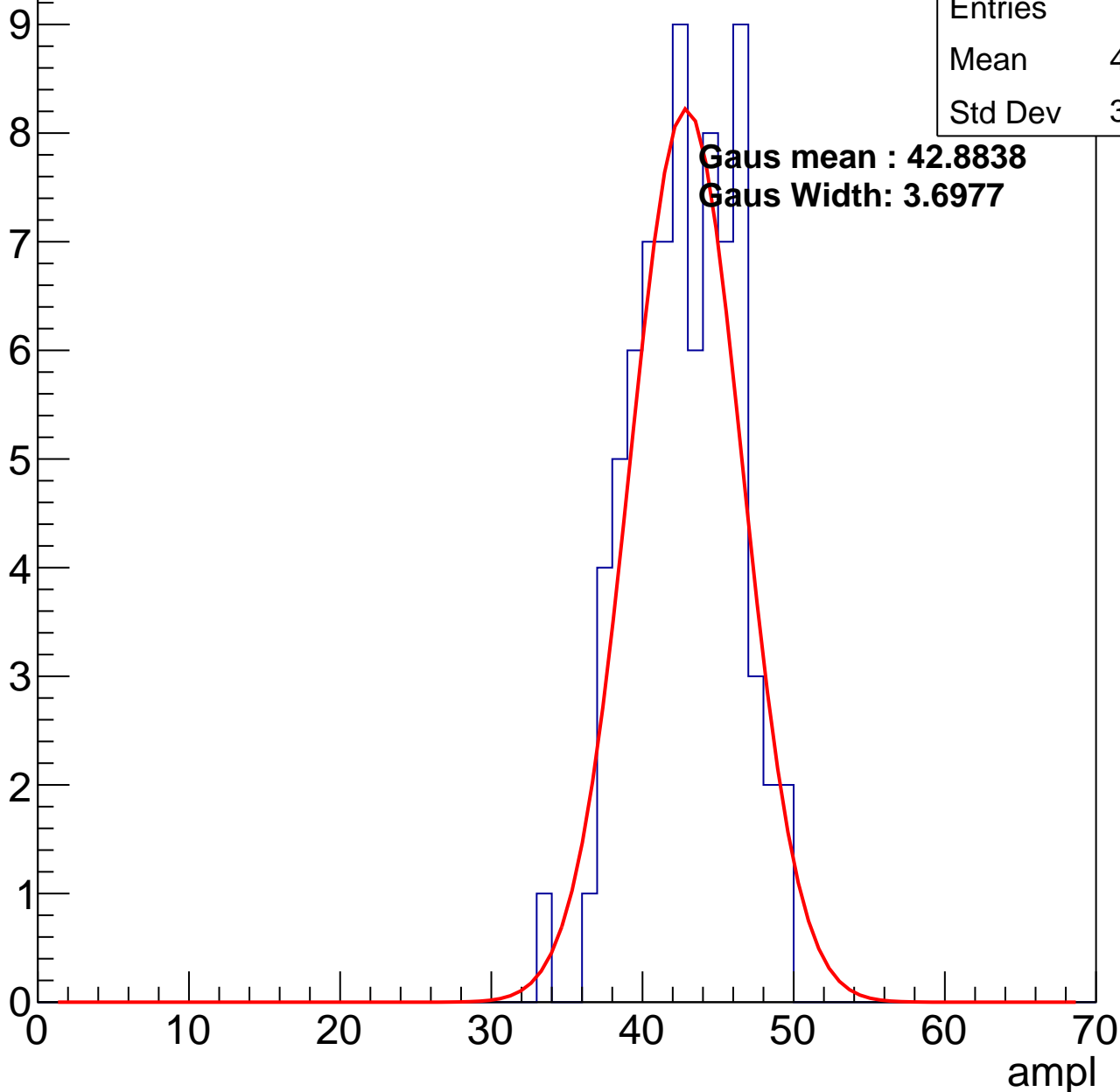
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	42.34
Std Dev	3.356

Gaus mean : 42.8838

Gaus Width: 3.6977

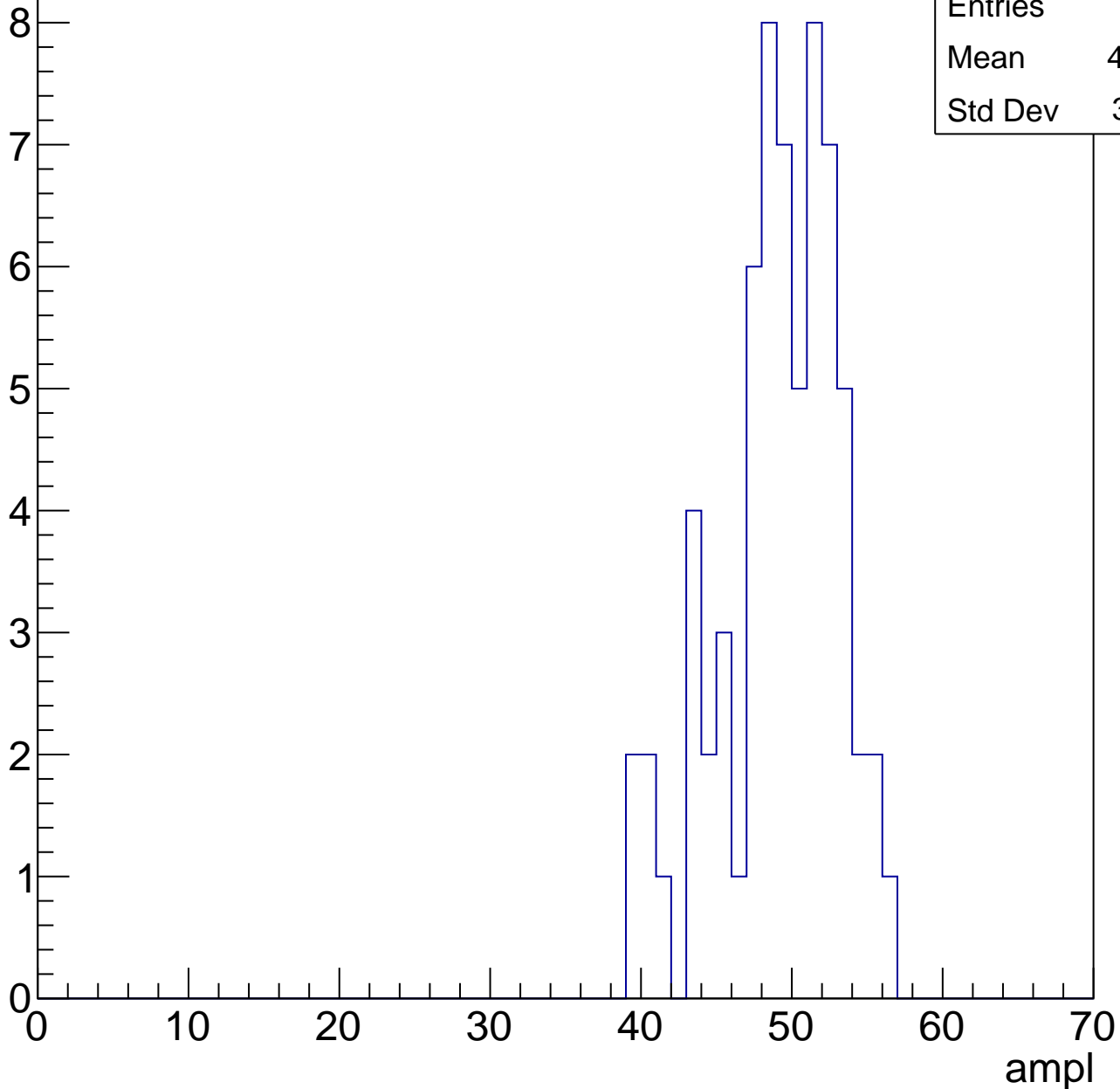


B0L102S, U12-ch74, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.64
Std Dev	3.991

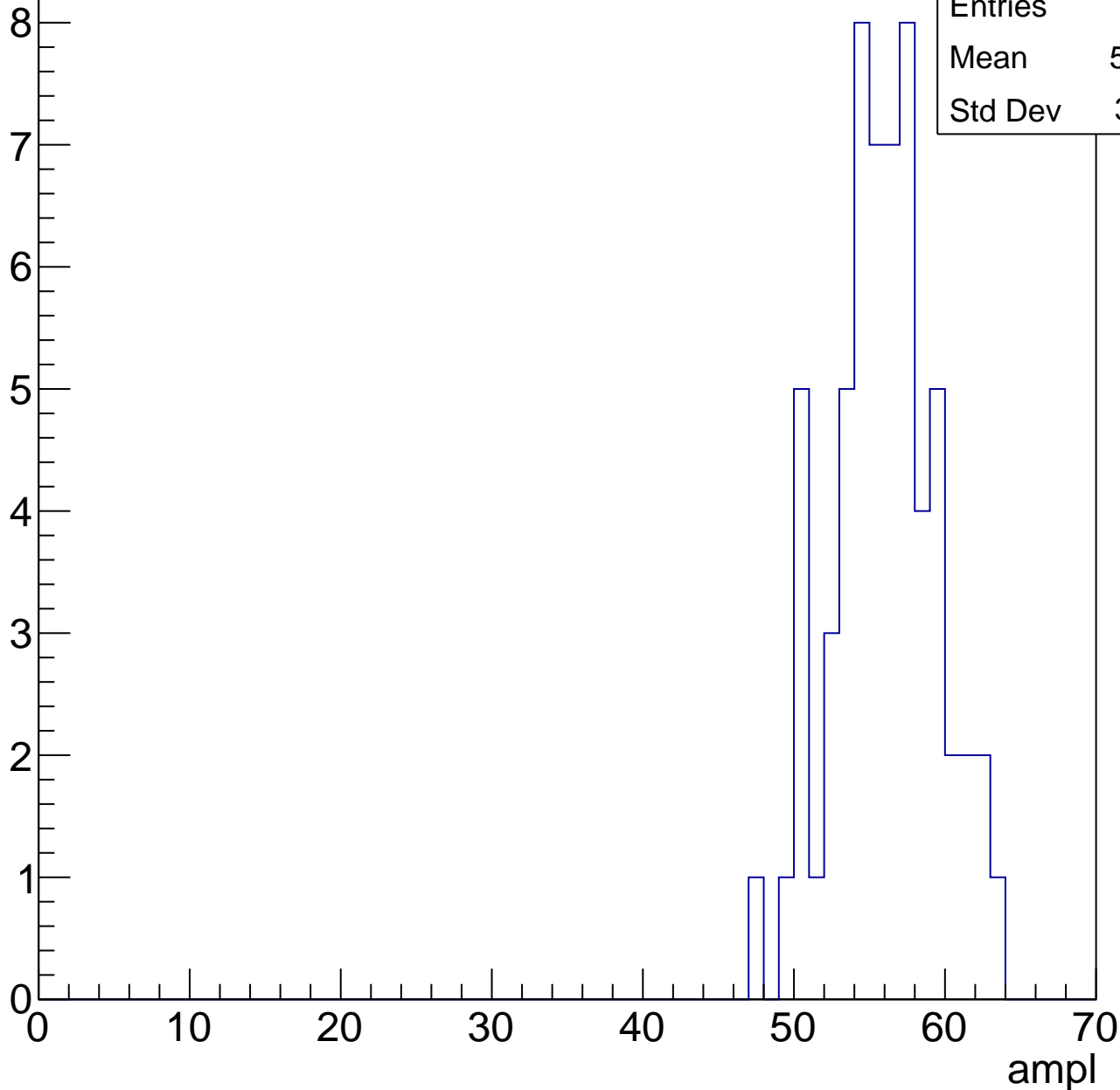


B0L102S, U12-ch74, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	55.47
Std Dev	3.411

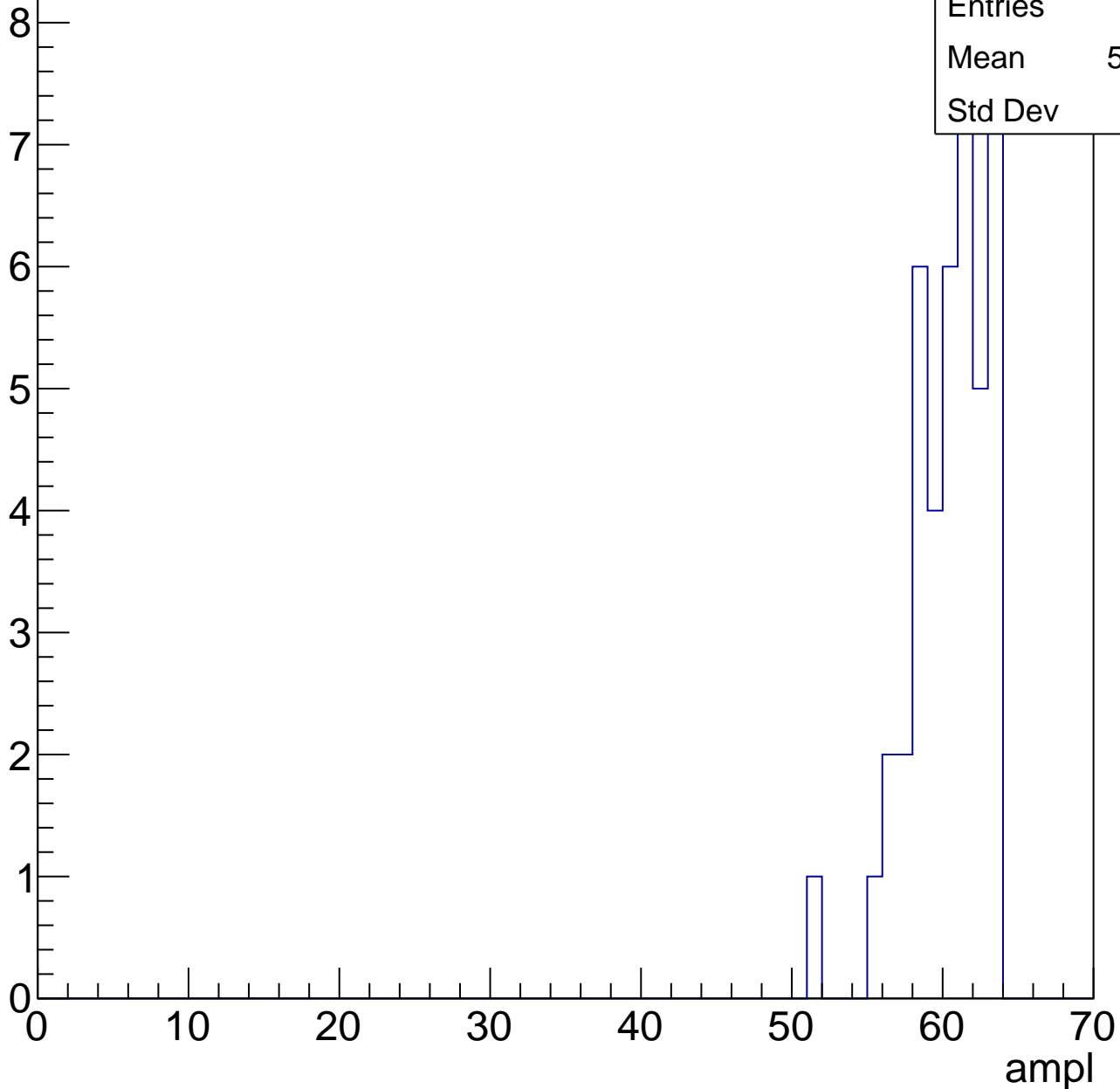


B0L102S, U12-ch74, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

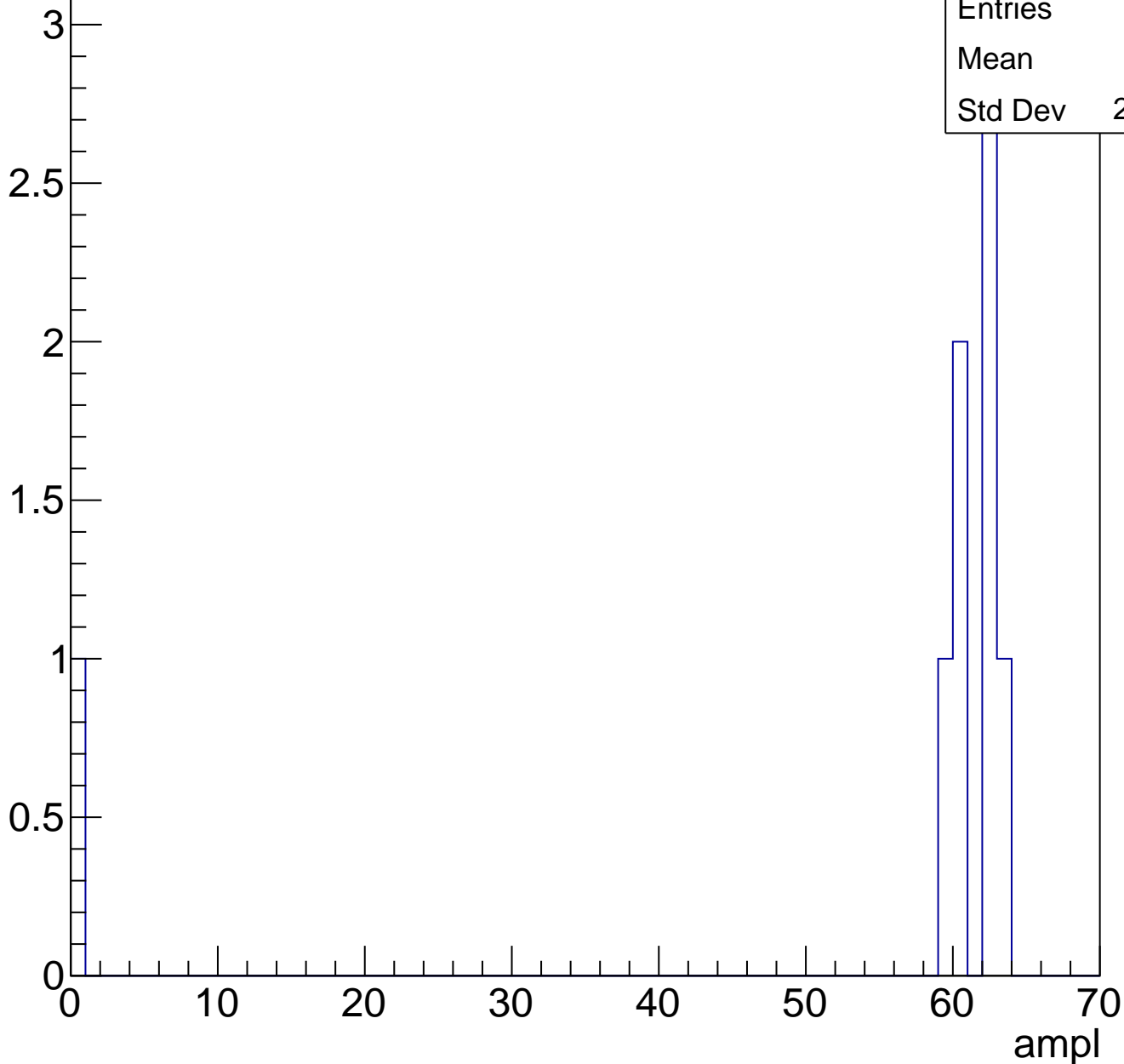
Entries	43
Mean	59.95
Std Dev	2.57



B0L102S, U12-ch74, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch74, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch75, adc0

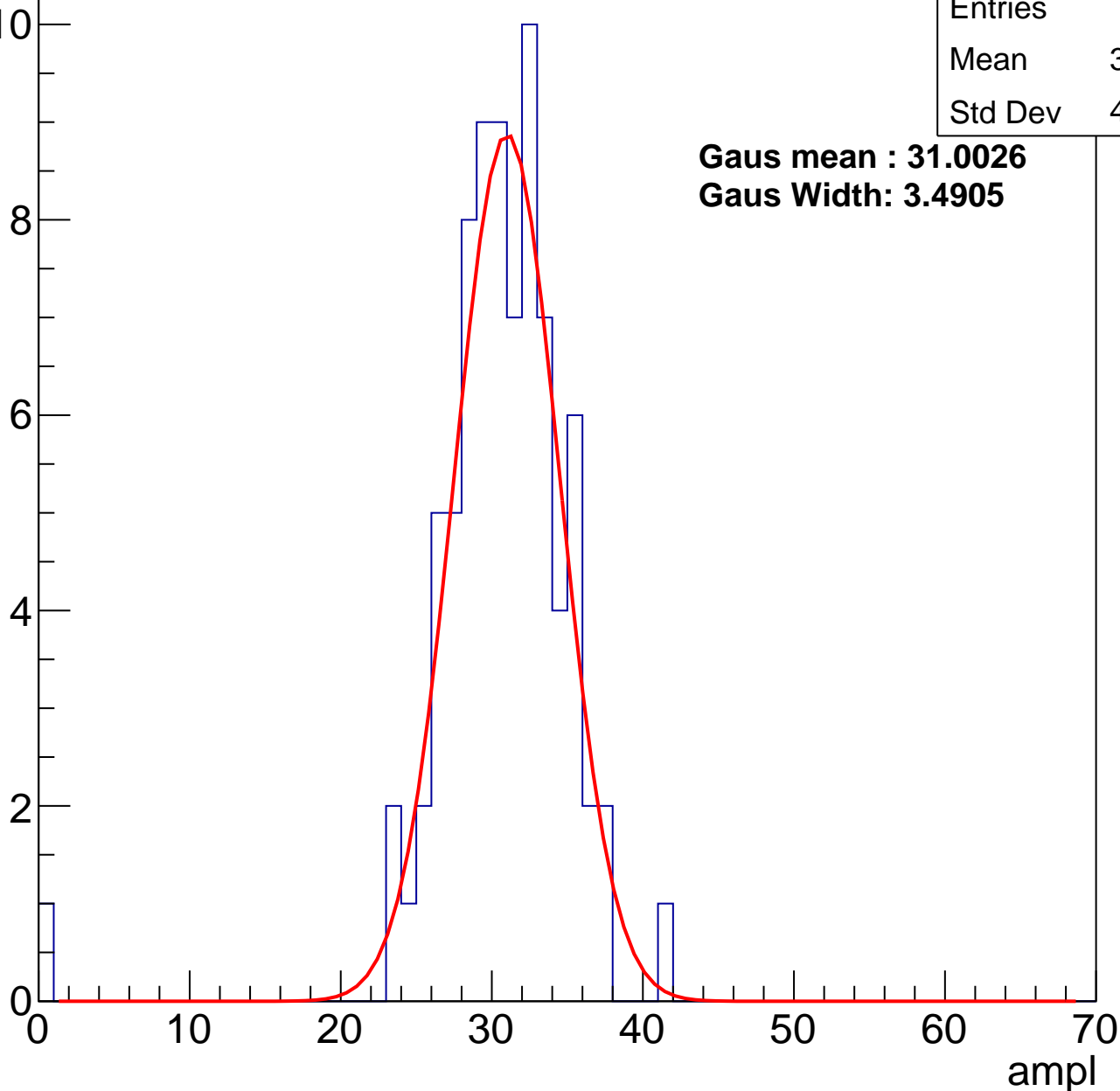
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	30.14
Std Dev	4.796

Gaus mean : 31.0026

Gaus Width: 3.4905



B0L102S, U12-ch75, adc1

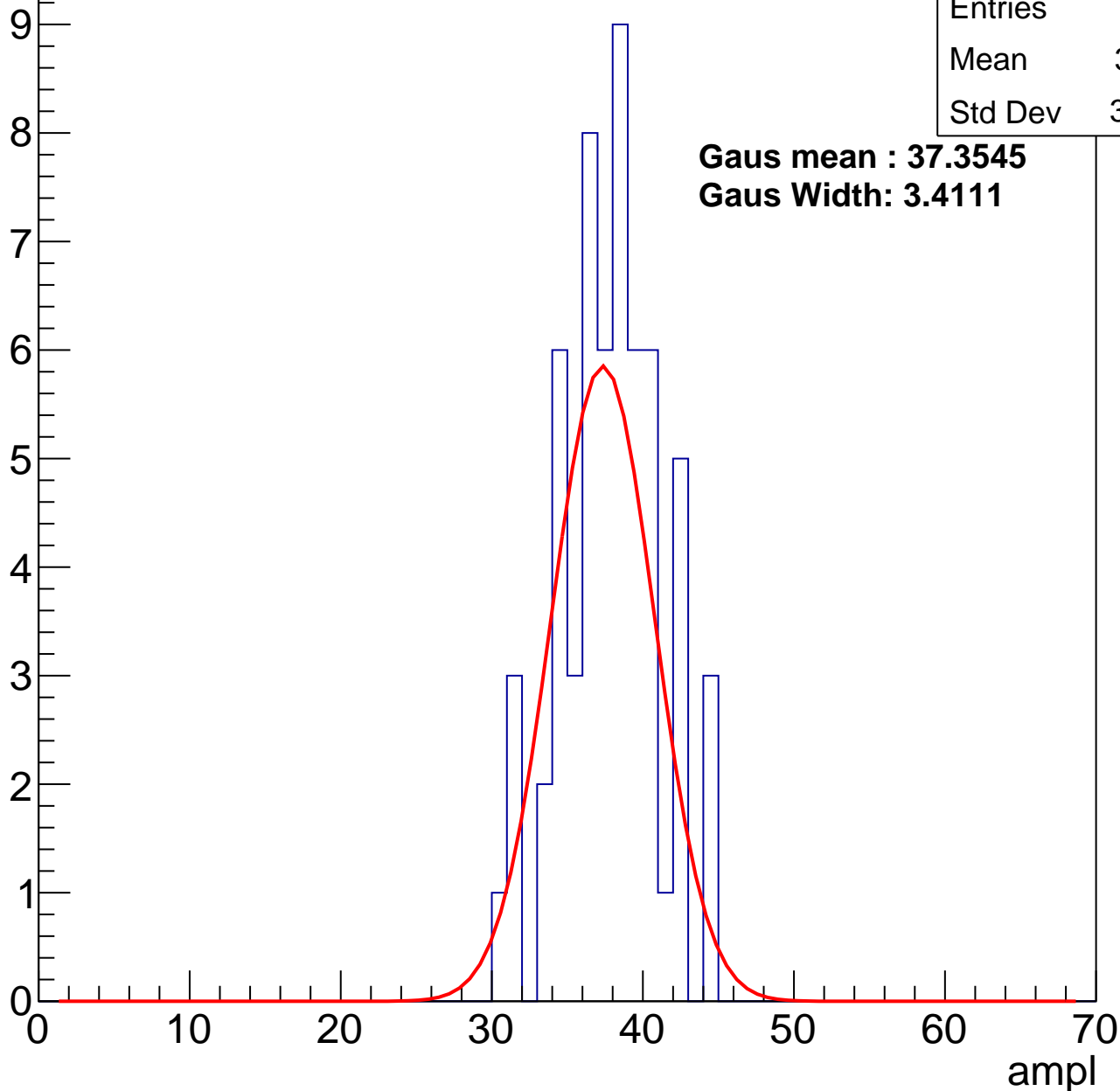
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	37.41
Std Dev	3.263

Gaus mean : 37.3545

Gaus Width: 3.4111



B0L102S, U12-ch75, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	68
Mean	43.66
Std Dev	3.42

Gaus mean : 44.2925

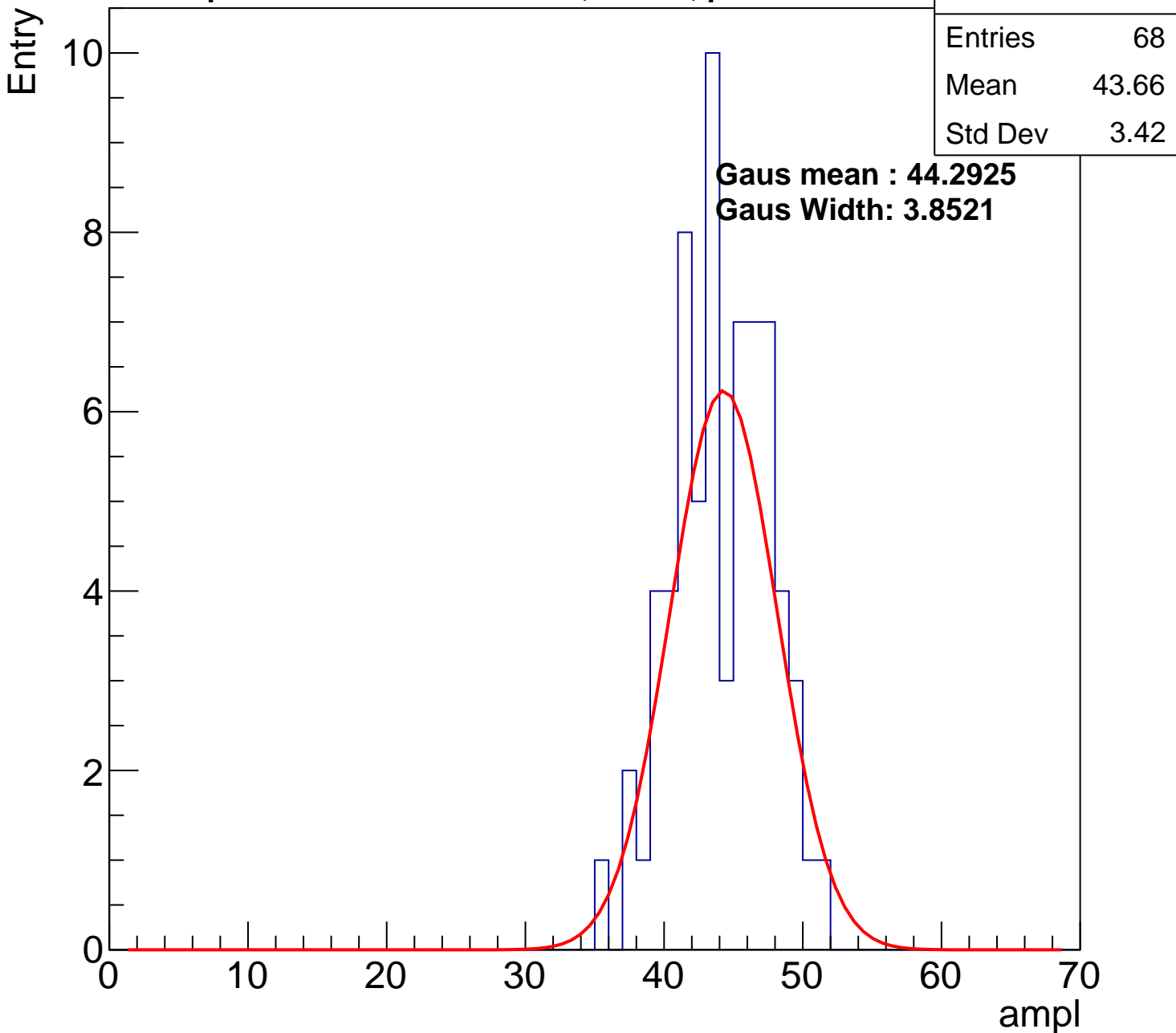
Gaus Width: 3.8521

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

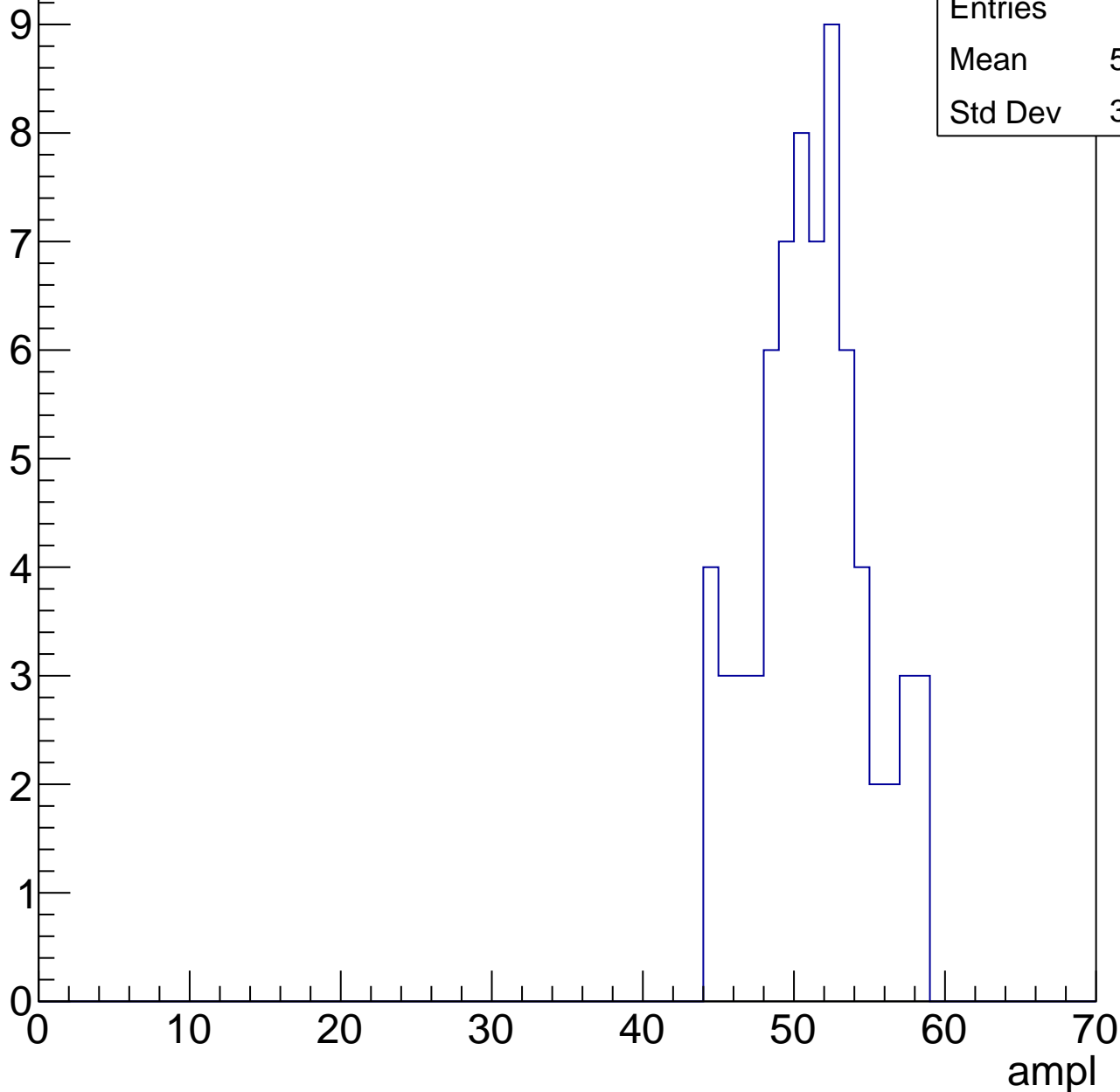


B0L102S, U12-ch75, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	50.67
Std Dev	3.616

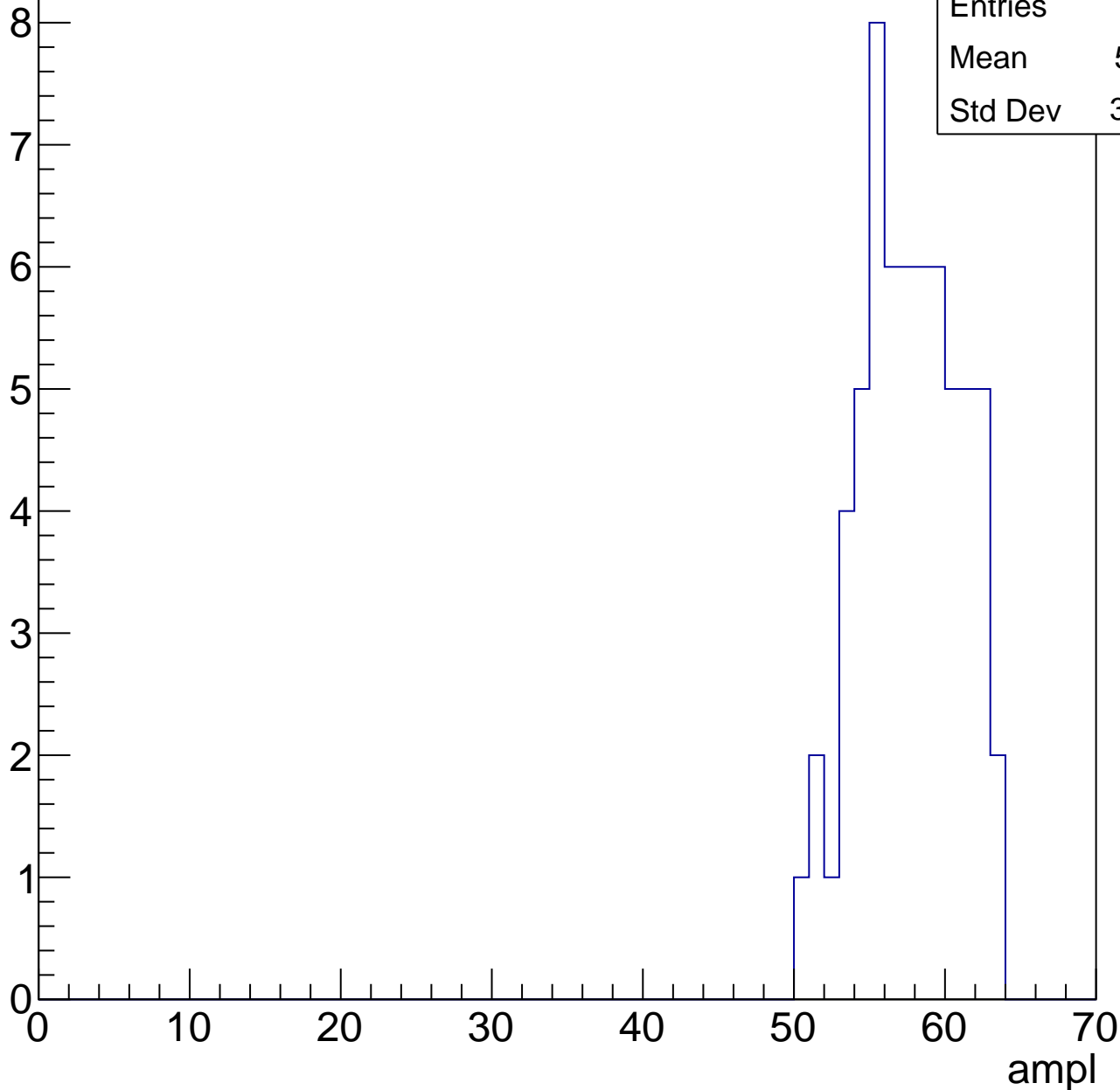


B0L102S, U12-ch75, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	57.21
Std Dev	3.219

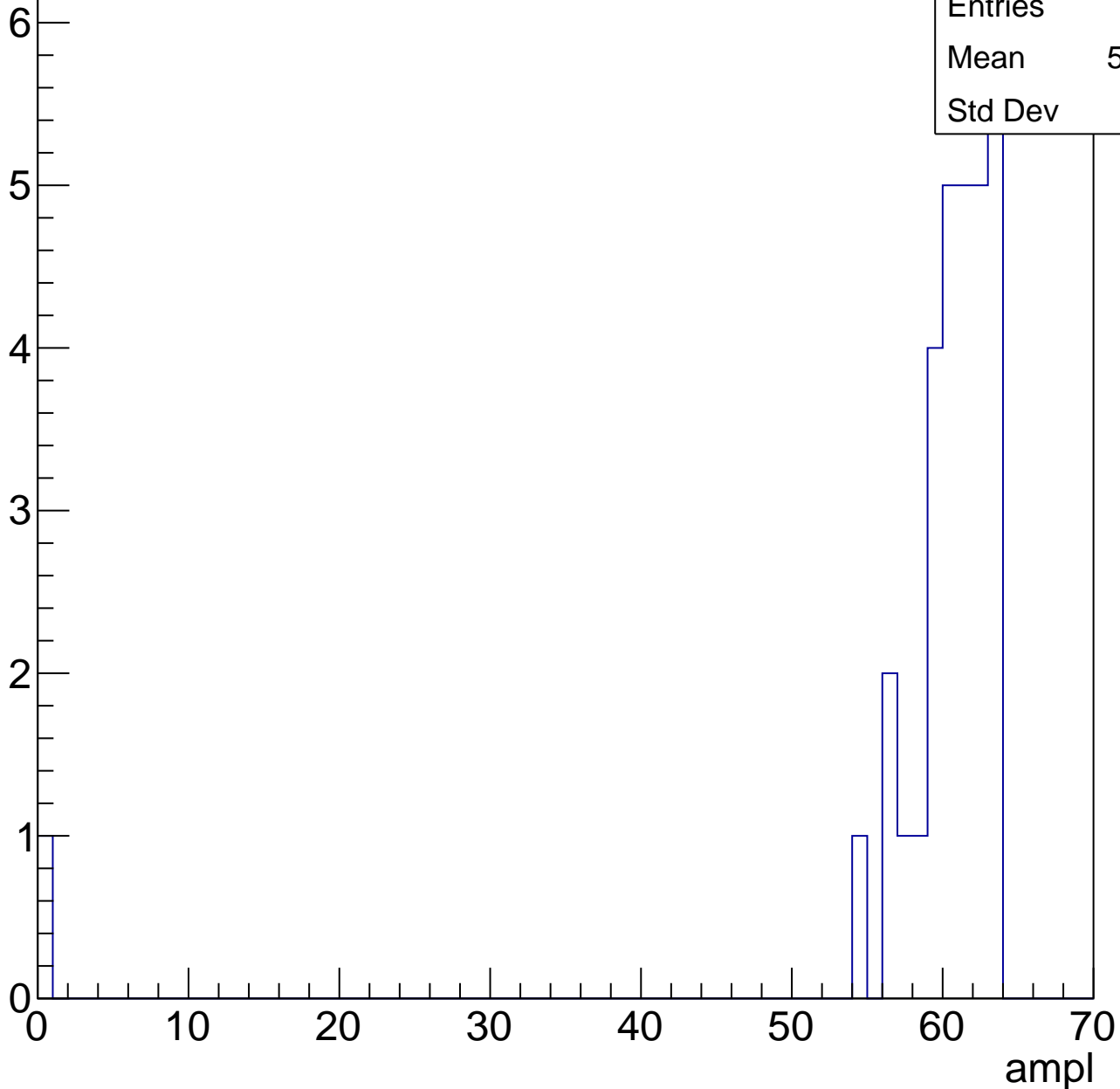


B0L102S, U12-ch75, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	31
Mean	58.39
Std Dev	10.9



B0L102S, U12-ch75, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch75, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch76, adc0

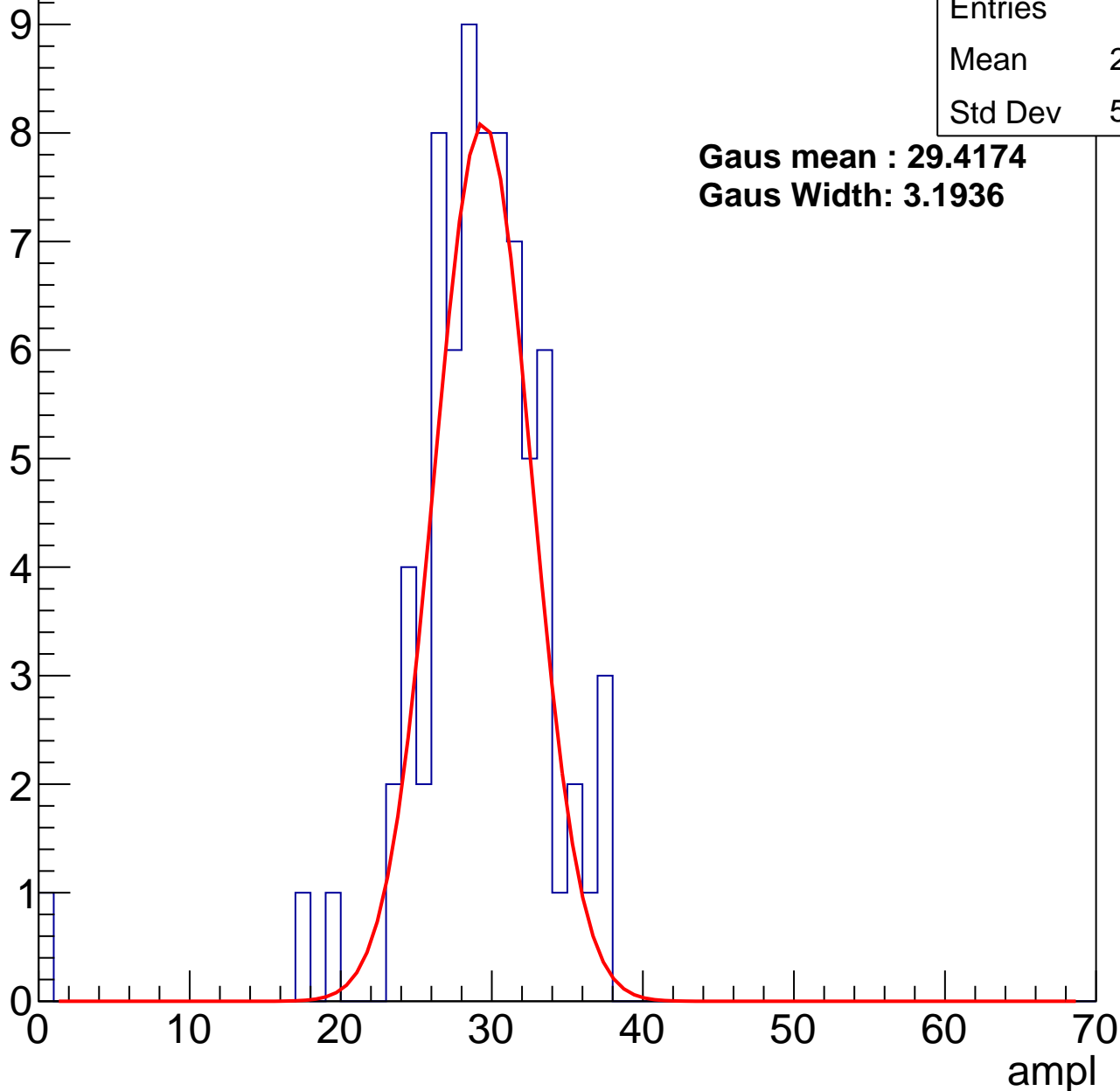
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	28.64
Std Dev	5.032

Gaus mean : 29.4174

Gaus Width: 3.1936



B0L102S, U12-ch76, adc1

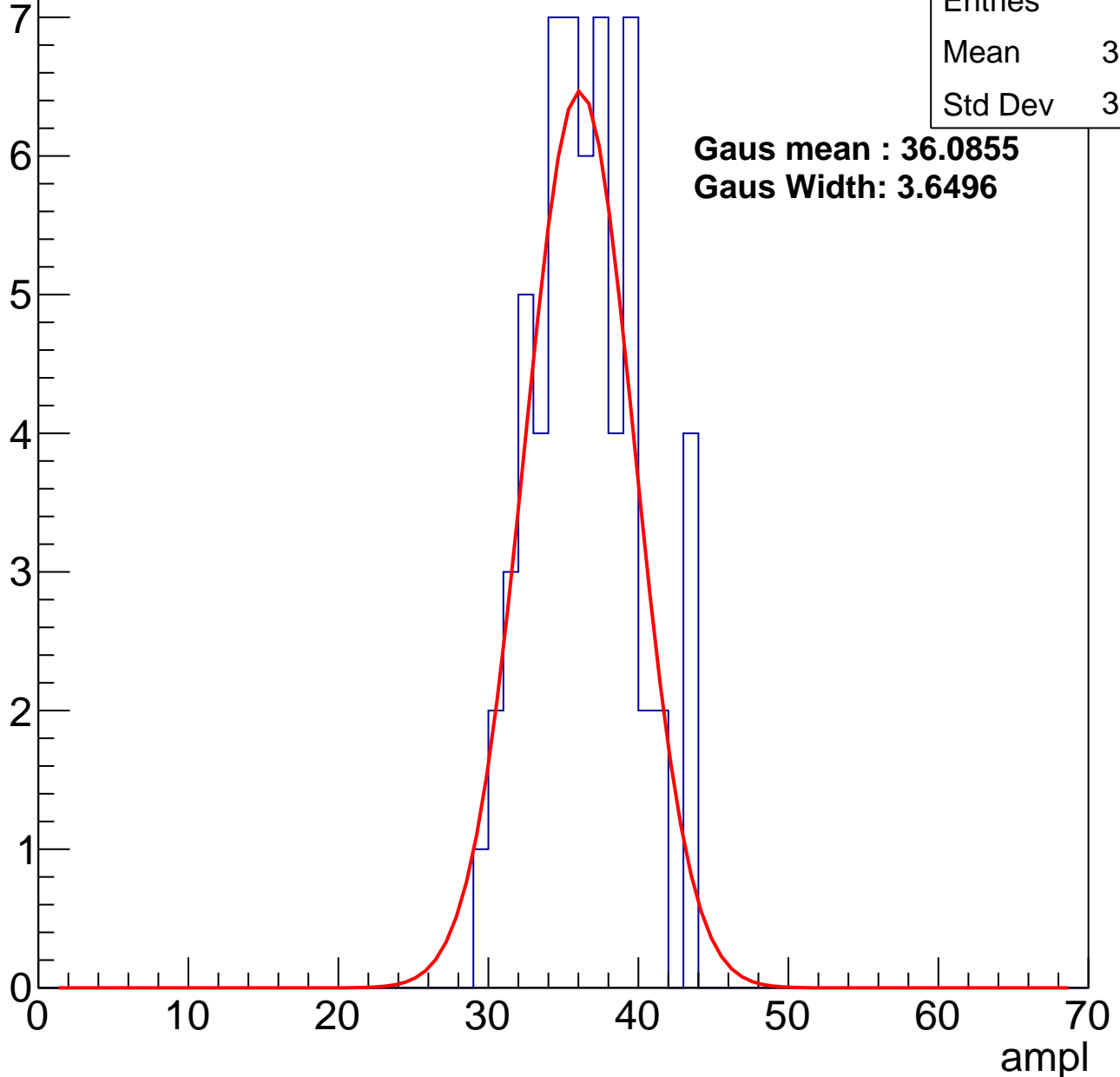
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	35.92
Std Dev	3.413

Gaus mean : 36.0855

Gaus Width: 3.6496



B0L102S, U12-ch76, adc2

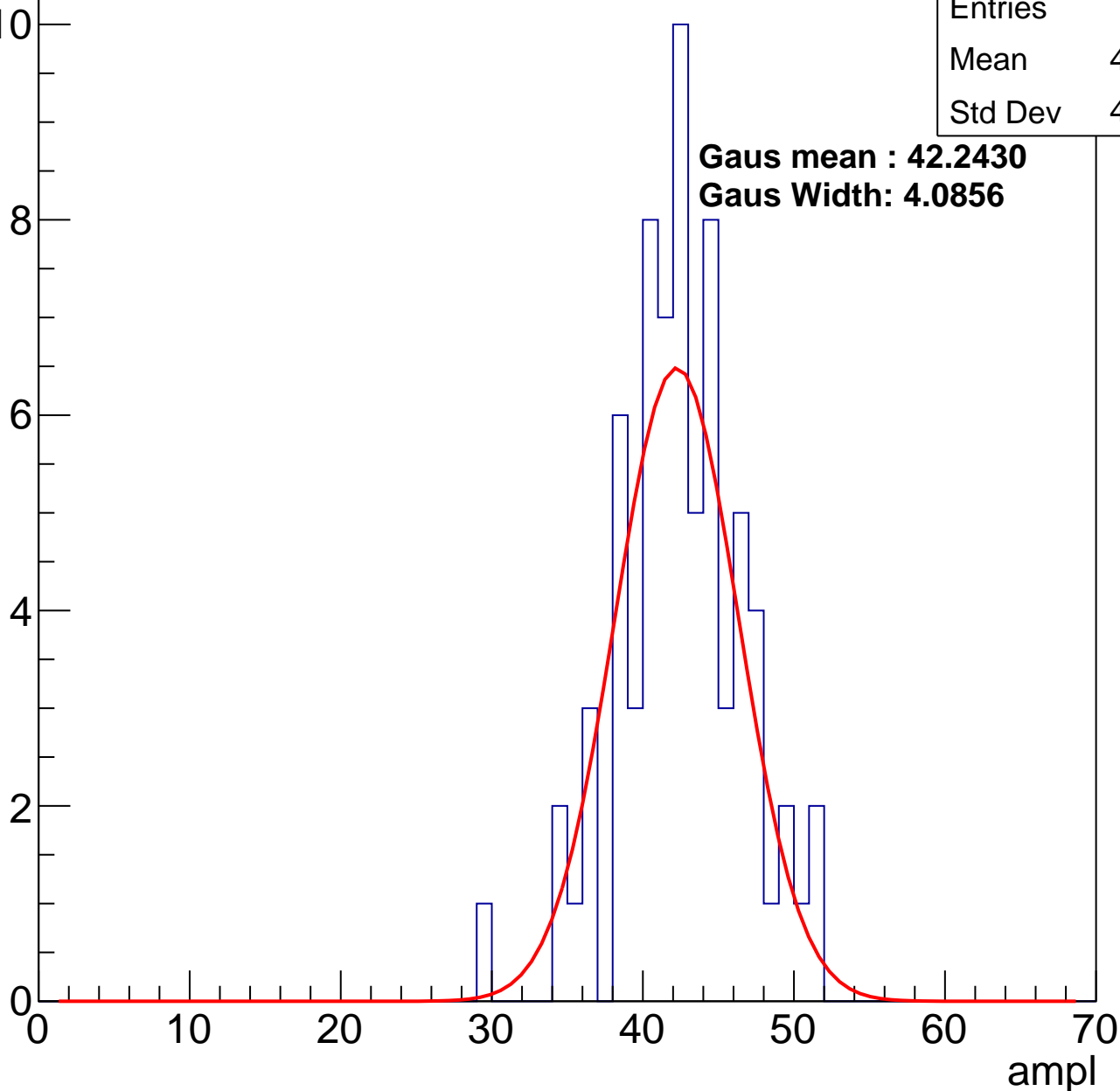
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.08
Std Dev	4.116

Gaus mean : 42.2430

Gaus Width: 4.0856

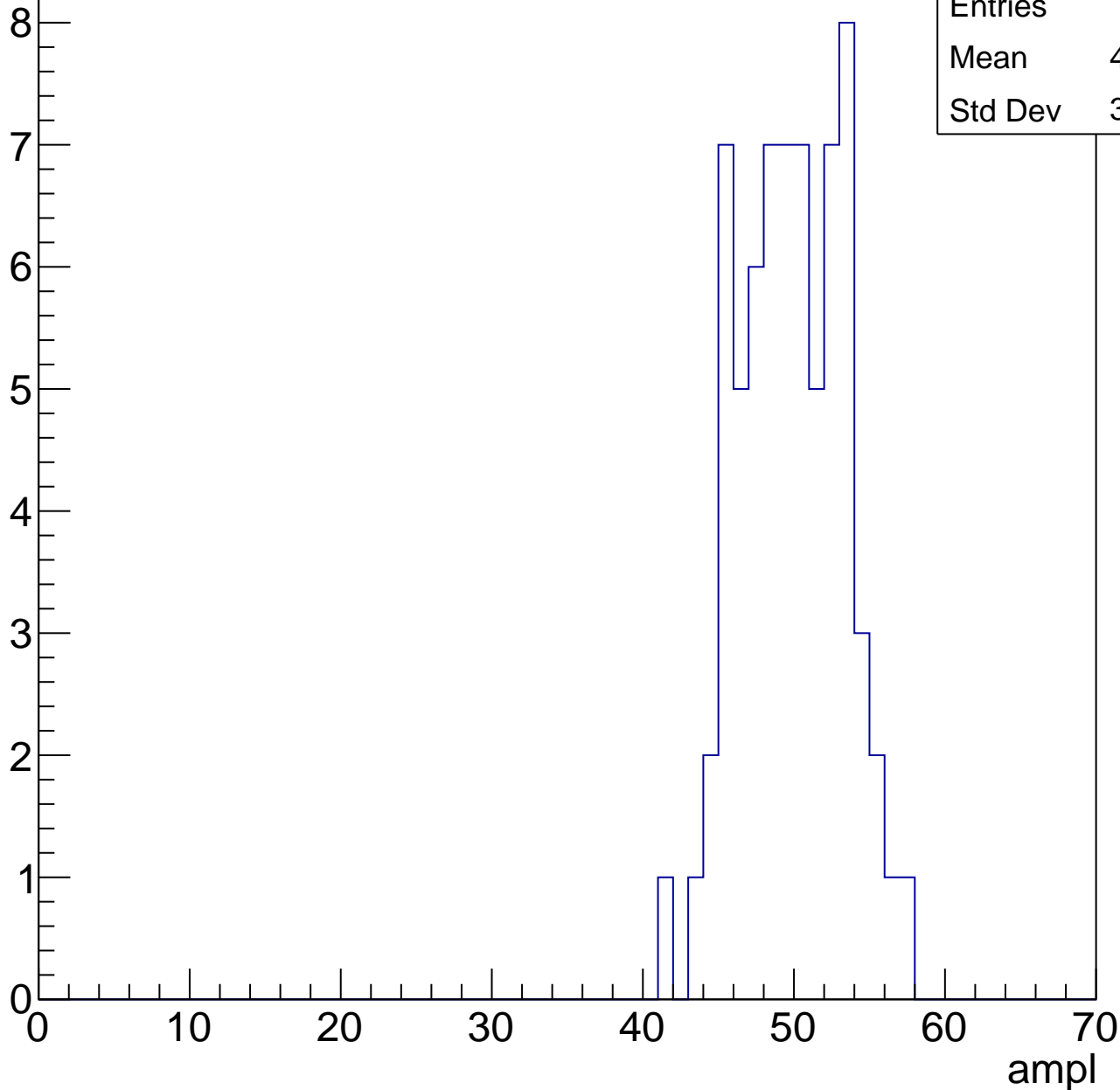


B0L102S, U12-ch76, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.37
Std Dev	3.394

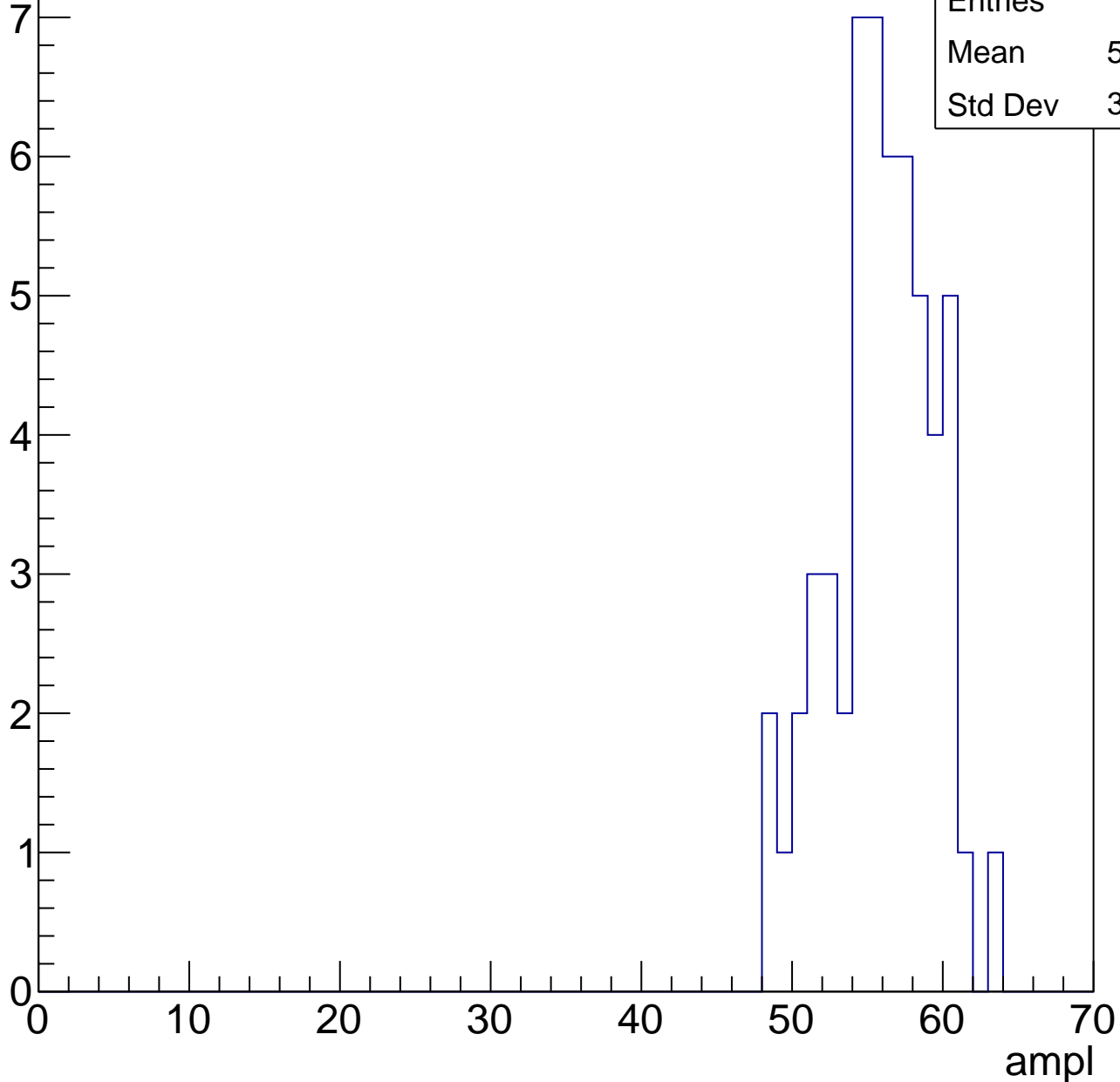


B0L102S, U12-ch76, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	55.47
Std Dev	3.373

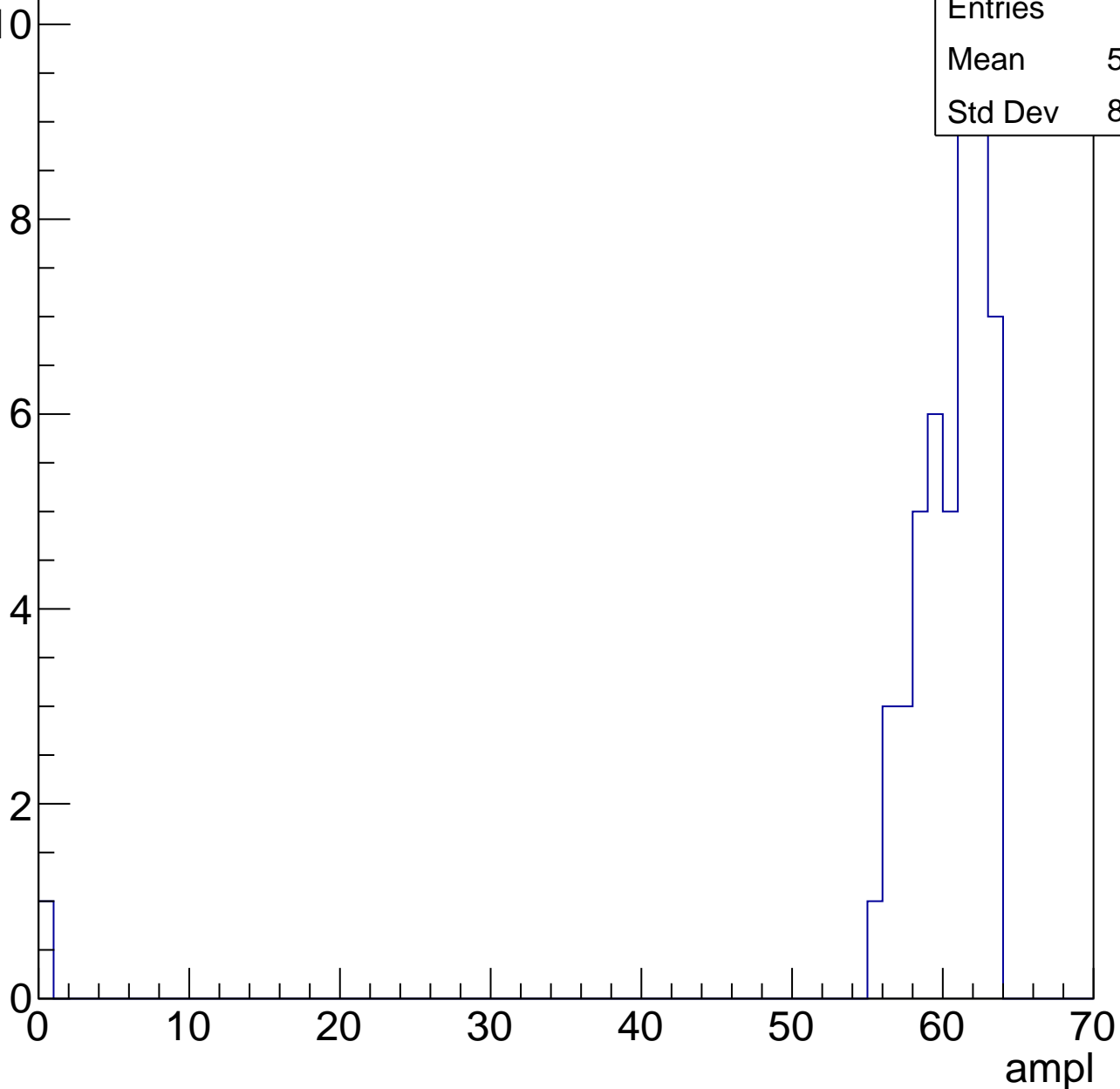


B0L102S, U12-ch76, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.96
Std Dev	8.699



B0L102S, U12-ch76, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L102S, U12-ch76, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch77, adc0

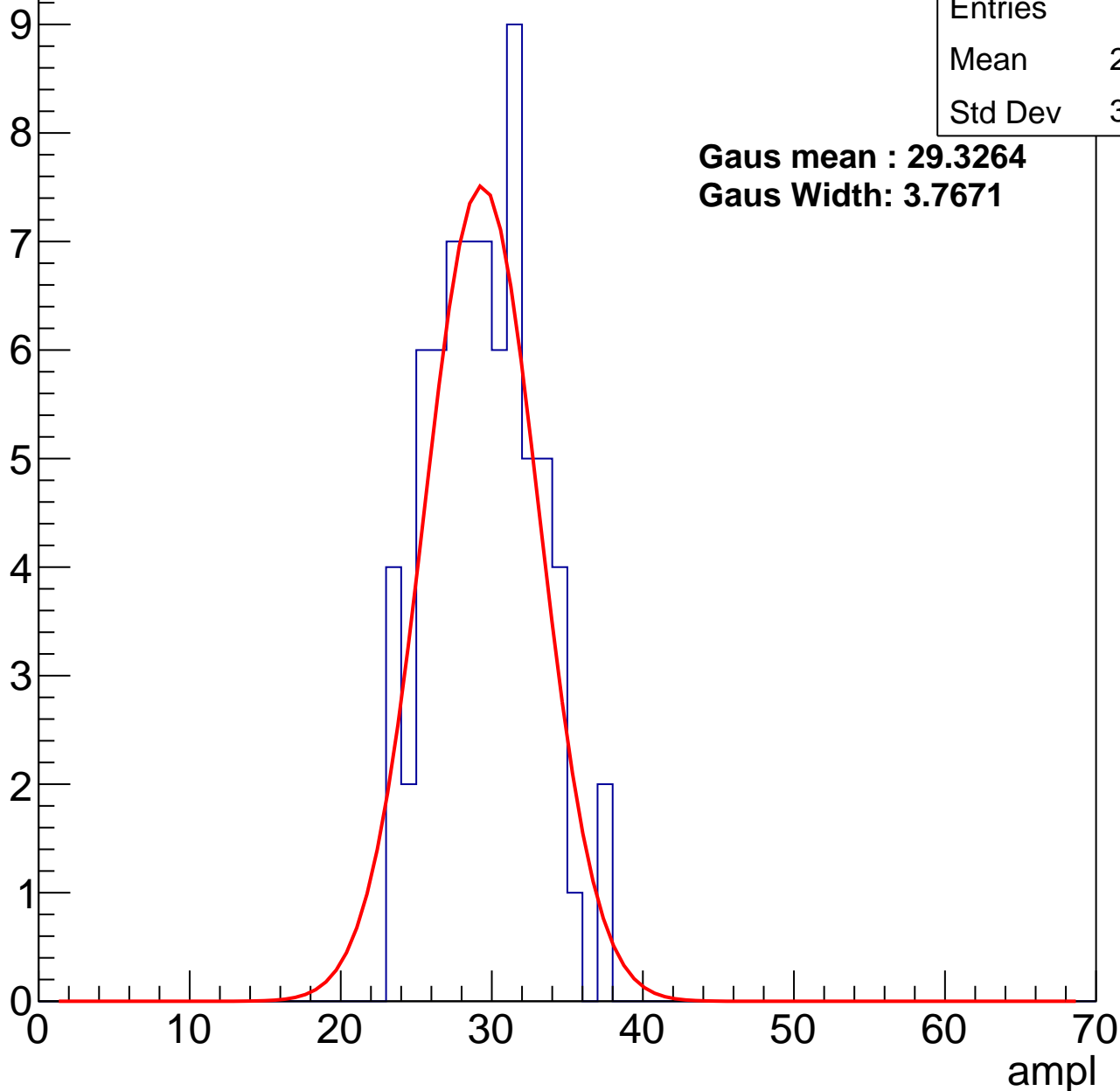
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.06
Std Dev	3.377

Gaus mean : 29.3264

Gaus Width: 3.7671



B0L102S, U12-ch77, adc1

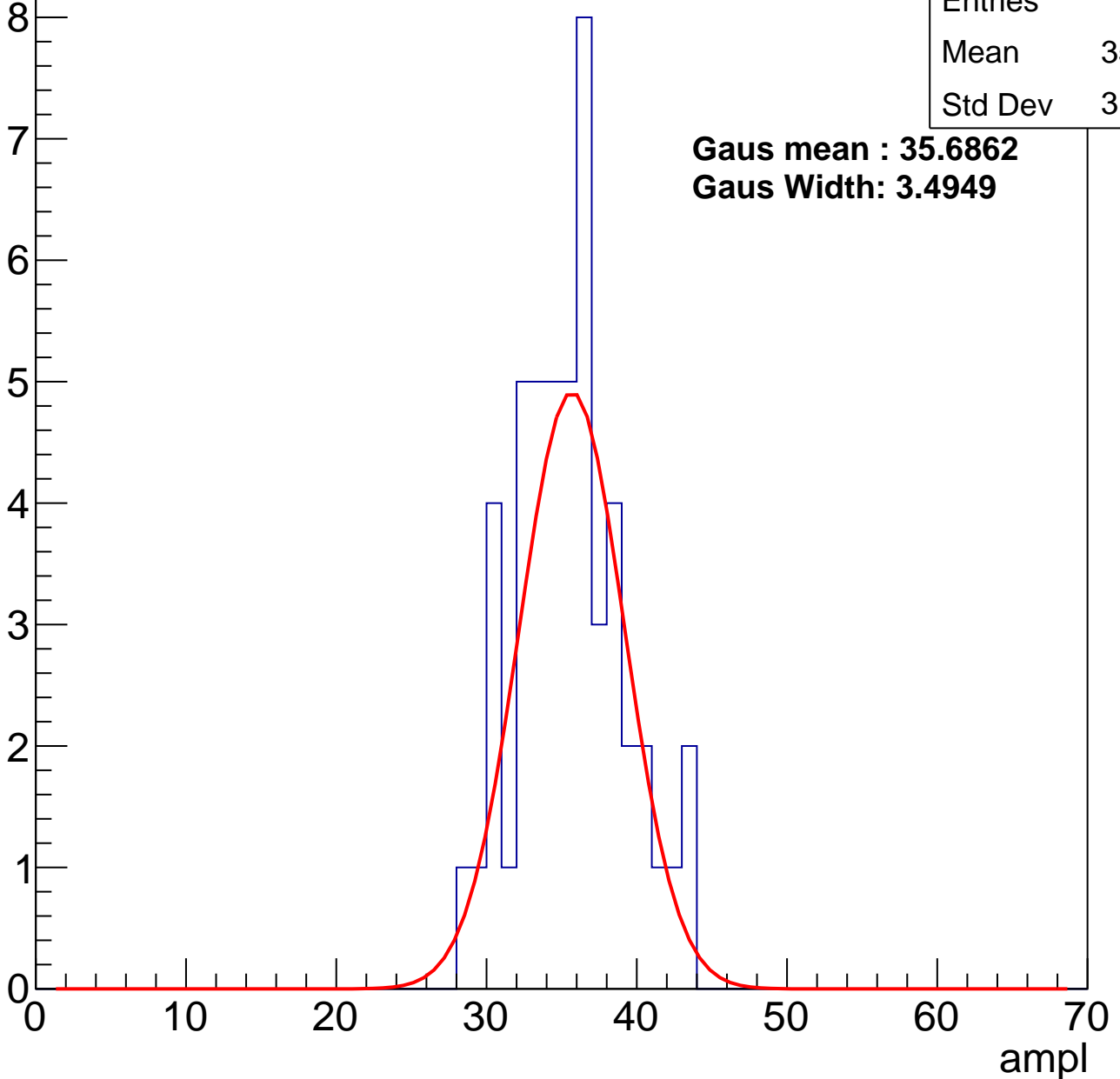
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	35.12
Std Dev	3.525

Gaus mean : 35.6862

Gaus Width: 3.4949



B0L102S, U12-ch77, adc2

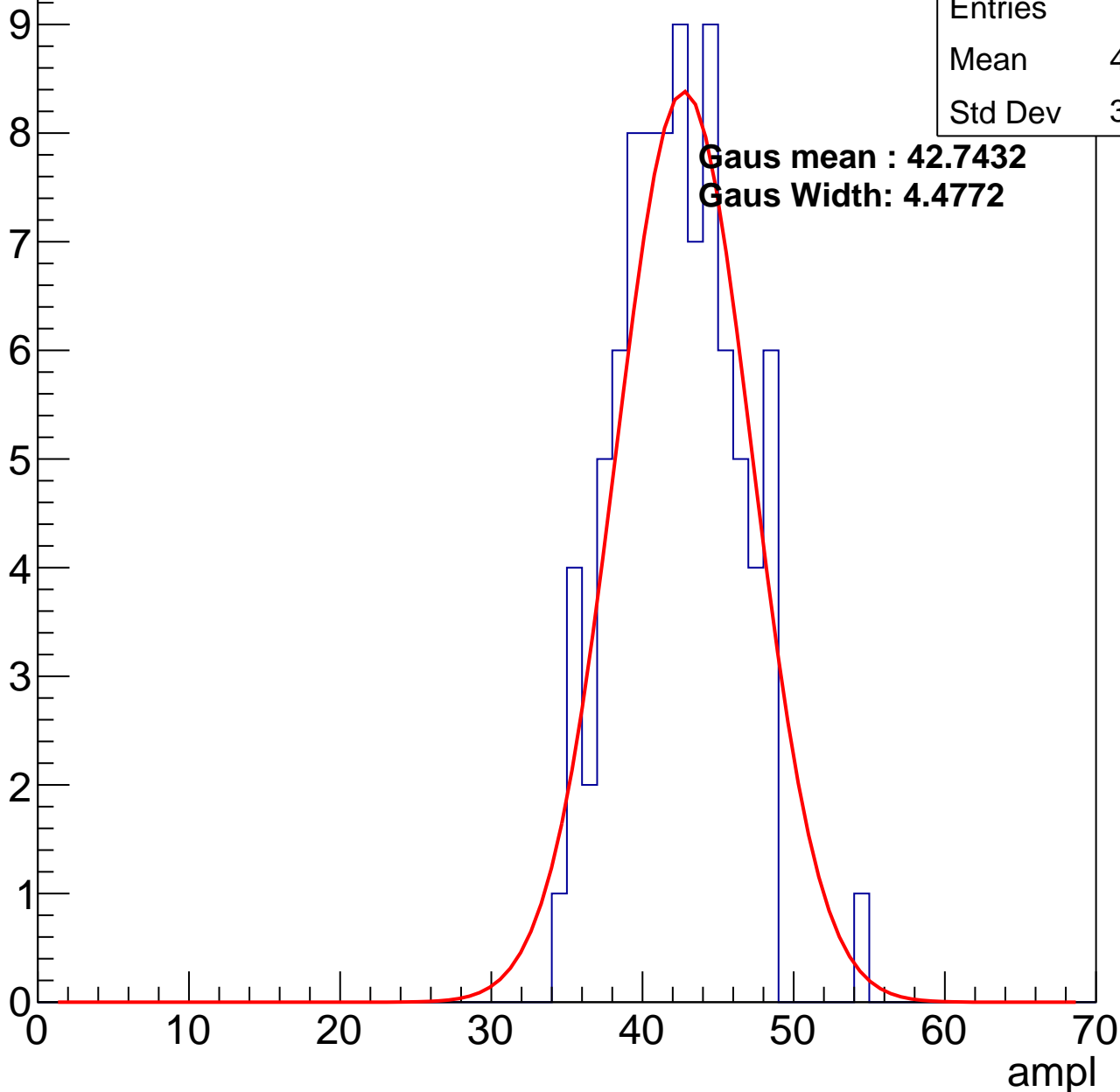
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	41.84
Std Dev	3.822

Gaus mean : 42.7432

Gaus Width: 4.4772

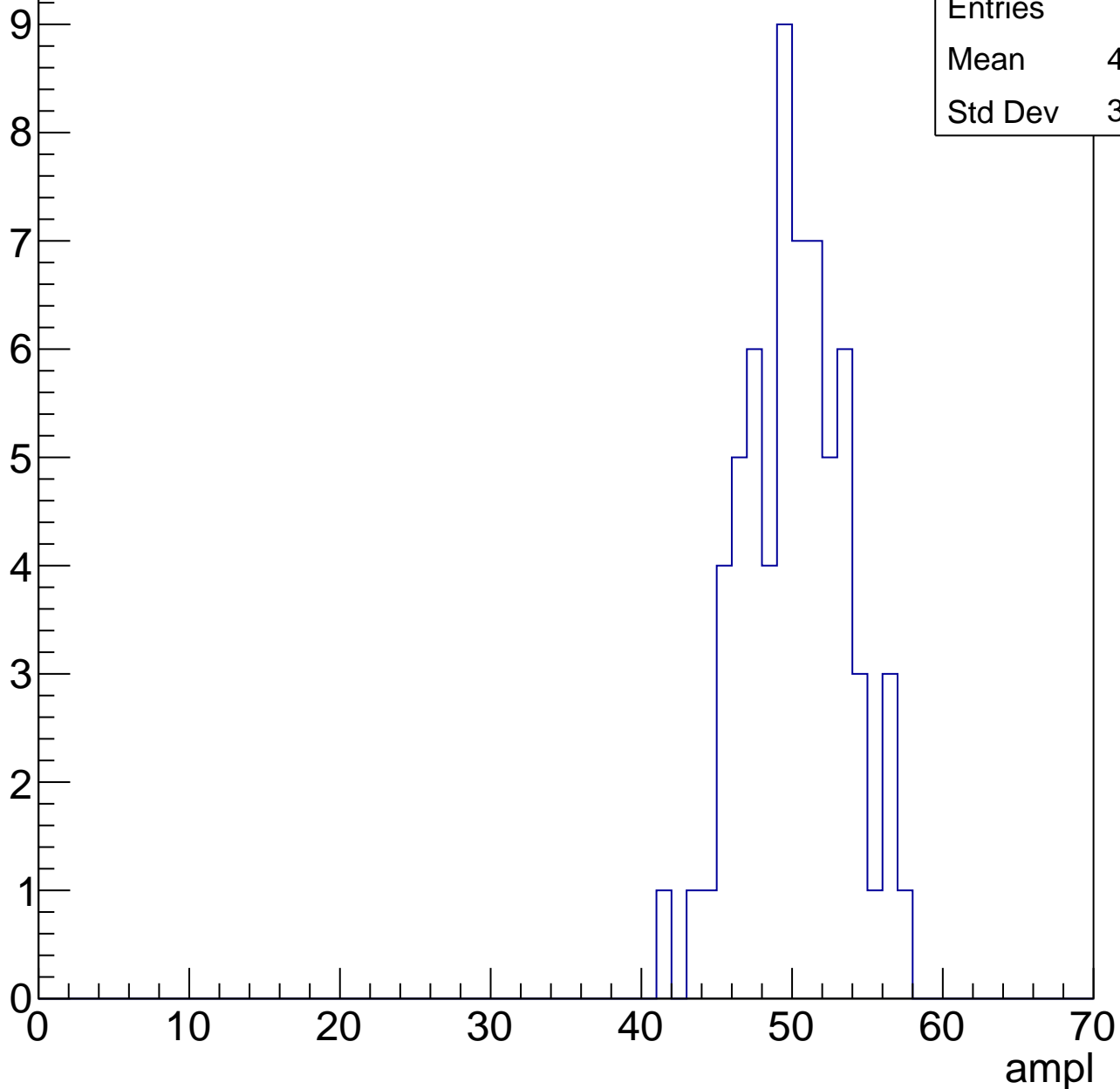


B0L102S, U12-ch77, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

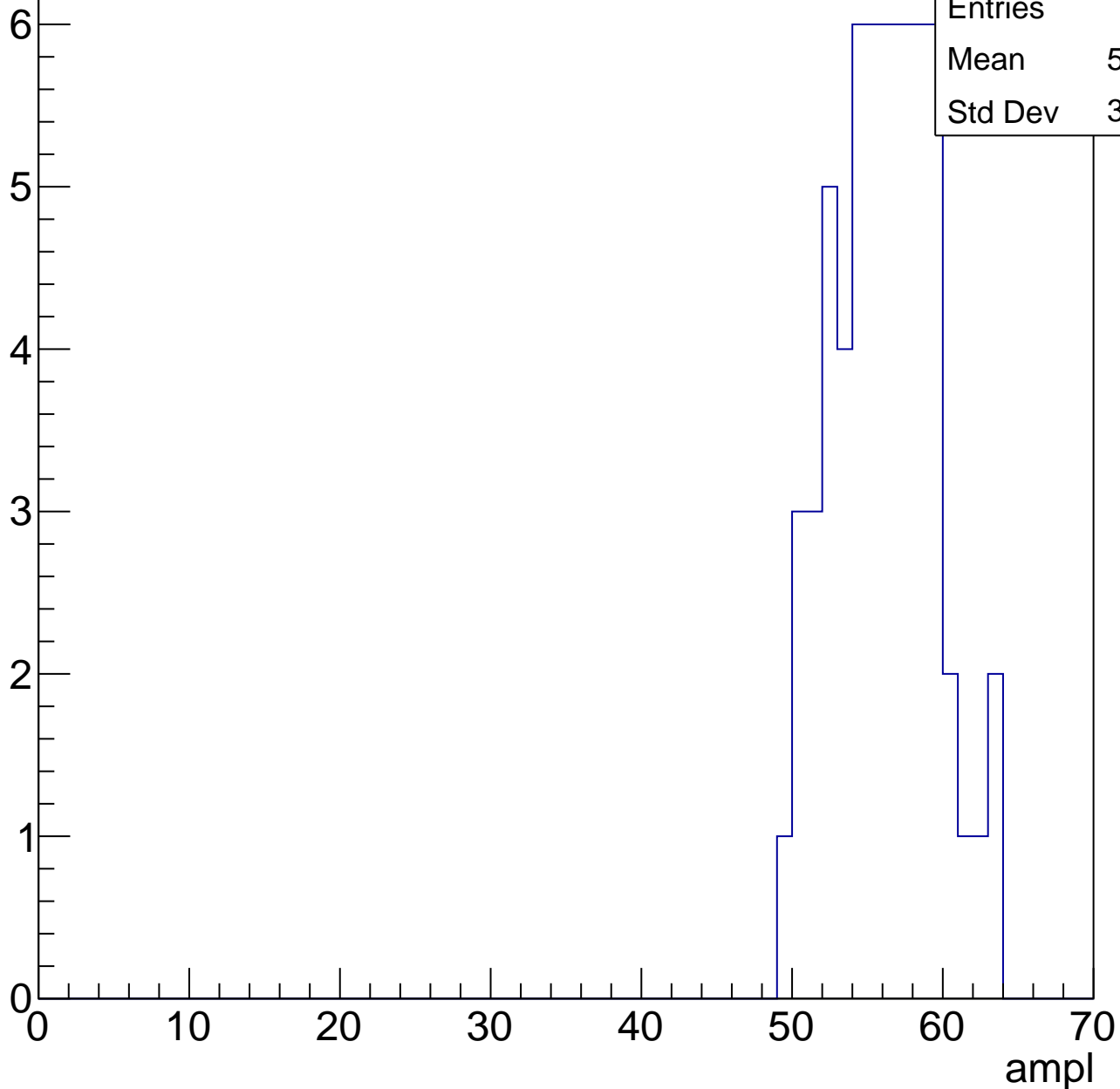
Entries	64
Mean	49.69
Std Dev	3.377



B0L102S, U12-ch77, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	58
Mean	55.64
Std Dev	3.336

B0L102S, U12-ch77, adc5

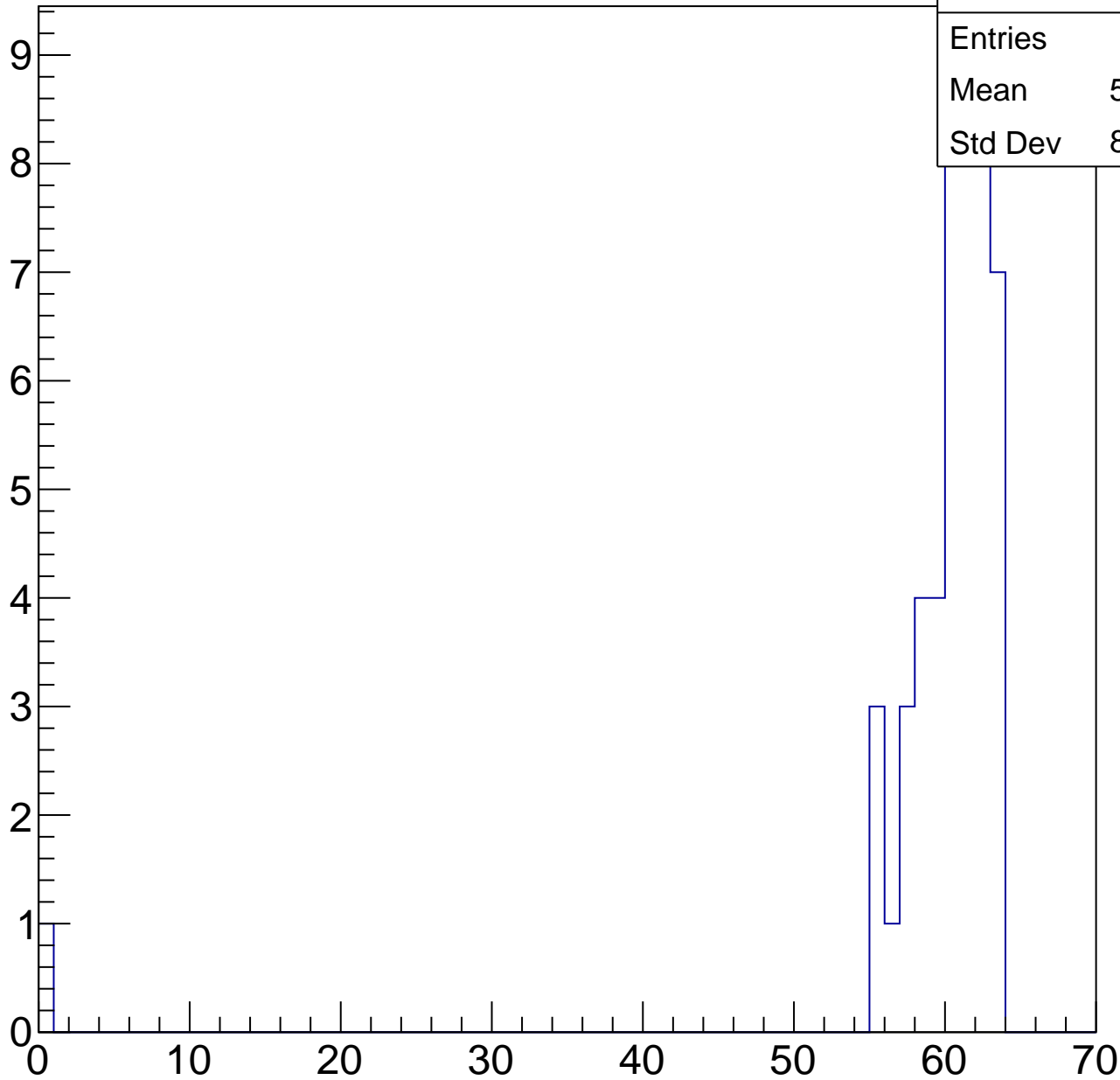
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.94
Std Dev	8.796

ampl



B0L102S, U12-ch77, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0

B0L102S, U12-ch77, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch78, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	79
Mean	28.53
Std Dev	4.794

Gaus mean : 30.4391

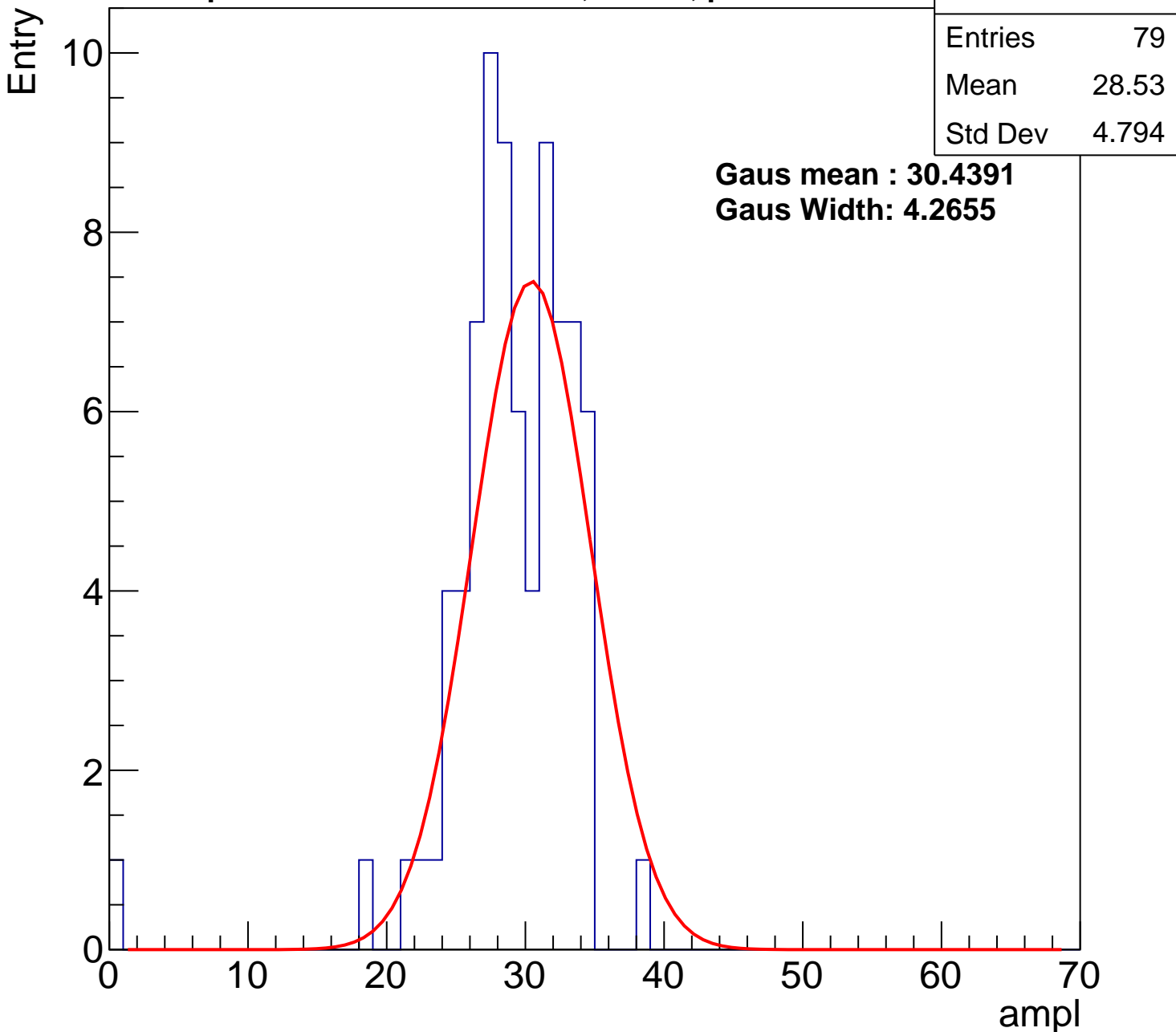
Gaus Width: 4.2655

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch78, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	36.68
Std Dev	3.475

Gaus mean : 37.8521

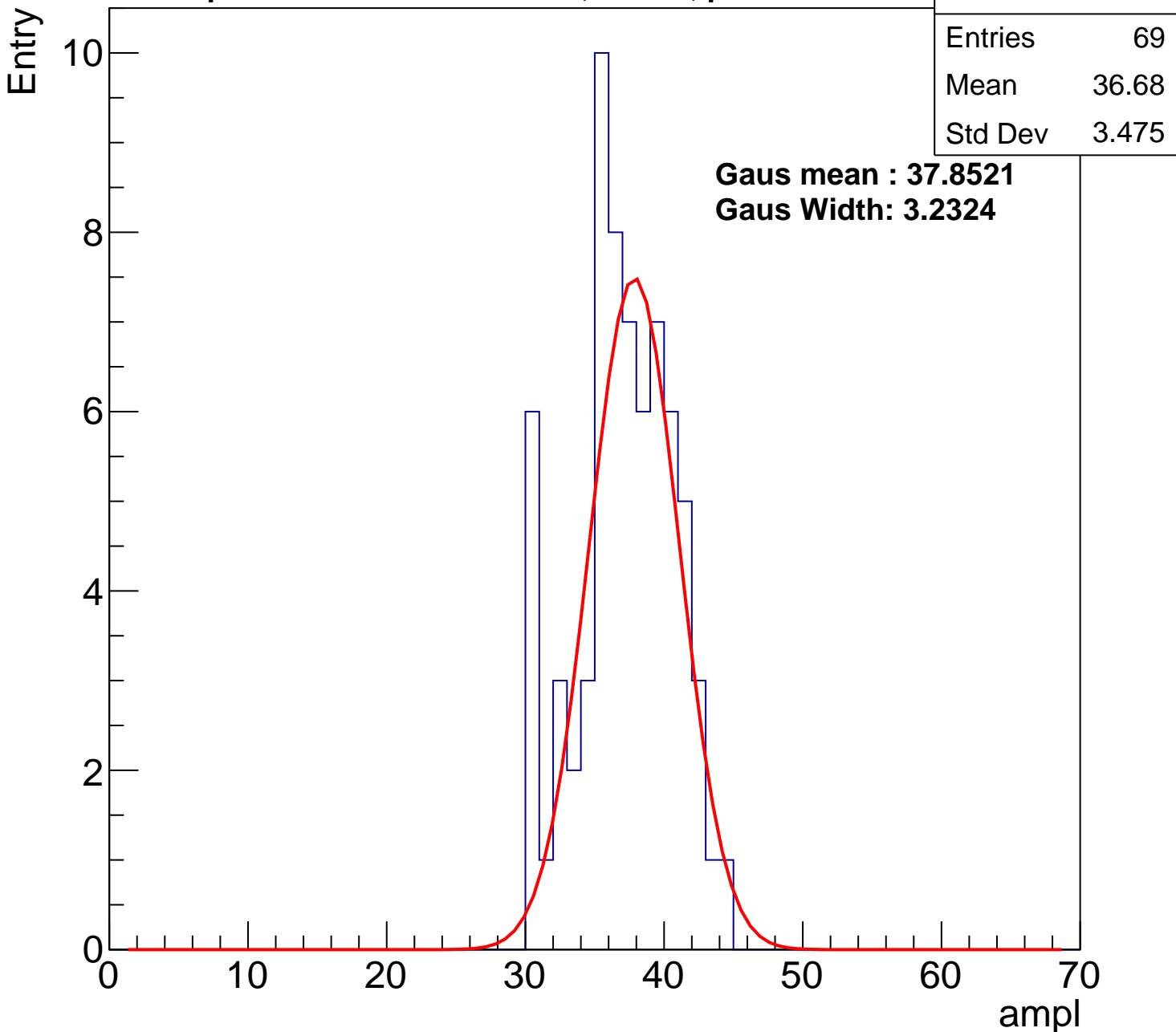
Gaus Width: 3.2324

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch78, adc2

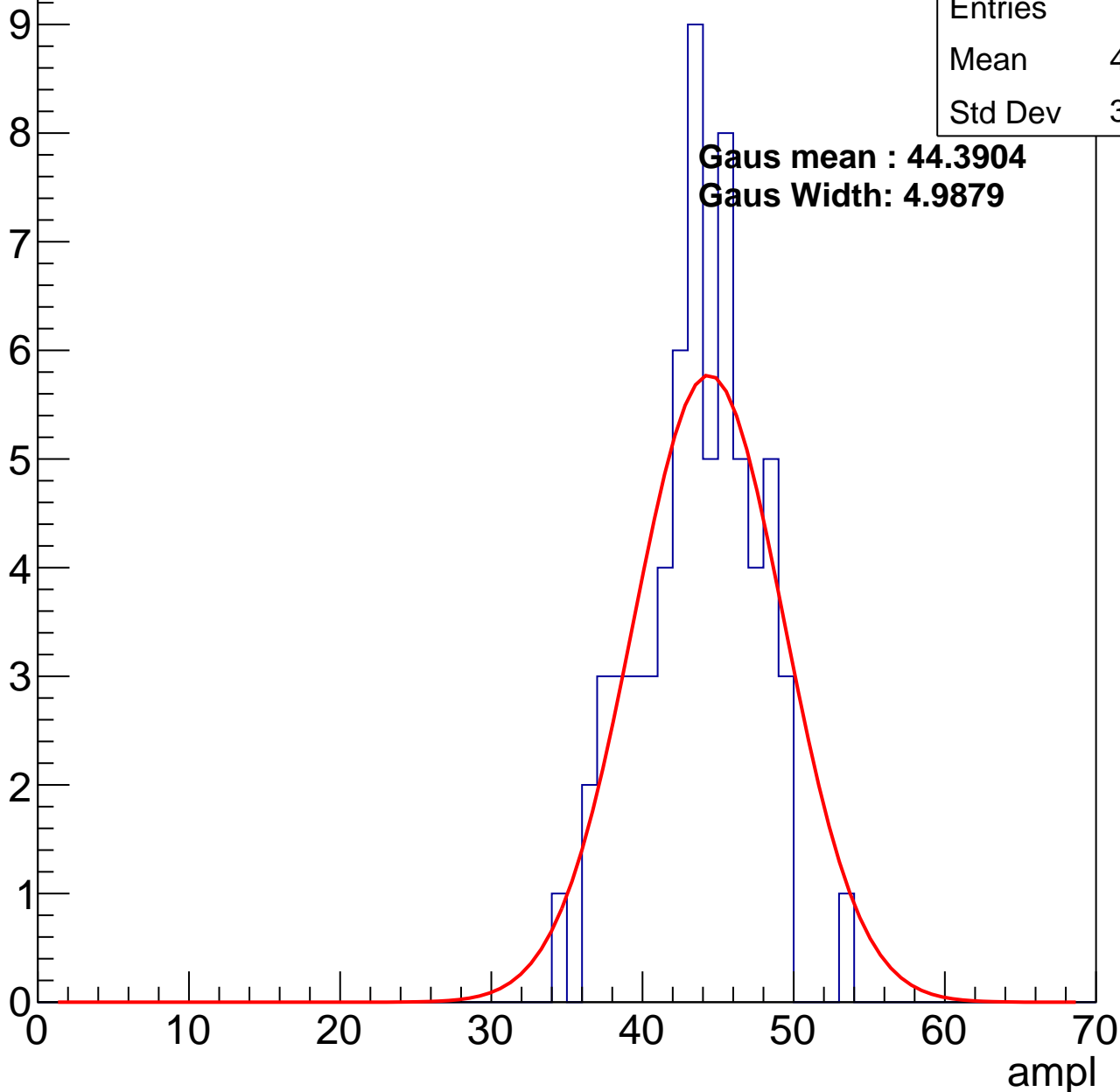
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.22
Std Dev	3.793

Gaus mean : 44.3904

Gaus Width: 4.9879



B0L102S, U12-ch78, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	50.48
Std Dev	4.235

Entry

10

8

6

4

2

0

0

10

20

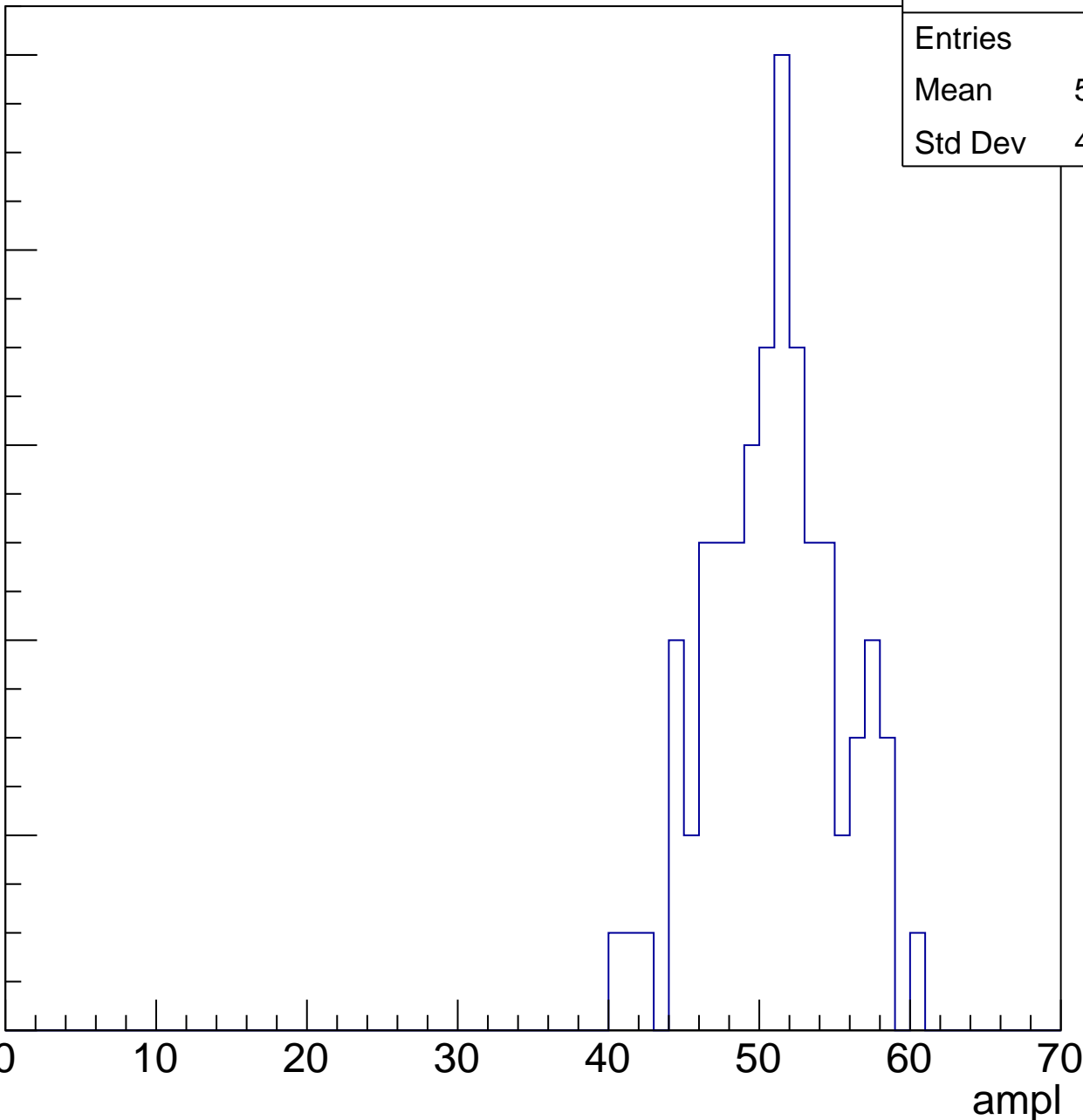
30

40

50

60

ampl

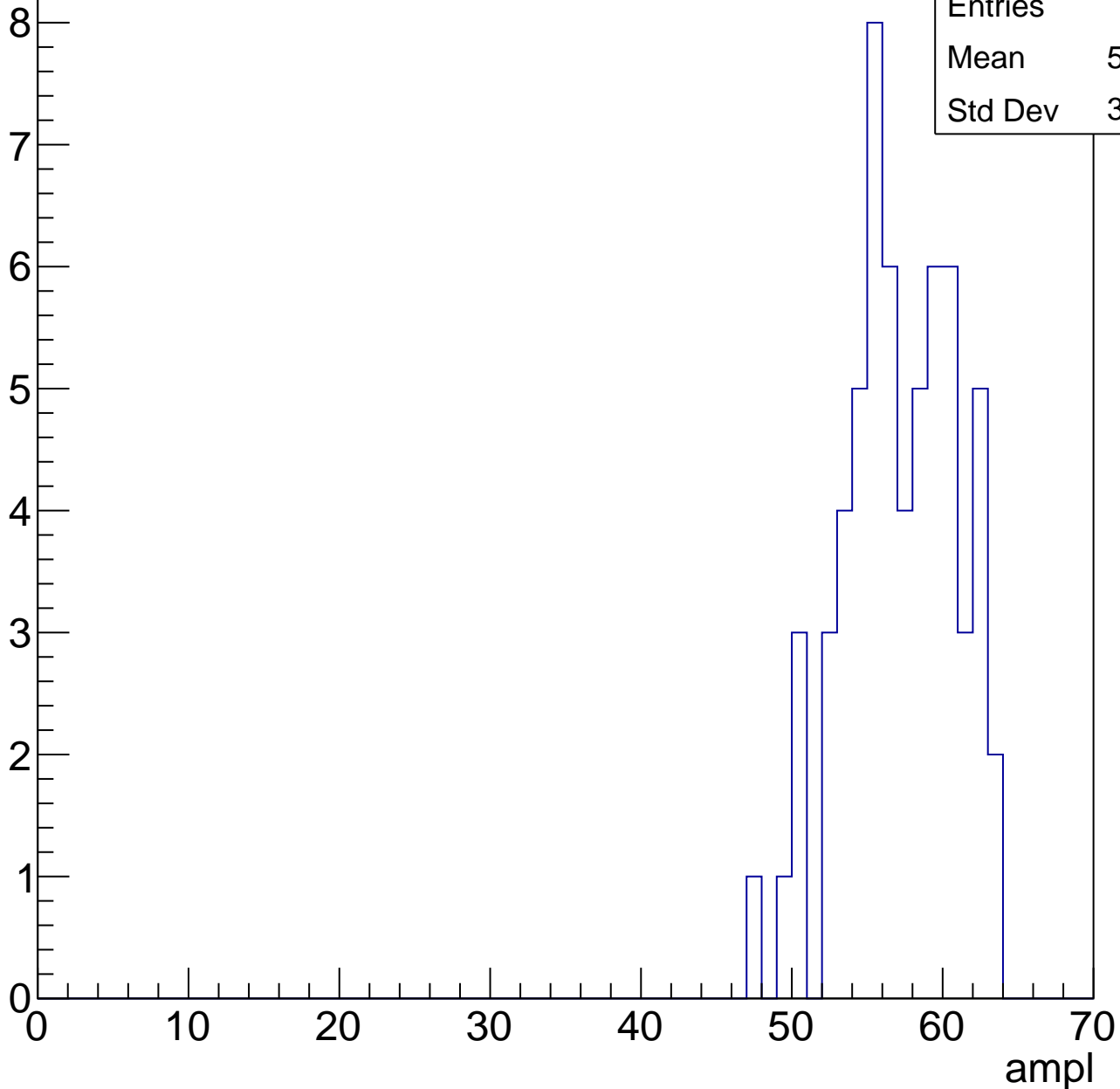


B0L102S, U12-ch78, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	56.63
Std Dev	3.708

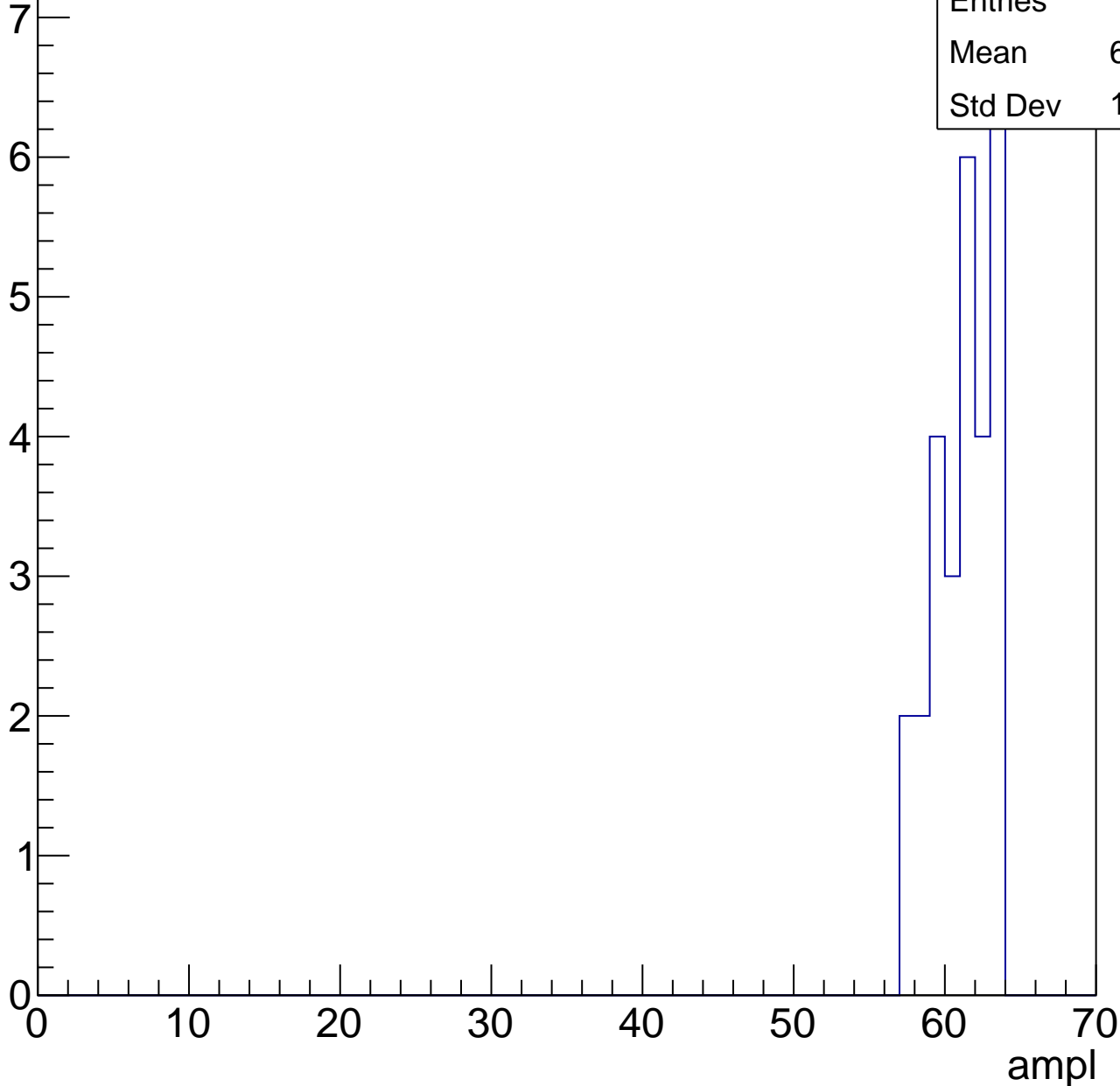


B0L102S, U12-ch78, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

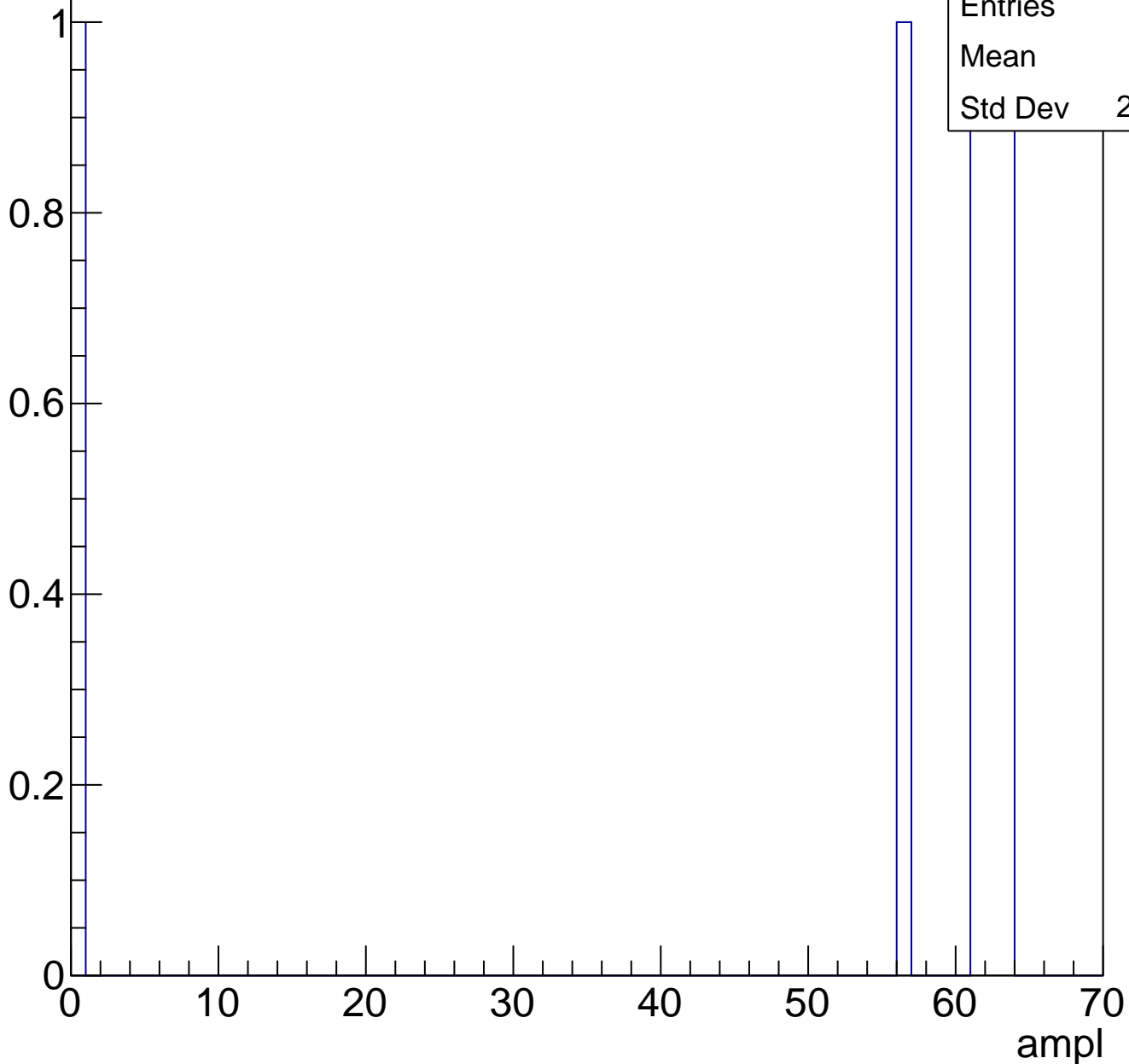
Entries	28
Mean	60.75
Std Dev	1.883



B0L102S, U12-ch78, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch78, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch79, adc0

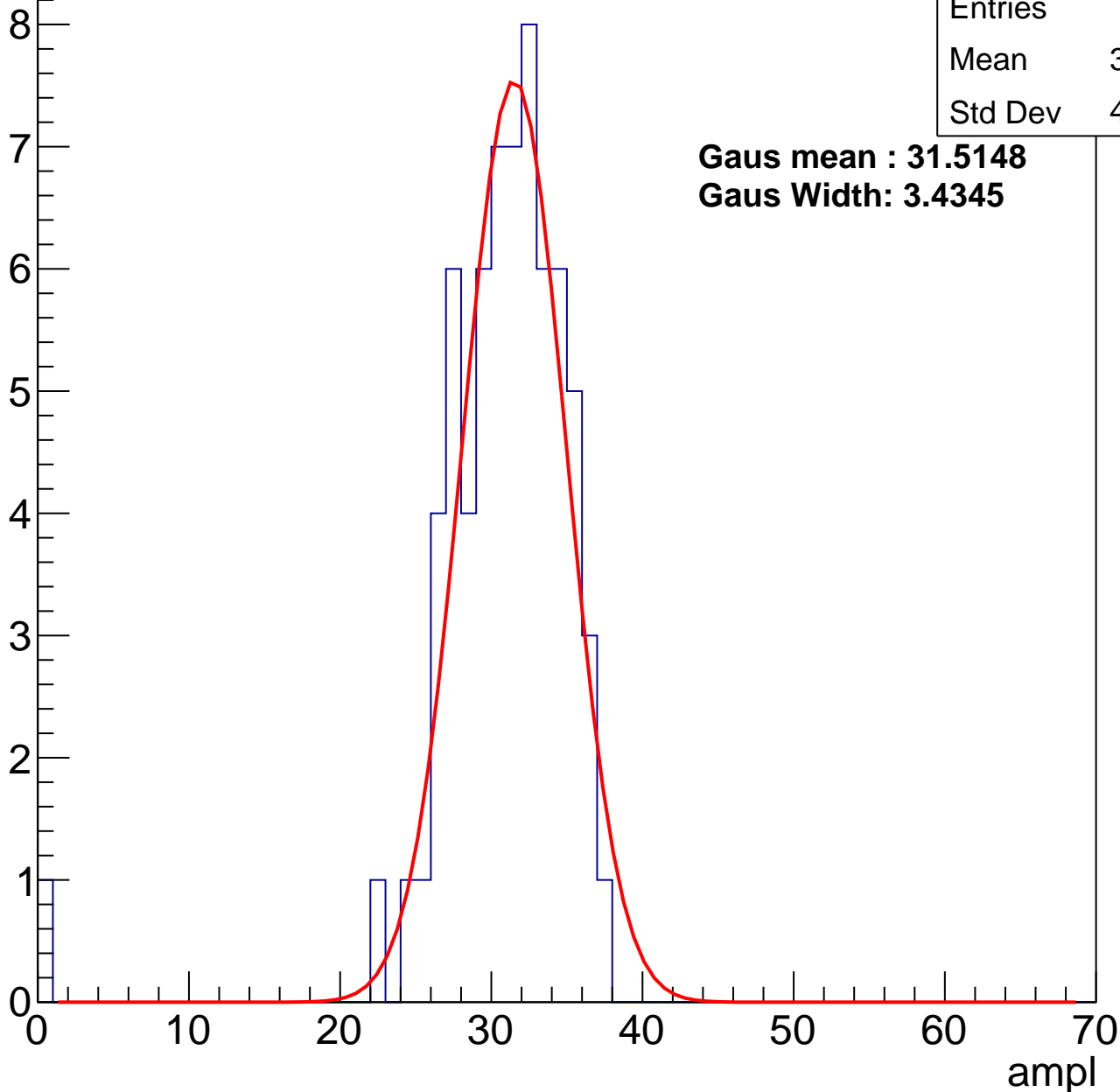
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	30.27
Std Dev	4.934

Gaus mean : 31.5148

Gaus Width: 3.4345



B0L102S, U12-ch79, adc1

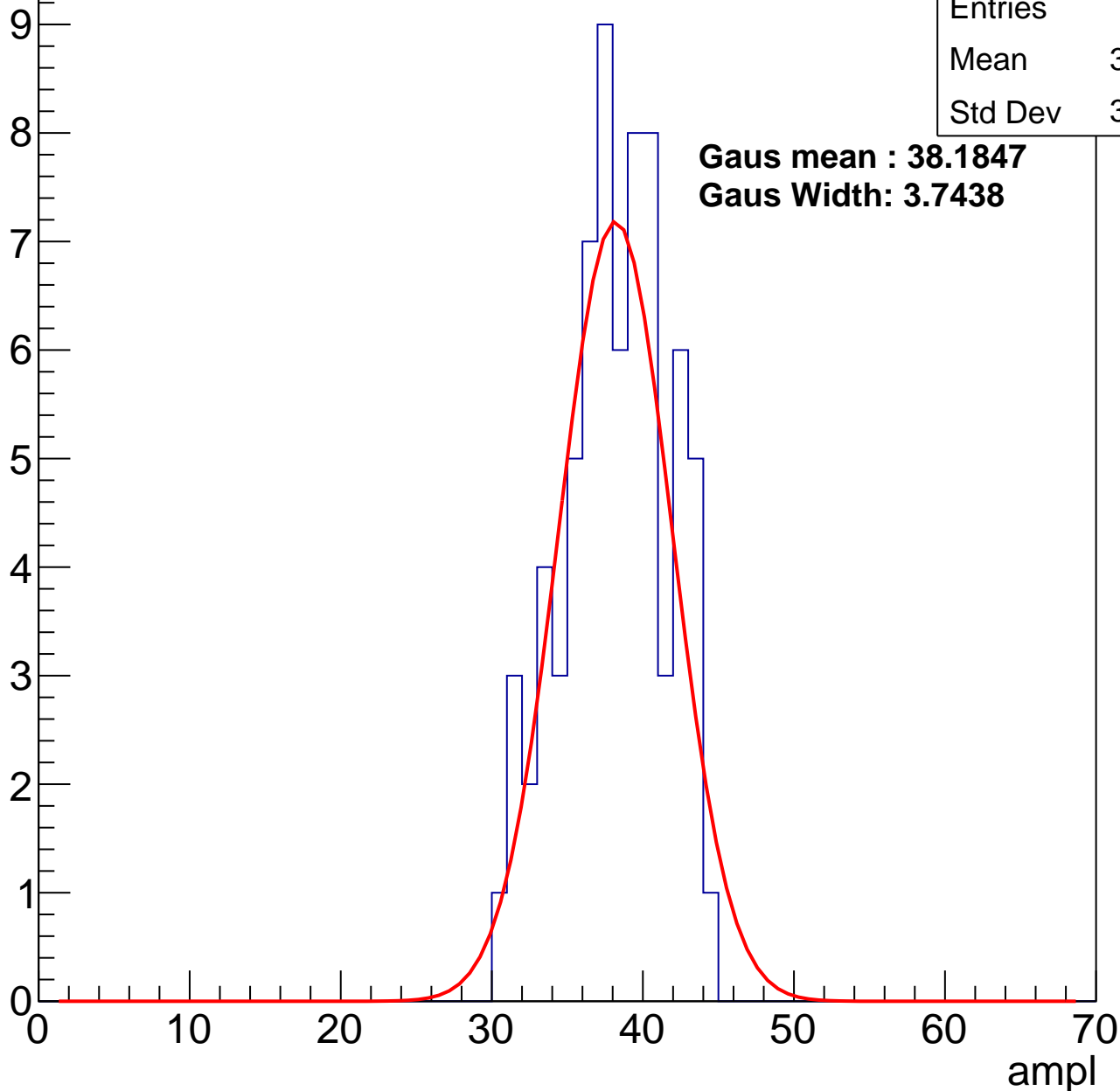
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	37.68
Std Dev	3.418

Gaus mean : 38.1847

Gaus Width: 3.7438



B0L102S, U12-ch79, adc2

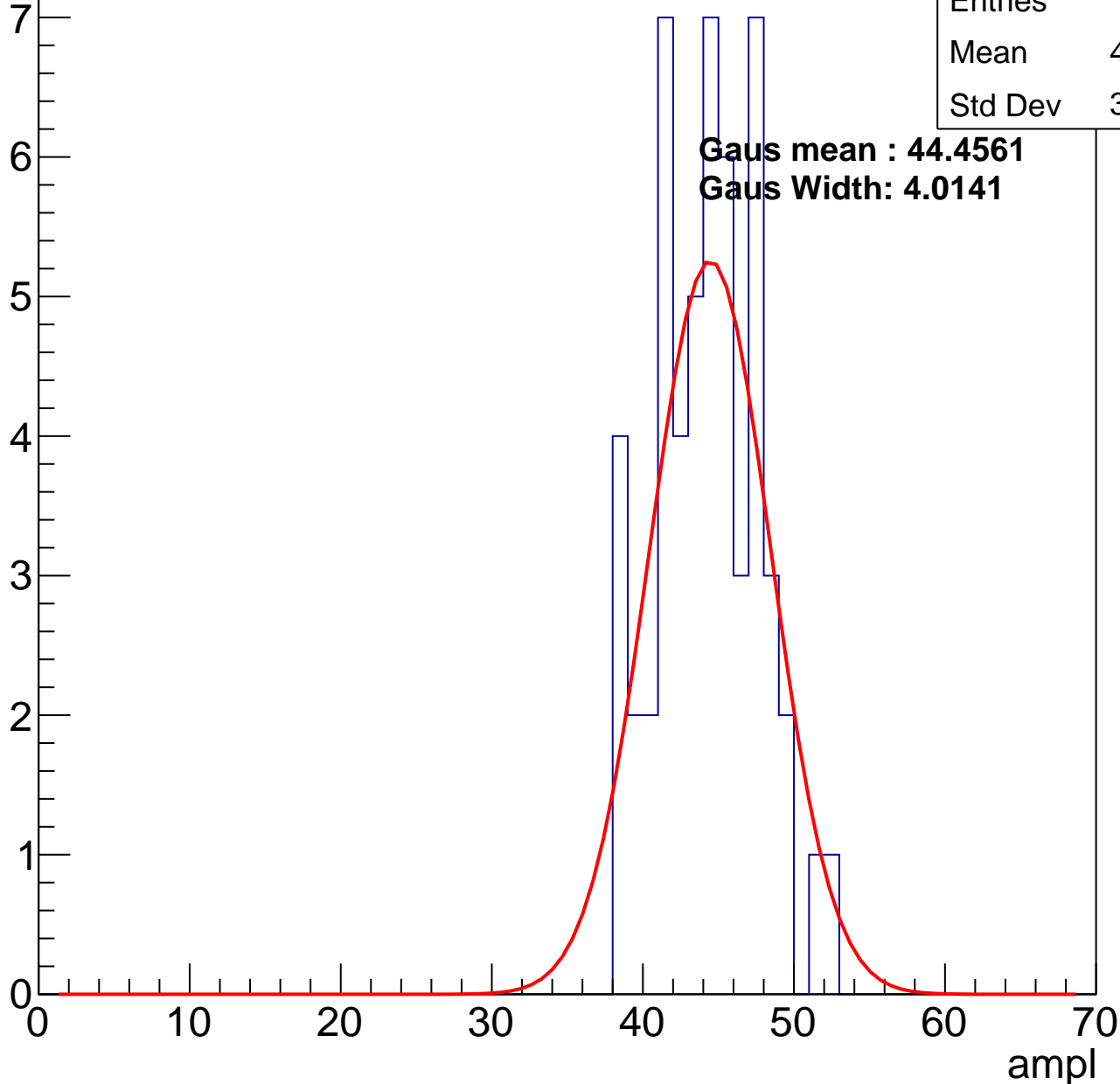
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	43.89
Std Dev	3.337

Gaus mean : 44.4561

Gaus Width: 4.0141

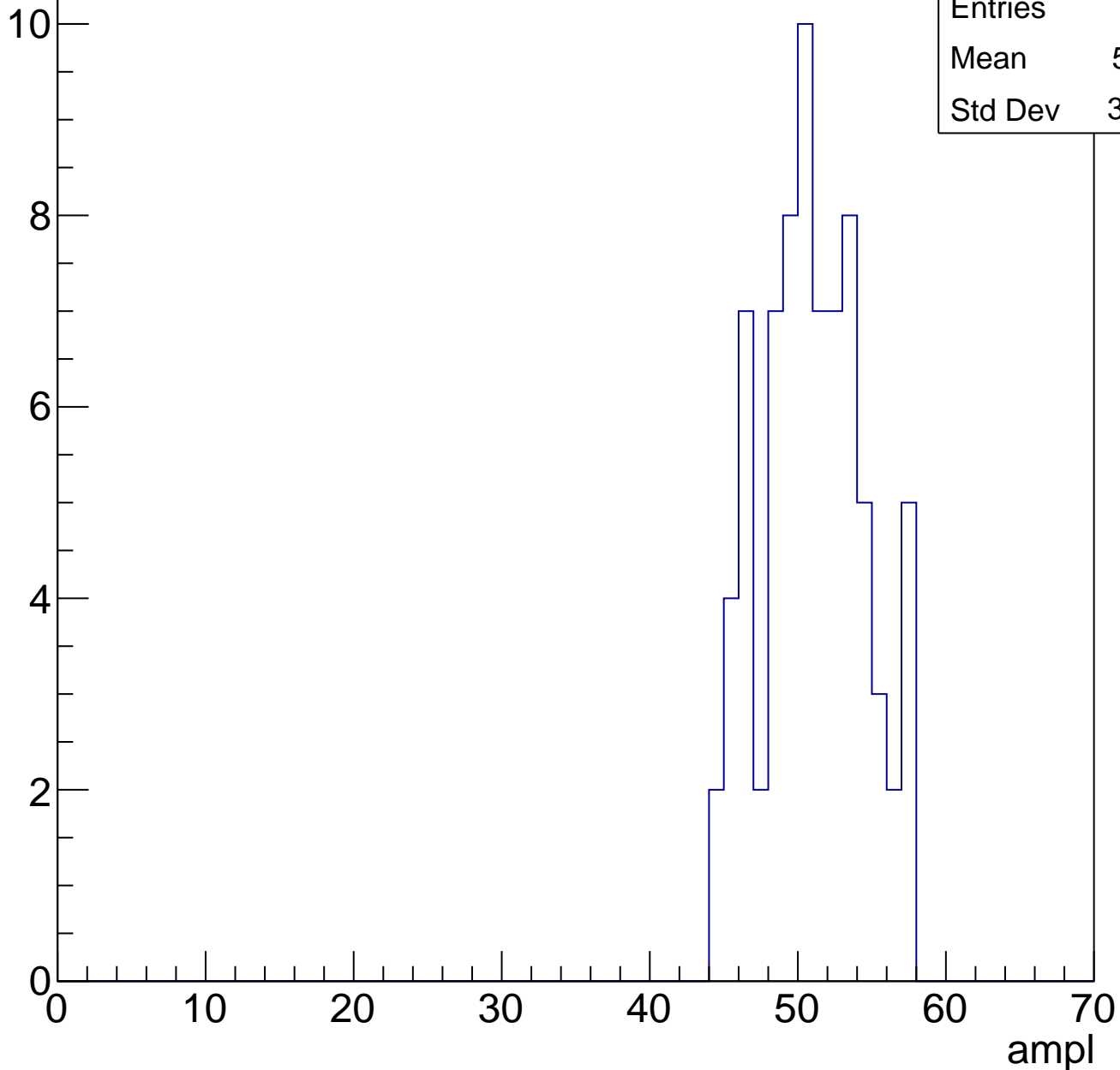


B0L102S, U12-ch79, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	50.51
Std Dev	3.414

Entry

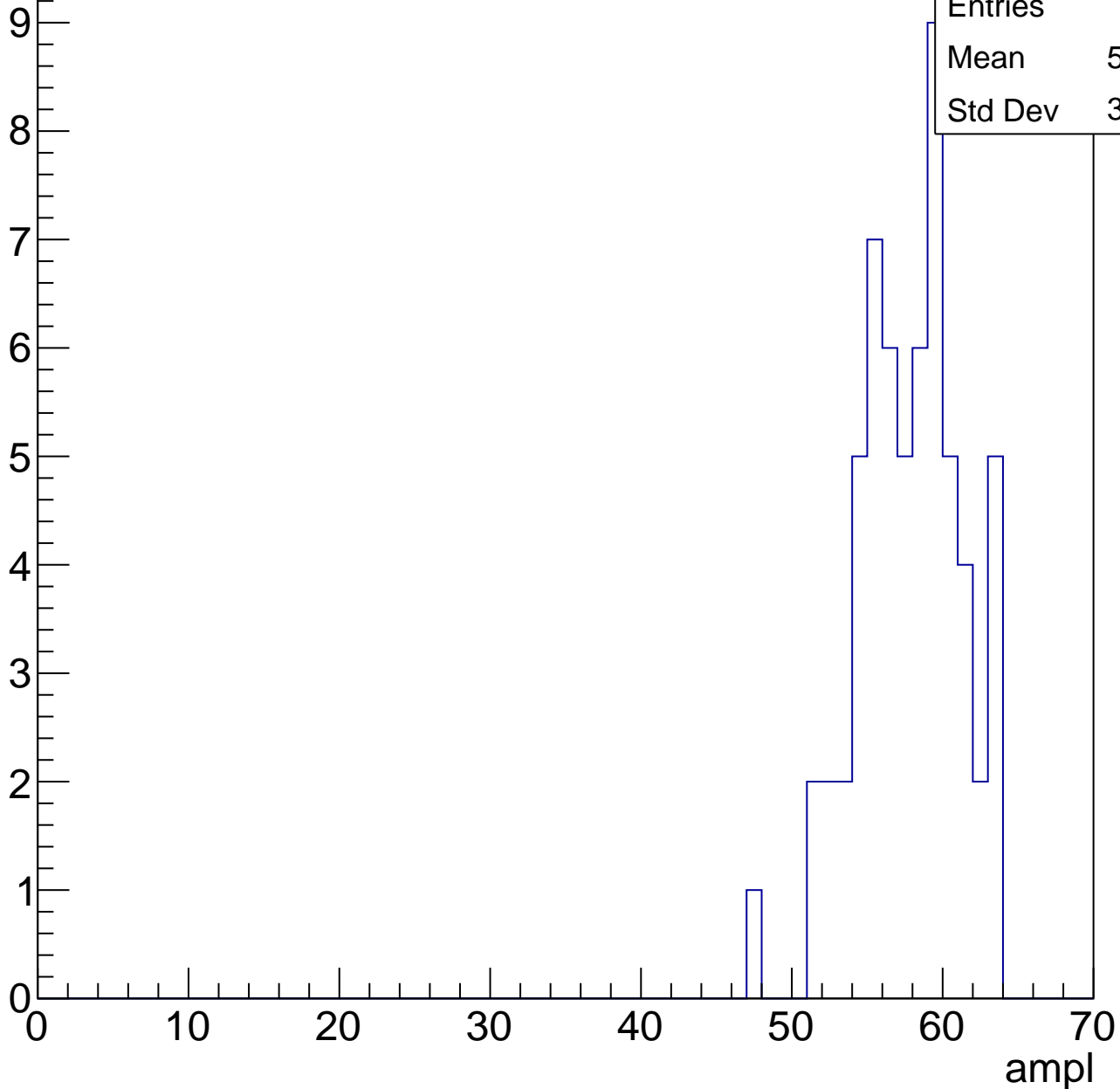


B0L102S, U12-ch79, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	57.33
Std Dev	3.405

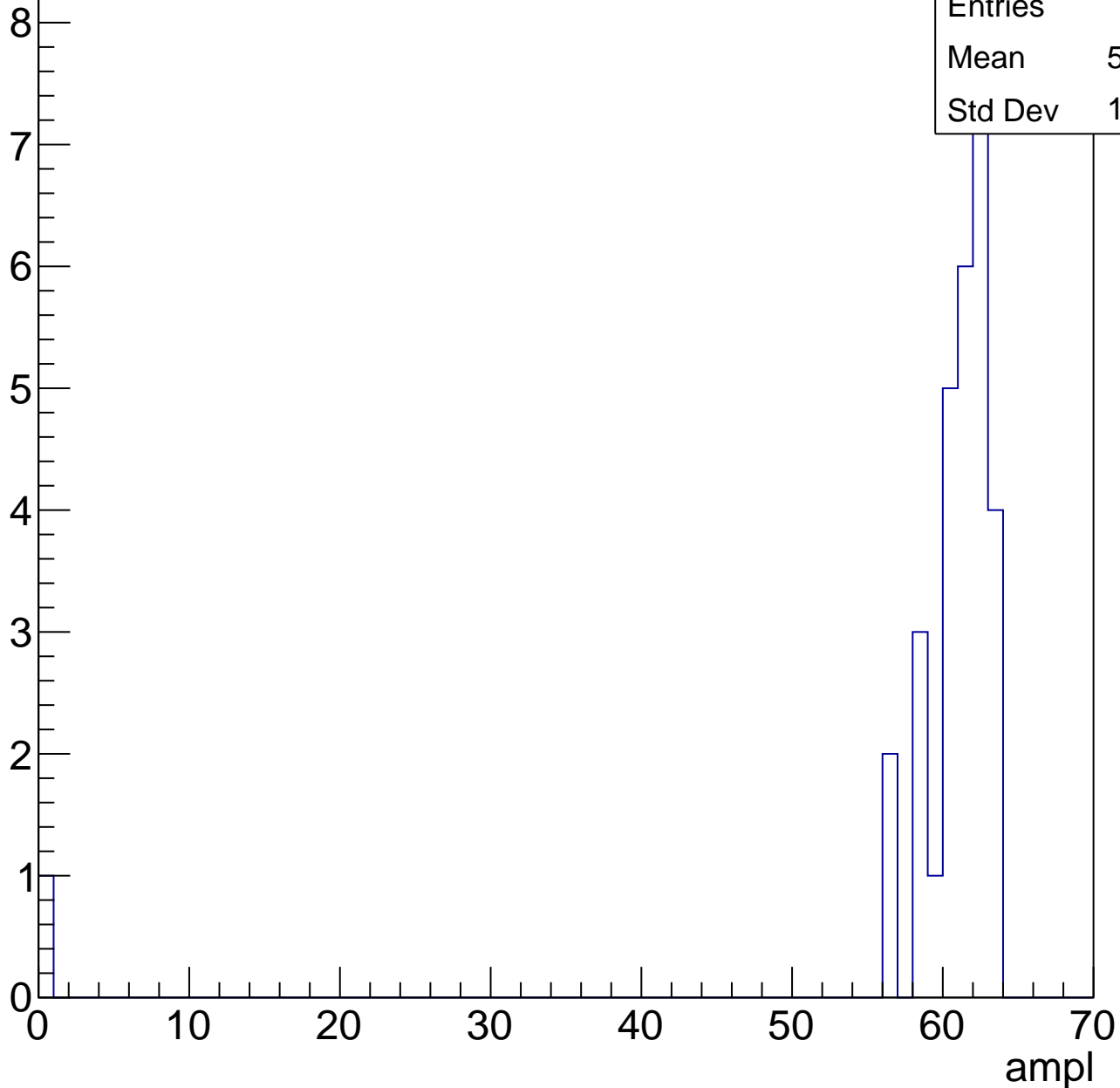


B0L102S, U12-ch79, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

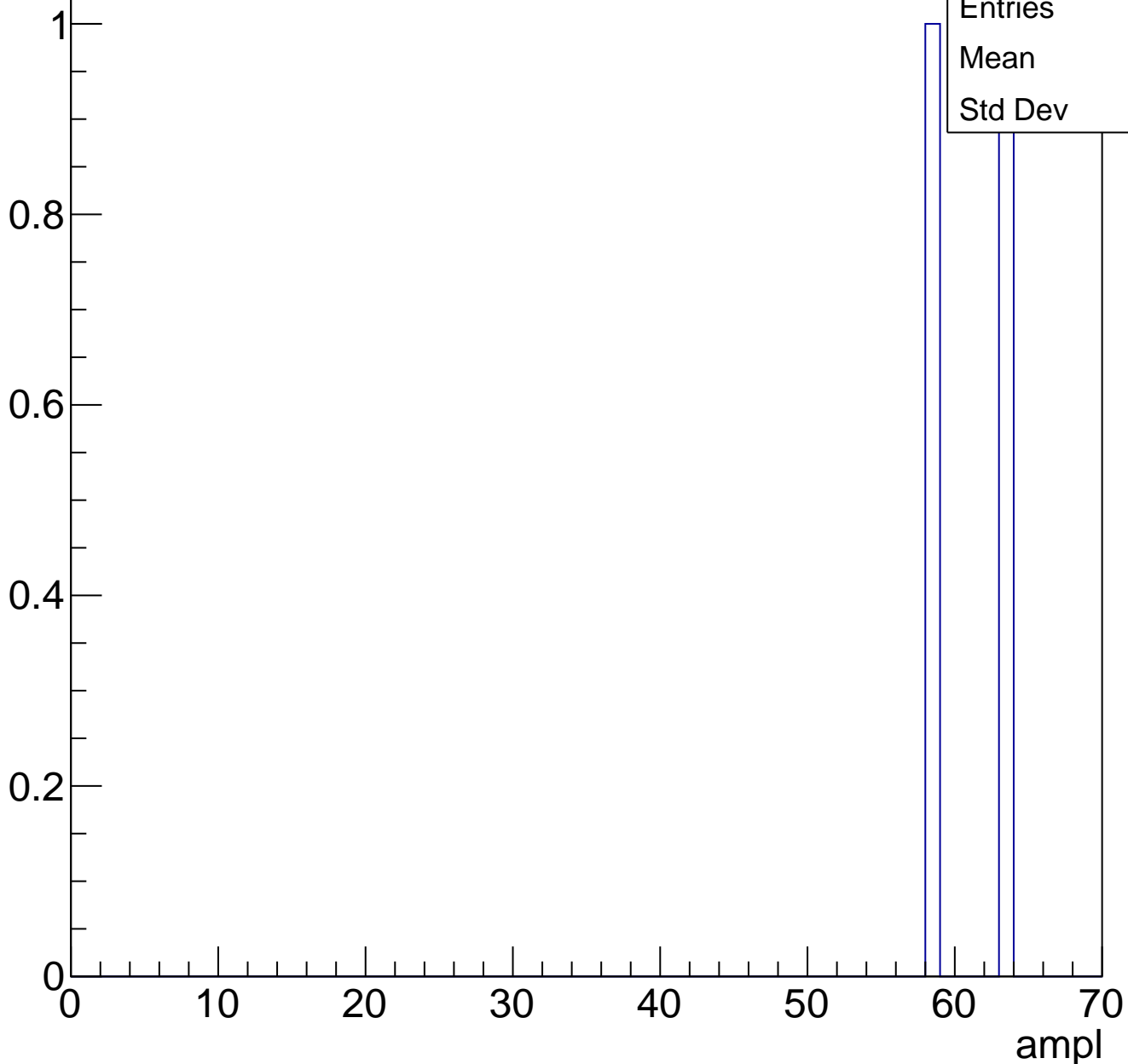
Entries	30
Mean	58.63
Std Dev	11.05



B0L102S, U12-ch79, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch79, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch80, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	65
Mean	28.25
Std Dev	3.347

Gaus mean : 28.6846

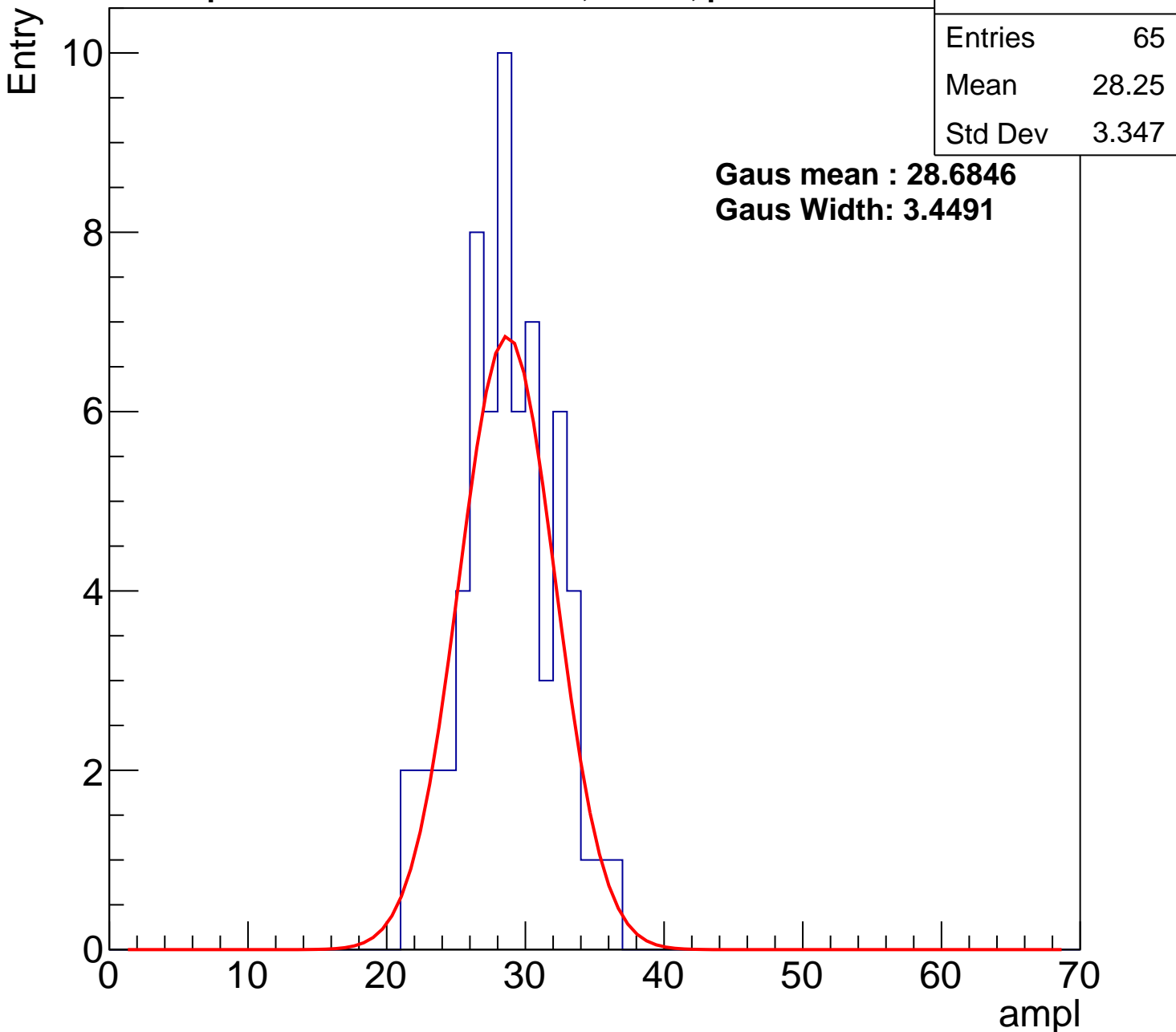
Gaus Width: 3.4491

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch80, adc1

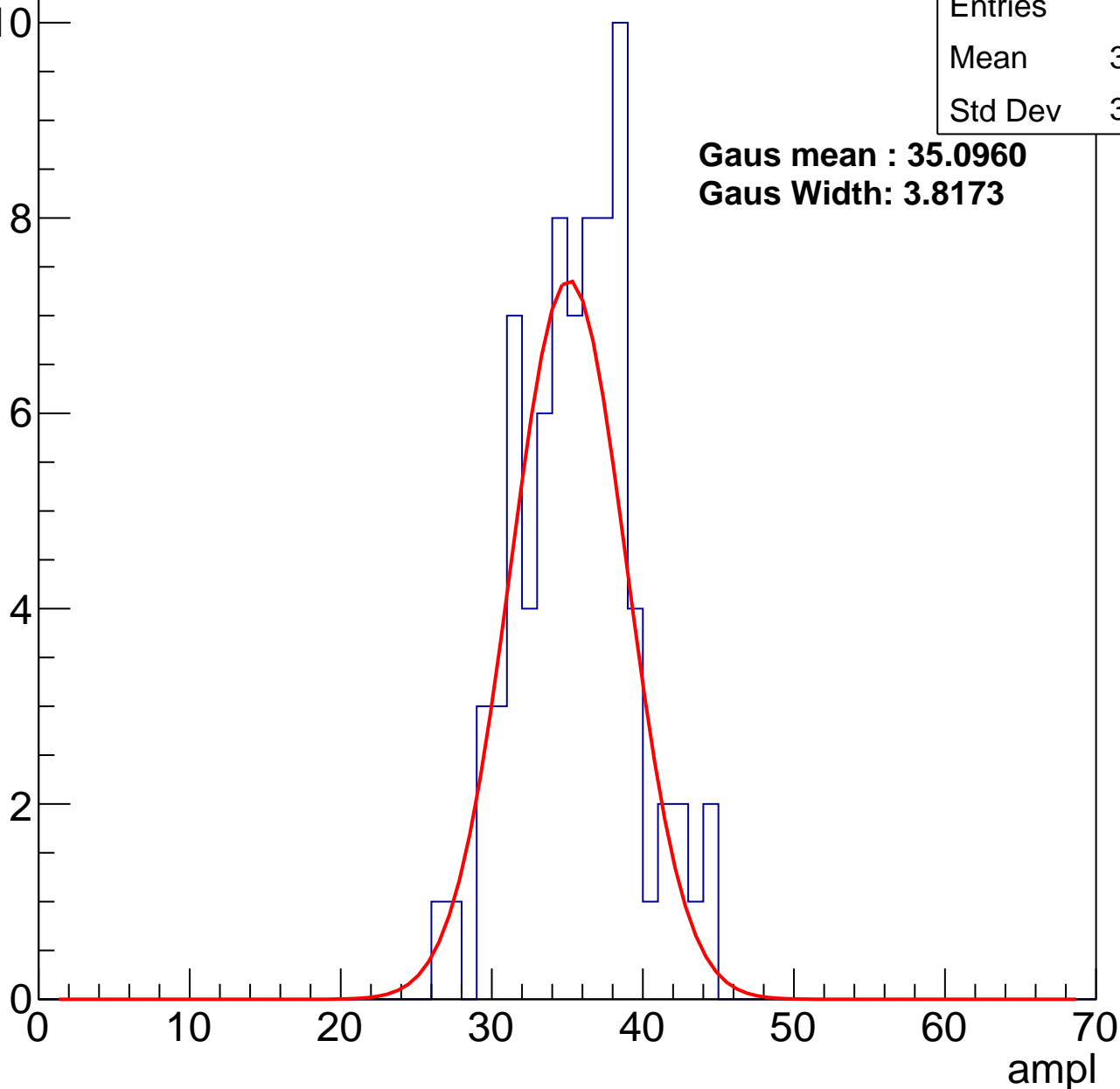
calib_packv5_042523_0143.root, FC#12, port B1

Entries	78
Mean	35.22
Std Dev	3.788

Gaus mean : 35.0960

Gaus Width: 3.8173

Entry



B0L102S, U12-ch80, adc2

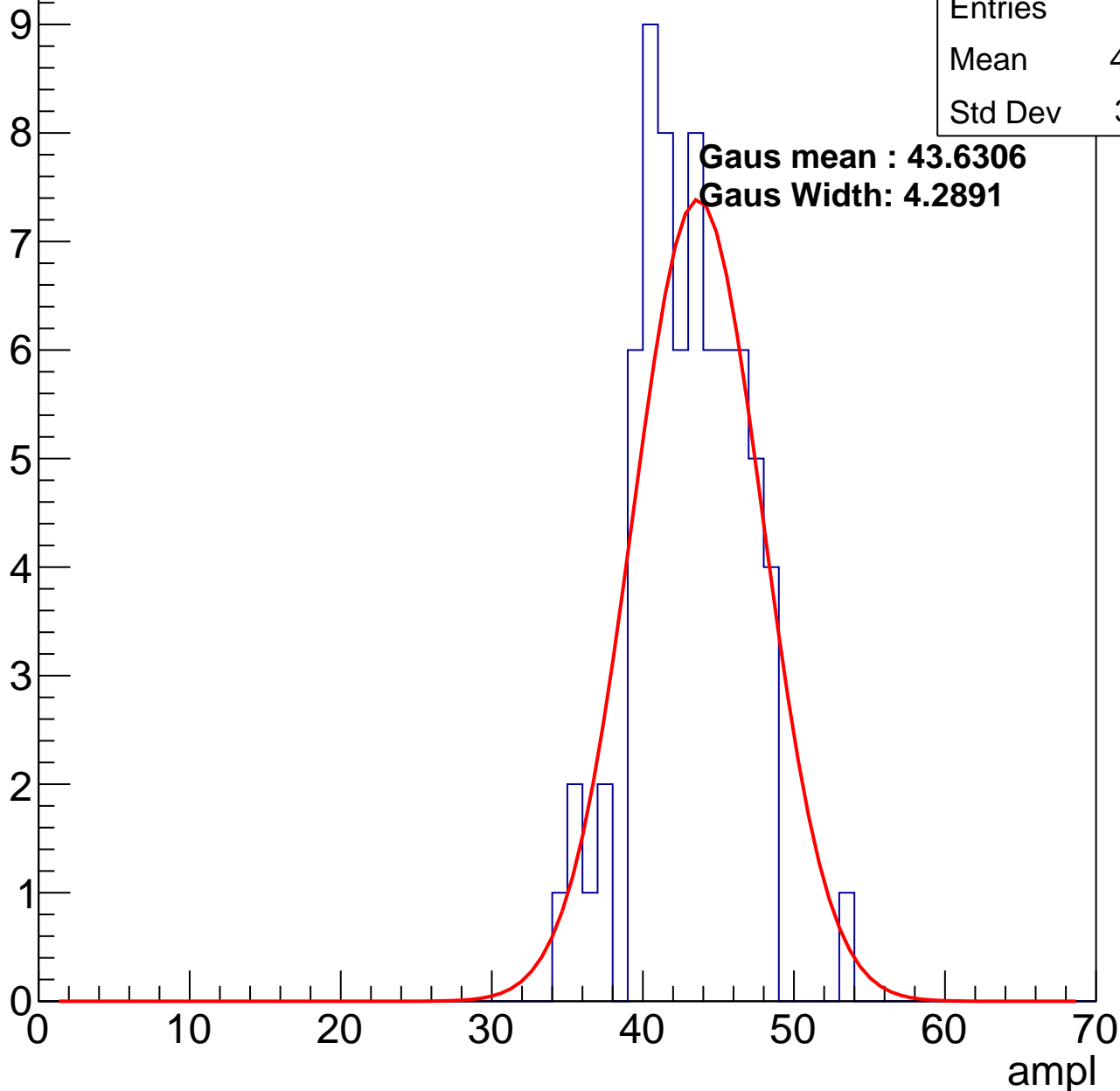
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	42.56
Std Dev	3.551

Gaus mean : 43.6306

Gaus Width: 4.2891

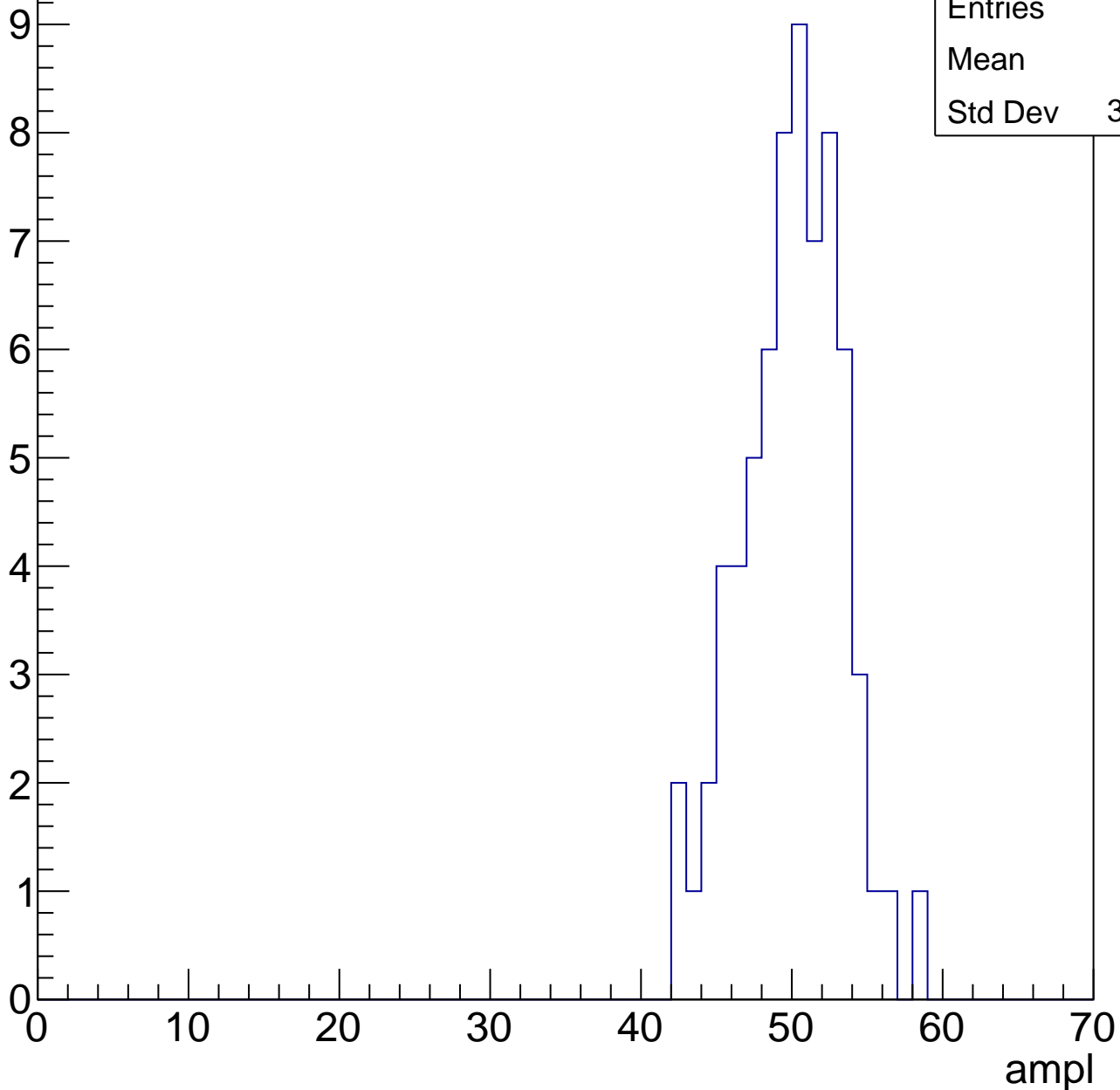


B0L102S, U12-ch80, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.5
Std Dev	3.297

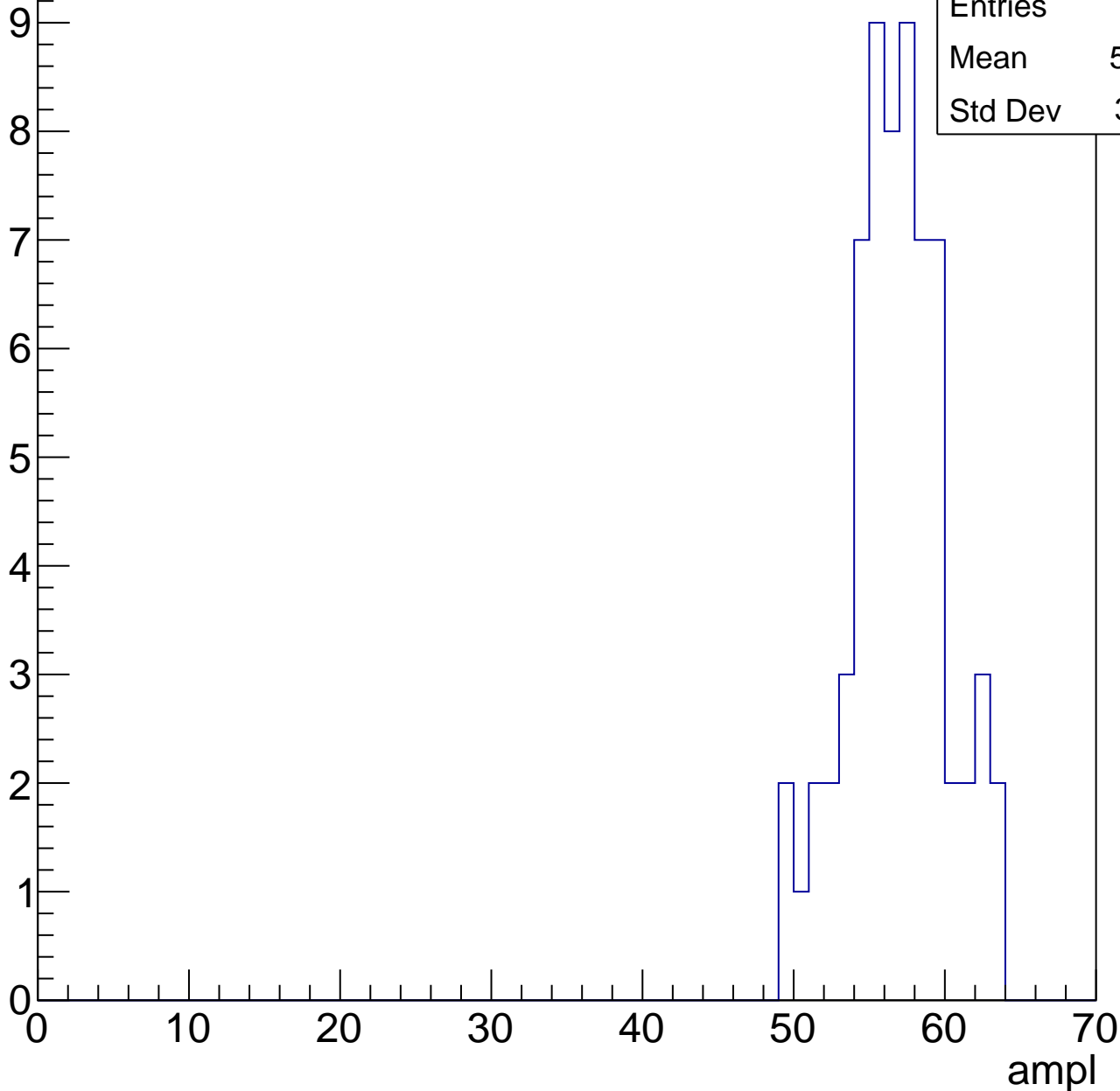


B0L102S, U12-ch80, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	56.36
Std Dev	3.161

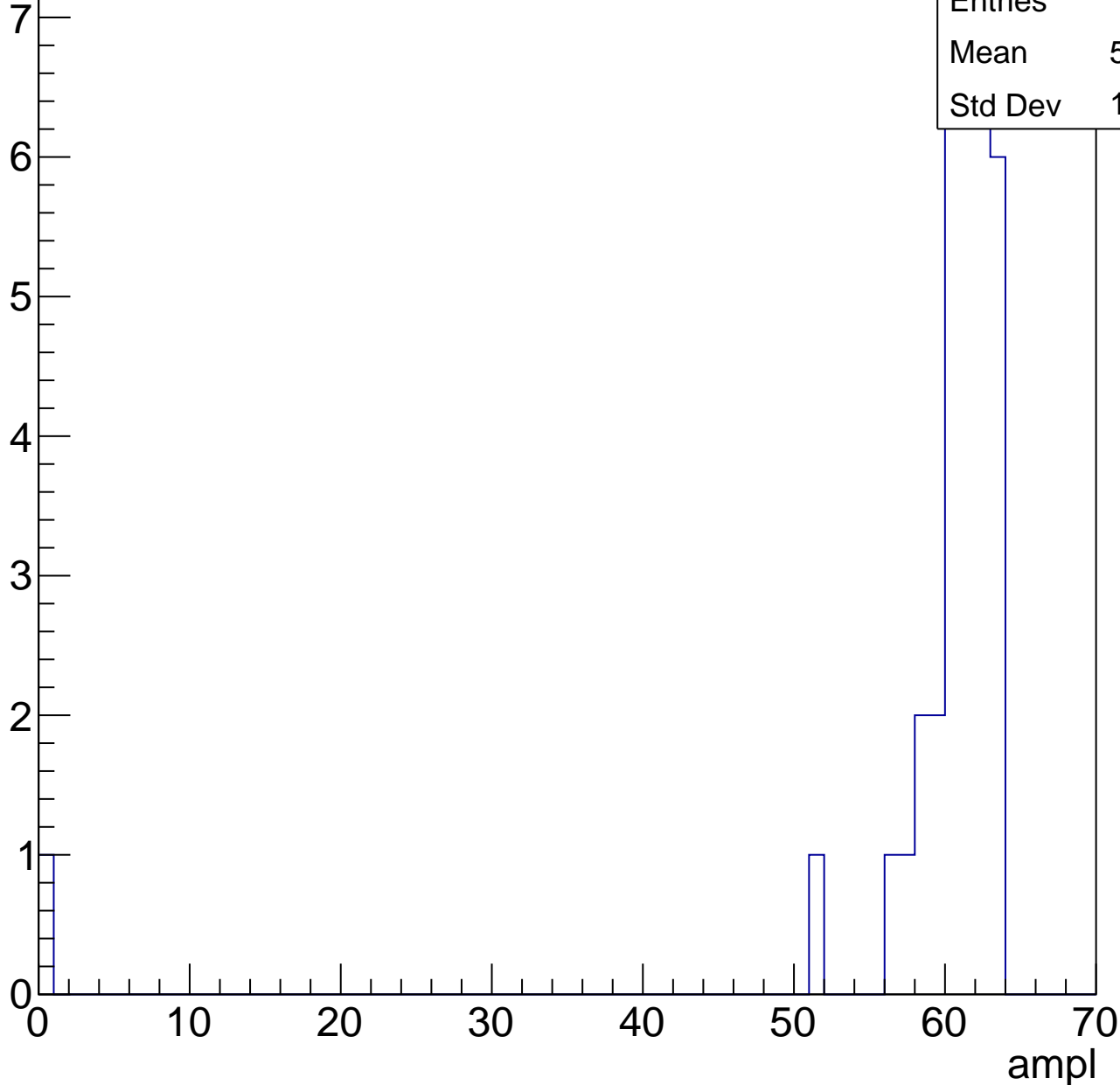


B0L102S, U12-ch80, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

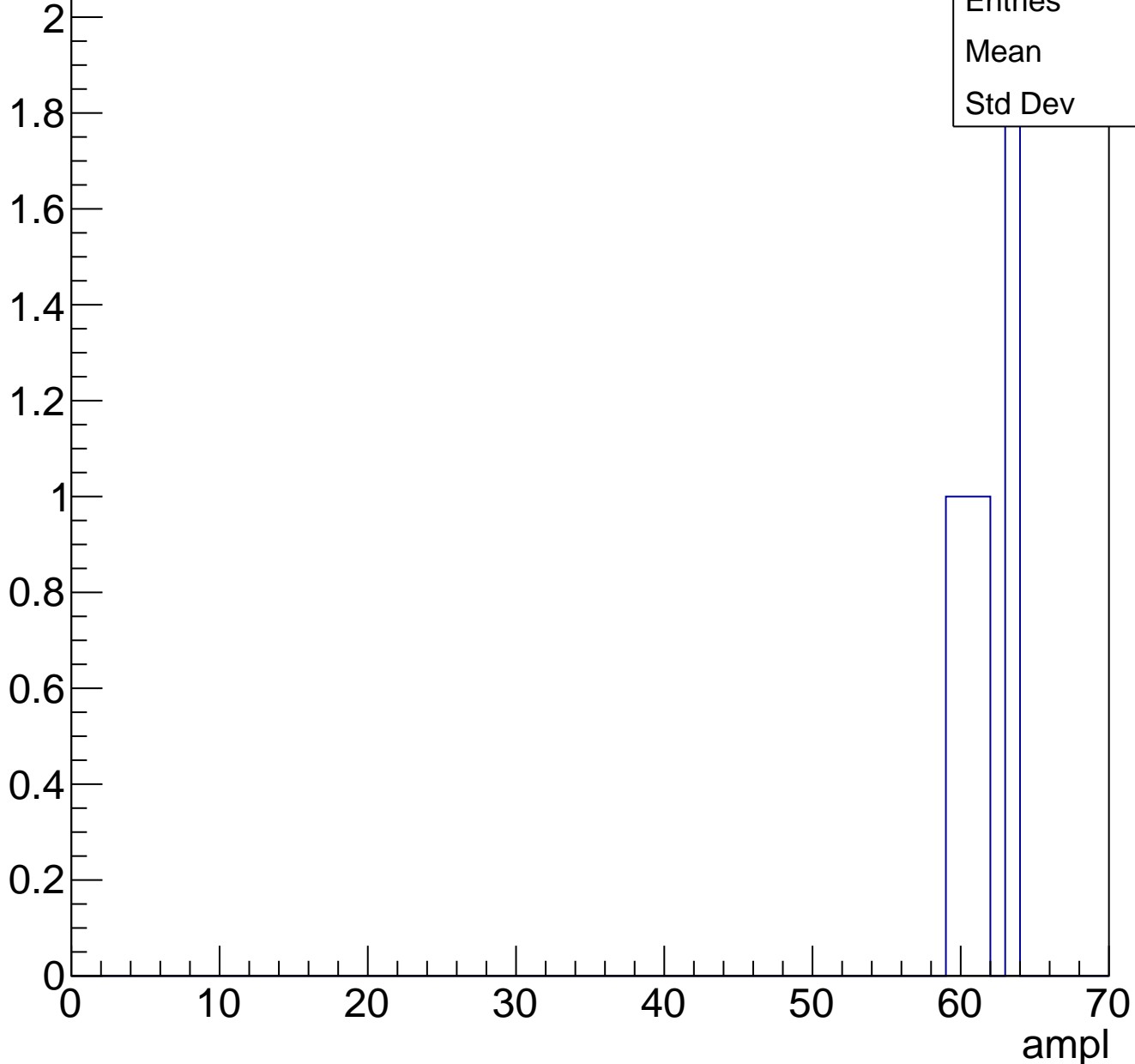
Entries	35
Mean	58.77
Std Dev	10.35



B0L102S, U12-ch80, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch80, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch81, adc0

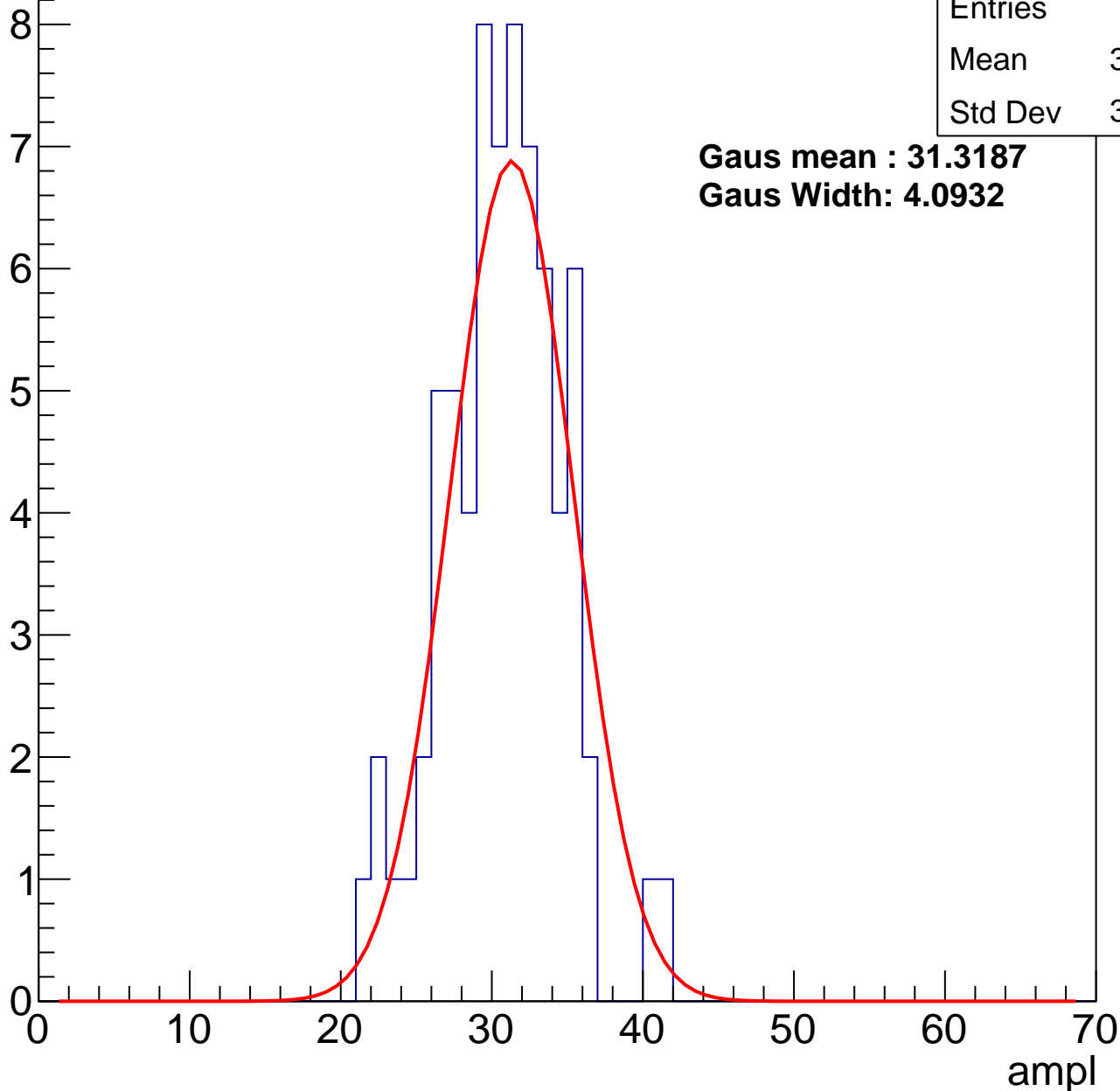
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	30.28
Std Dev	3.908

Gaus mean : 31.3187

Gaus Width: 4.0932



B0L102S, U12-ch81, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	64
Mean	36.48
Std Dev	5.69

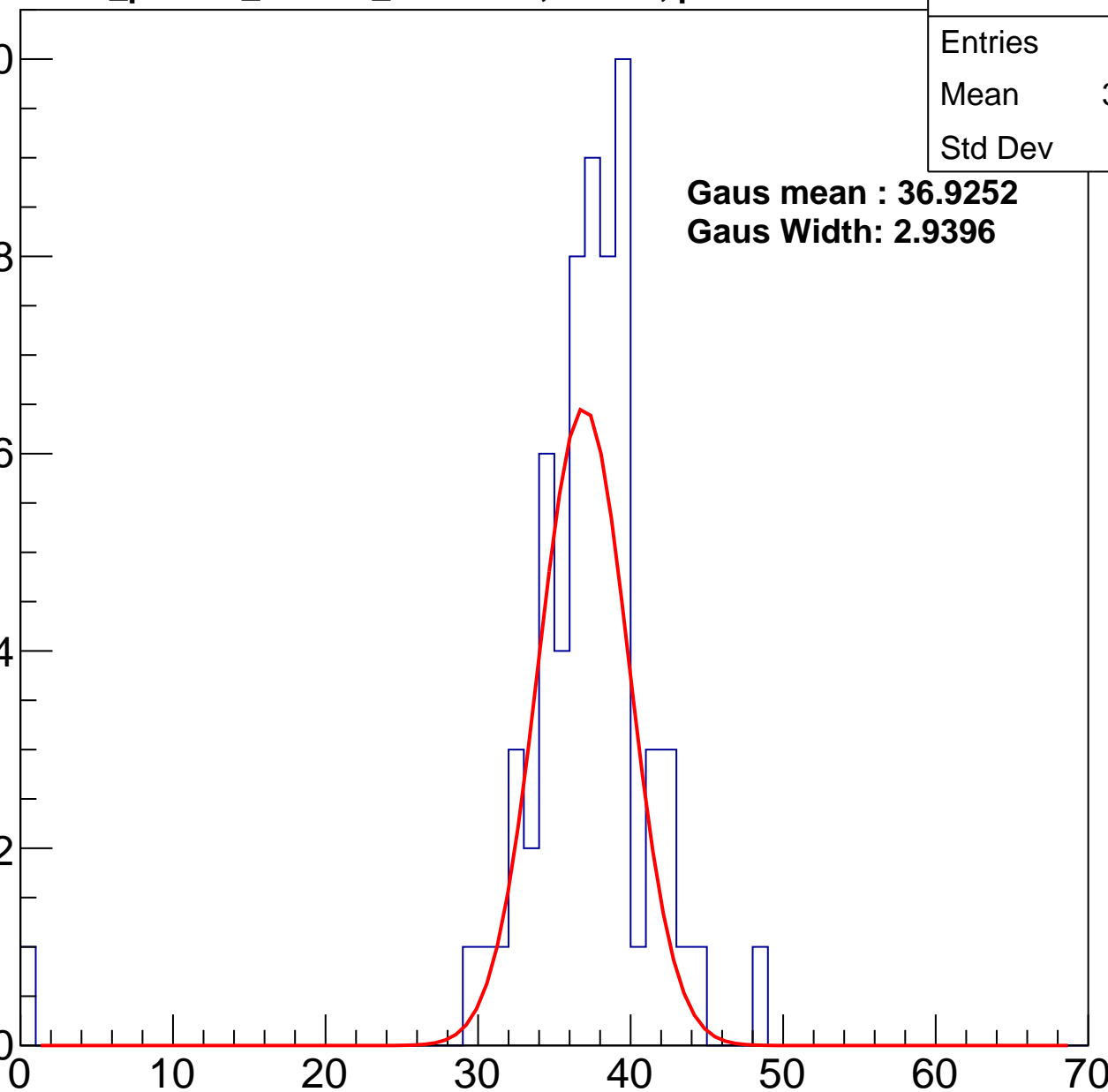
Gaus mean : 36.9252

Gaus Width: 2.9396

Entry

10
8
6
4
2
0

ampl



B0L102S, U12-ch81, adc2

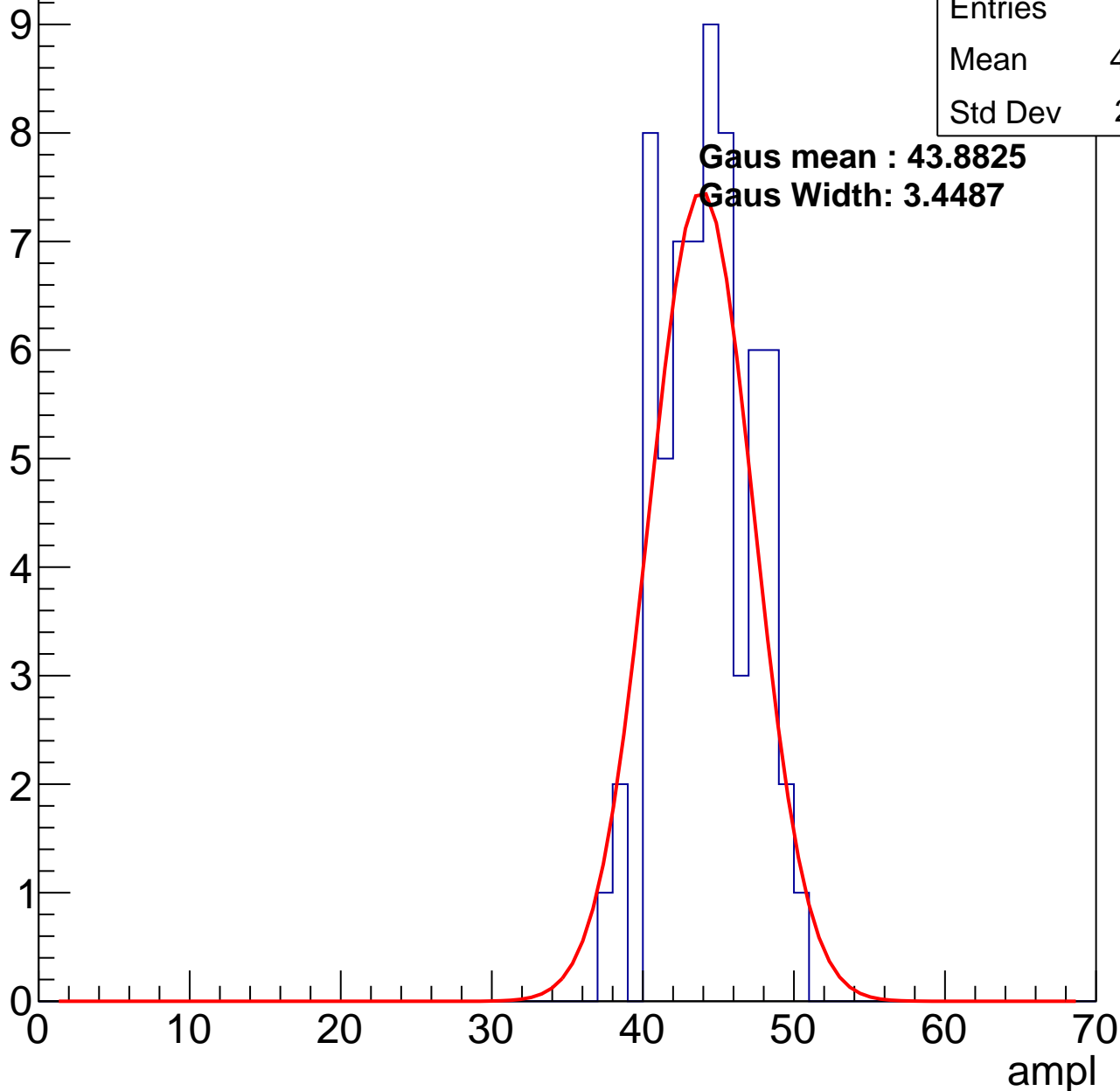
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.77
Std Dev	2.991

Gaus mean : 43.8825

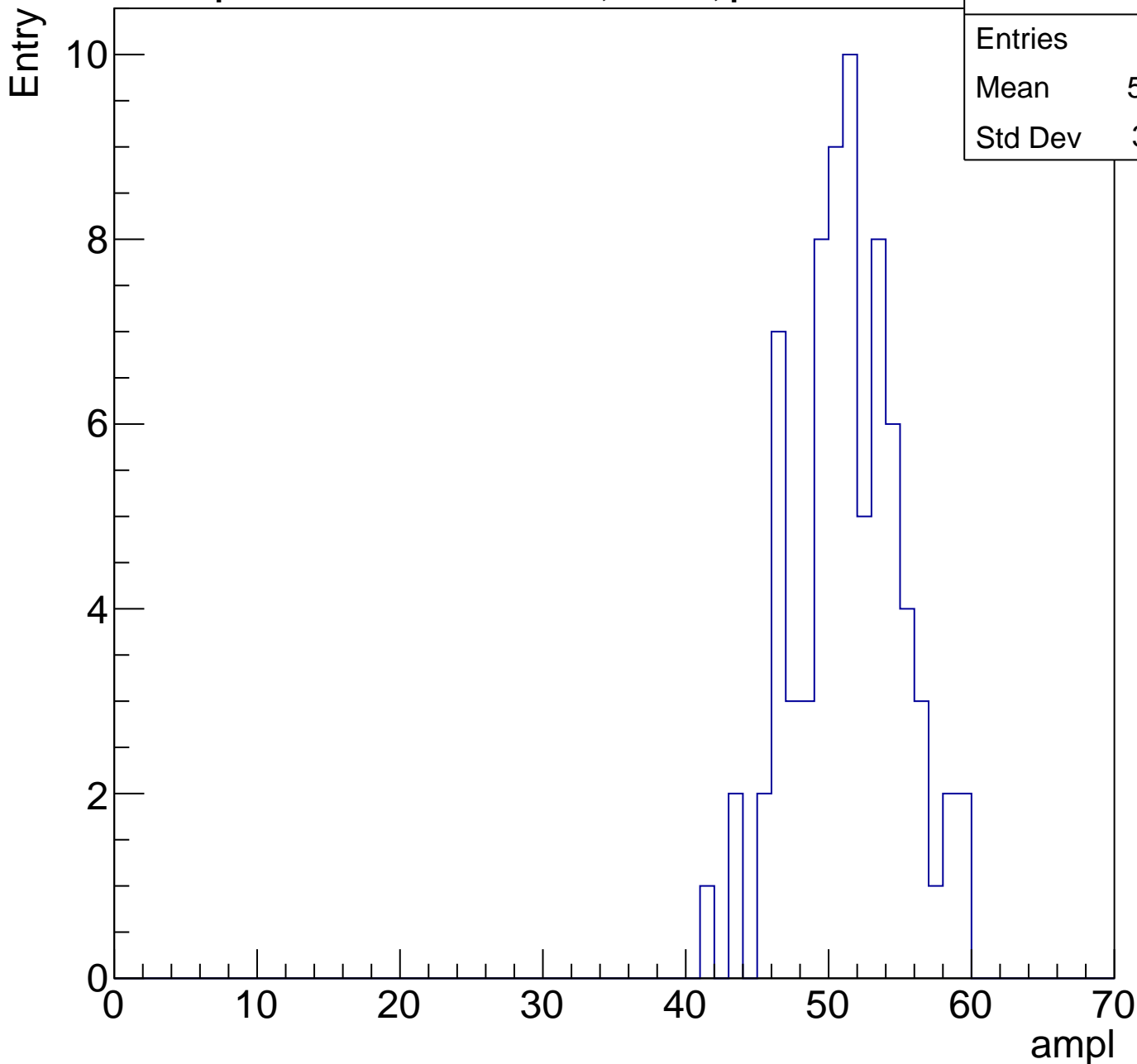
Gaus Width: 3.4487



B0L102S, U12-ch81, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	50.83
Std Dev	3.771

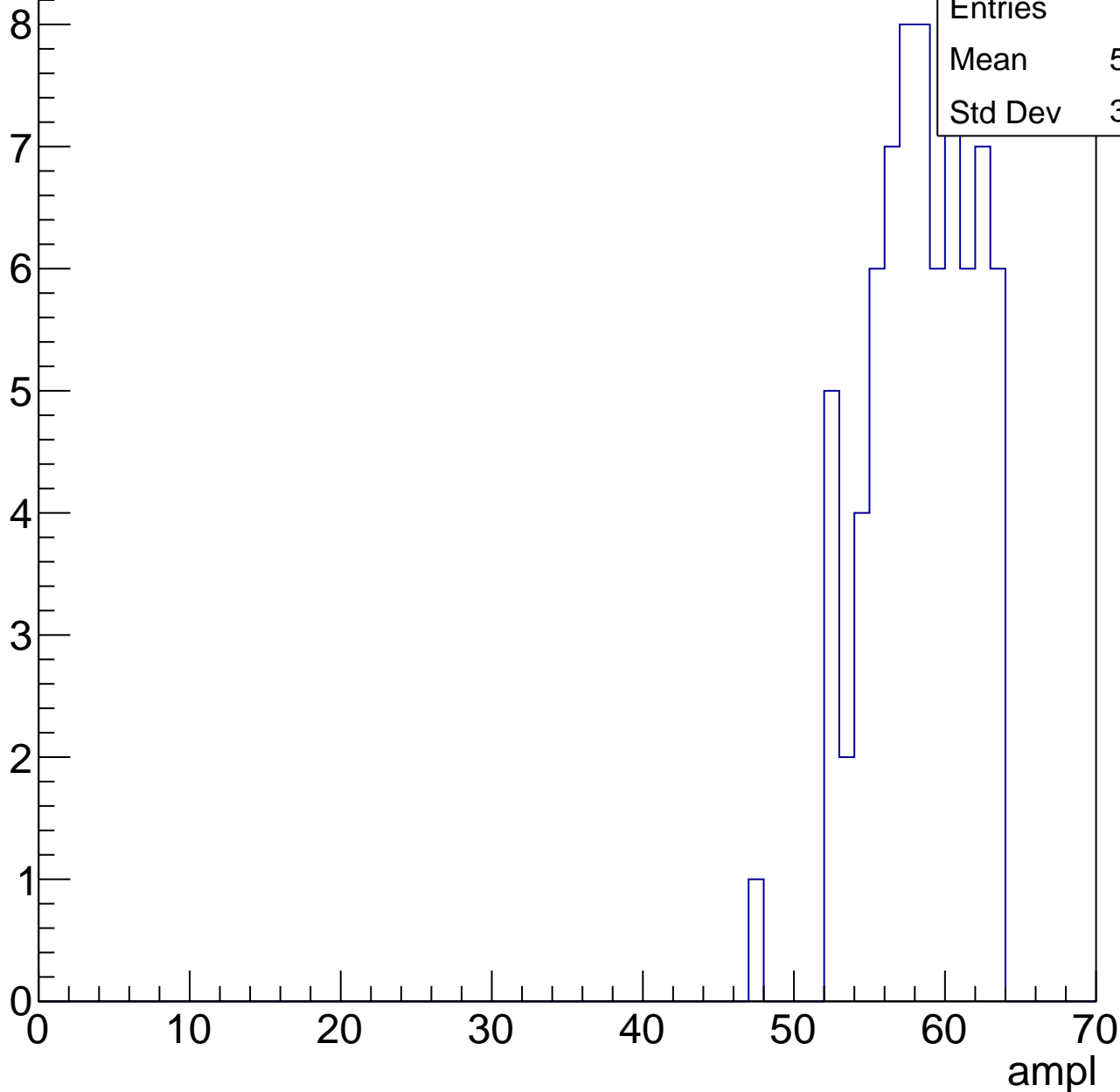


B0L102S, U12-ch81, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

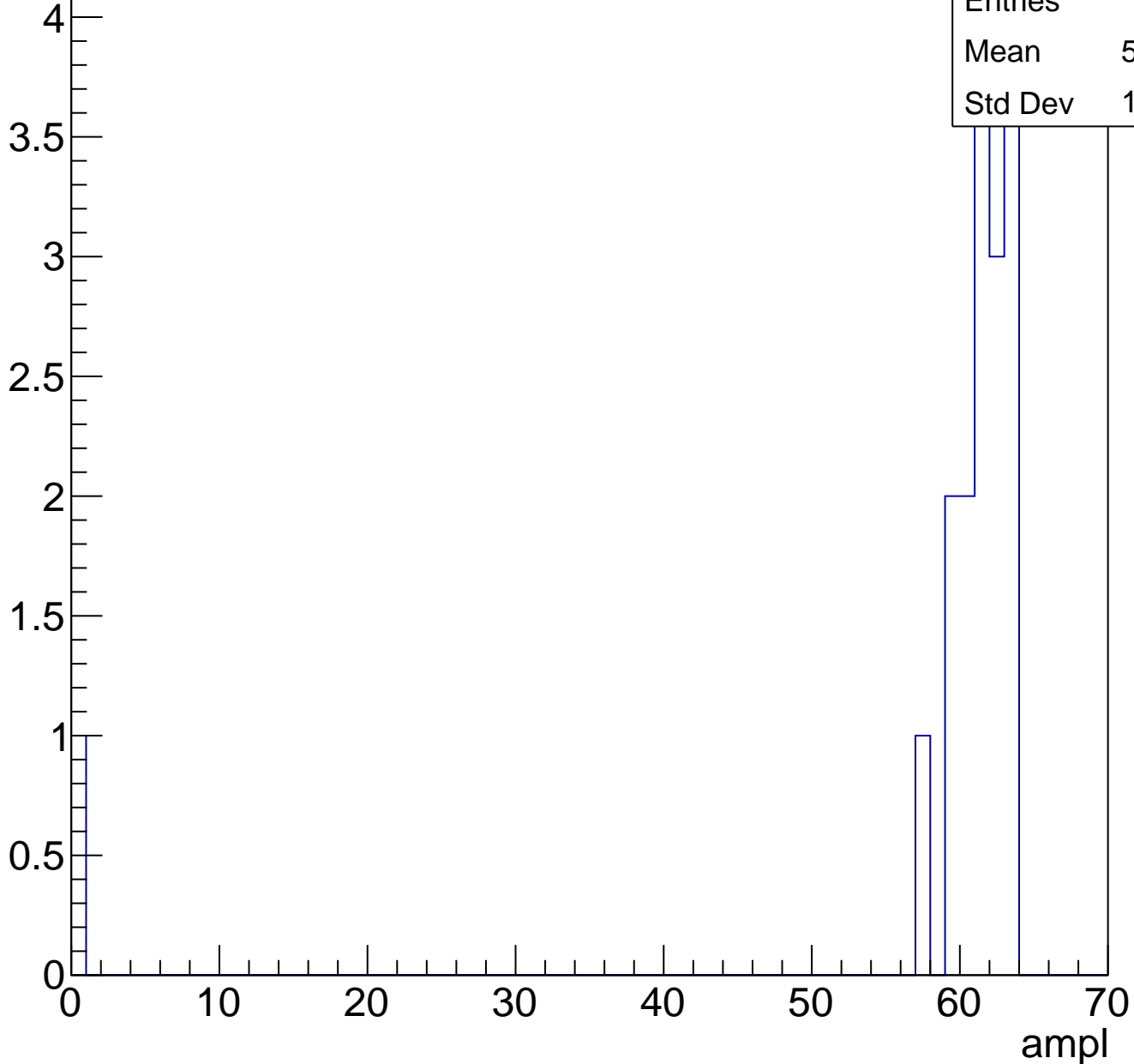
Entries	74
Mean	57.88
Std Dev	3.405



B0L102S, U12-ch81, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch81, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch81, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch82, adc0

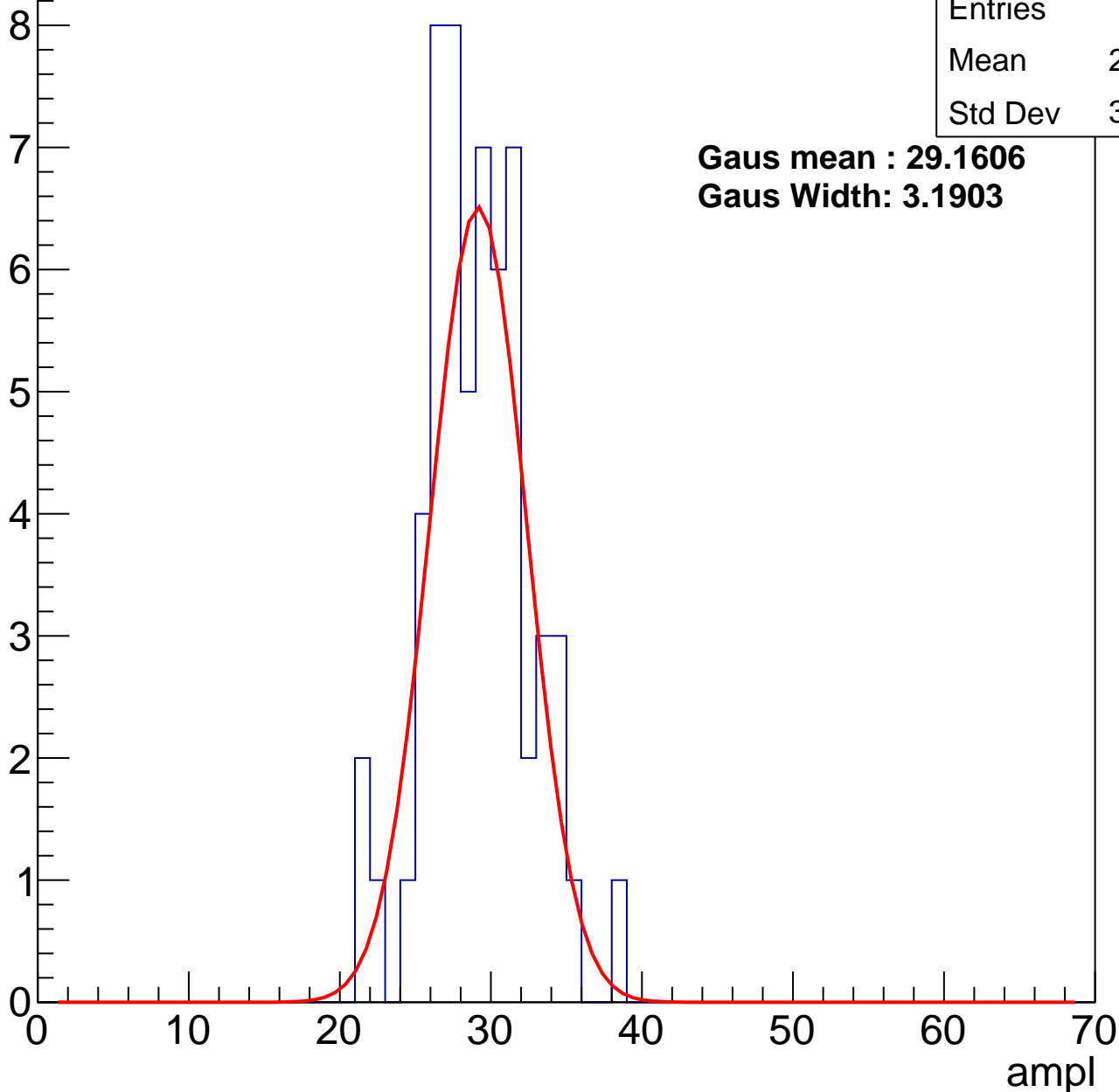
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	28.64
Std Dev	3.343

Gaus mean : 29.1606

Gaus Width: 3.1903



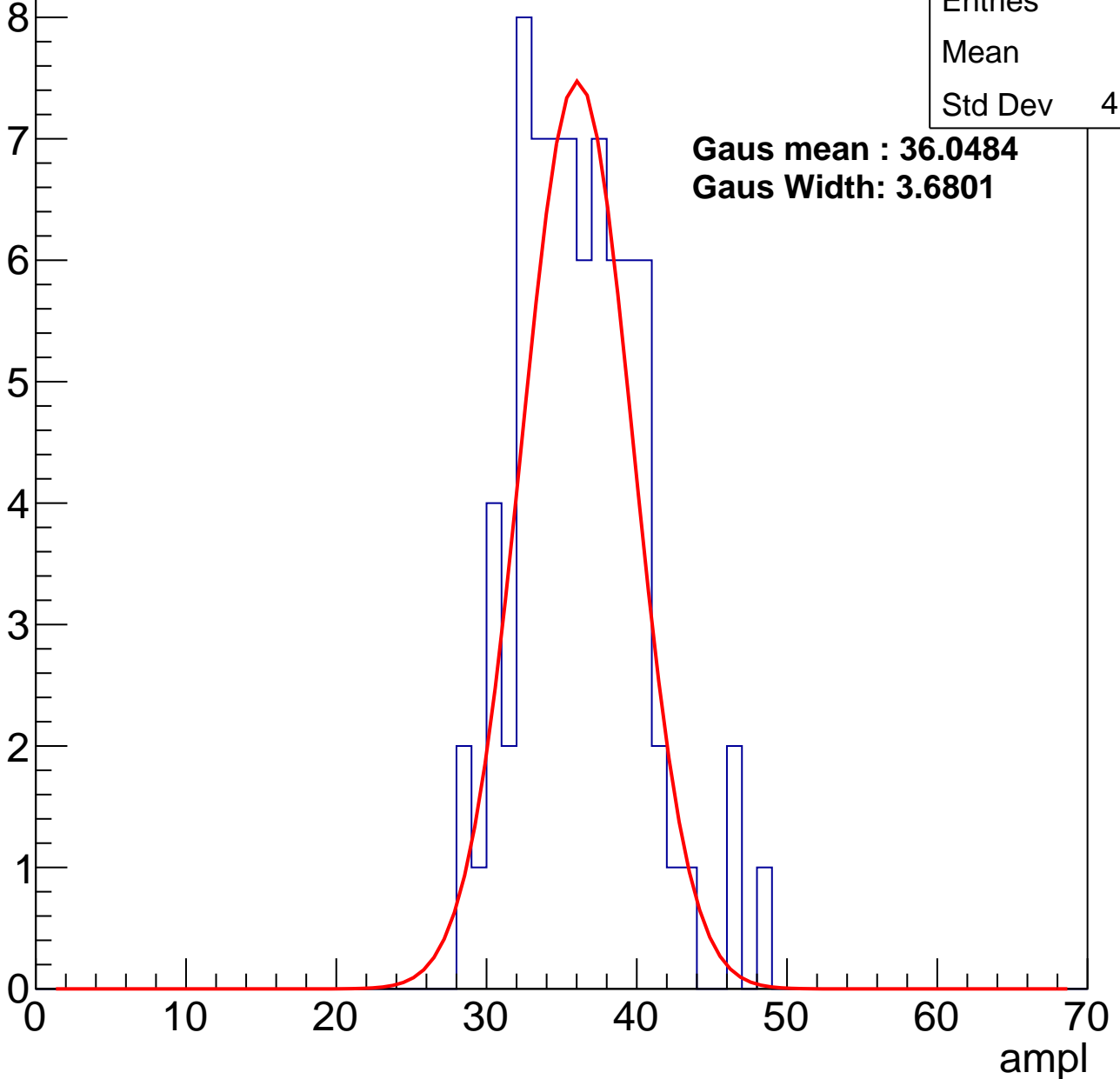
B0L102S, U12-ch82, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	35.8
Std Dev	4.062

Gaus mean : 36.0484
Gaus Width: 3.6801



B0L102S, U12-ch82, adc2

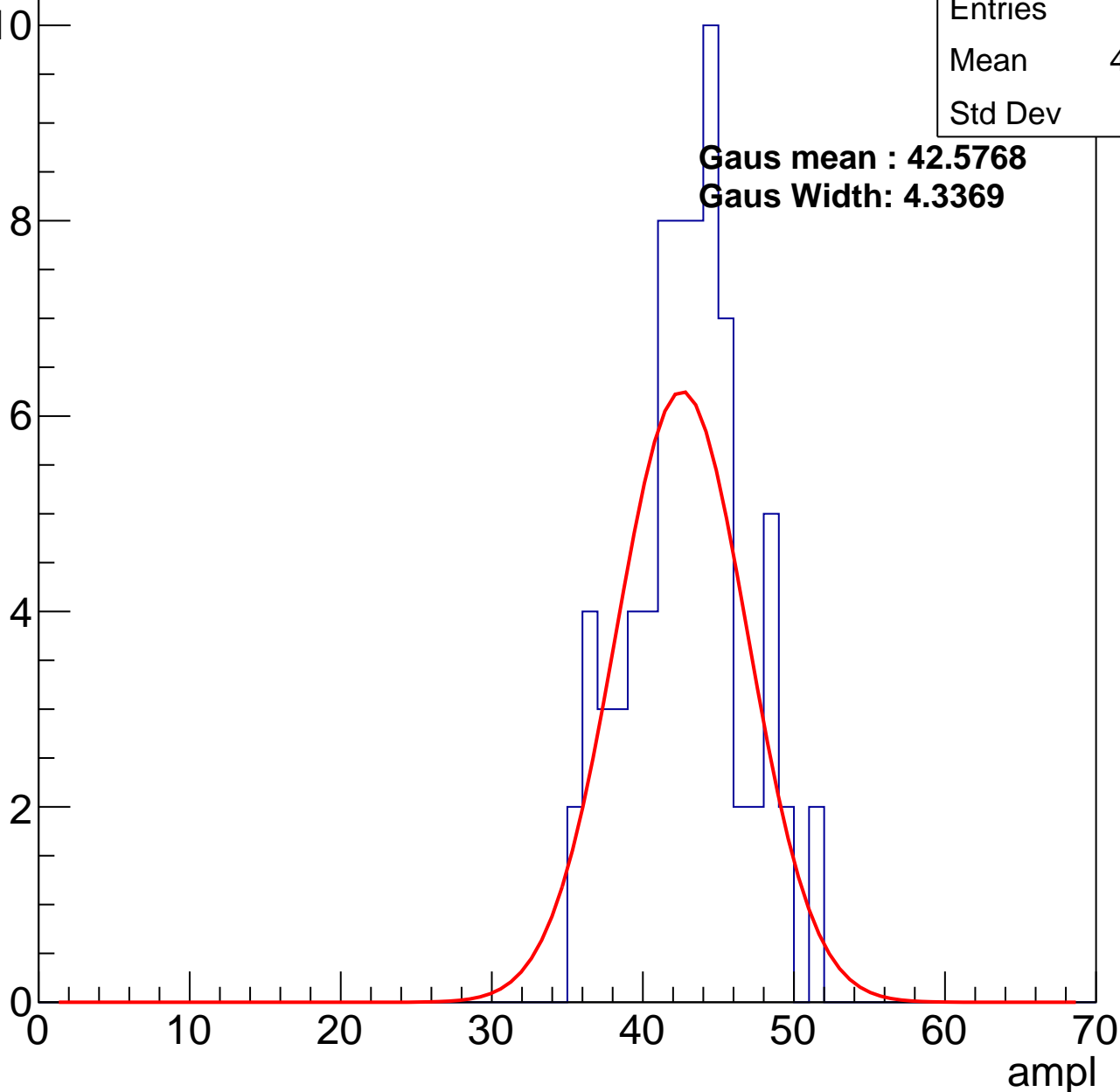
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	42.49
Std Dev	3.75

Gaus mean : 42.5768

Gaus Width: 4.3369

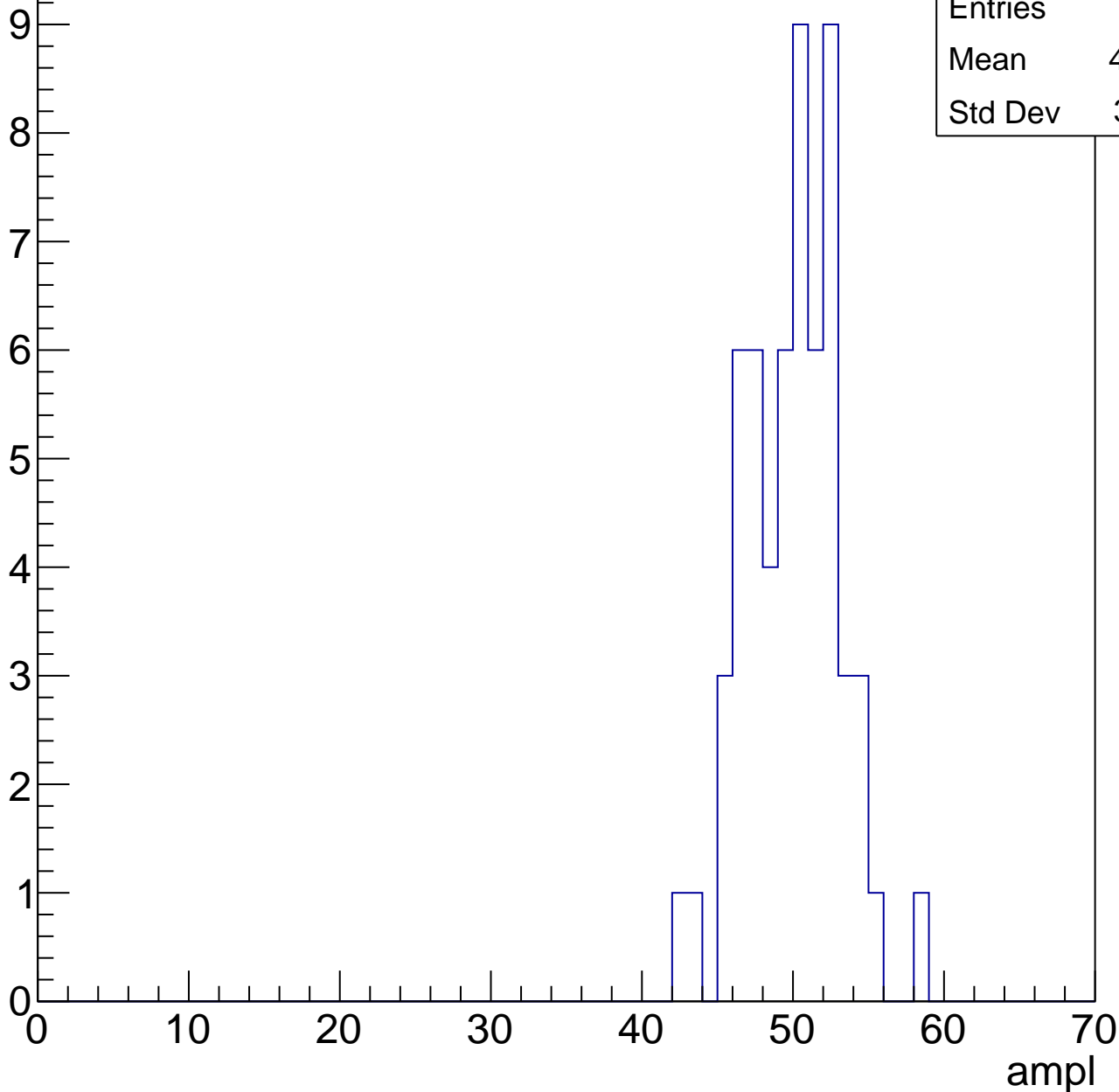


B0L102S, U12-ch82, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	49.53
Std Dev	3.061

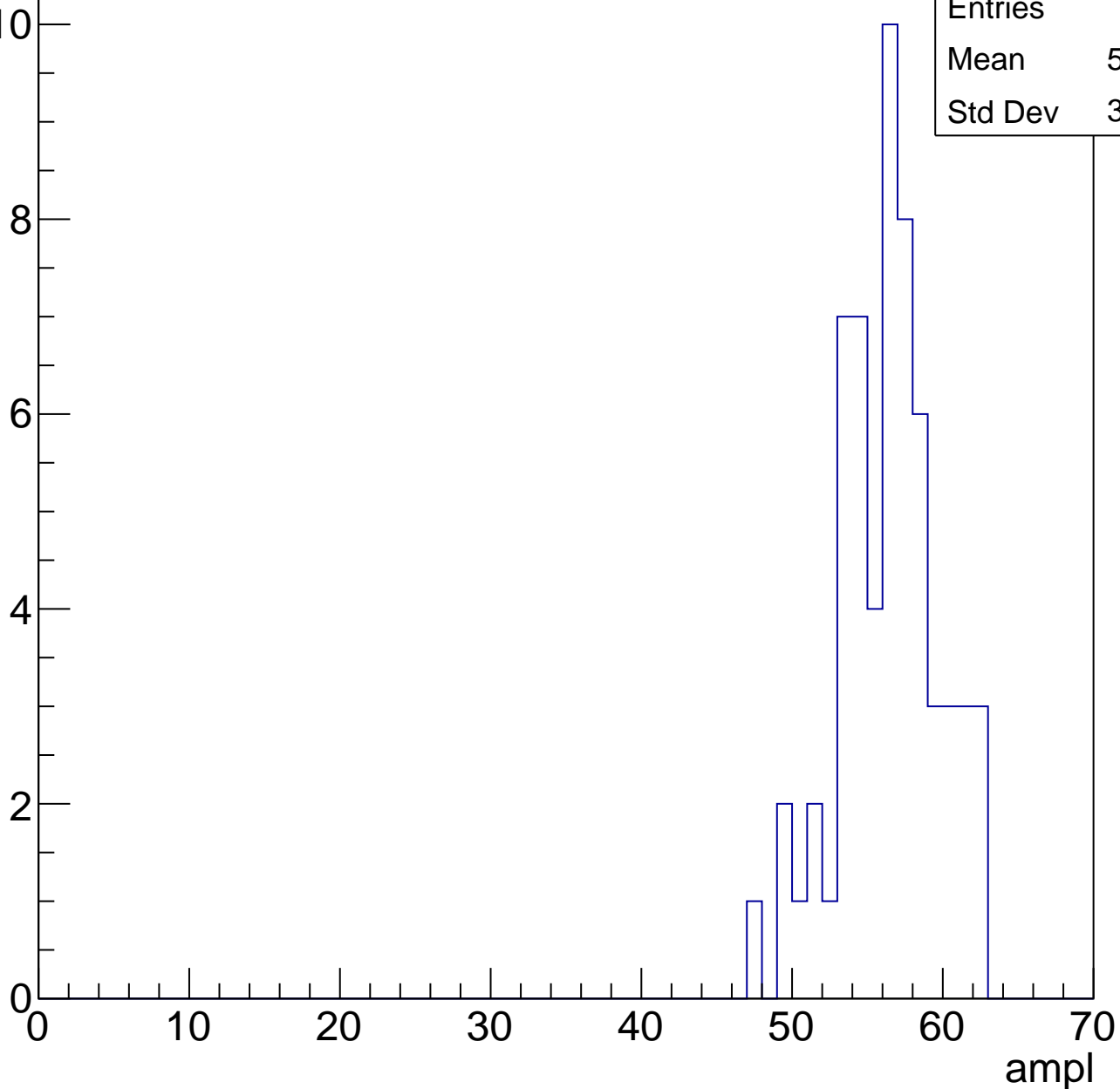


B0L102S, U12-ch82, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.87
Std Dev	3.302

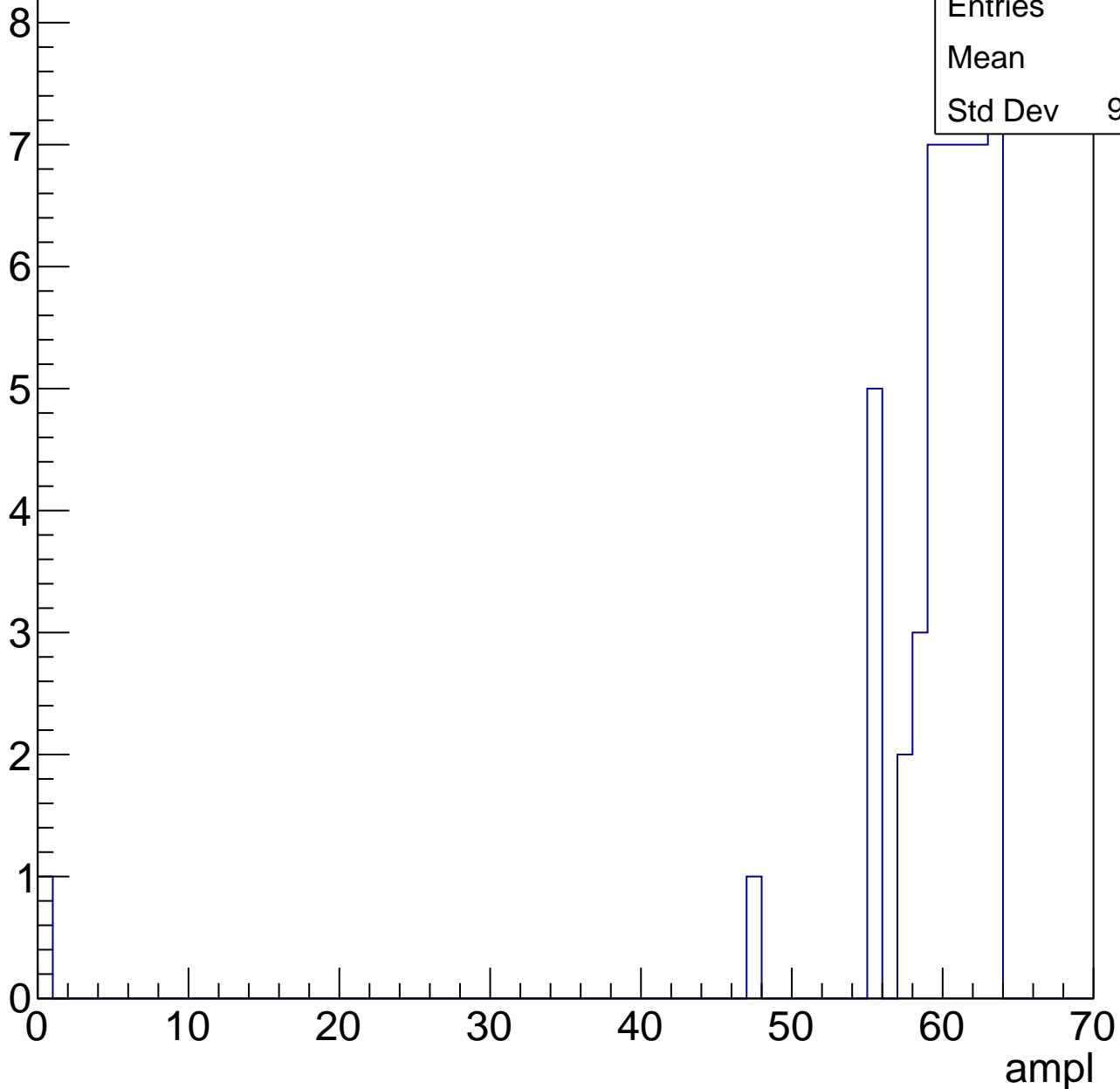


B0L102S, U12-ch82, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	58.5
Std Dev	9.048



B0L102S, U12-ch82, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch82, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L102S, U12-ch83, adc0

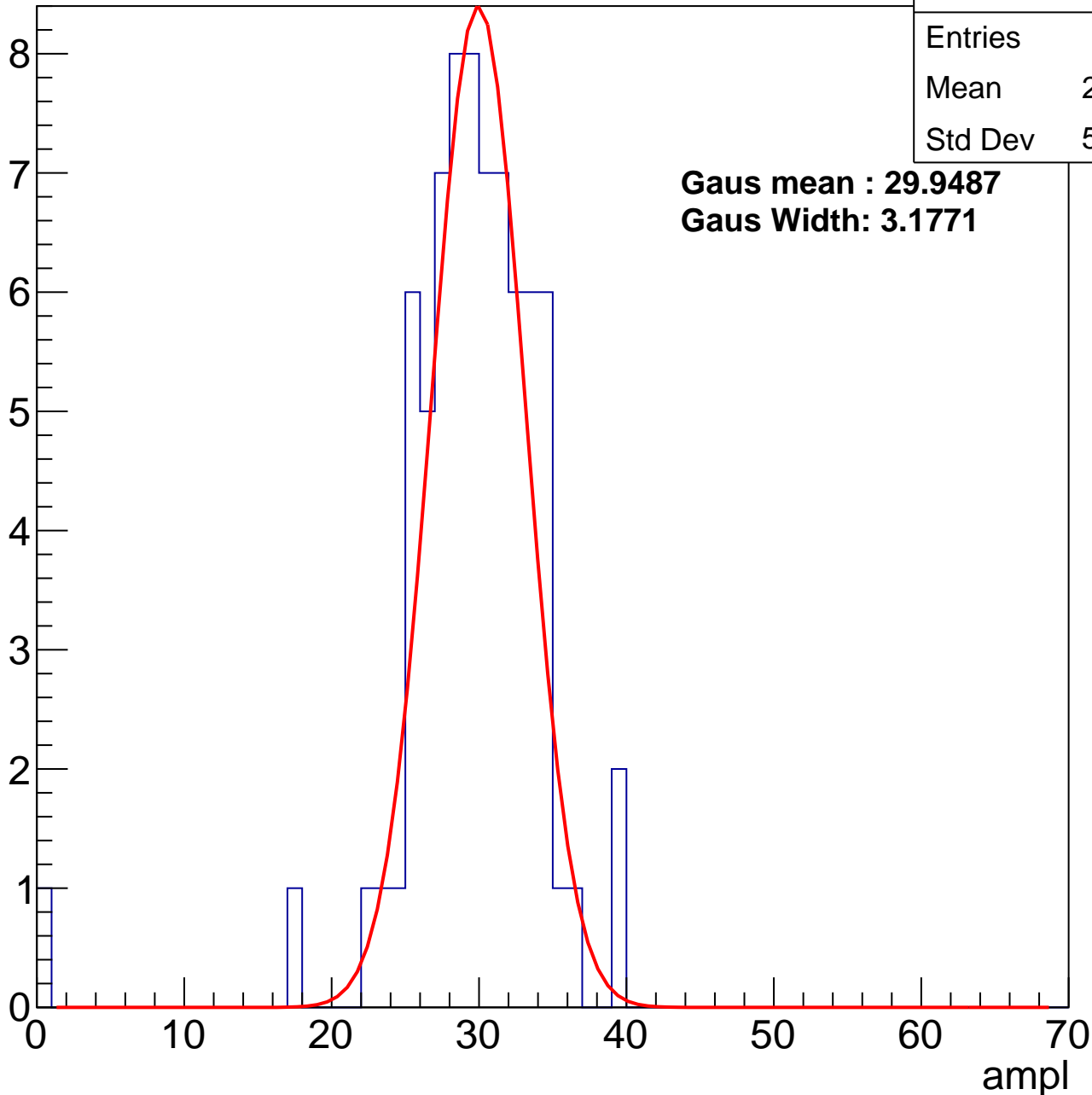
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	29.08
Std Dev	5.022

Gaus mean : 29.9487

Gaus Width: 3.1771



B0L102S, U12-ch83, adc1

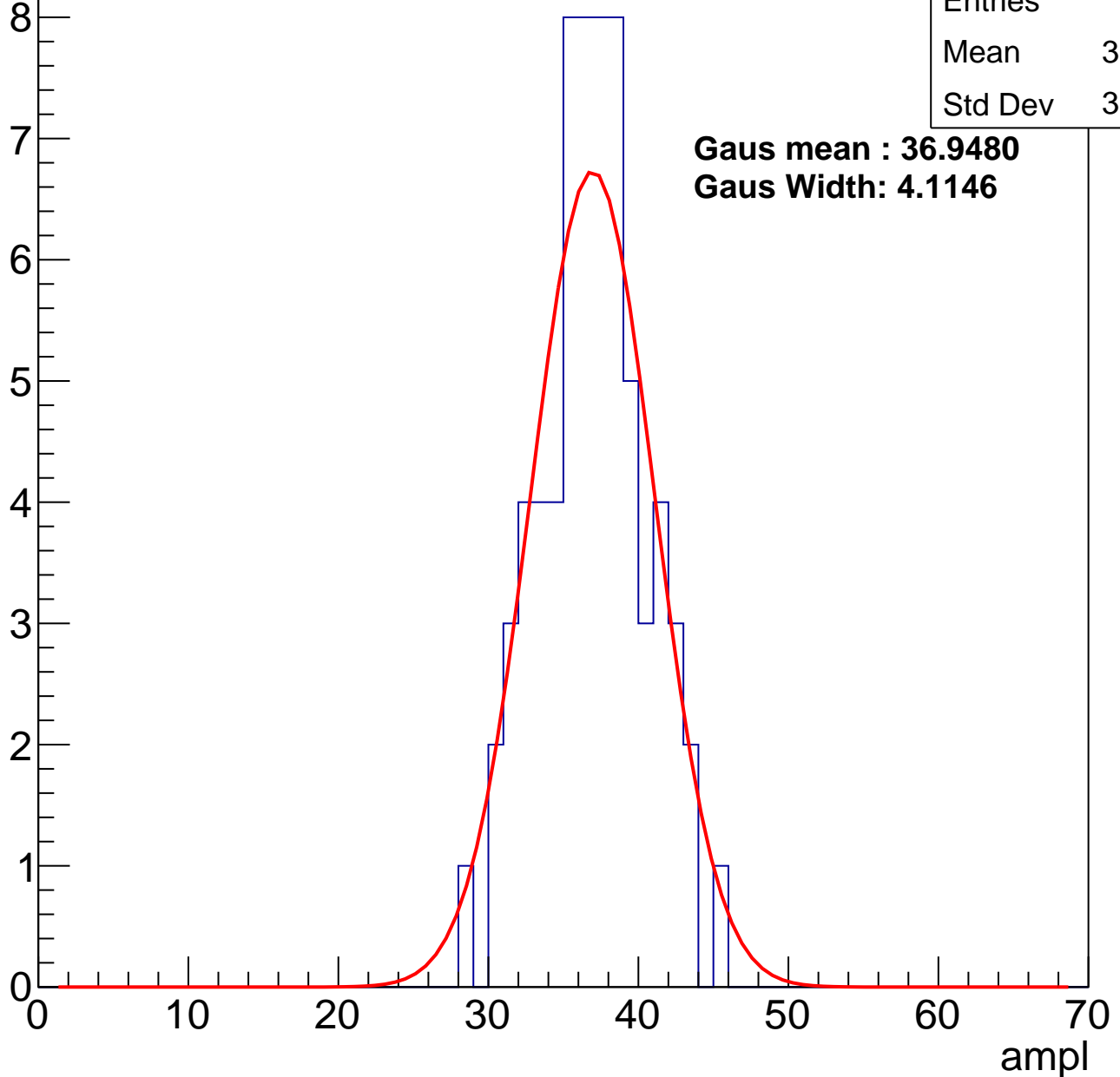
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.49
Std Dev	3.513

Gaus mean : 36.9480

Gaus Width: 4.1146



B0L102S, U12-ch83, adc2

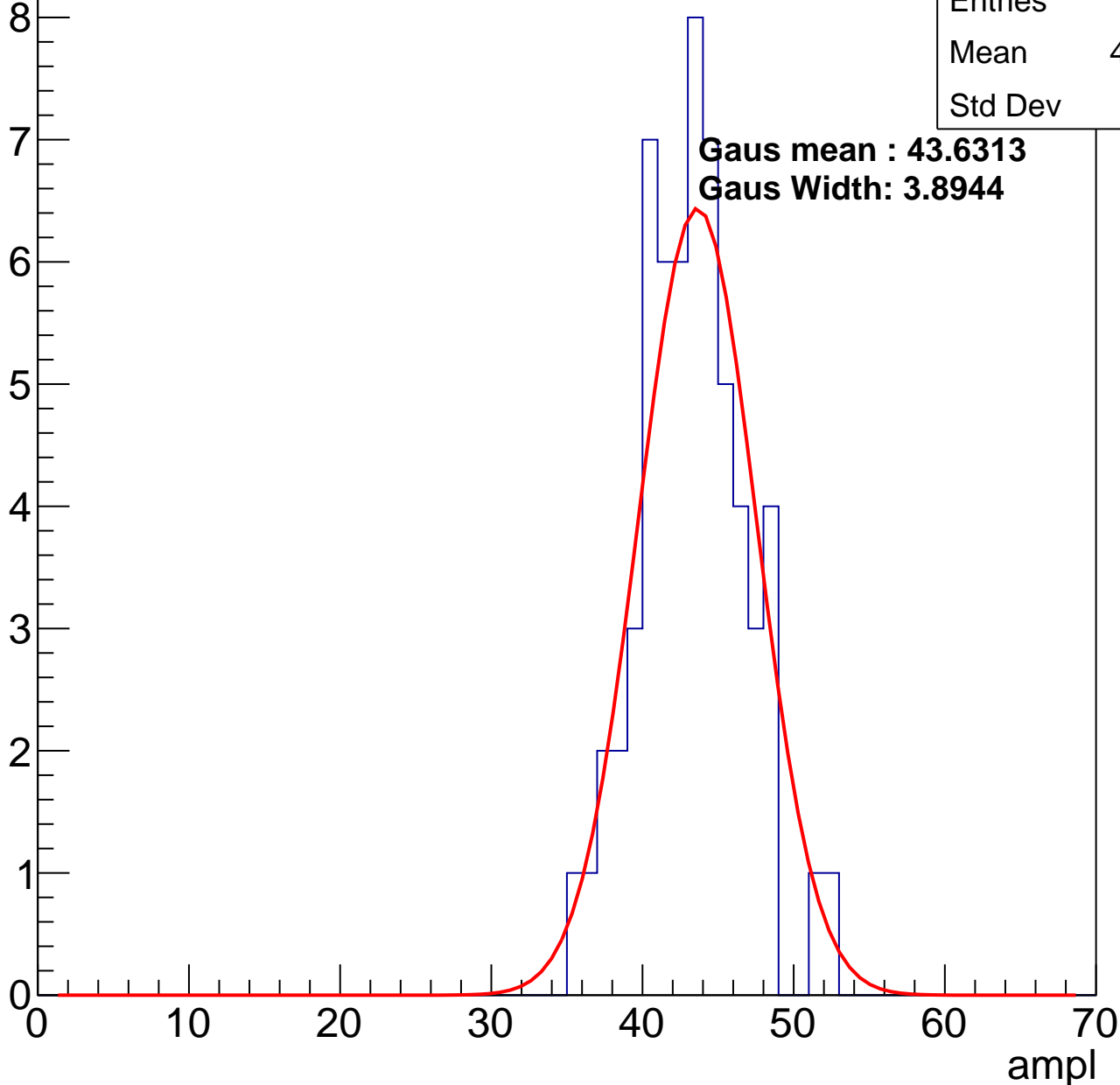
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	42.84
Std Dev	3.46

Gaus mean : 43.6313

Gaus Width: 3.8944

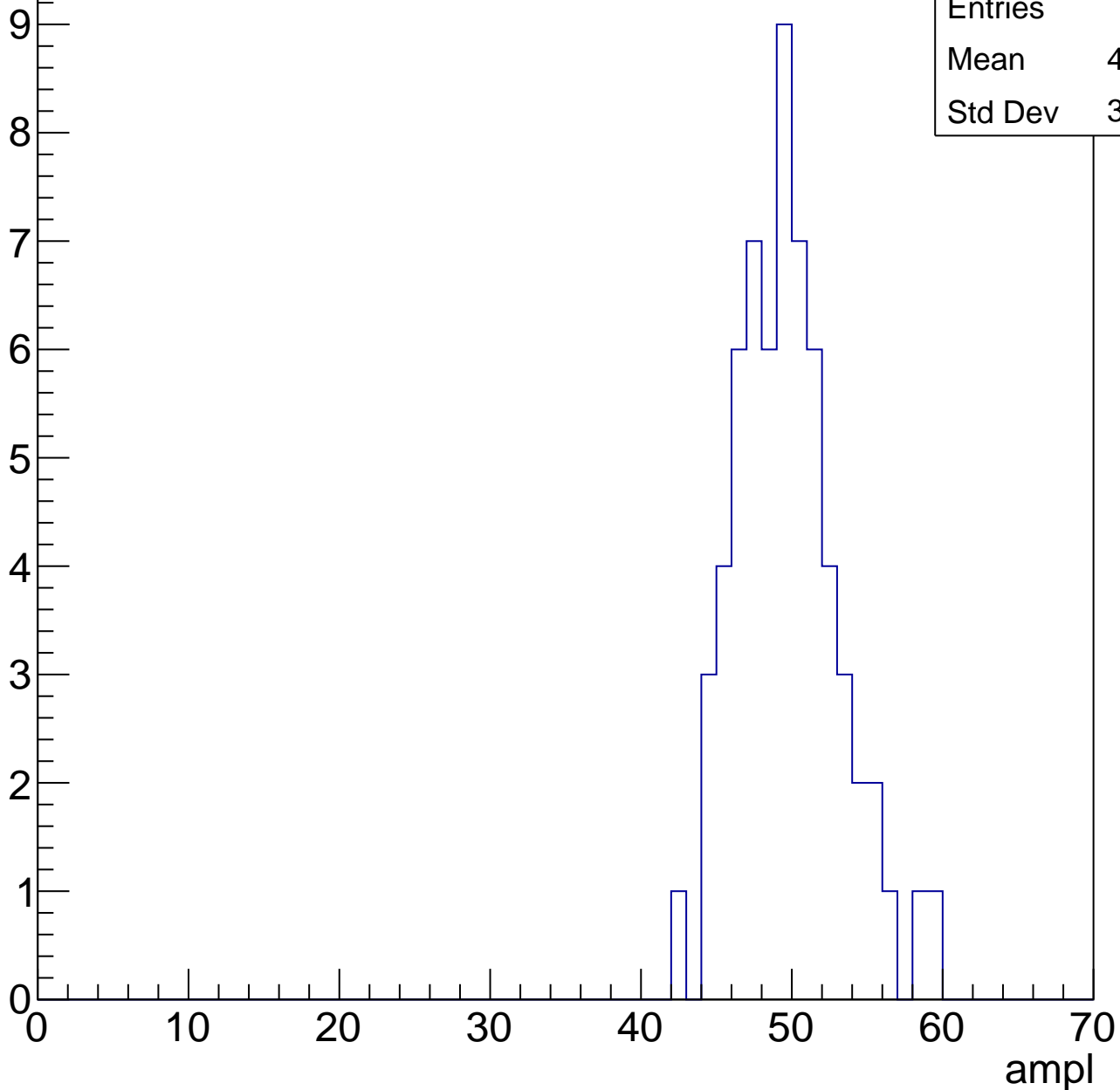


B0L102S, U12-ch83, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.24
Std Dev	3.426

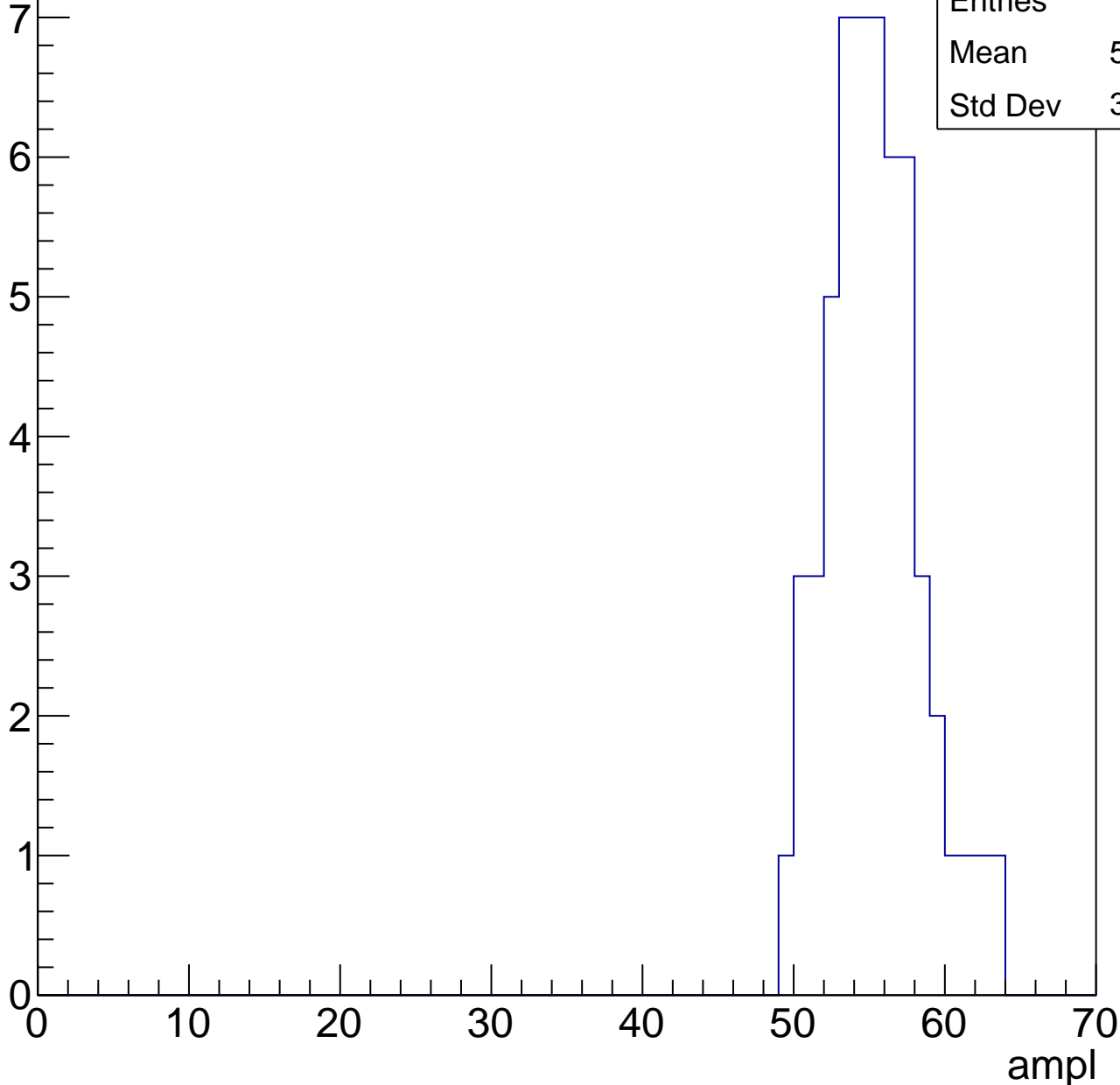


B0L102S, U12-ch83, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	54.85
Std Dev	3.045



B0L102S, U12-ch83, adc5

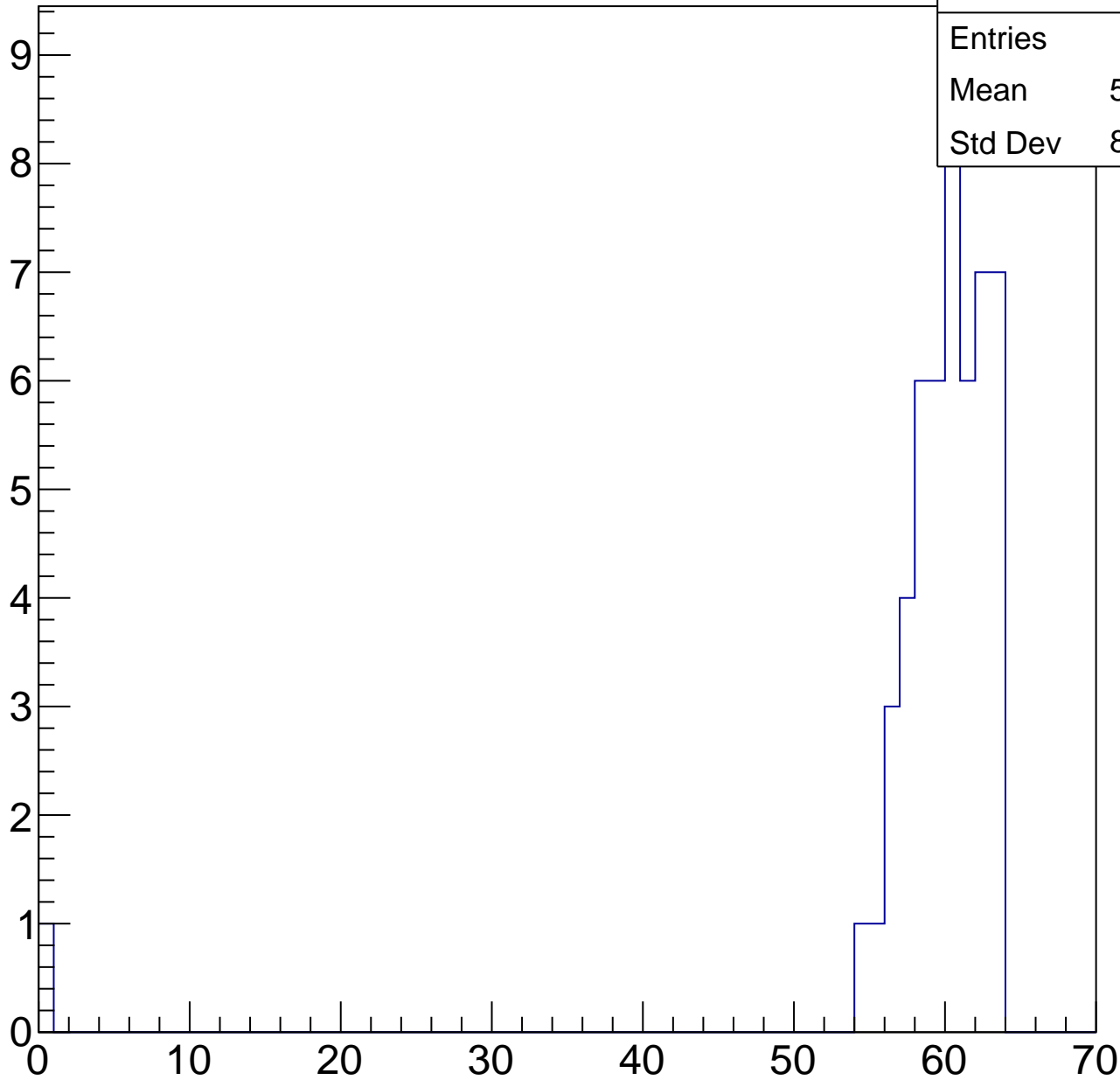
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.59
Std Dev	8.598

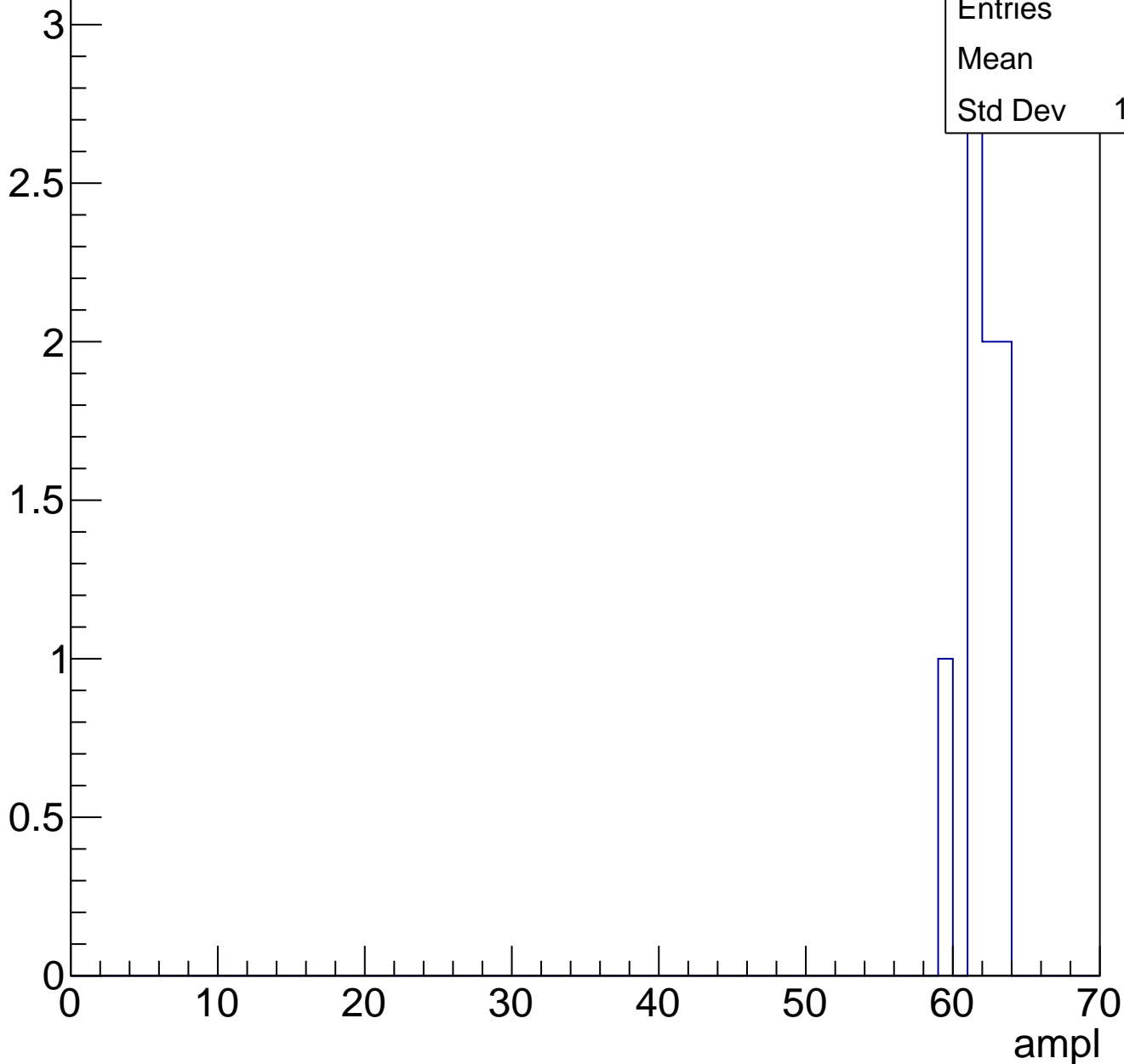
ampl



B0L102S, U12-ch83, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch83, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch84, adc0

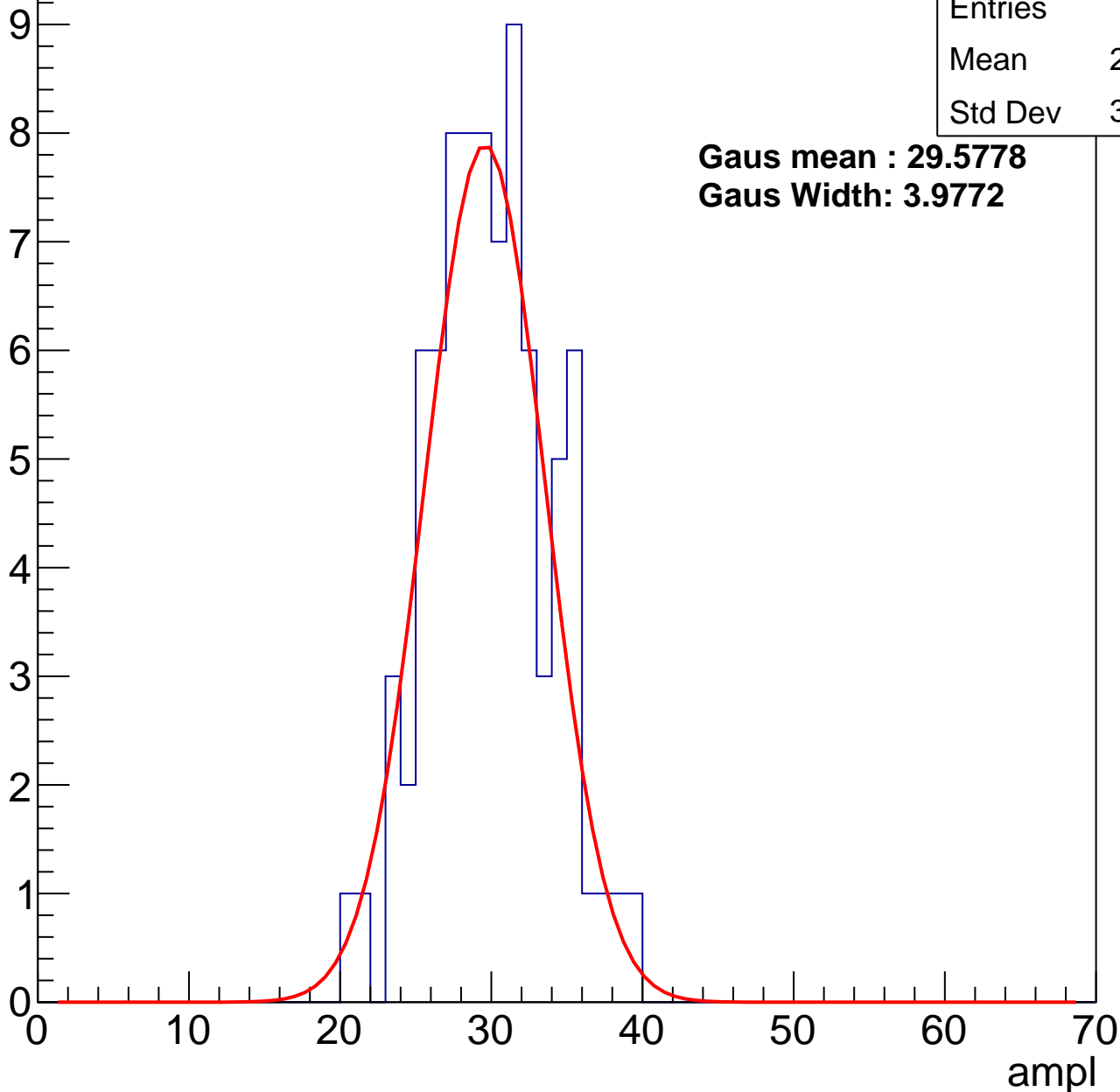
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	29.47
Std Dev	3.888

Gaus mean : 29.5778

Gaus Width: 3.9772



B0L102S, U12-ch84, adc1

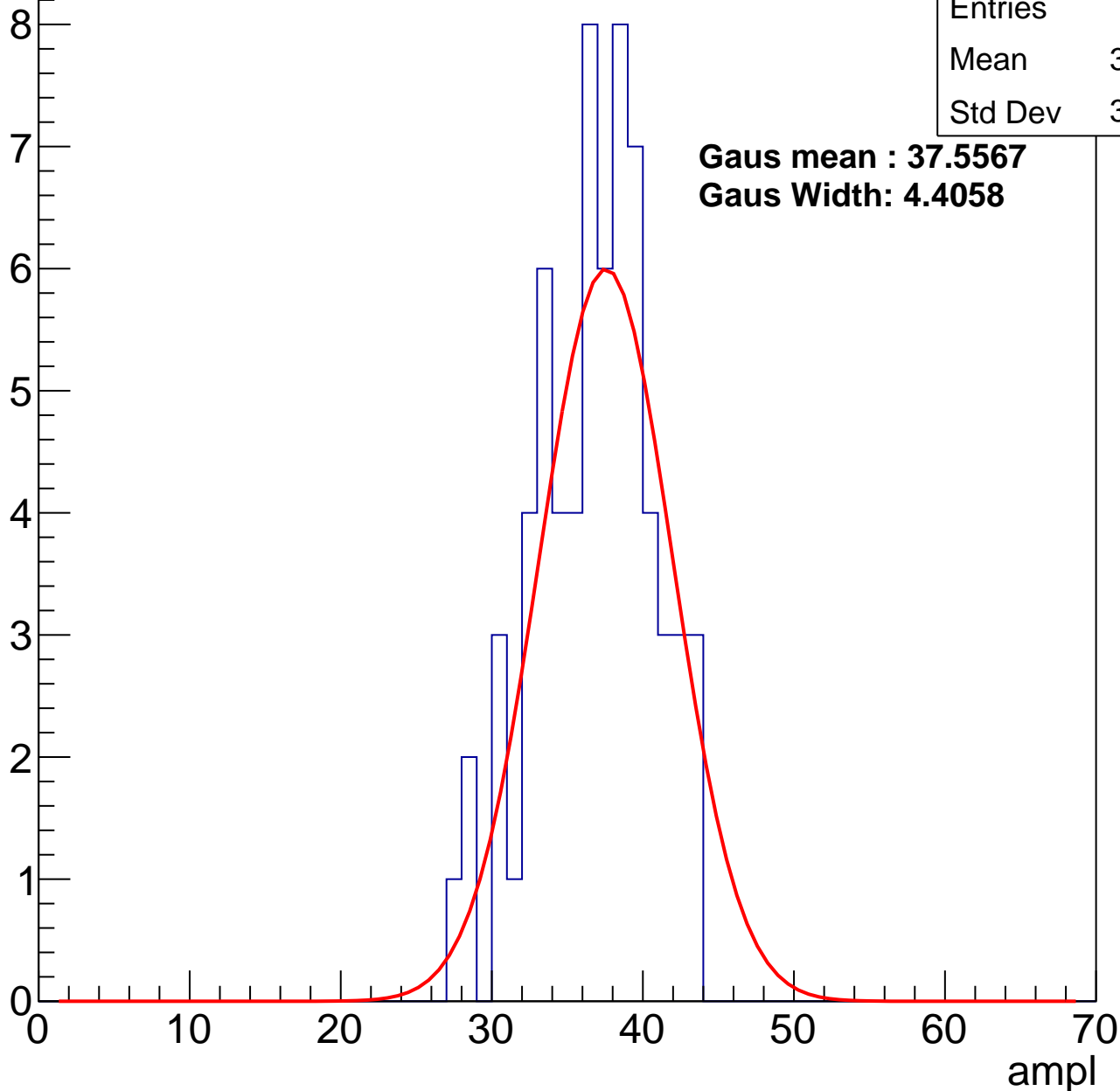
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.28
Std Dev	3.804

Gaus mean : 37.5567

Gaus Width: 4.4058



B0L102S, U12-ch84, adc2

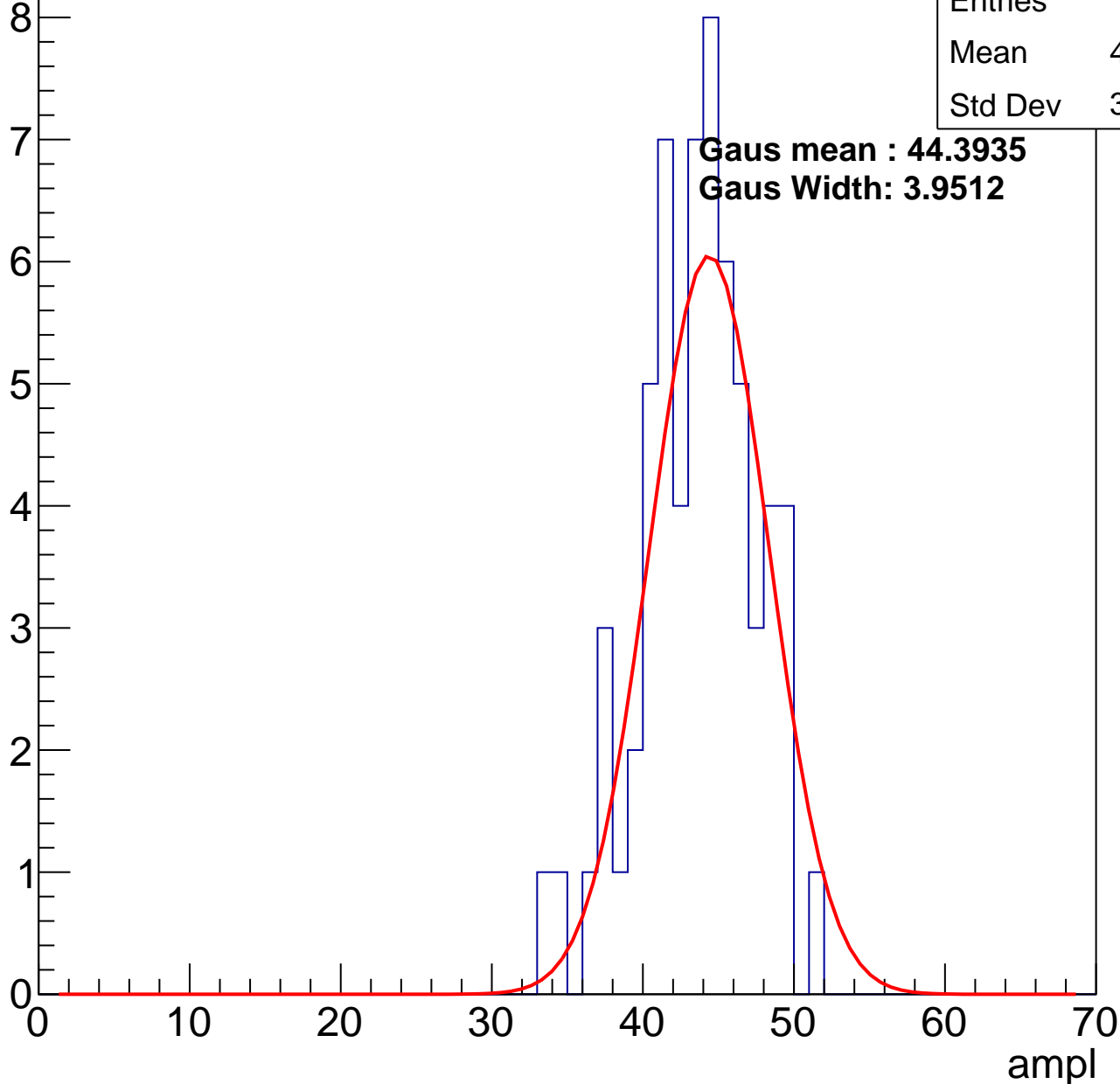
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.14
Std Dev	3.787

Gaus mean : 44.3935

Gaus Width: 3.9512

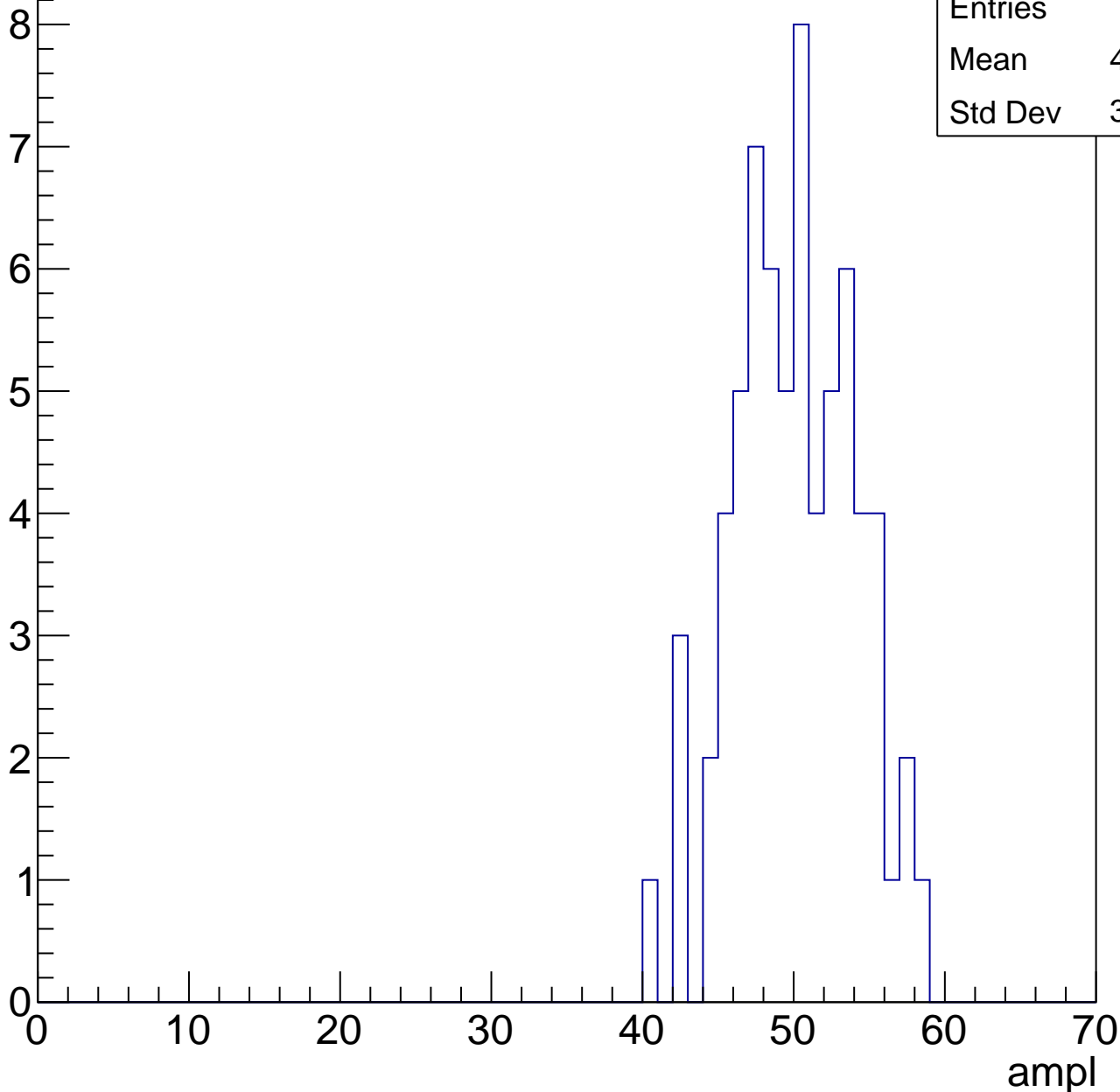


B0L102S, U12-ch84, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.59
Std Dev	3.957

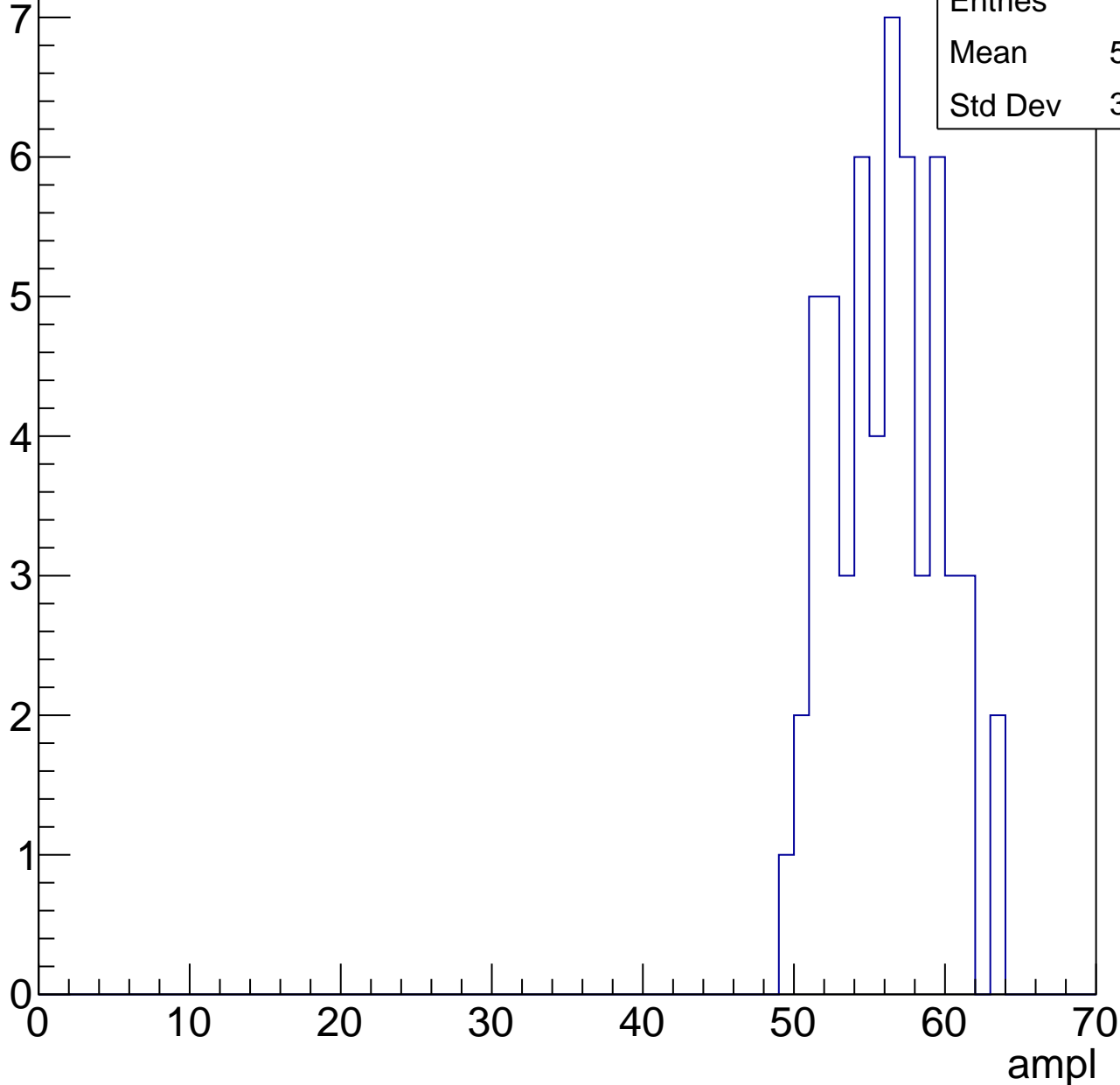


B0L102S, U12-ch84, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	55.68
Std Dev	3.454

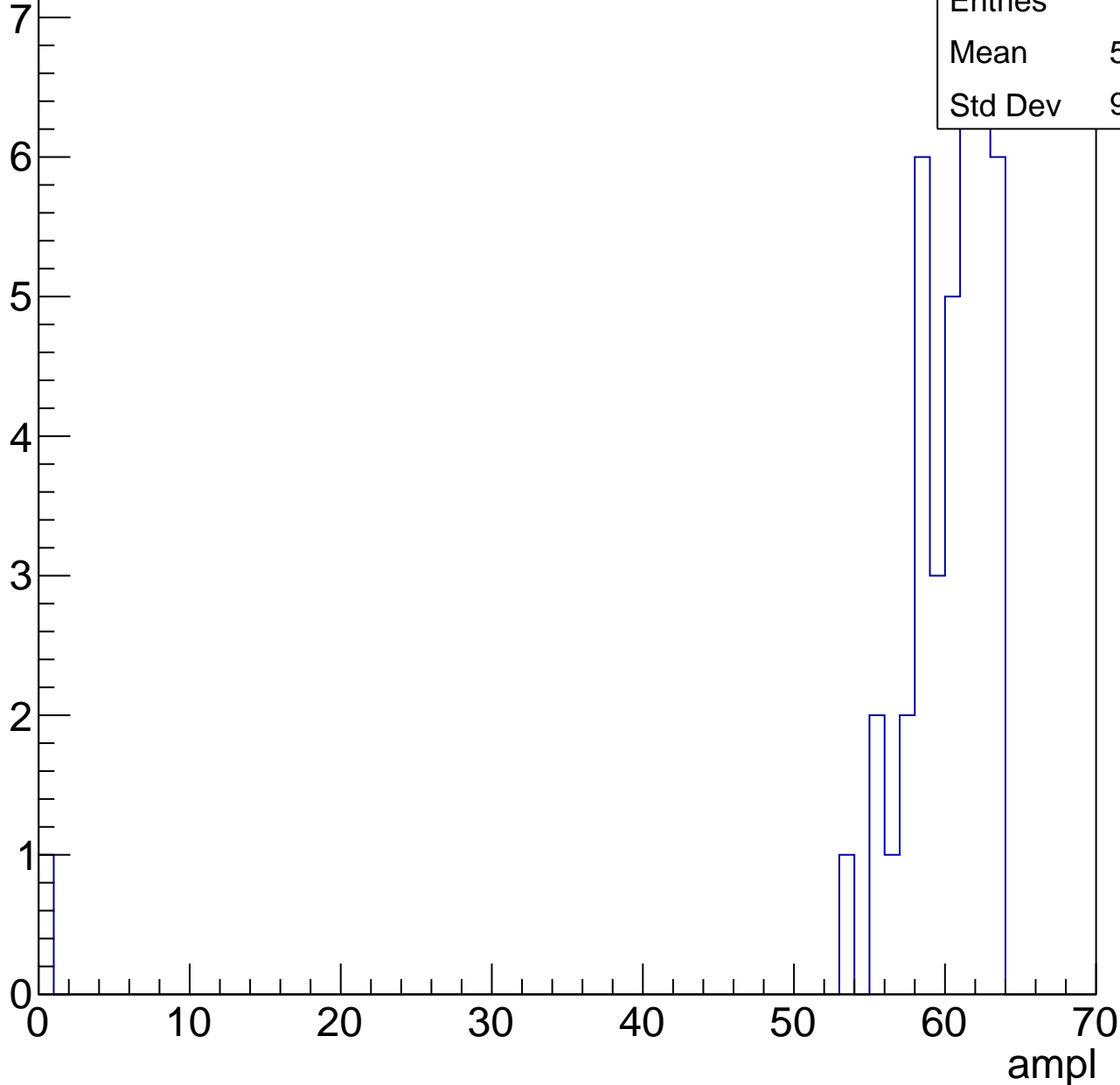


B0L102S, U12-ch84, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

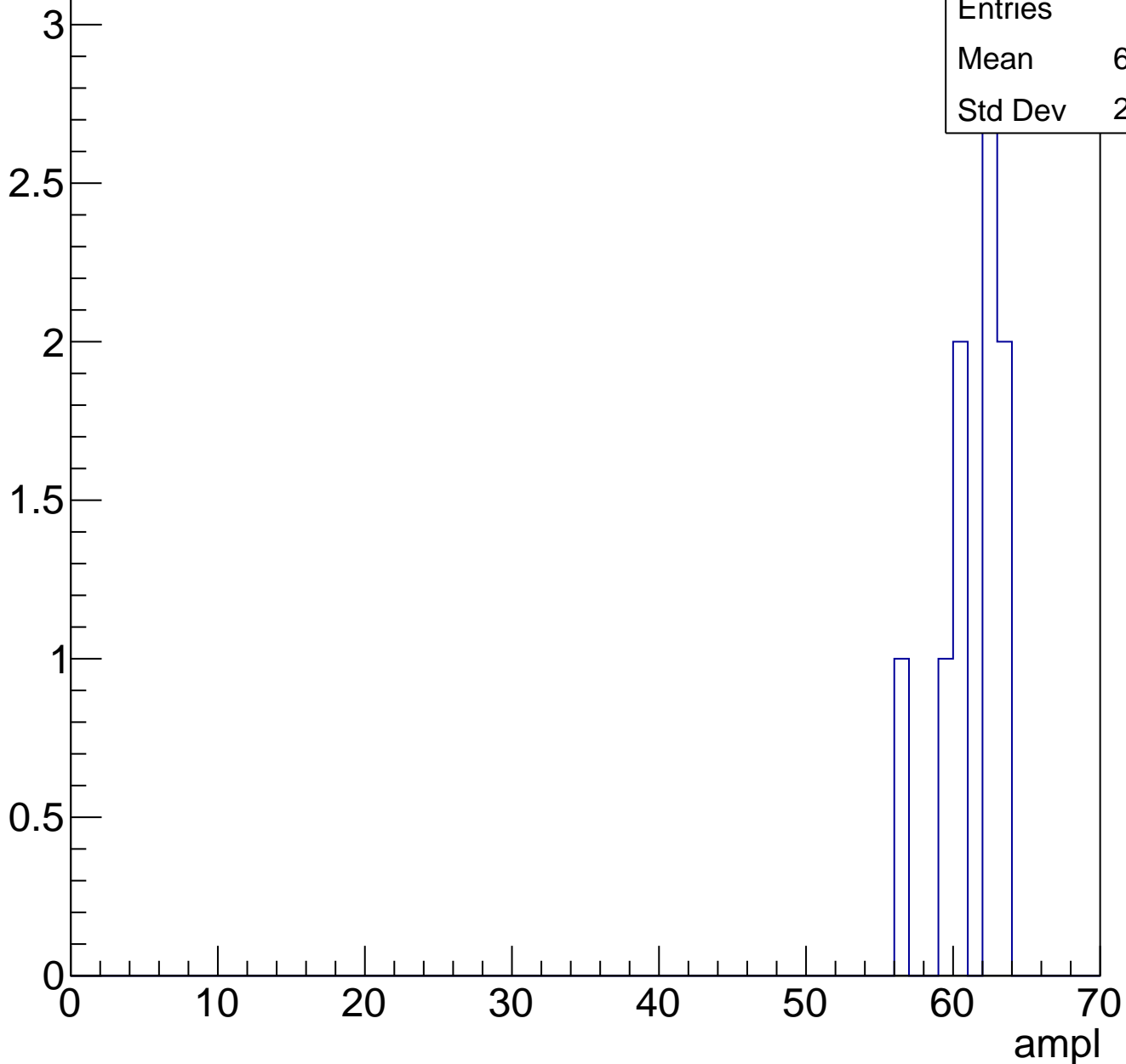
Entries	41
Mean	58.46
Std Dev	9.566



B0L102S, U12-ch84, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch84, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch85, adc0

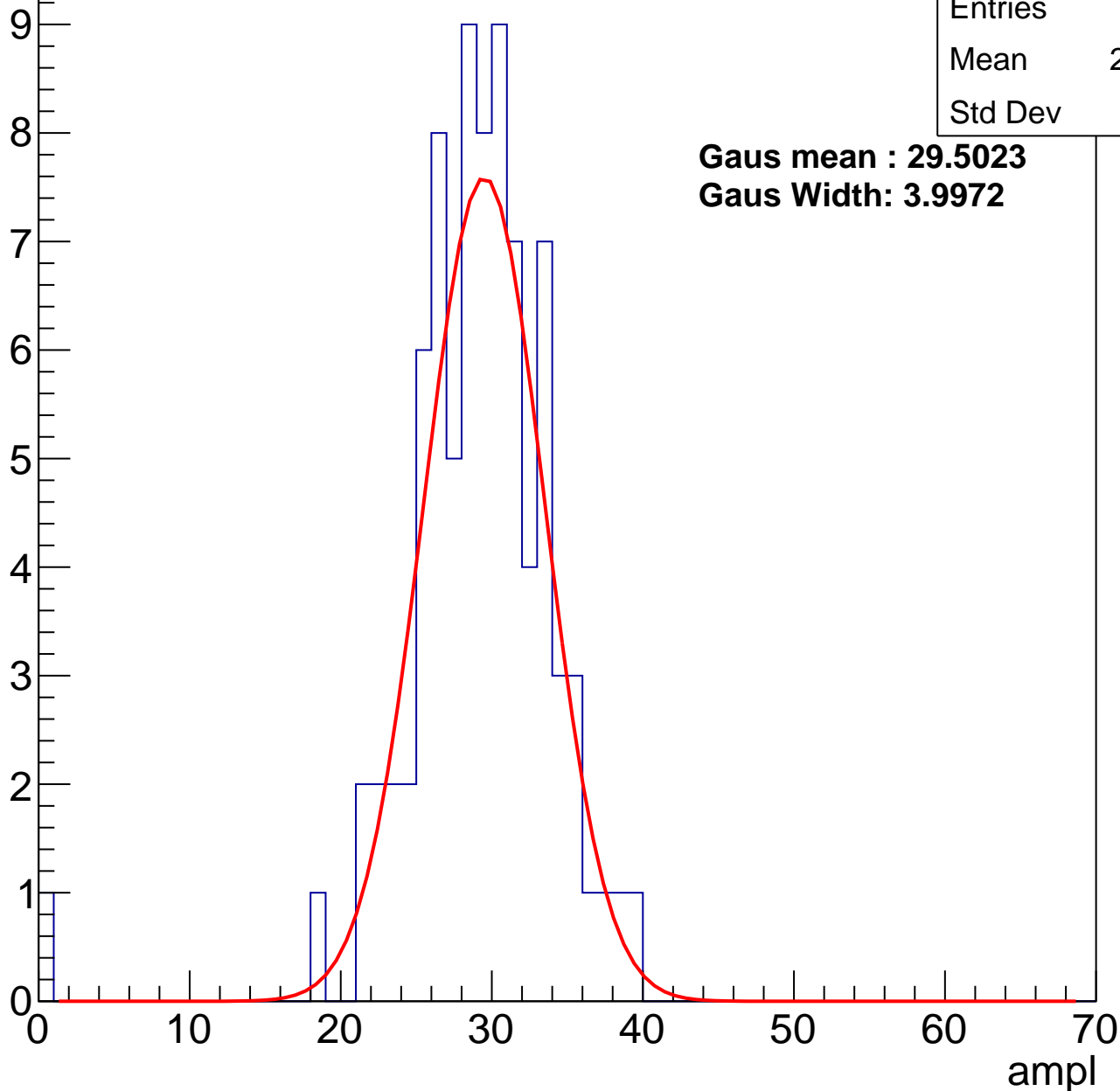
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	28.65
Std Dev	5.1

Gaus mean : 29.5023

Gaus Width: 3.9972



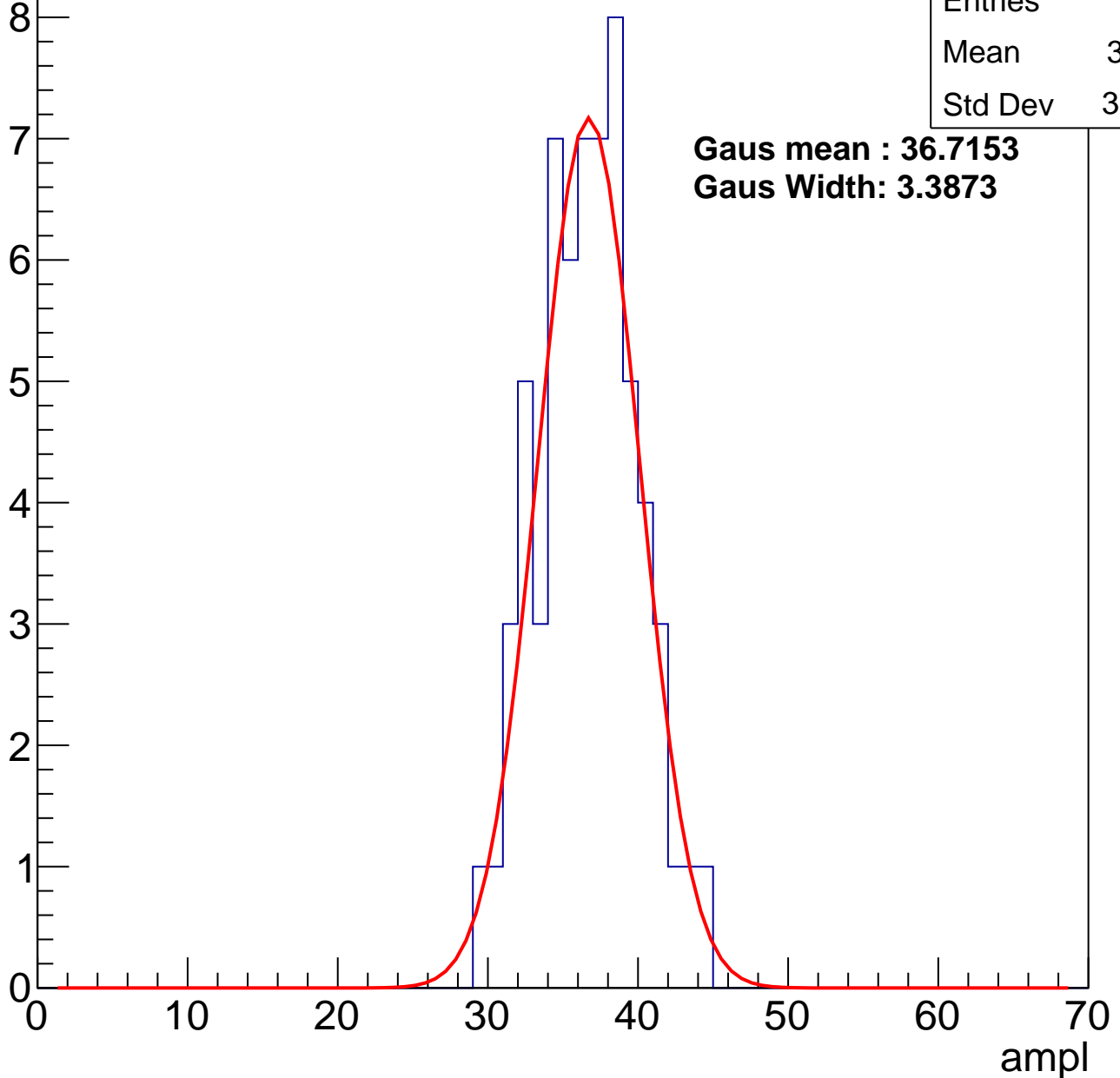
B0L102S, U12-ch85, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.21
Std Dev	3.262

Gaus mean : 36.7153
Gaus Width: 3.3873



B0L102S, U12-ch85, adc2

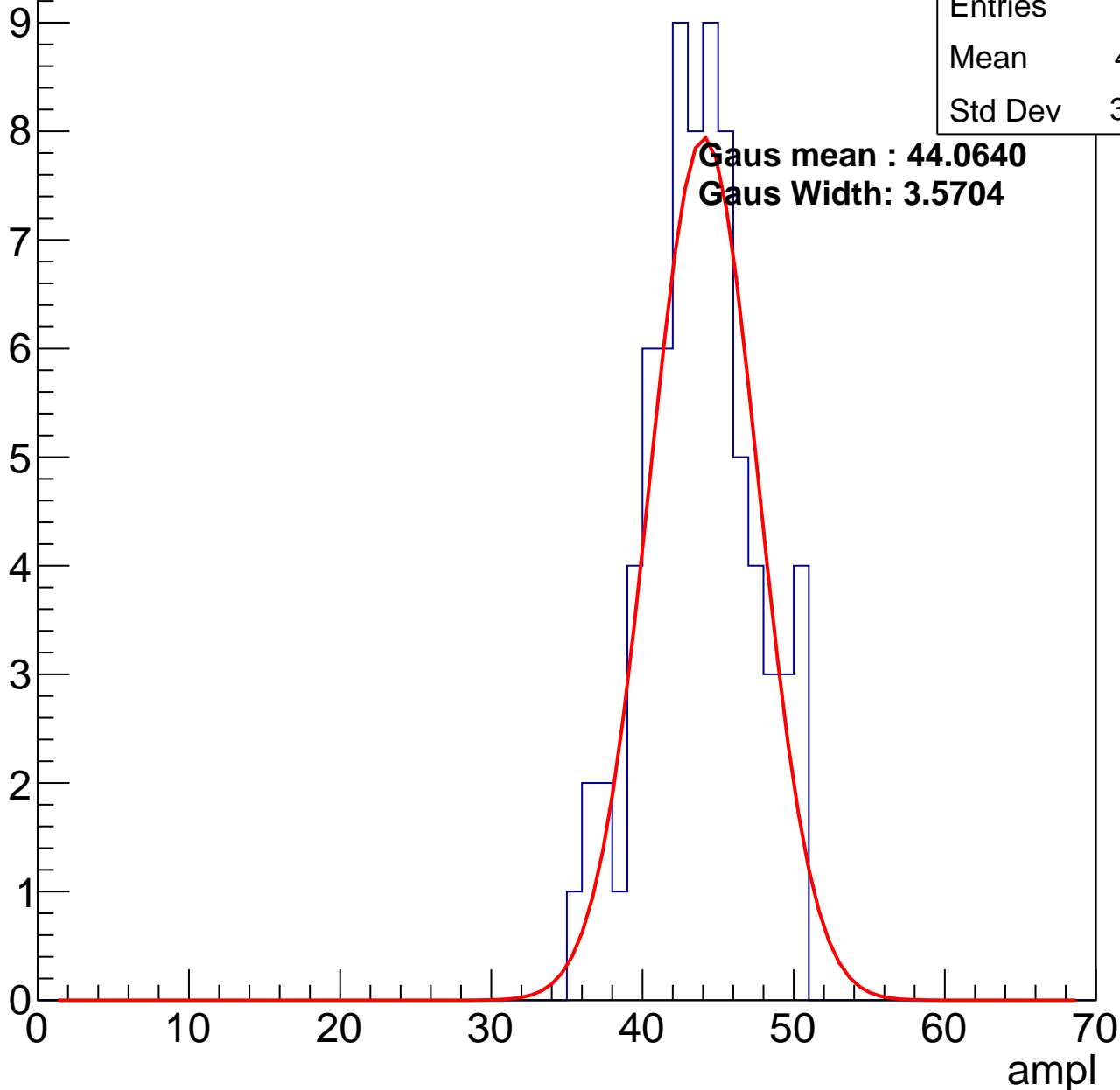
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	43.31
Std Dev	3.525

Gaus mean : 44.0640

Gaus Width: 3.5704

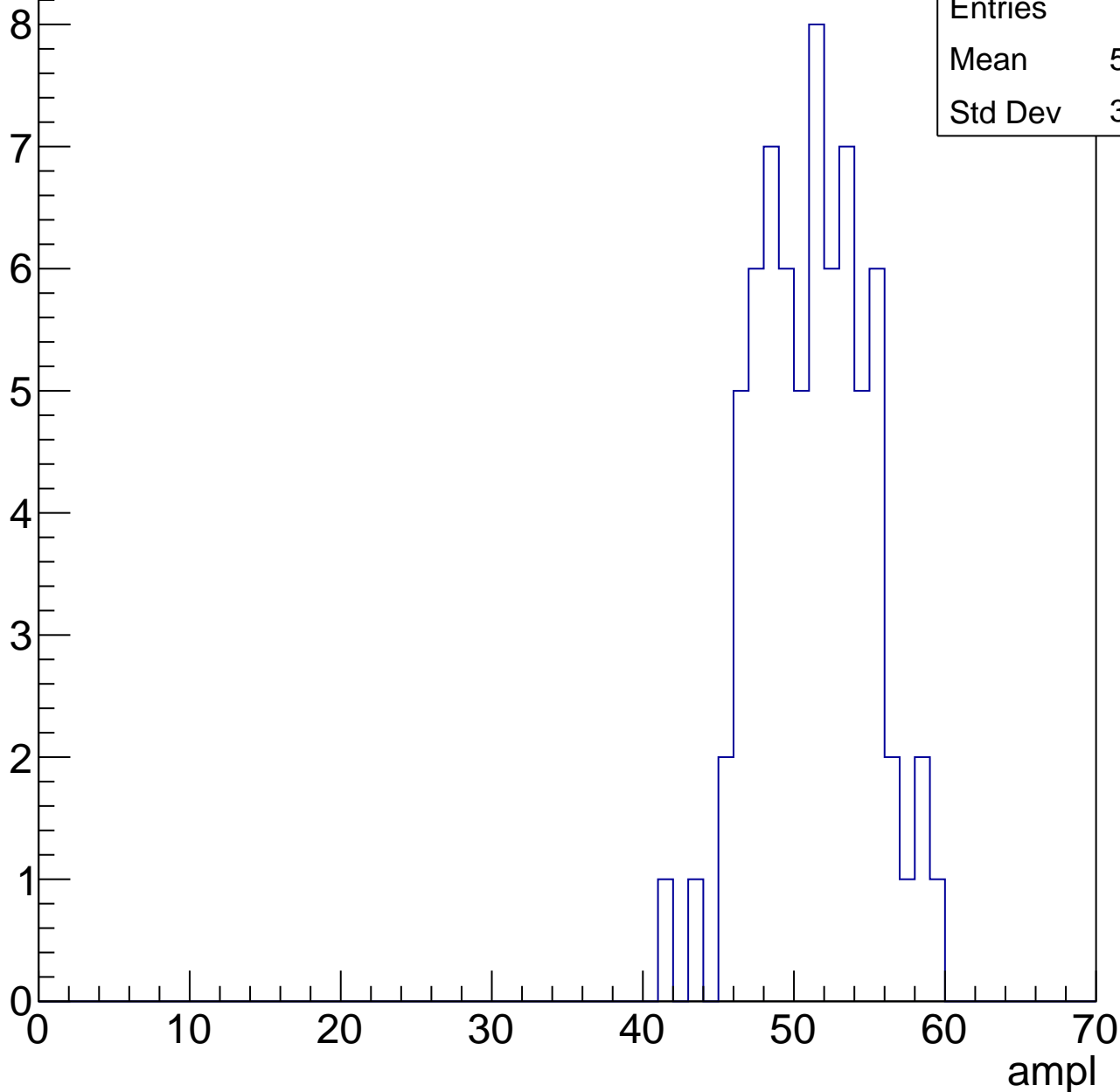


B0L102S, U12-ch85, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	50.72
Std Dev	3.689

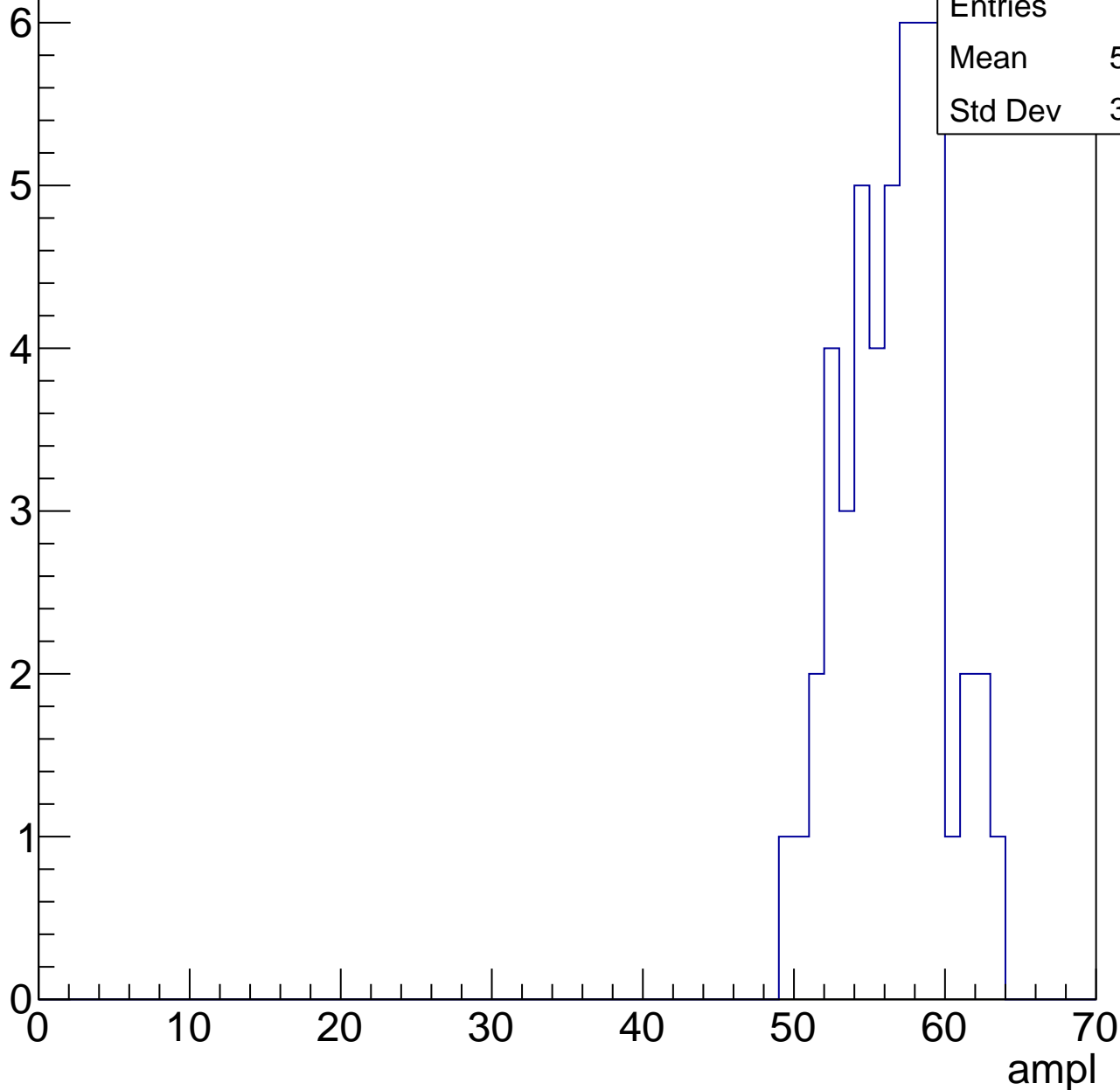


B0L102S, U12-ch85, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	56.14
Std Dev	3.258

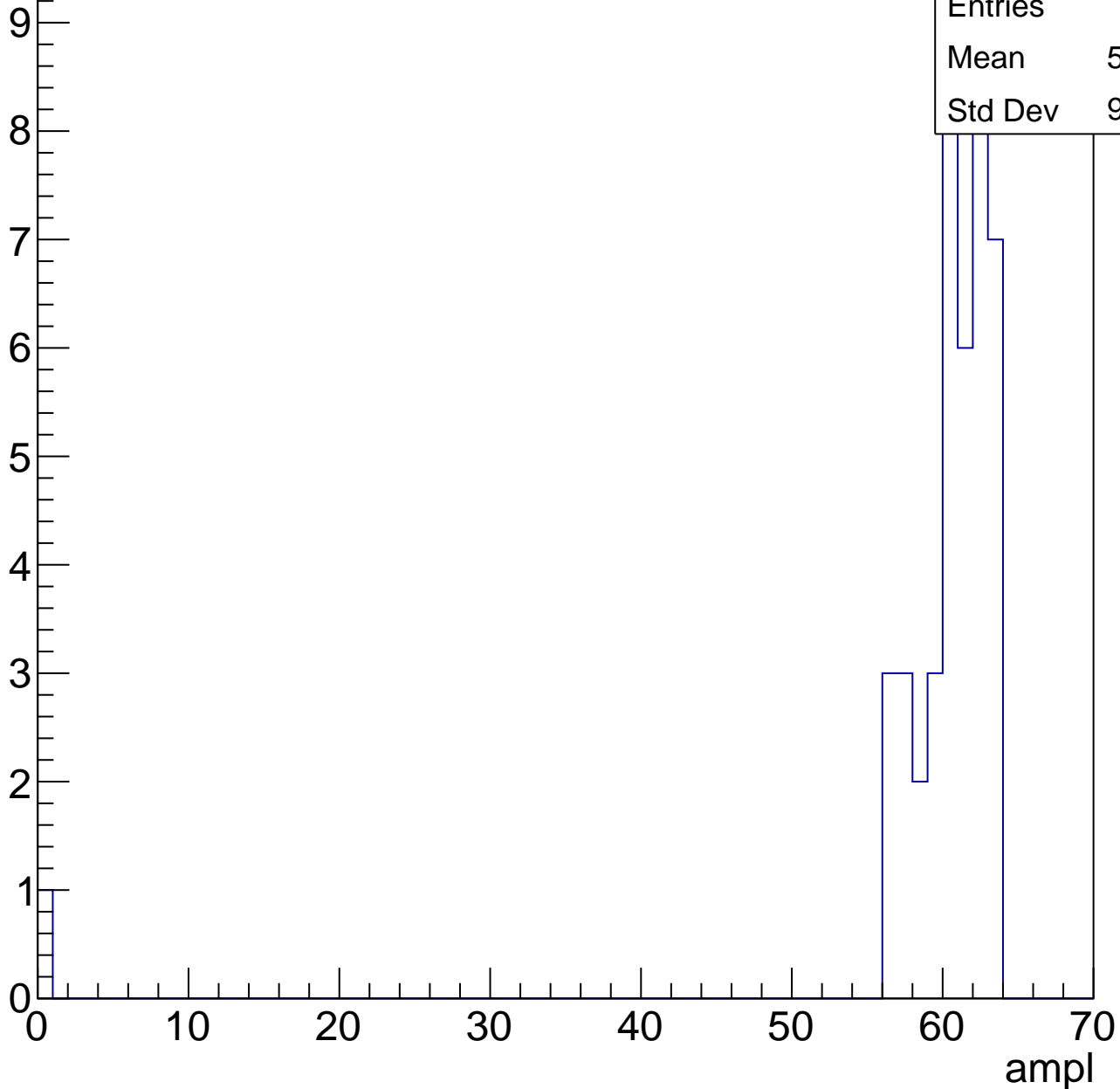


B0L102S, U12-ch85, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	58.93
Std Dev	9.435



B0L102S, U12-ch85, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch85, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch86, adc0

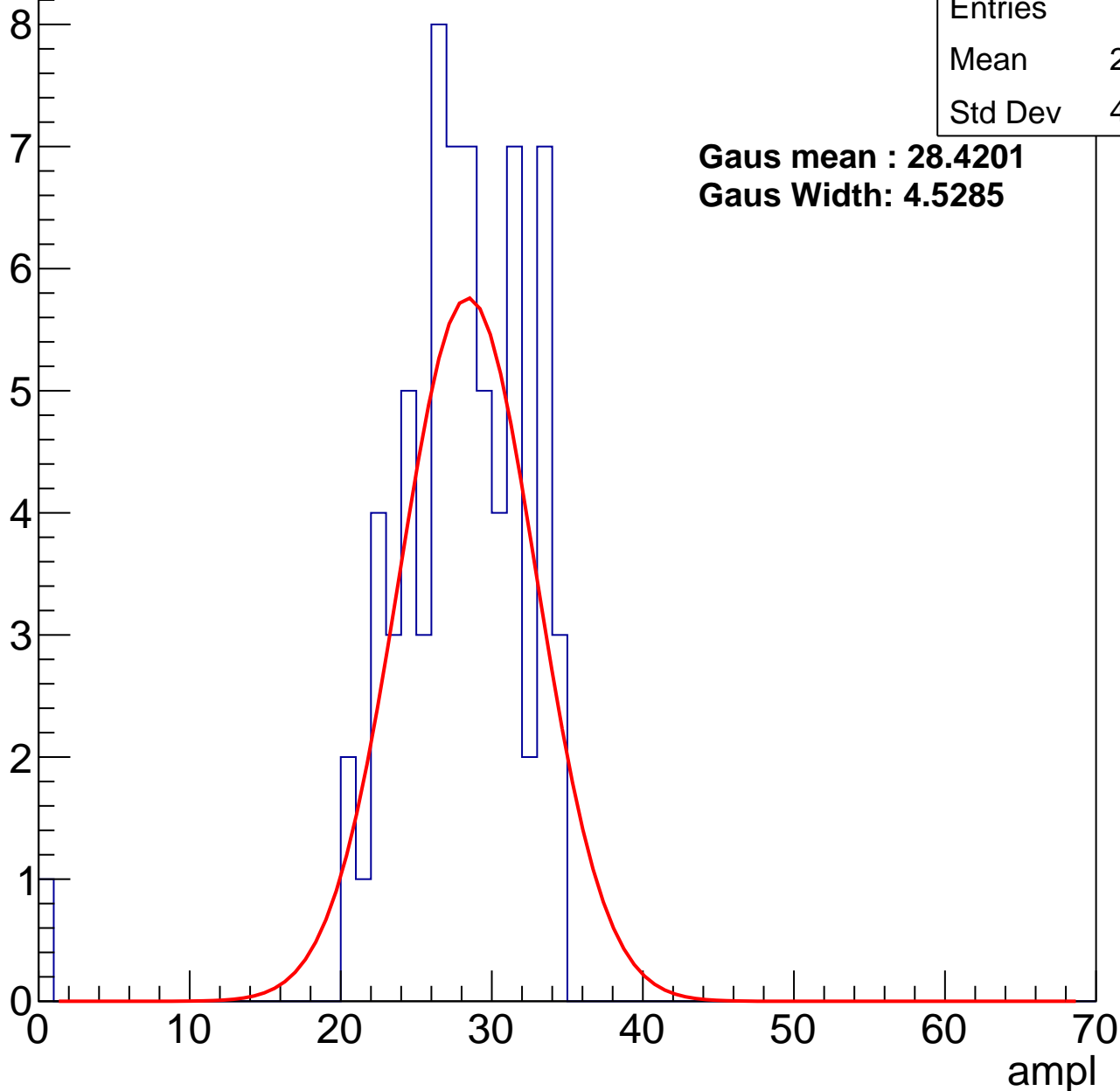
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	27.32
Std Dev	4.953

Gaus mean : 28.4201

Gaus Width: 4.5285



B0L102S, U12-ch86, adc1

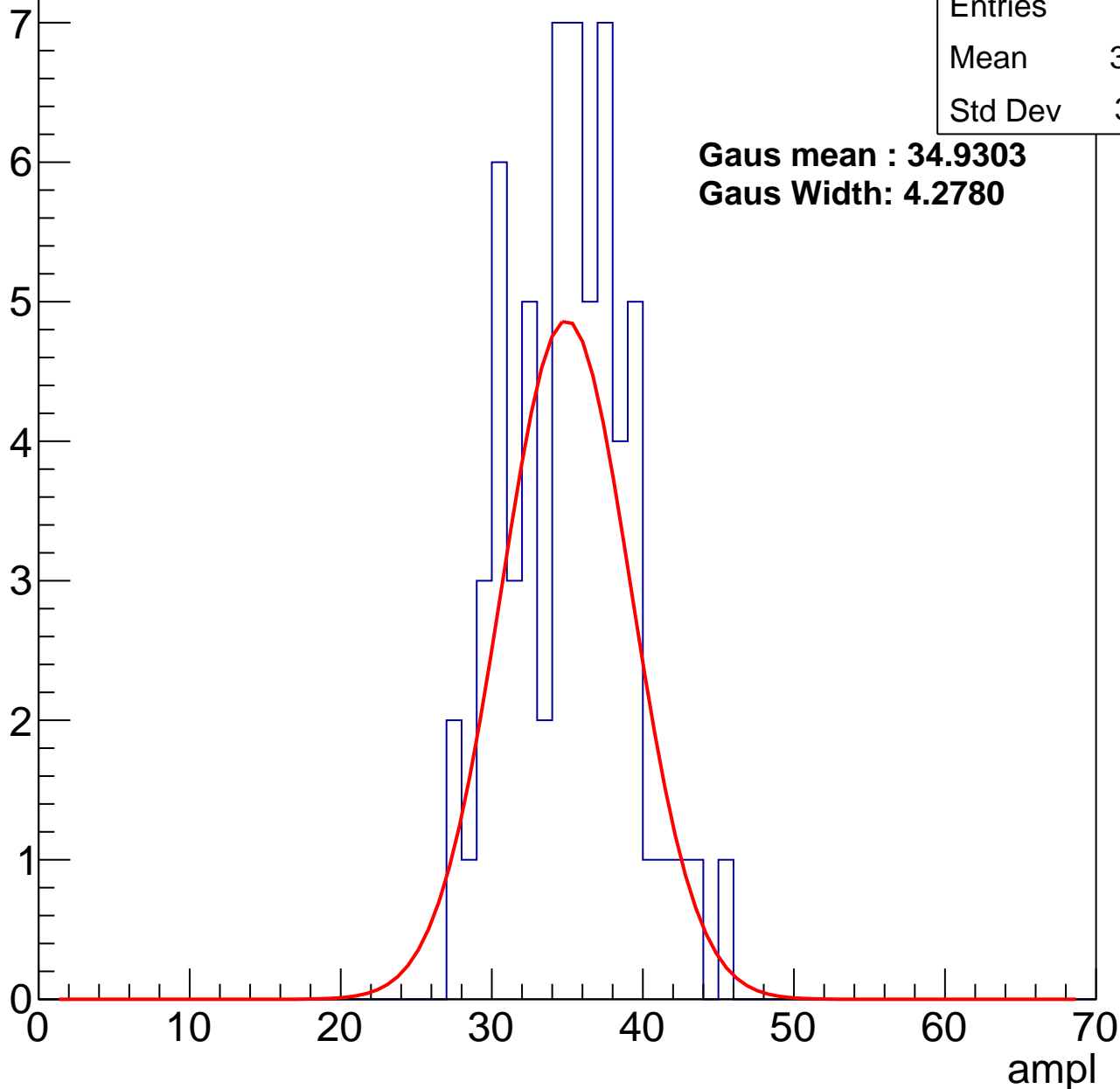
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	34.65
Std Dev	3.931

Gaus mean : 34.9303

Gaus Width: 4.2780



B0L102S, U12-ch86, adc2

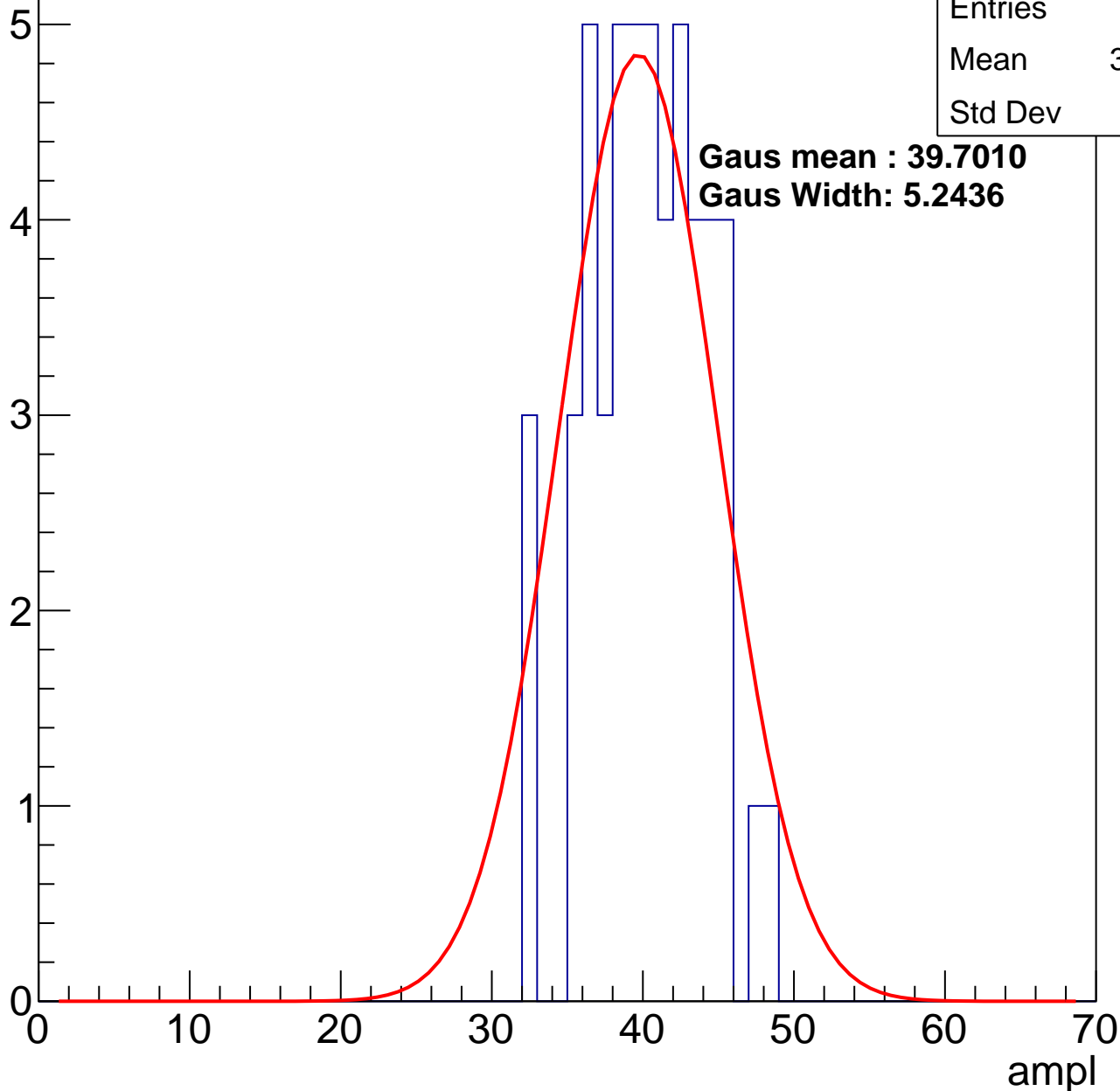
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	39.88
Std Dev	3.76

Gaus mean : 39.7010

Gaus Width: 5.2436

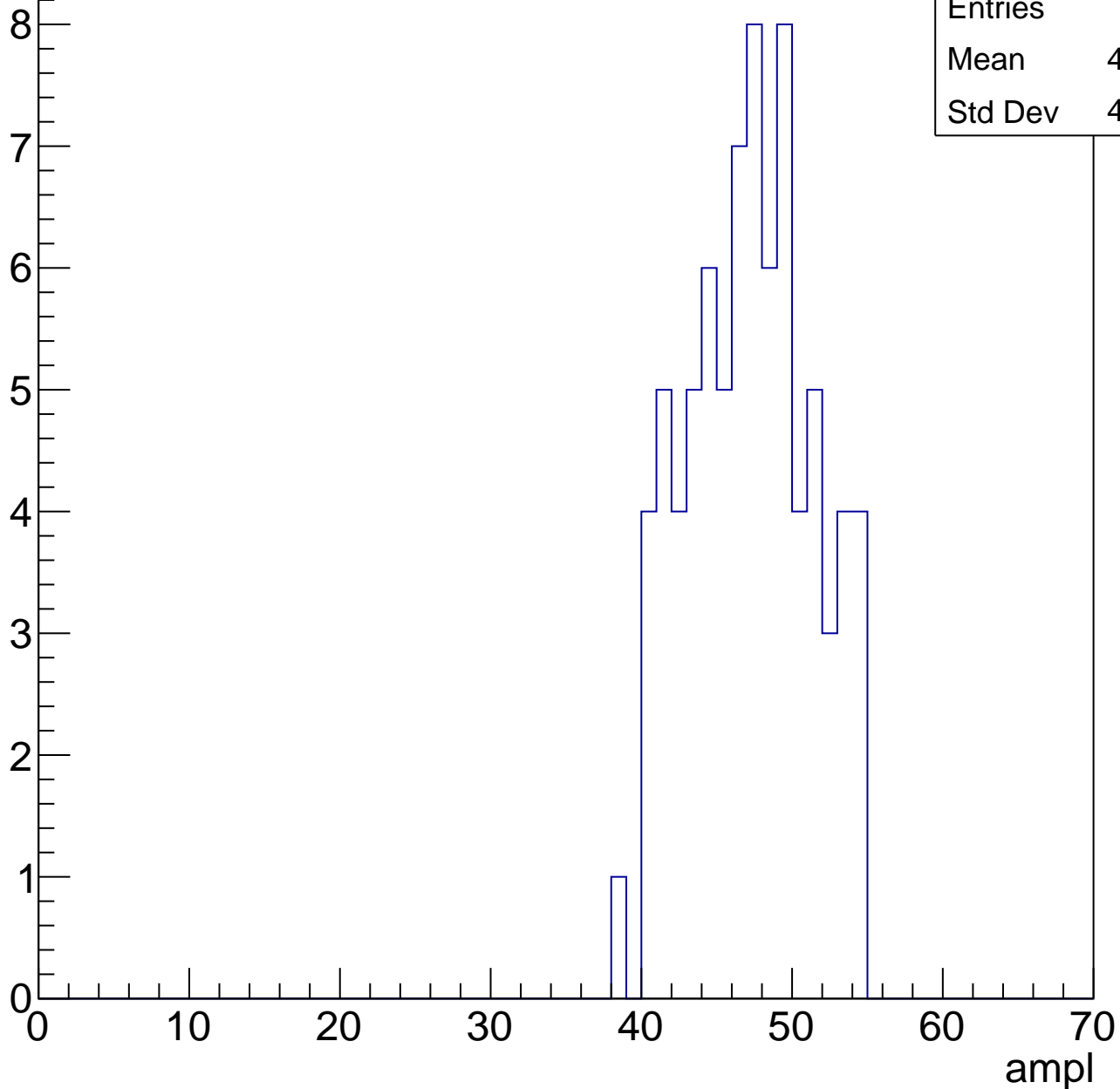


B0L102S, U12-ch86, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	46.73
Std Dev	4.027

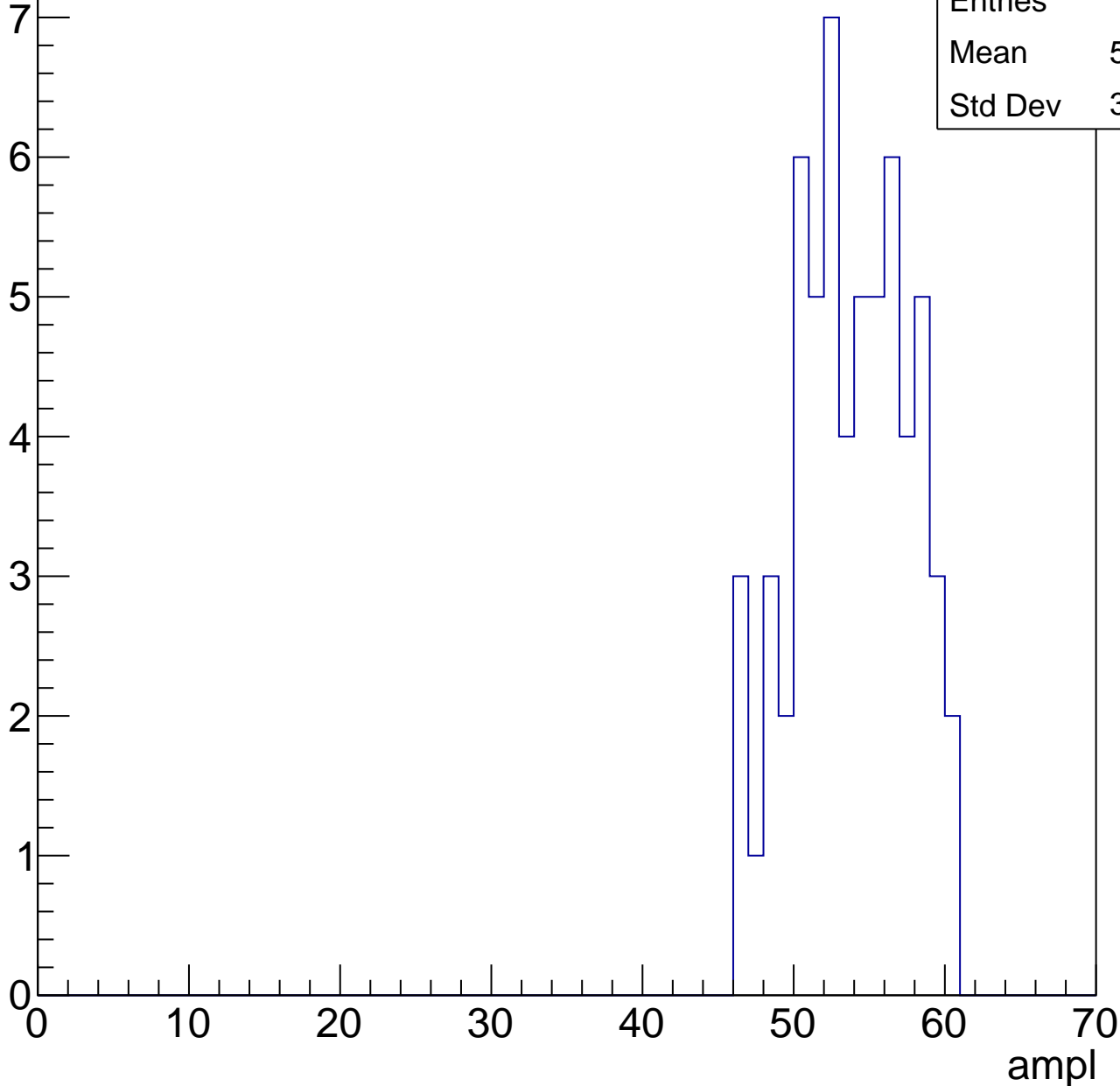


B0L102S, U12-ch86, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	53.34
Std Dev	3.706



B0L102S, U12-ch86, adc5

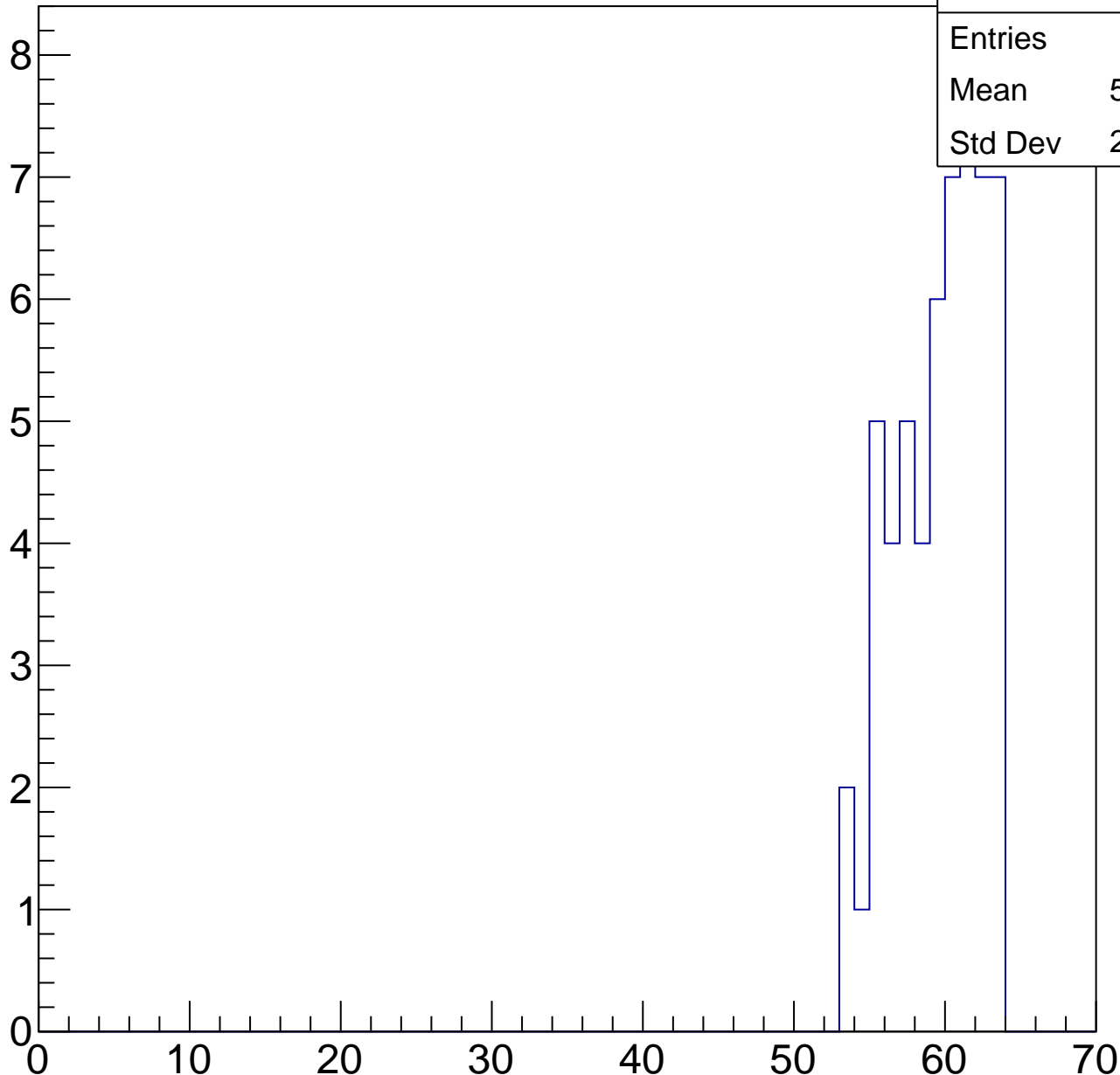
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.16
Std Dev	2.833

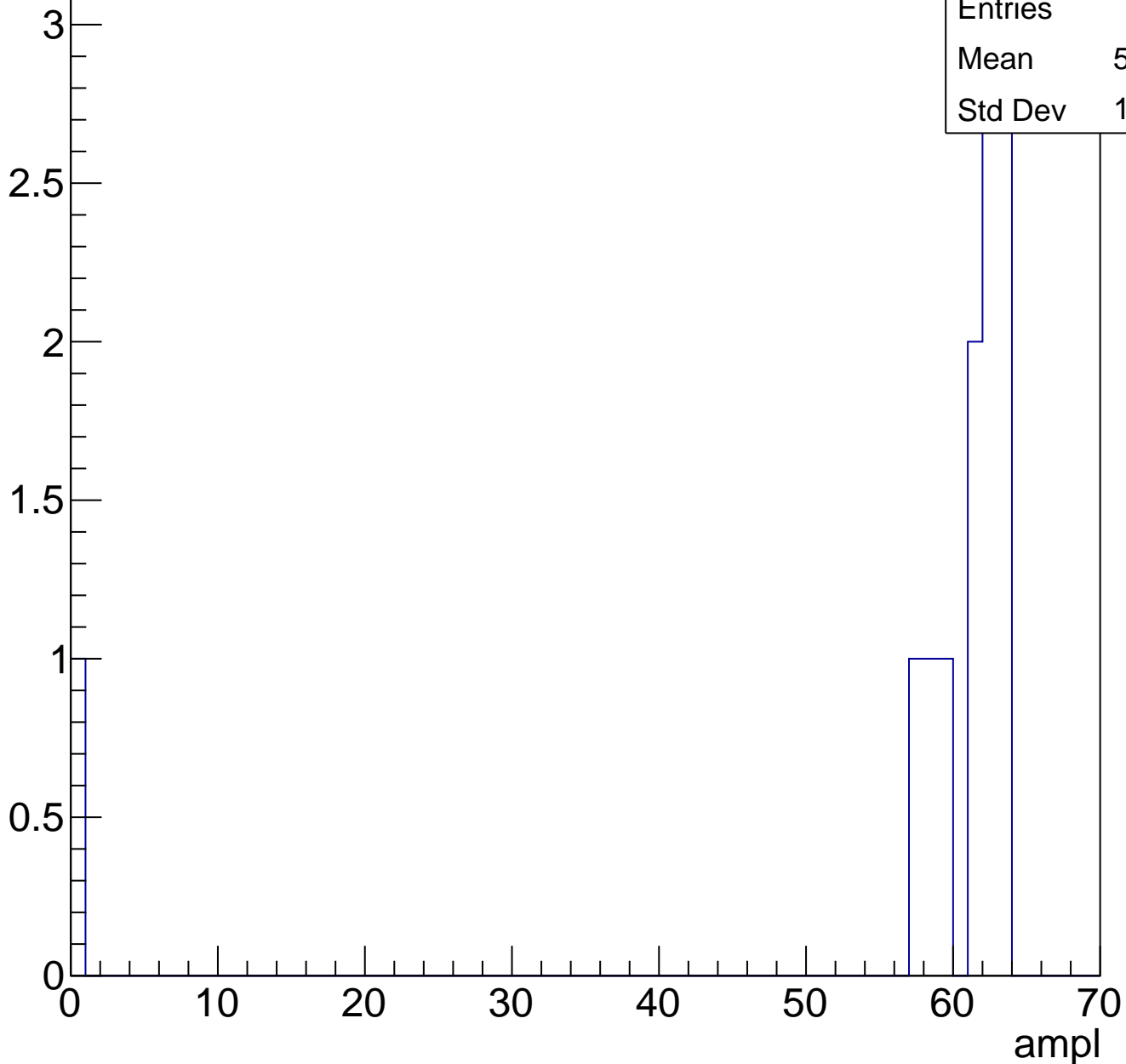
ampl



B0L102S, U12-ch86, adc6

calib_packv5_042523_0143.root, FC#12, port B1

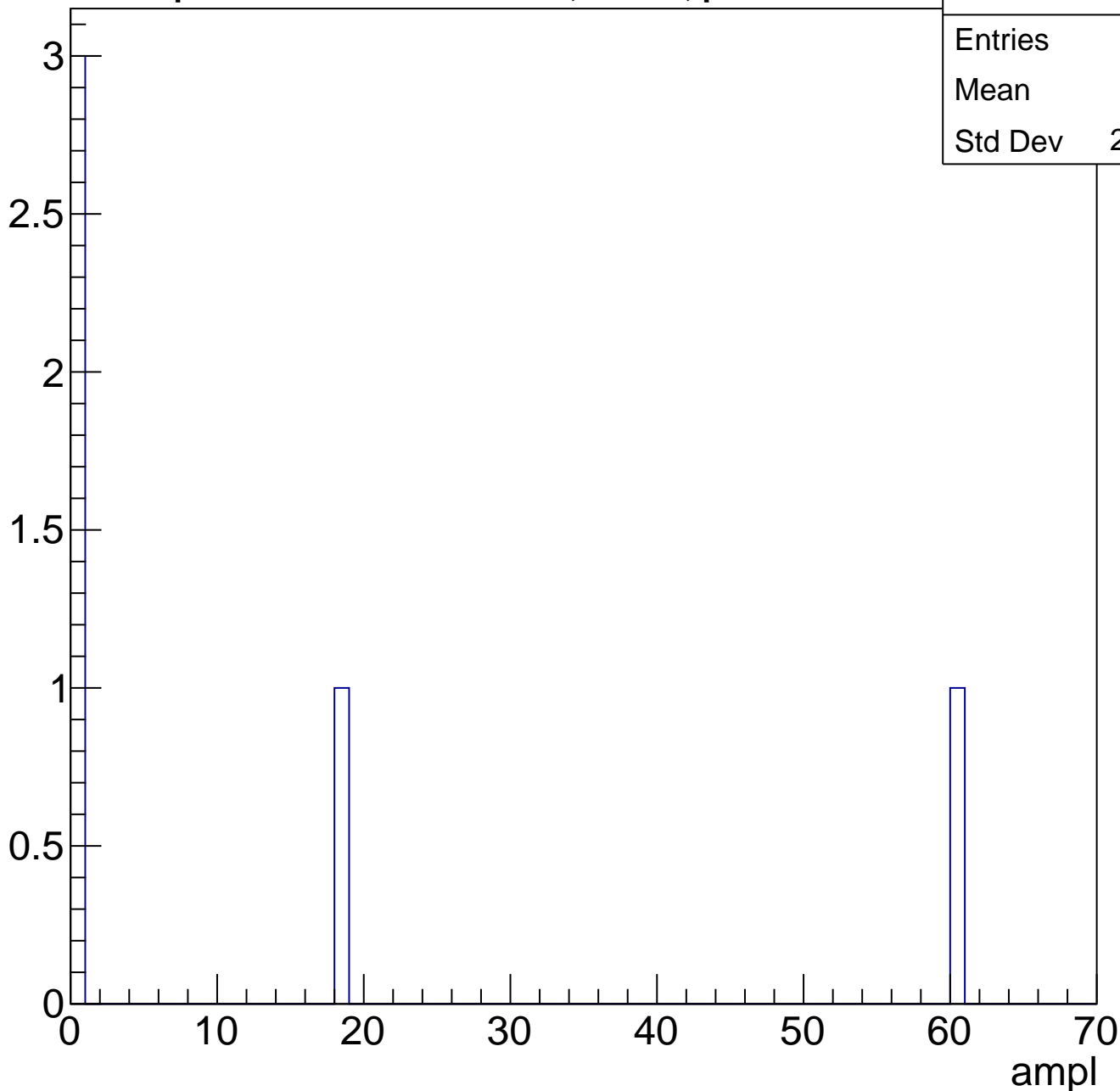
Entry



B0L102S, U12-ch86, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	15.6
Std Dev	23.27

B0L102S, U12-ch87, adc0

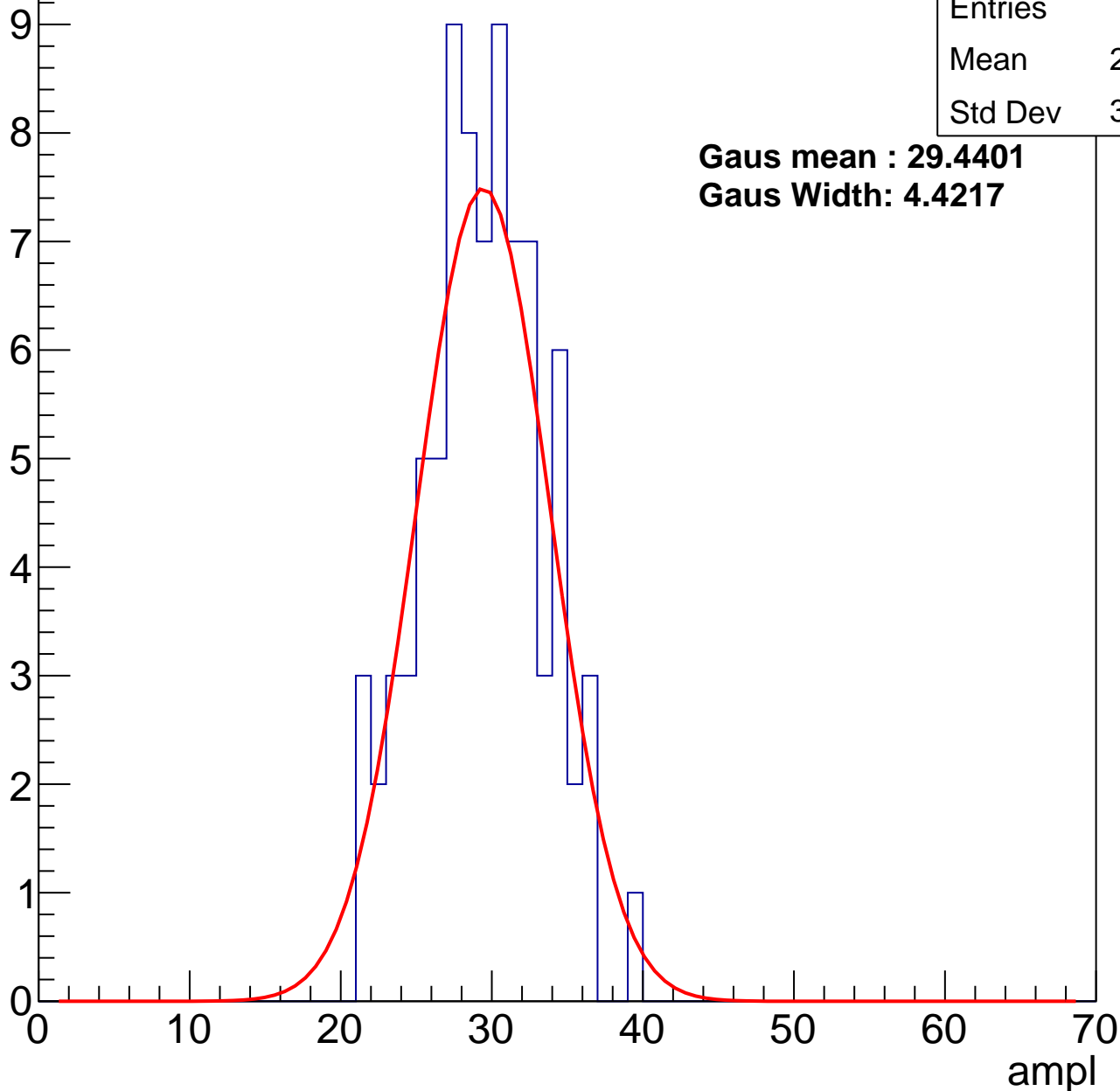
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	28.96
Std Dev	3.895

Gaus mean : 29.4401

Gaus Width: 4.4217



B0L102S, U12-ch87, adc1

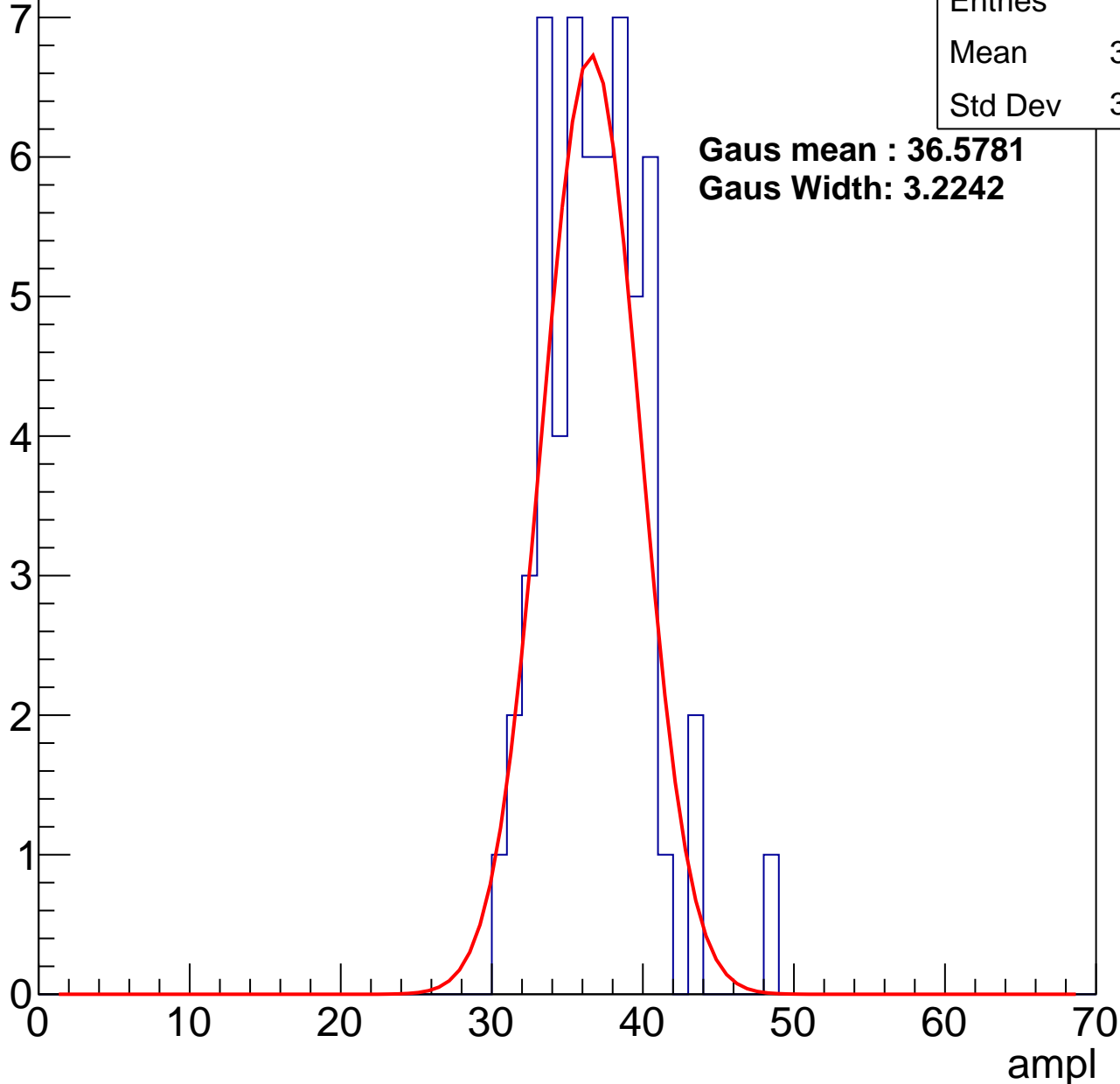
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	36.45
Std Dev	3.349

Gaus mean : 36.5781

Gaus Width: 3.2242



B0L102S, U12-ch87, adc2

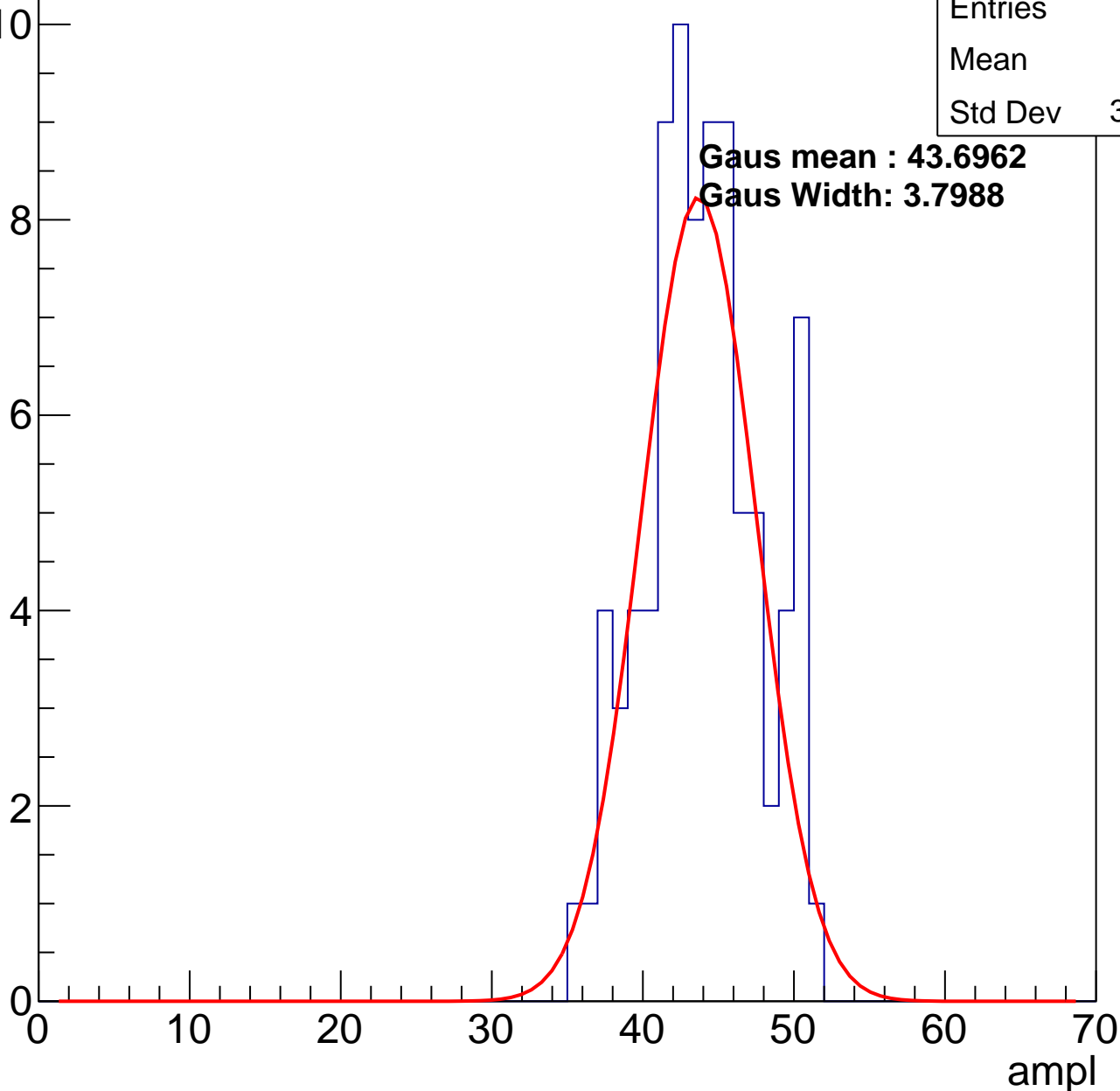
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	43.5
Std Dev	3.778

Gaus mean : 43.6962

Gaus Width: 3.7988

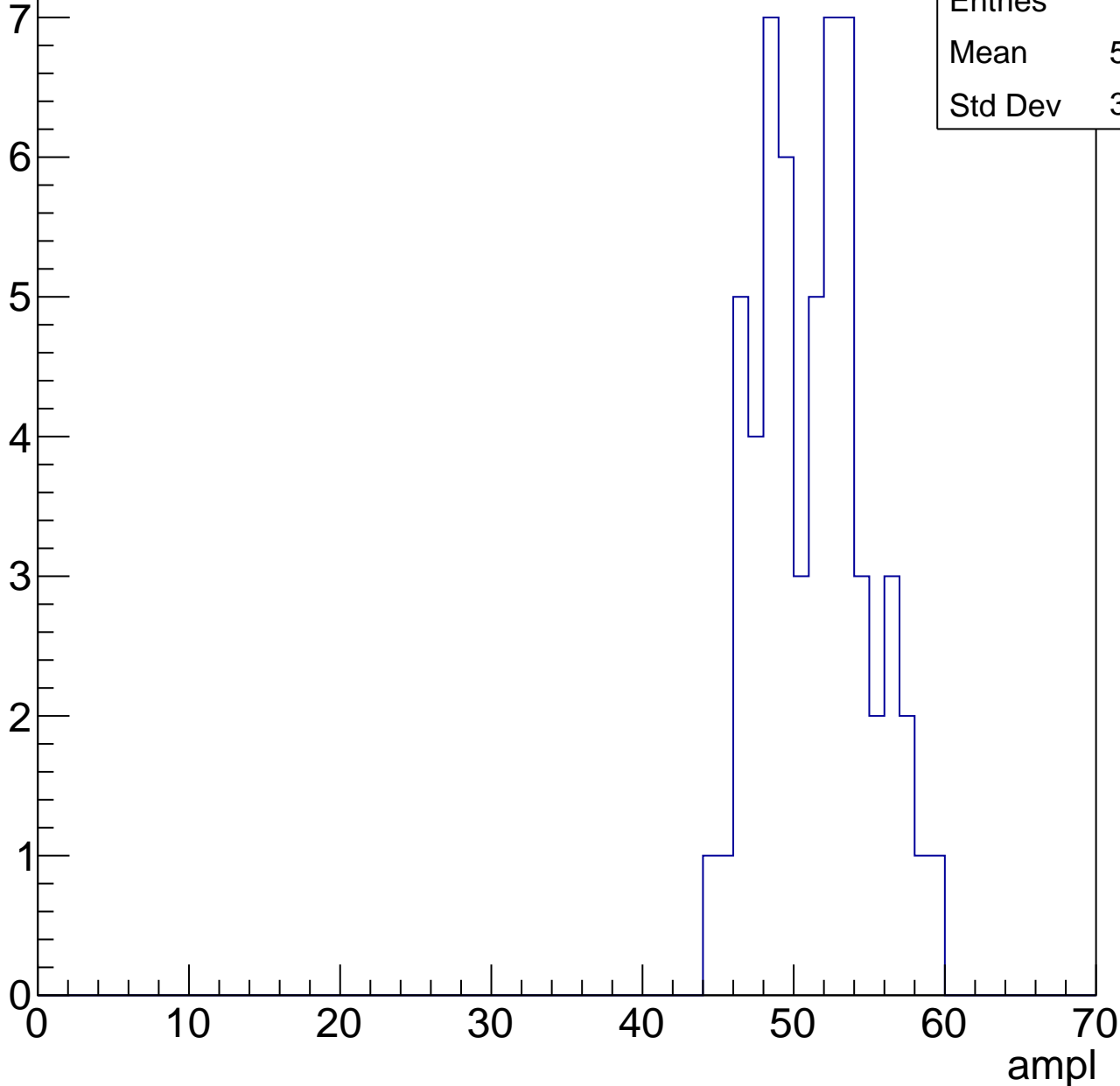


B0L102S, U12-ch87, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	50.83
Std Dev	3.514

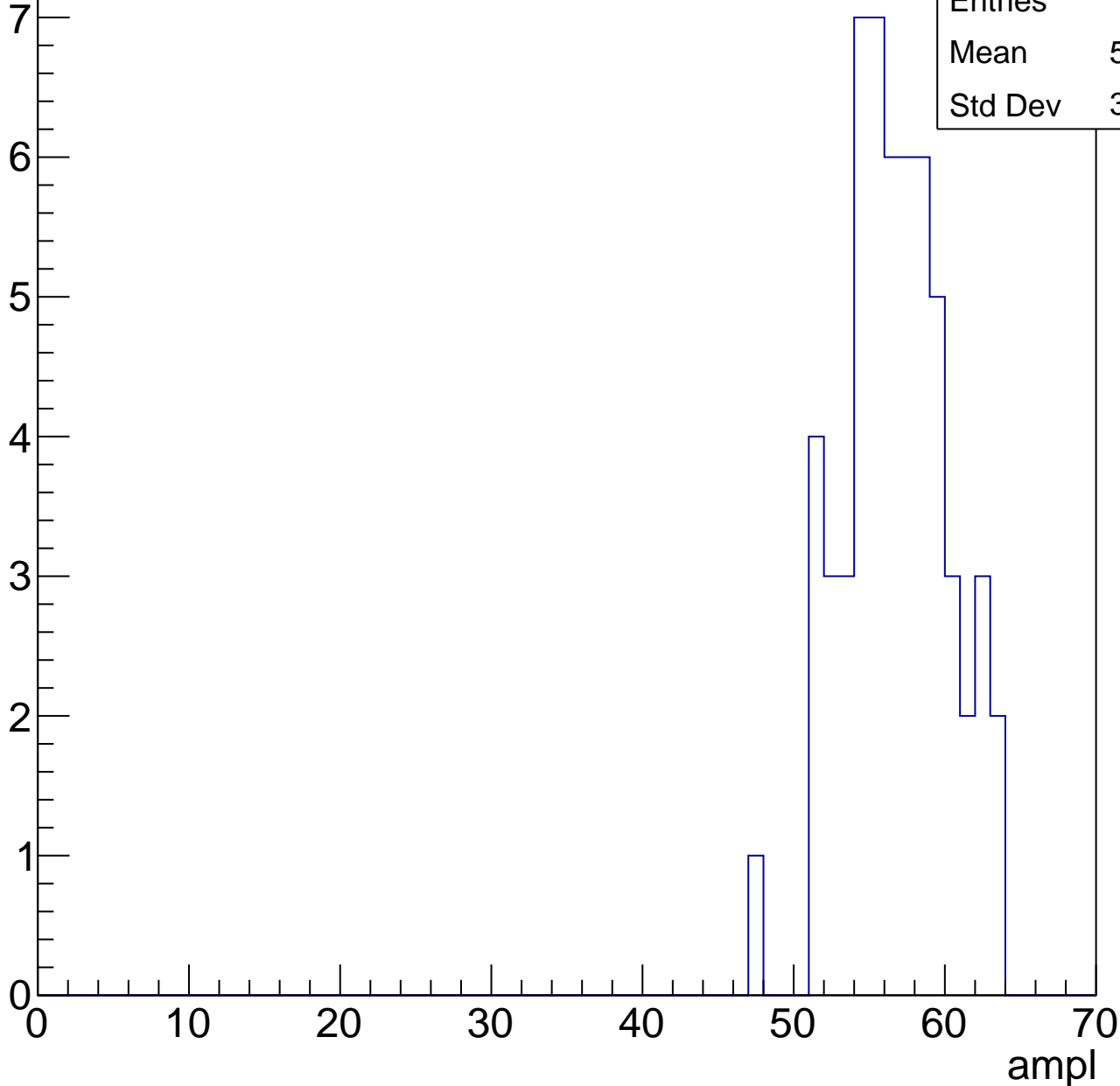


B0L102S, U12-ch87, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

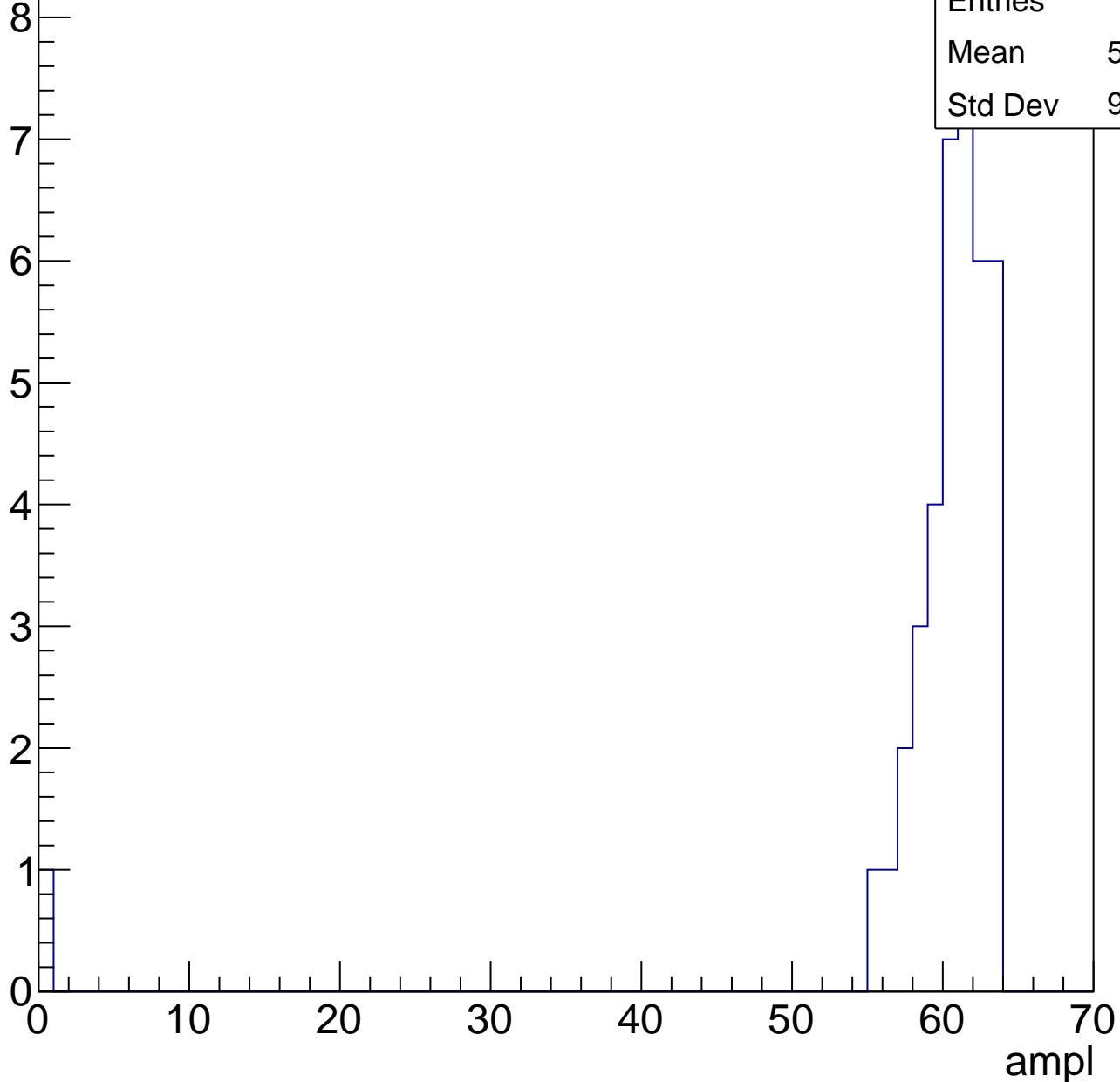
Entries	58
Mean	56.28
Std Dev	3.388



B0L102S, U12-ch87, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch87, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch87, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch88, adc0

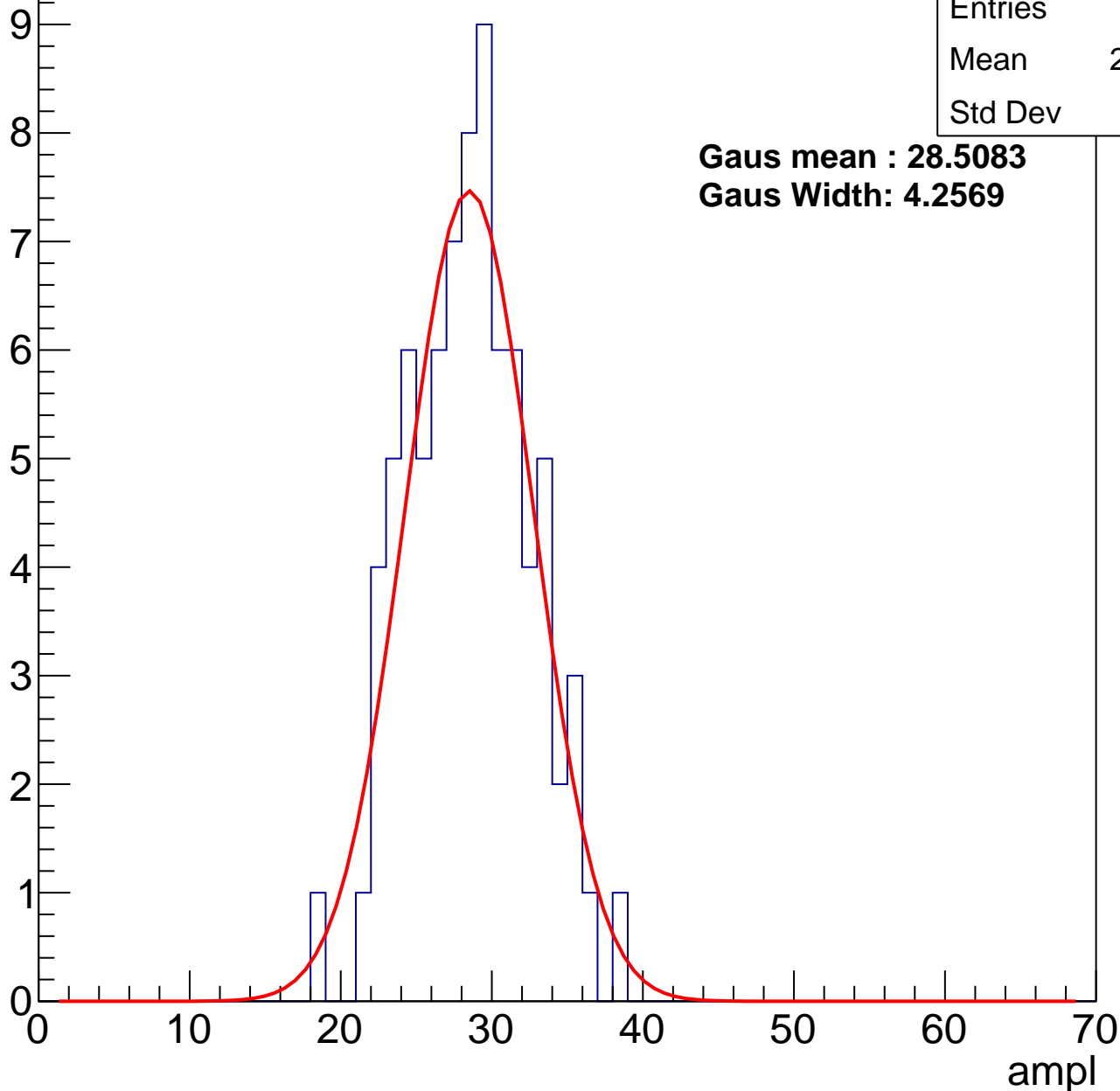
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	28.09
Std Dev	3.95

Gaus mean : 28.5083

Gaus Width: 4.2569



B0L102S, U12-ch88, adc1

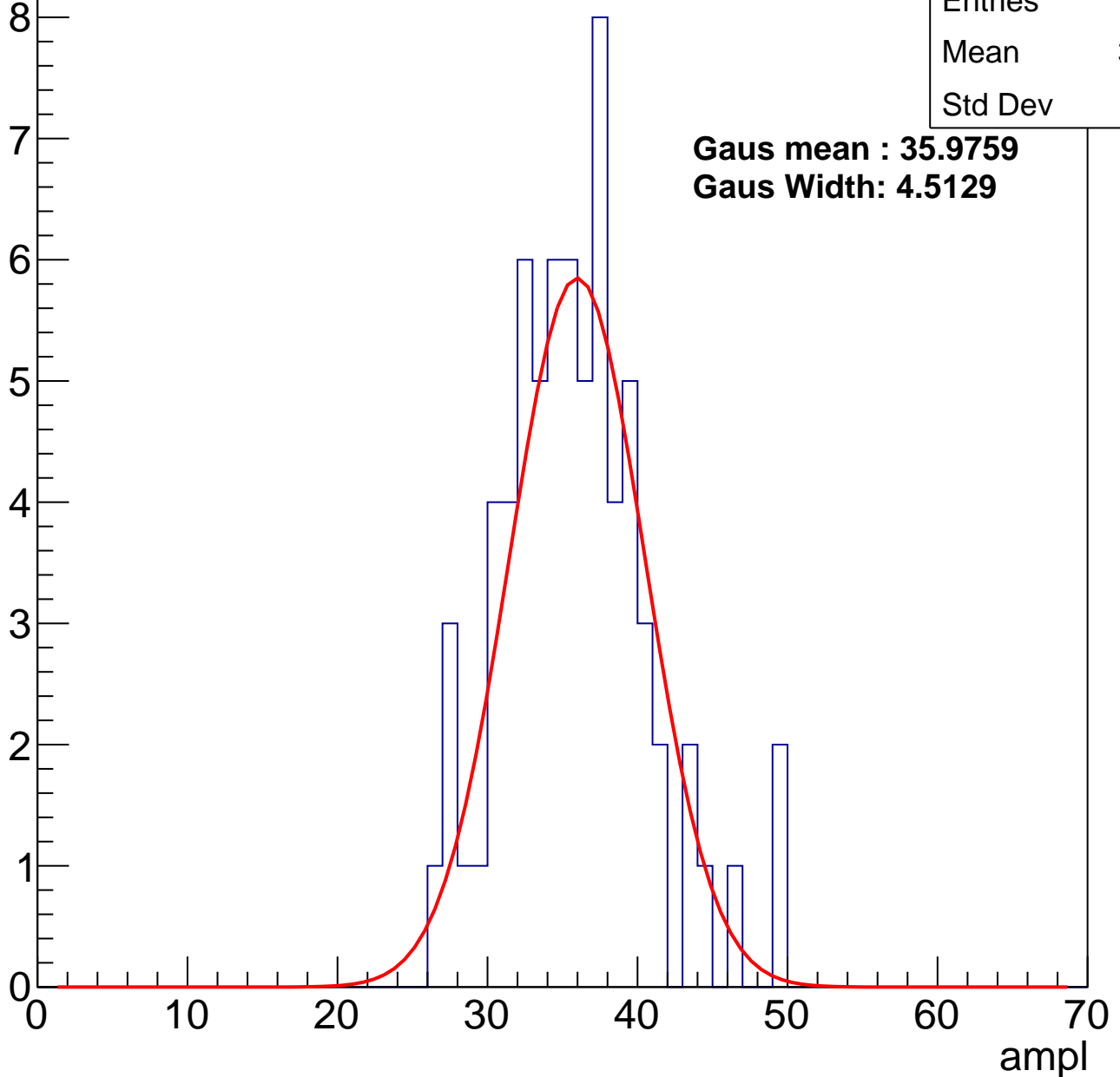
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	35.4
Std Dev	4.8

Gaus mean : 35.9759

Gaus Width: 4.5129



B0L102S, U12-ch88, adc2

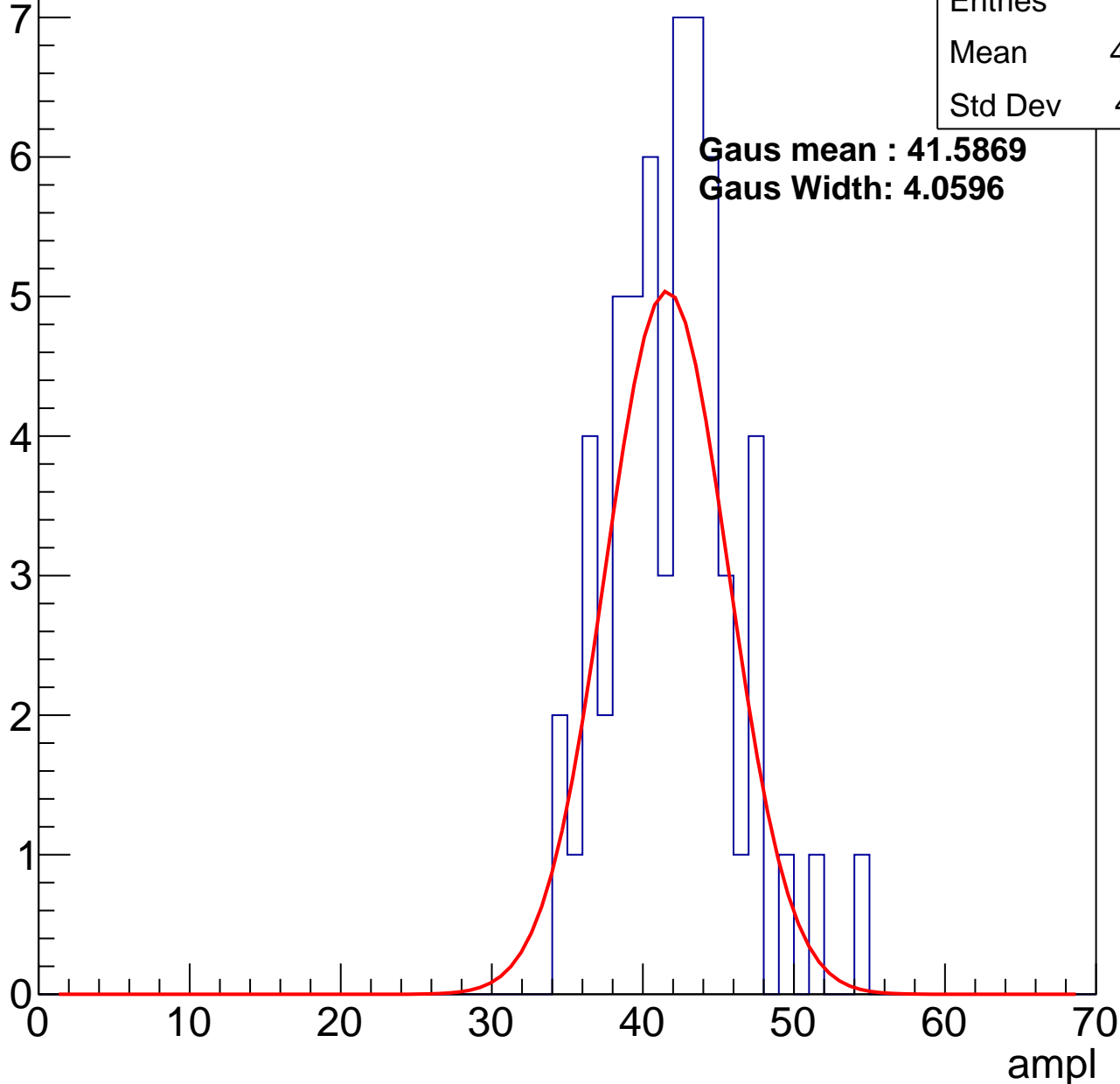
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	41.54
Std Dev	4.031

Gaus mean : 41.5869

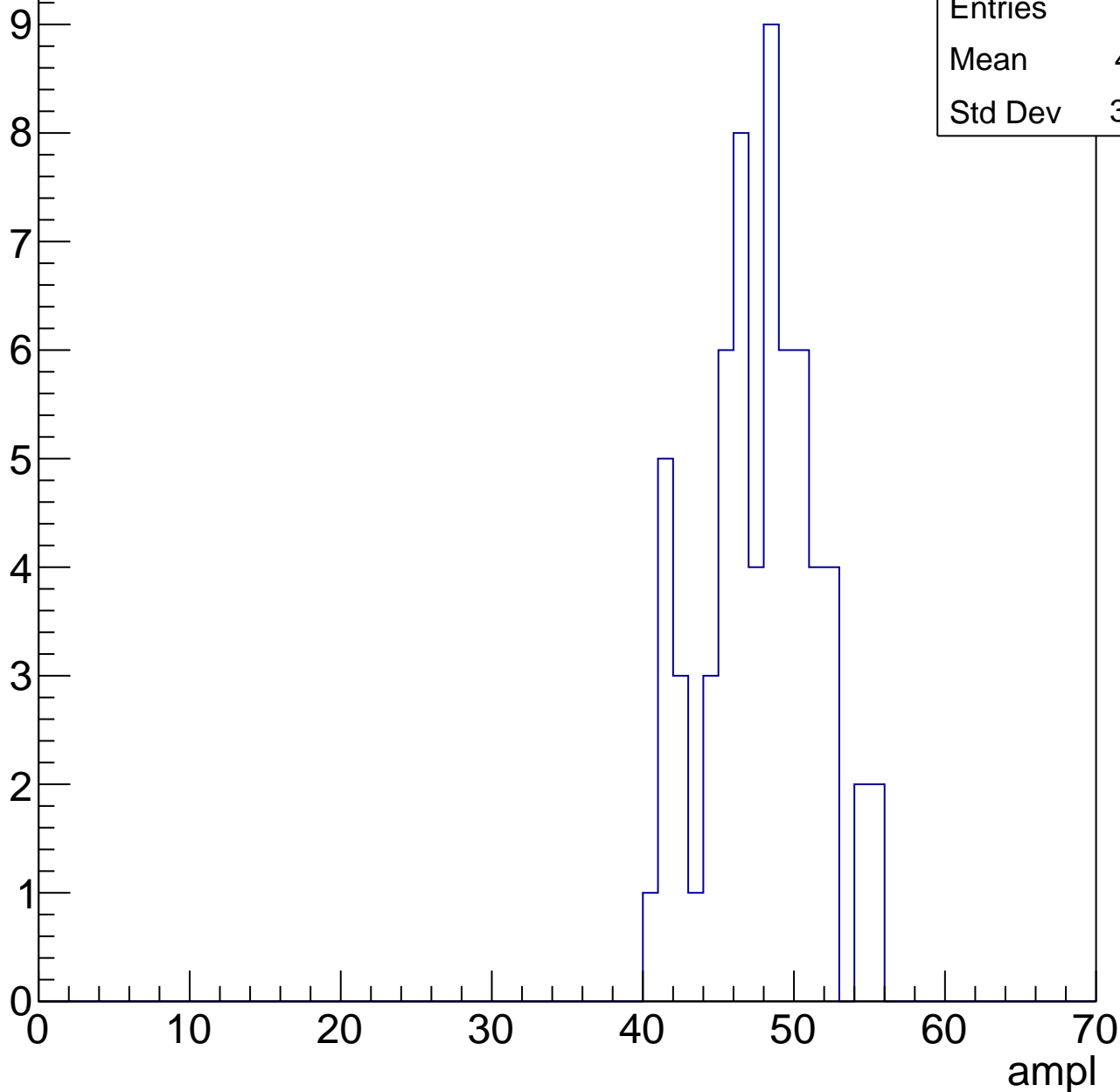
Gaus Width: 4.0596



B0L102S, U12-ch88, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

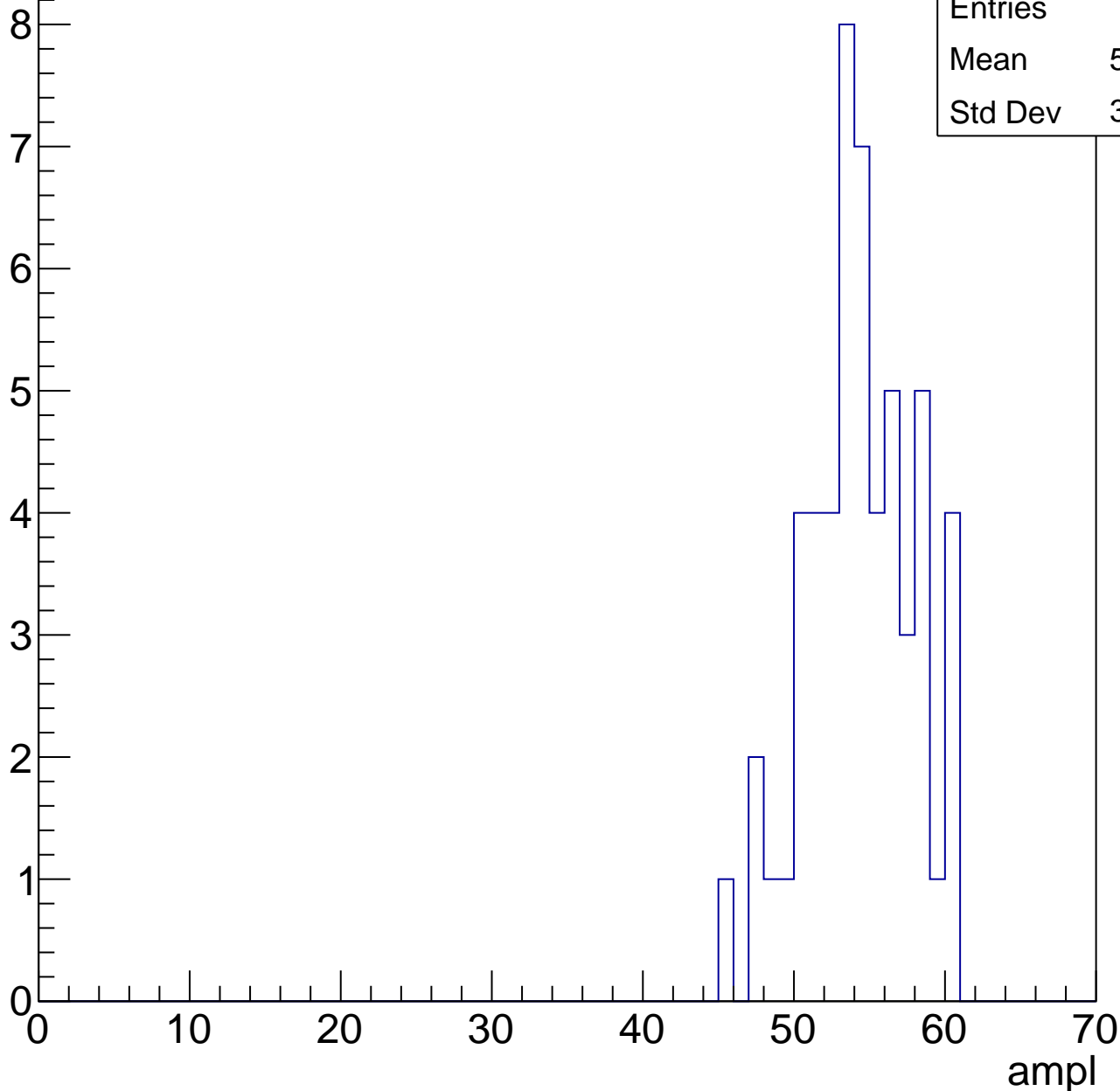


B0L102S, U12-ch88, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	53.89
Std Dev	3.505

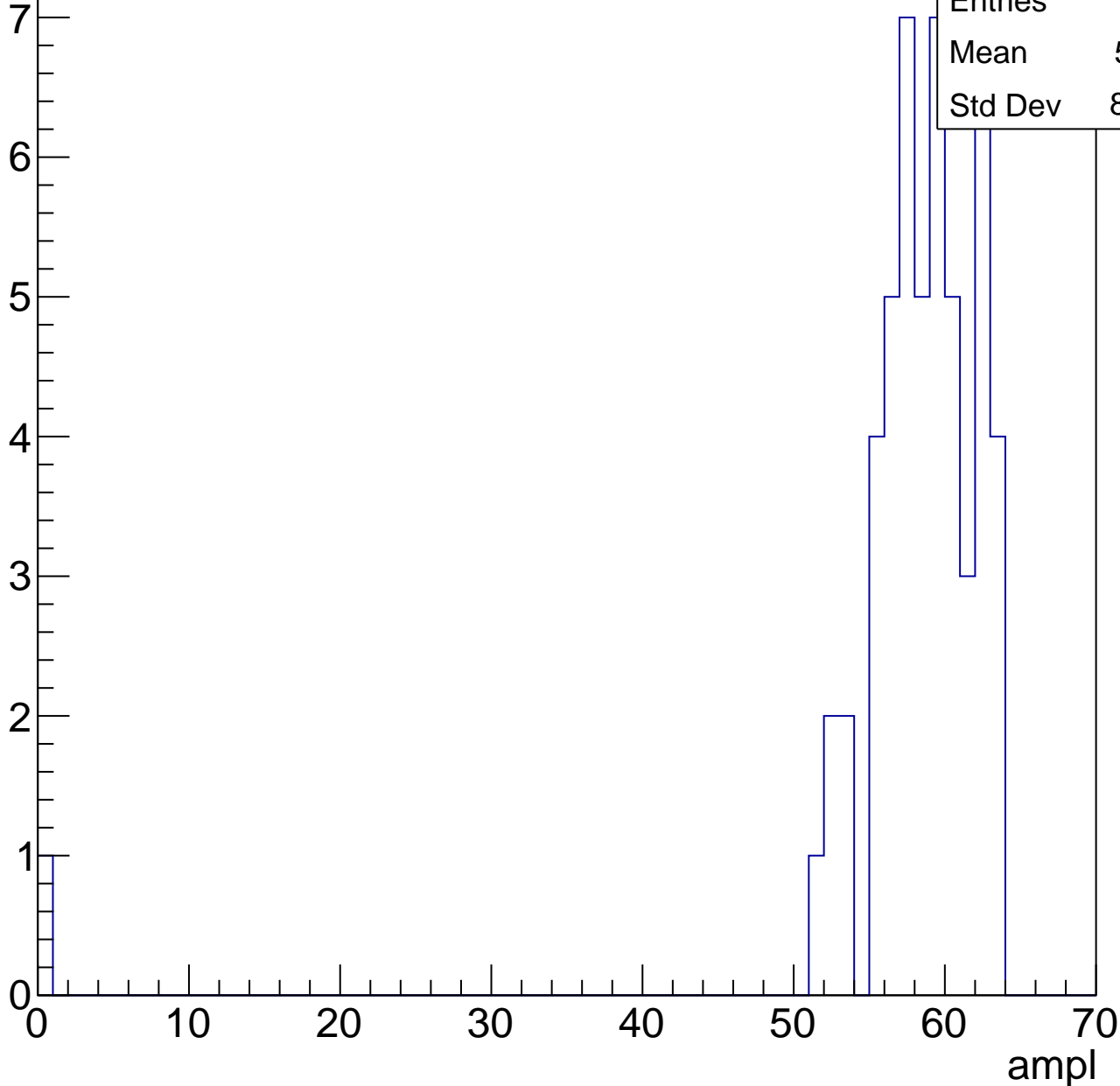


B0L102S, U12-ch88, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

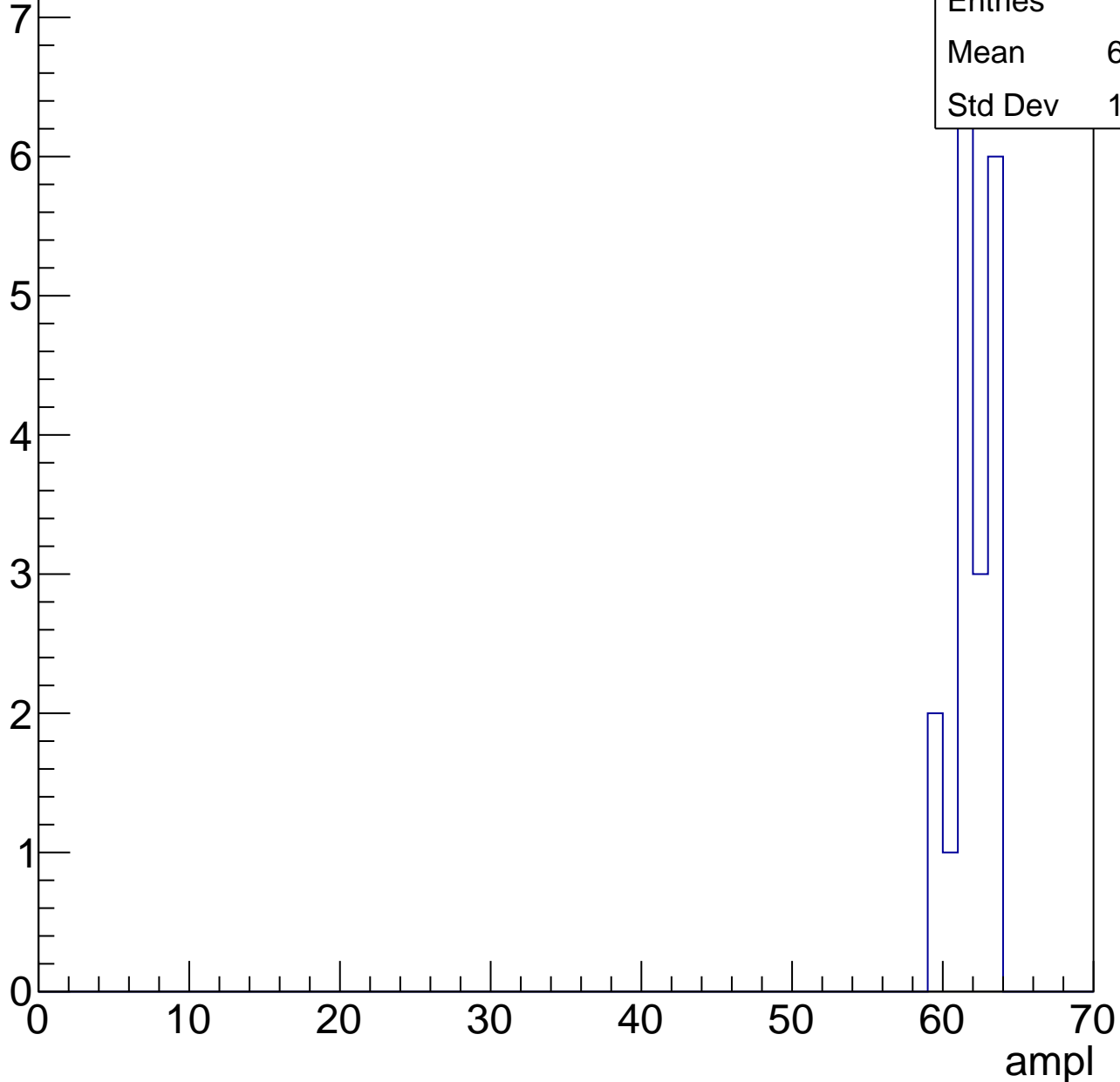
Entries	53
Mean	57.21
Std Dev	8.502



B0L102S, U12-ch88, adc6

calib_packv5_042523_0143.root, FC#12, port B1

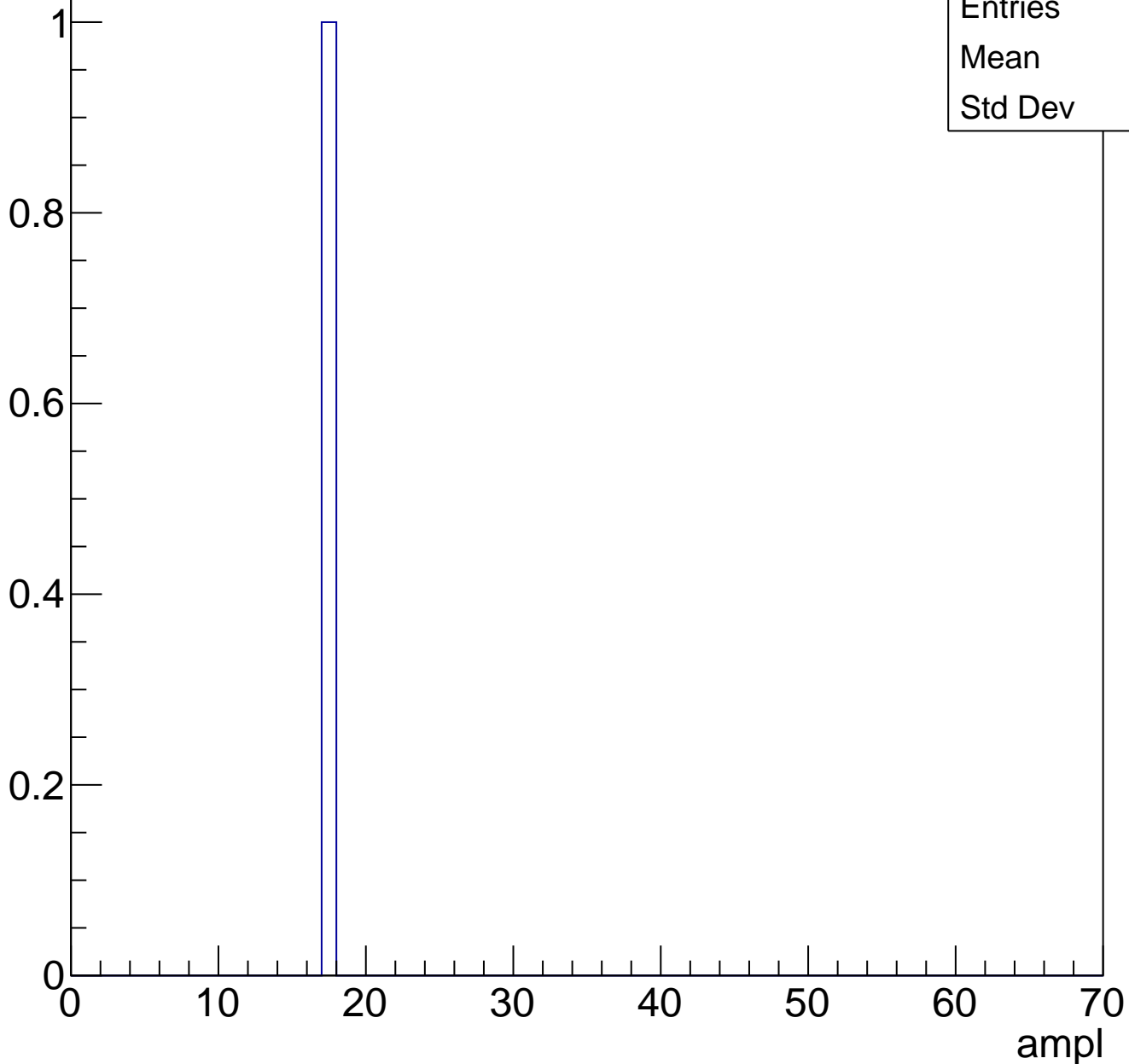
Entry



B0L102S, U12-ch88, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch89, adc0

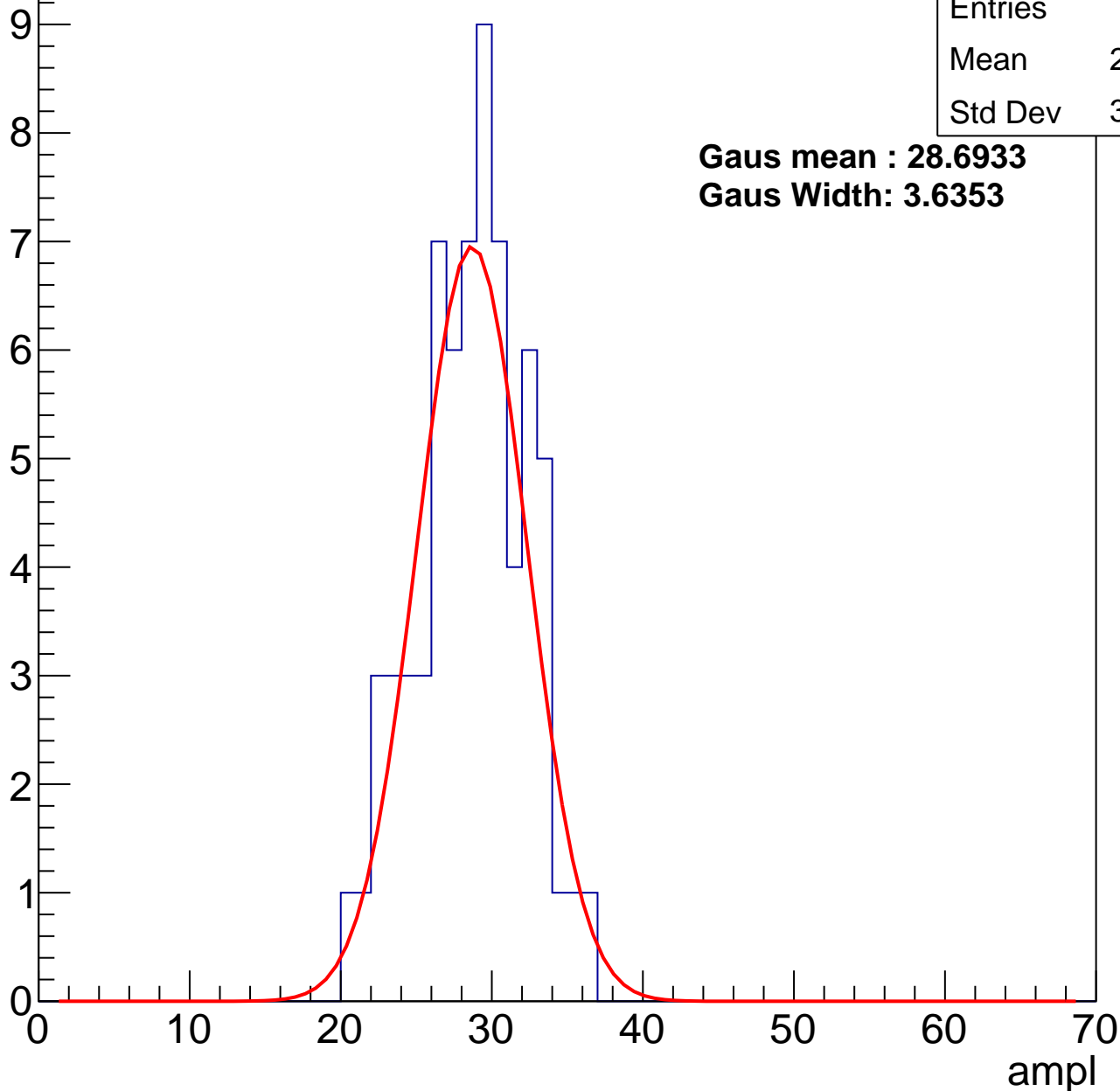
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.24
Std Dev	3.524

Gaus mean : 28.6933

Gaus Width: 3.6353



B0L102S, U12-ch89, adc1

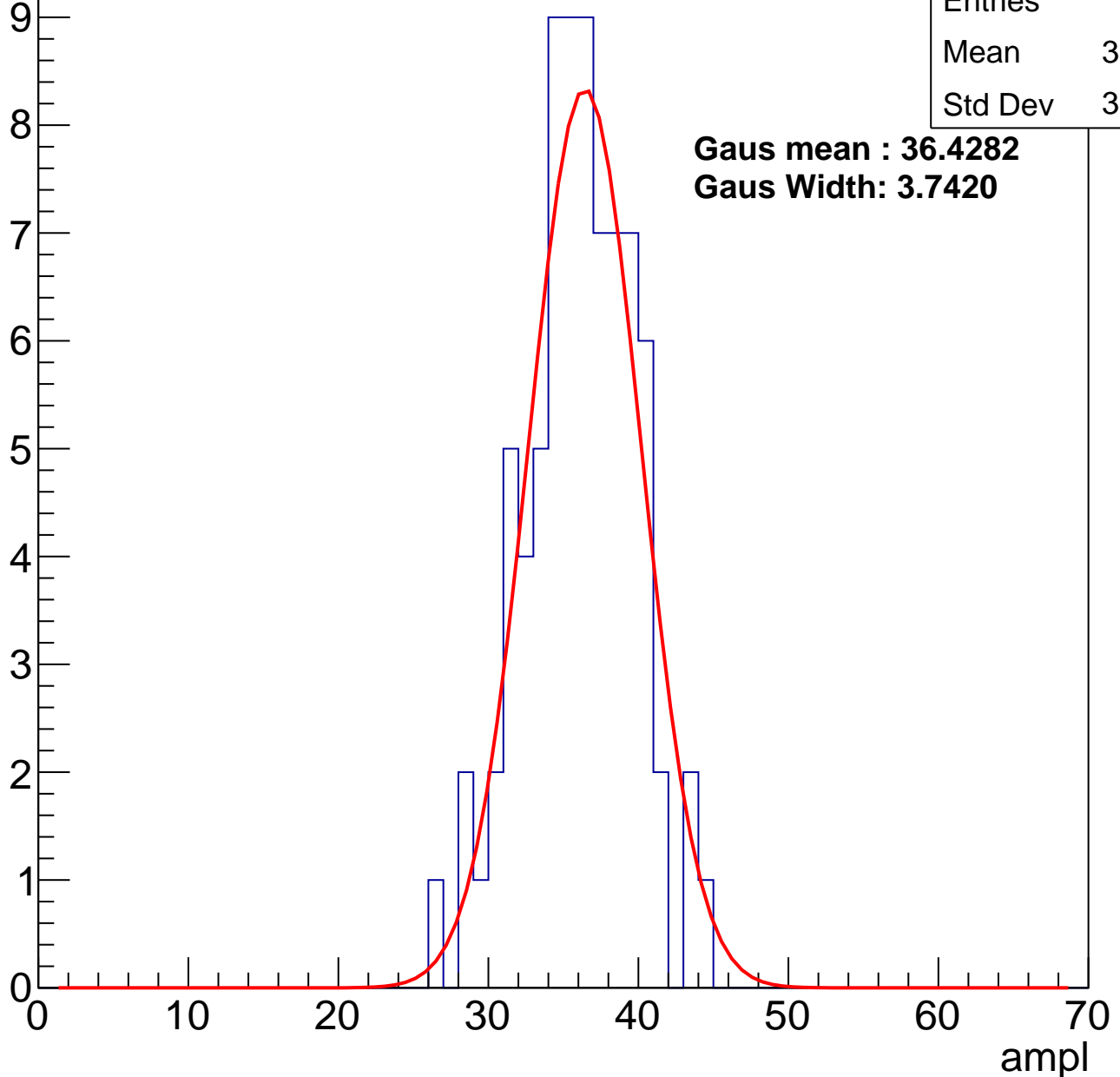
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.62
Std Dev	3.598

Gaus mean : 36.4282

Gaus Width: 3.7420



B0L102S, U12-ch89, adc2

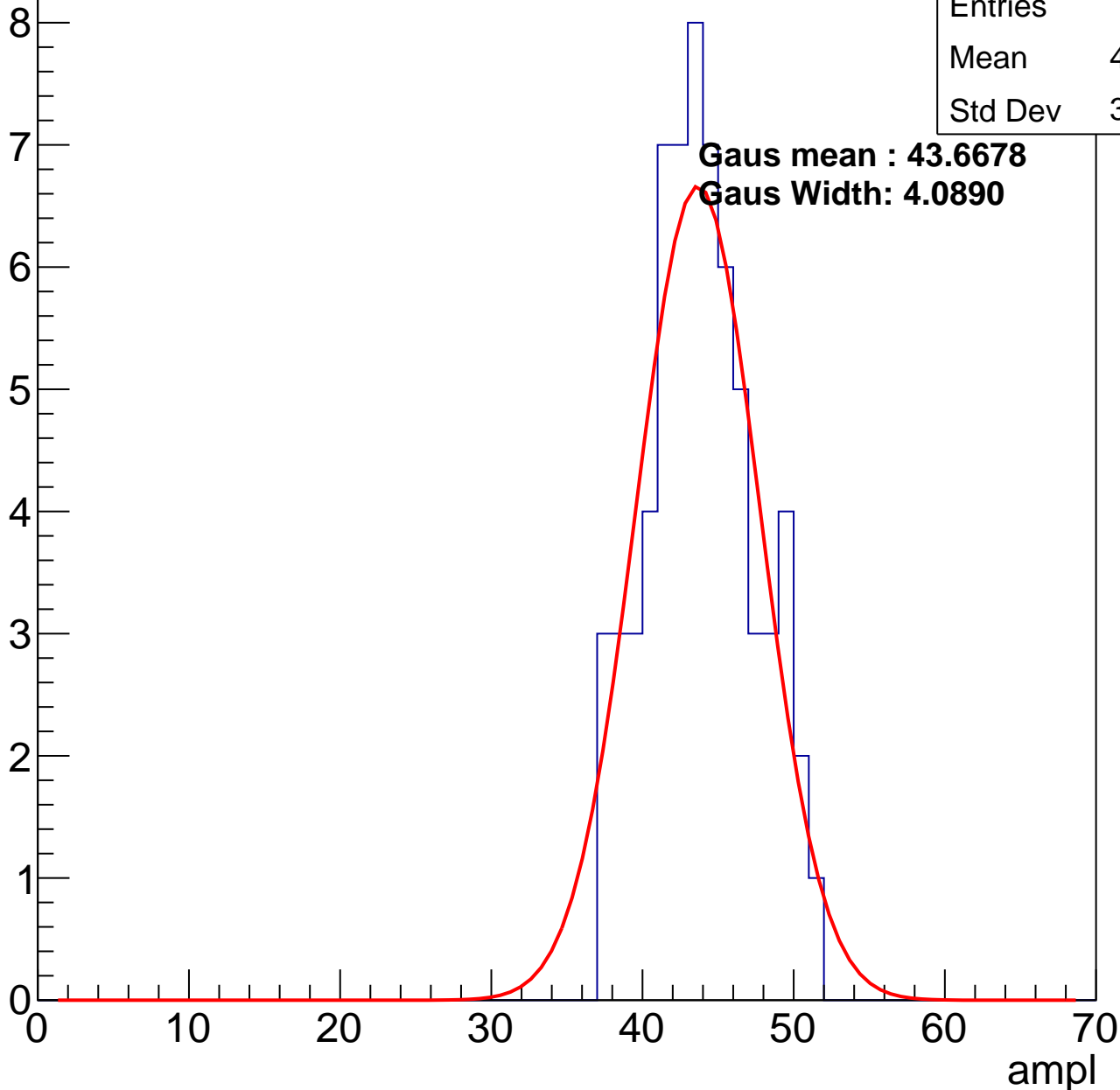
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	43.44
Std Dev	3.469

Gaus mean : 43.6678

Gaus Width: 4.0890

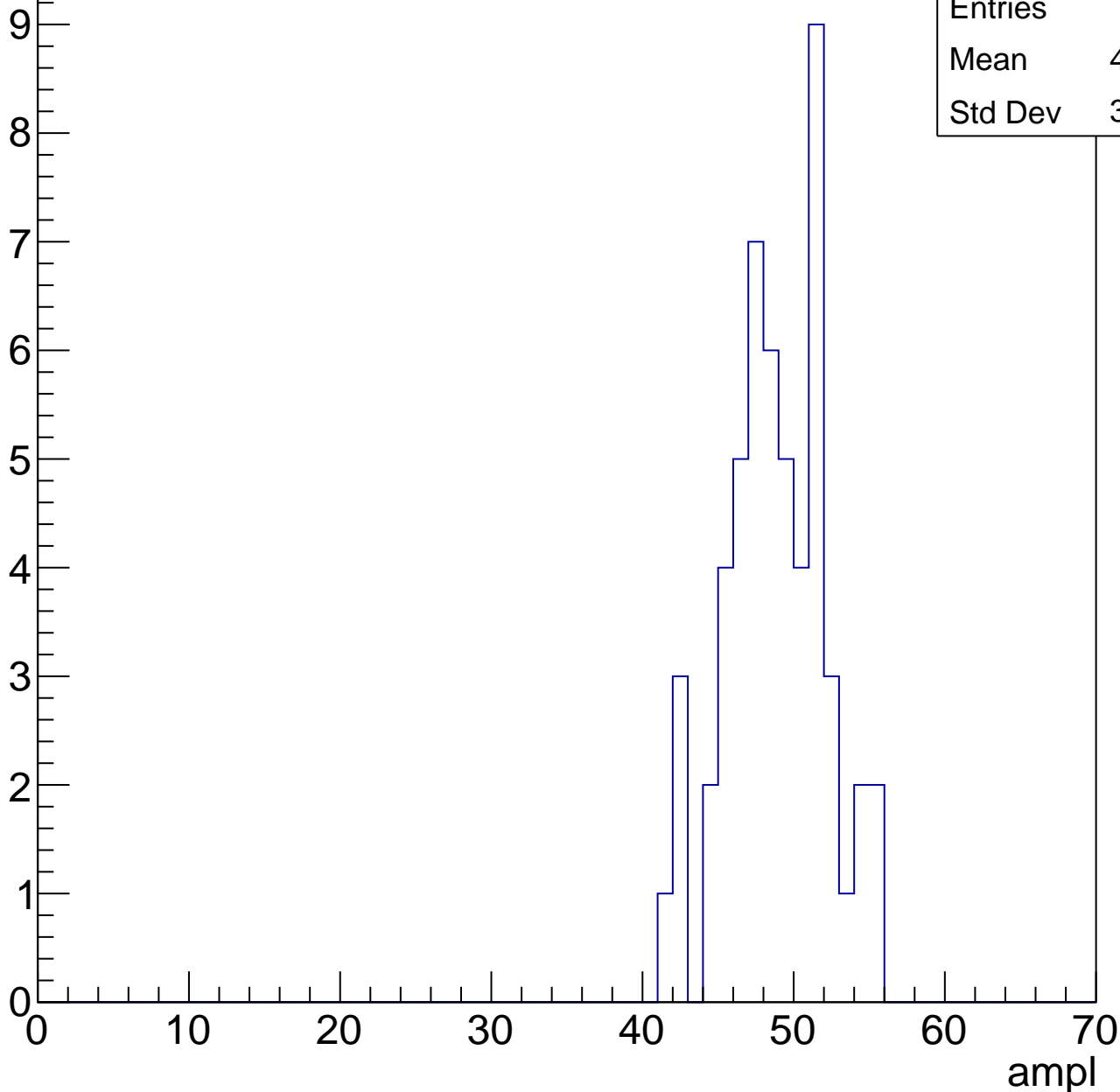


B0L102S, U12-ch89, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	48.39
Std Dev	3.302

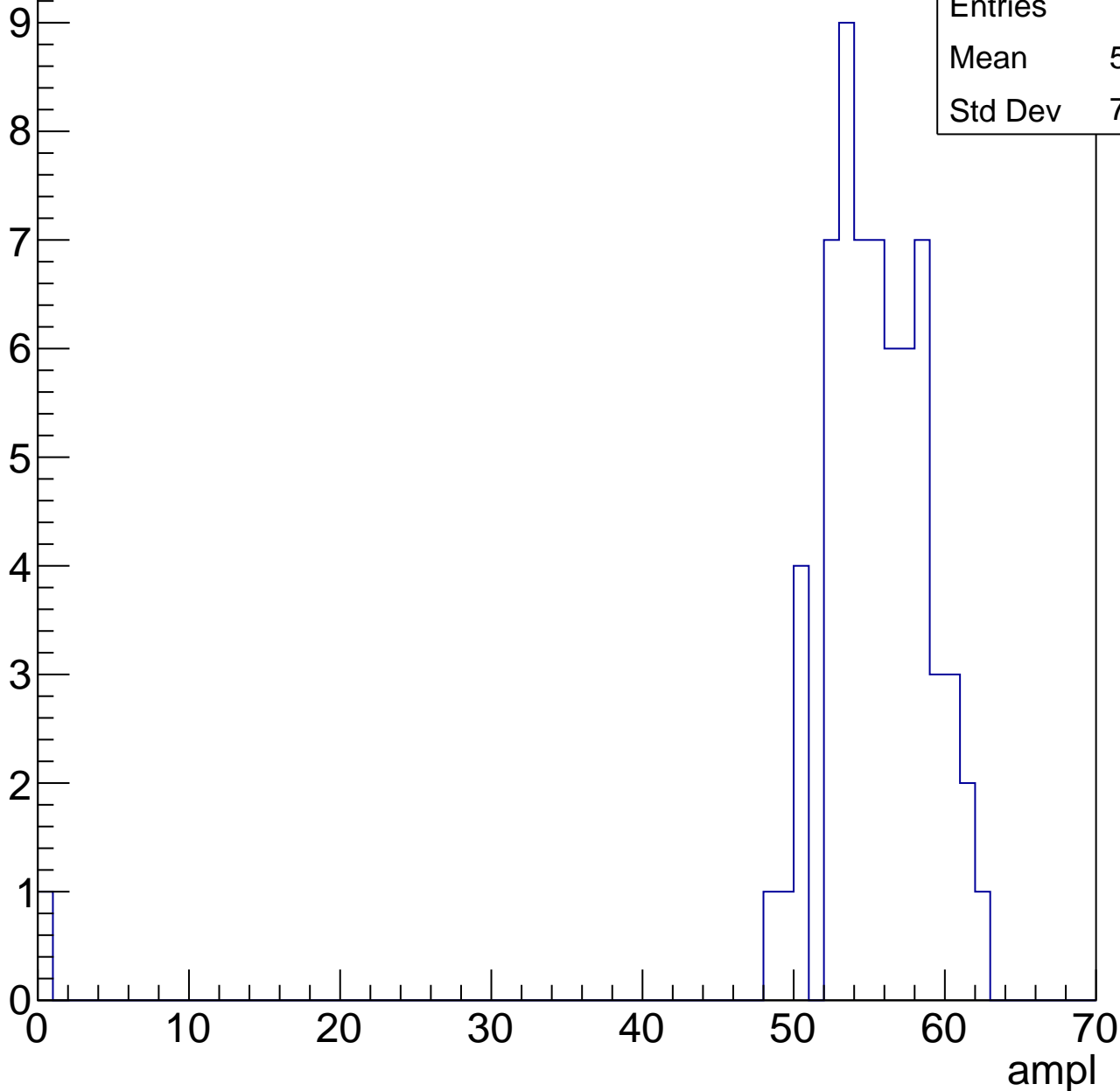


B0L102S, U12-ch89, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

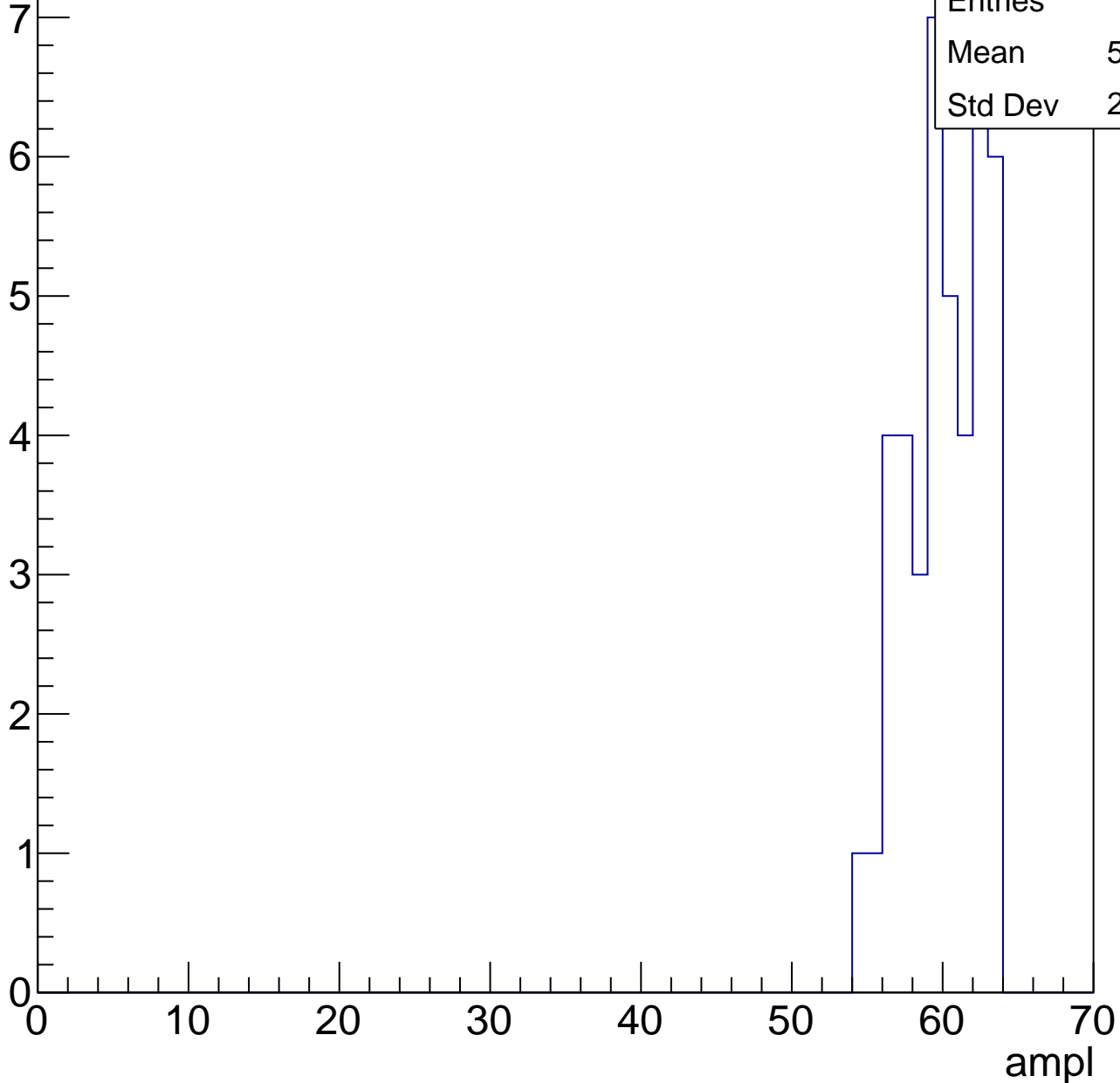
Entries	65
Mean	54.25
Std Dev	7.459



B0L102S, U12-ch89, adc5

calib_packv5_042523_0143.root, FC#12, port B1

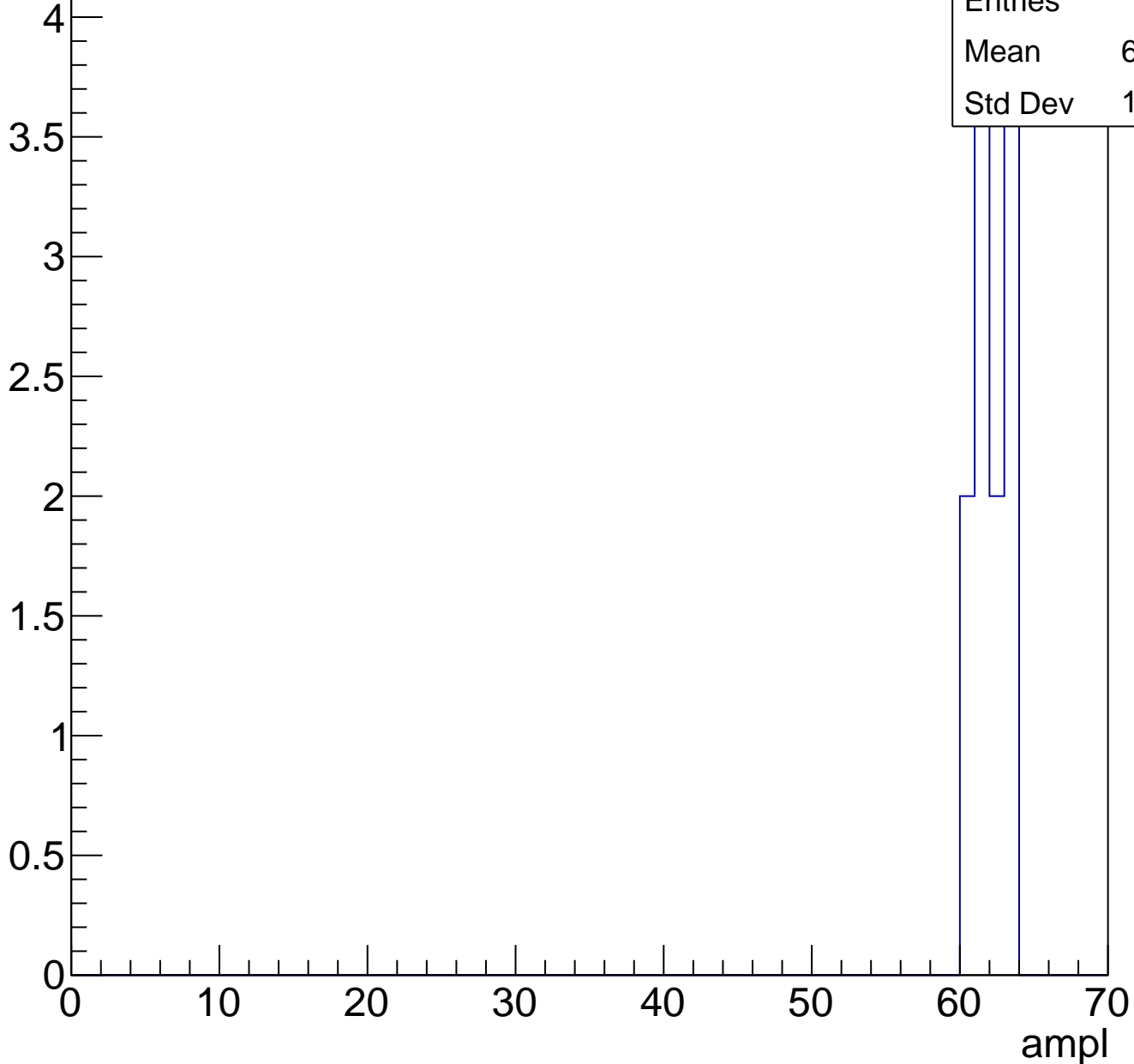
Entry



B0L102S, U12-ch89, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch89, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch90, adc0

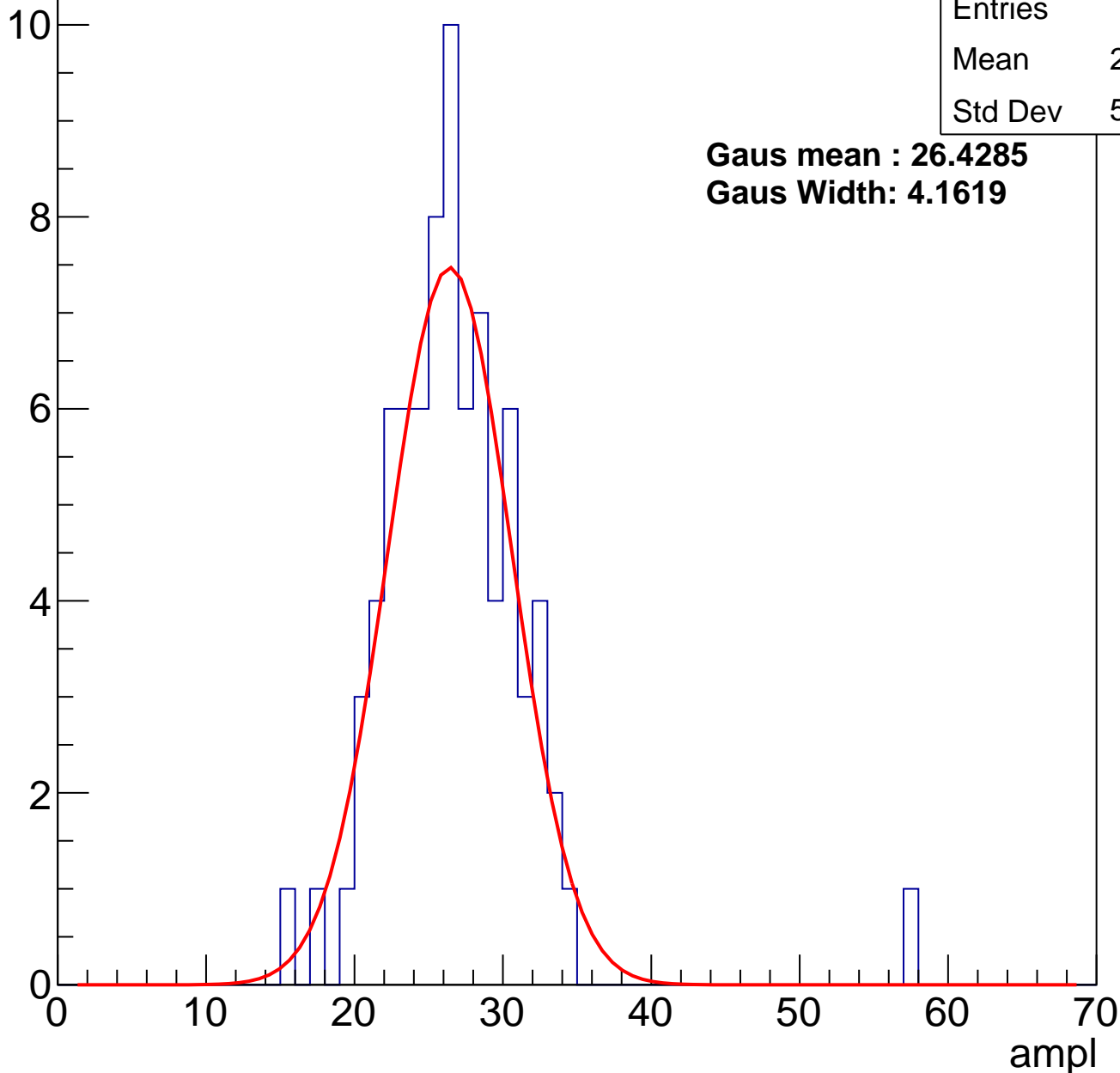
calib_packv5_042523_0143.root, FC#12, port B1

Entries	80
Mean	26.26
Std Dev	5.176

Gaus mean : 26.4285

Gaus Width: 4.1619

Entry



B0L102S, U12-ch90, adc1

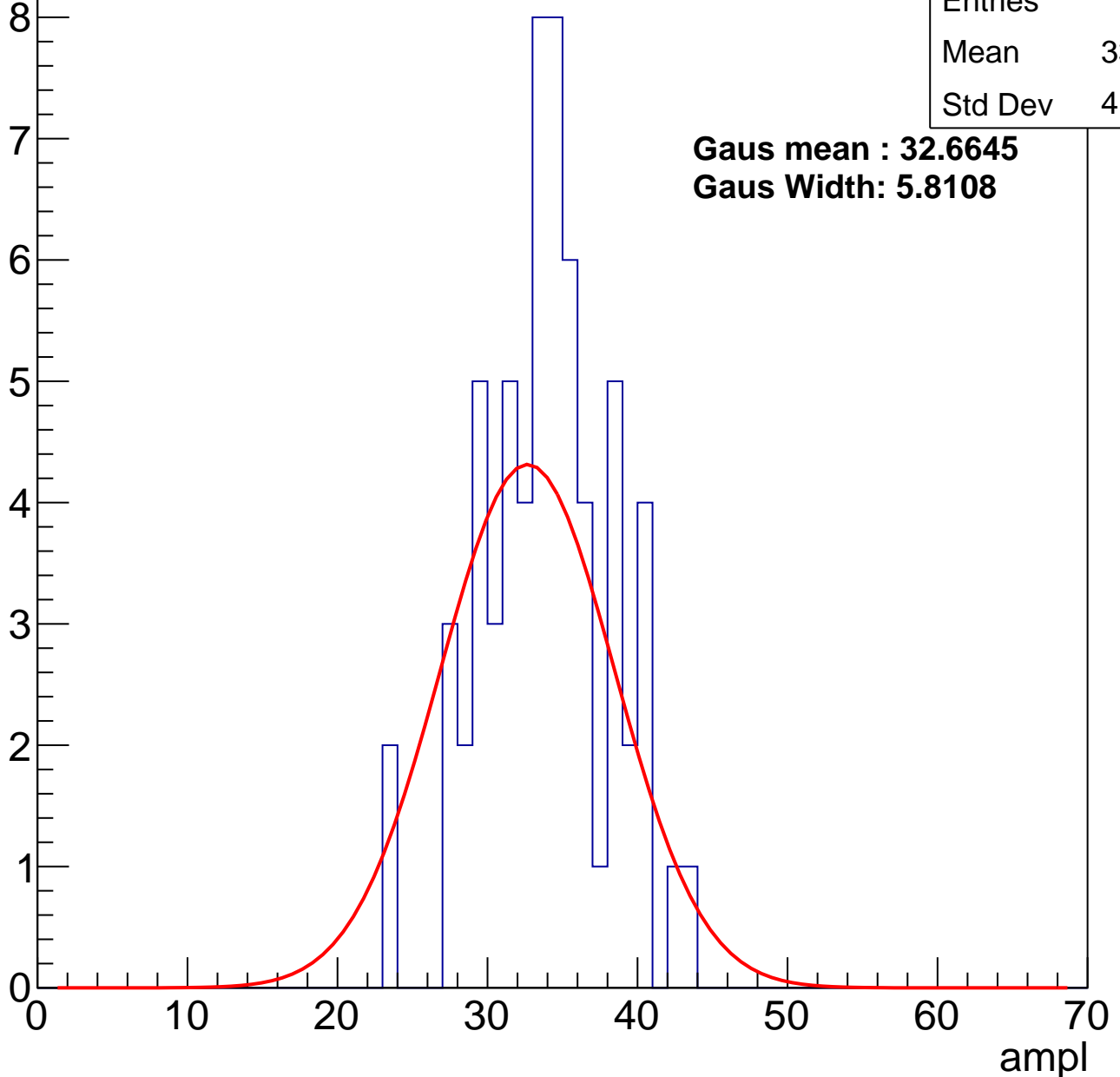
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	33.45
Std Dev	4.209

Gaus mean : 32.6645

Gaus Width: 5.8108



B0L102S, U12-ch90, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	81
Mean	39.58
Std Dev	4.502

Gaus mean : 39.7494

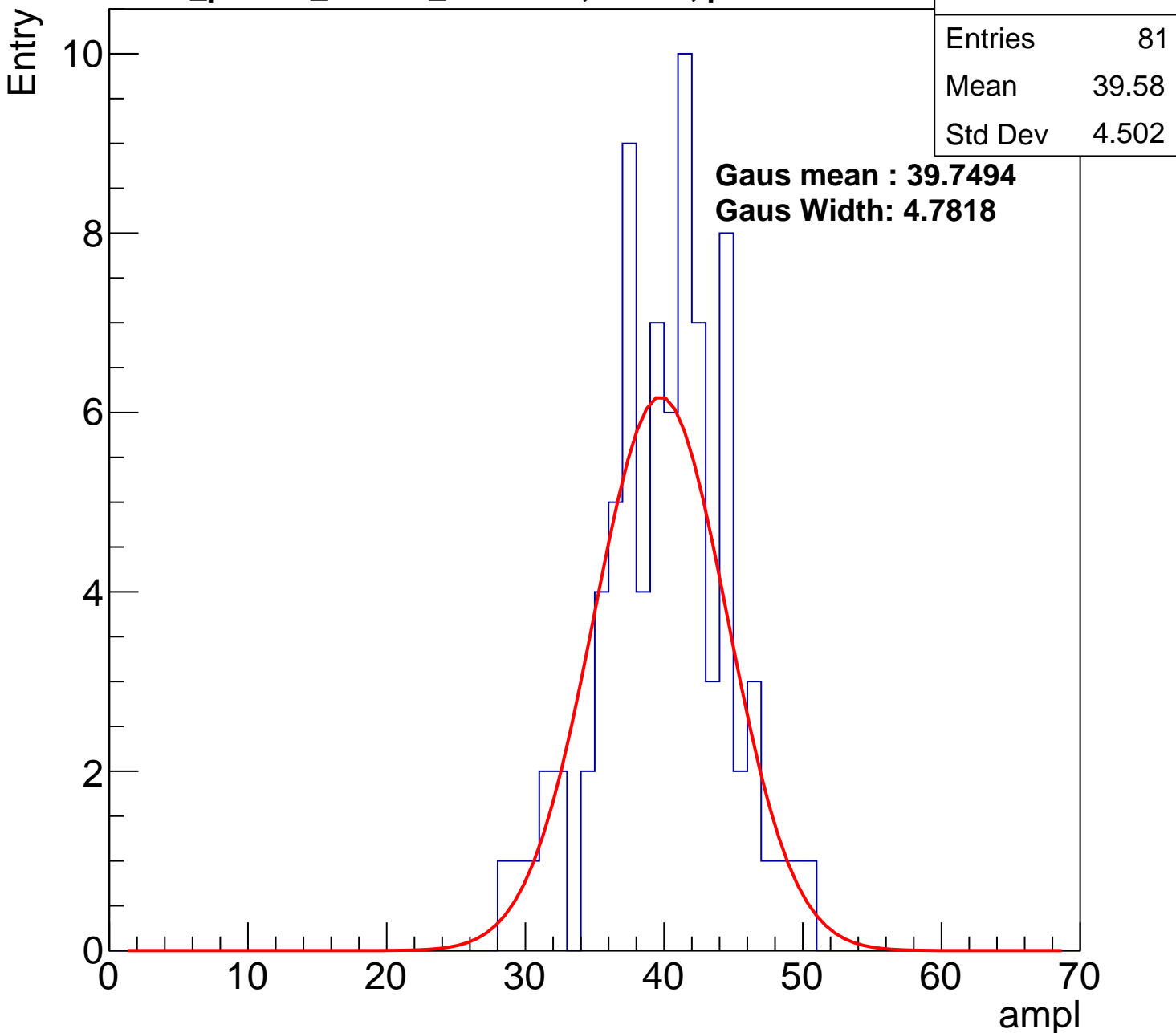
Gaus Width: 4.7818

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

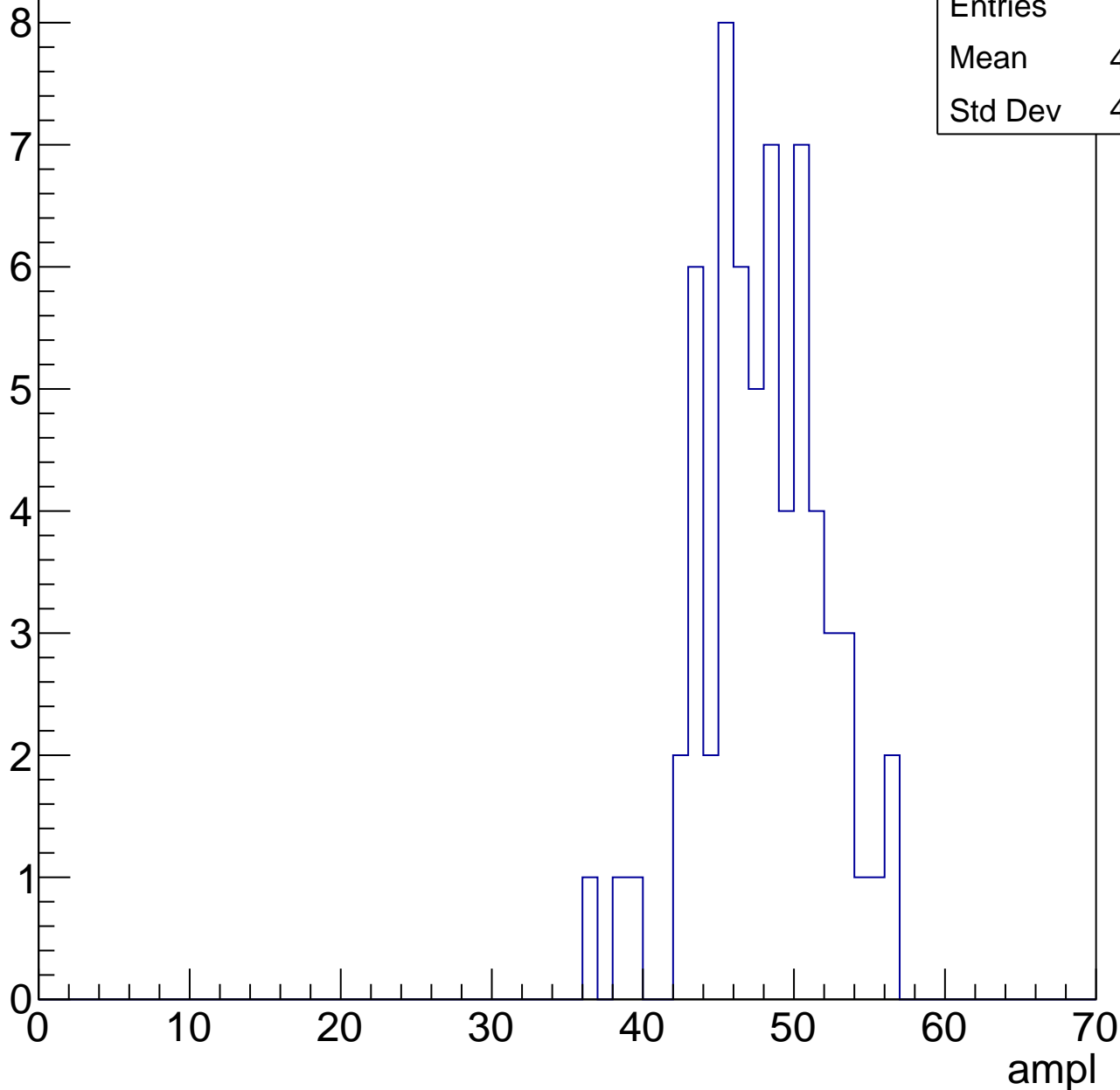


B0L102S, U12-ch90, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	47.44
Std Dev	4.077

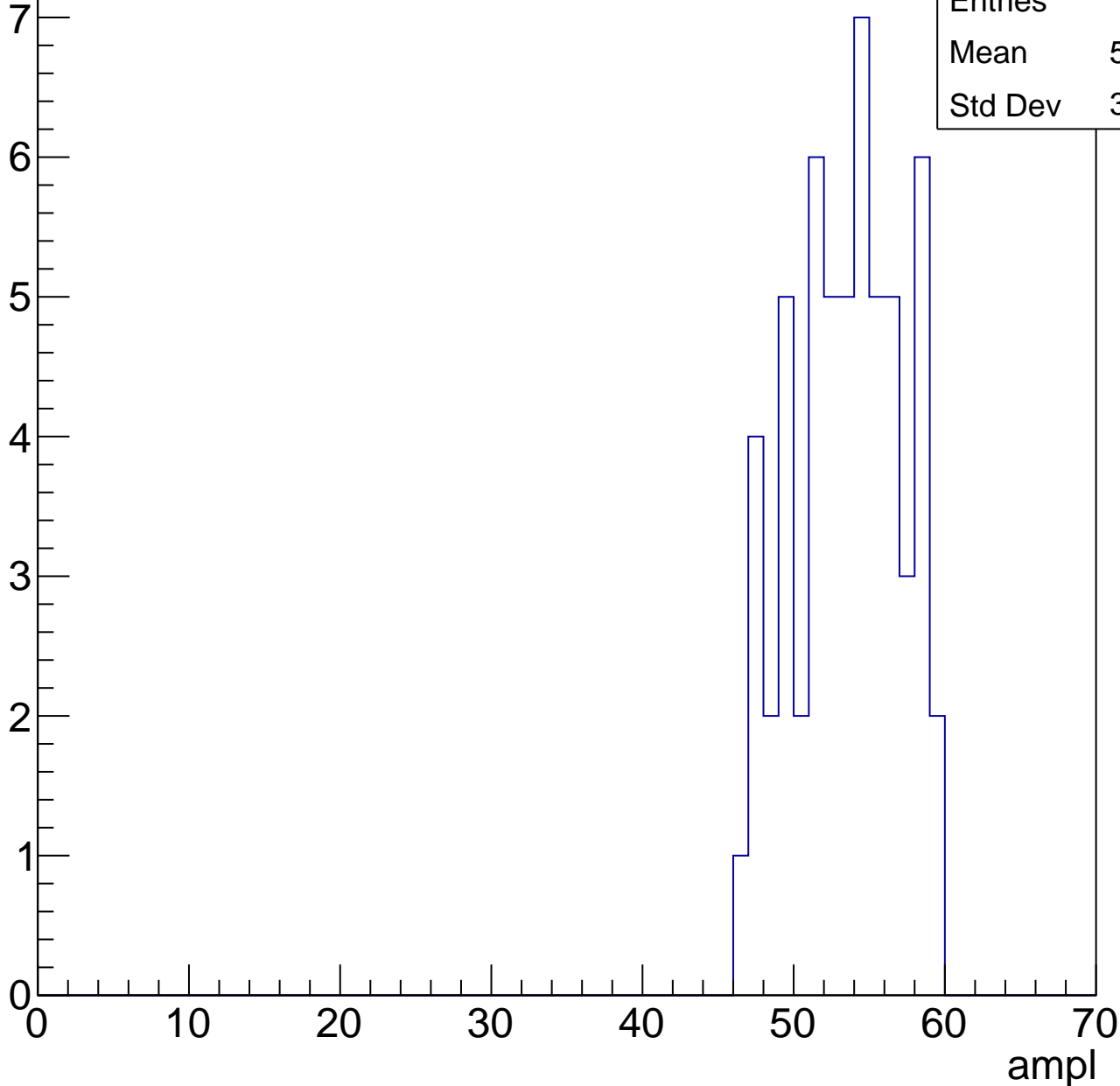


B0L102S, U12-ch90, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	53.03
Std Dev	3.504

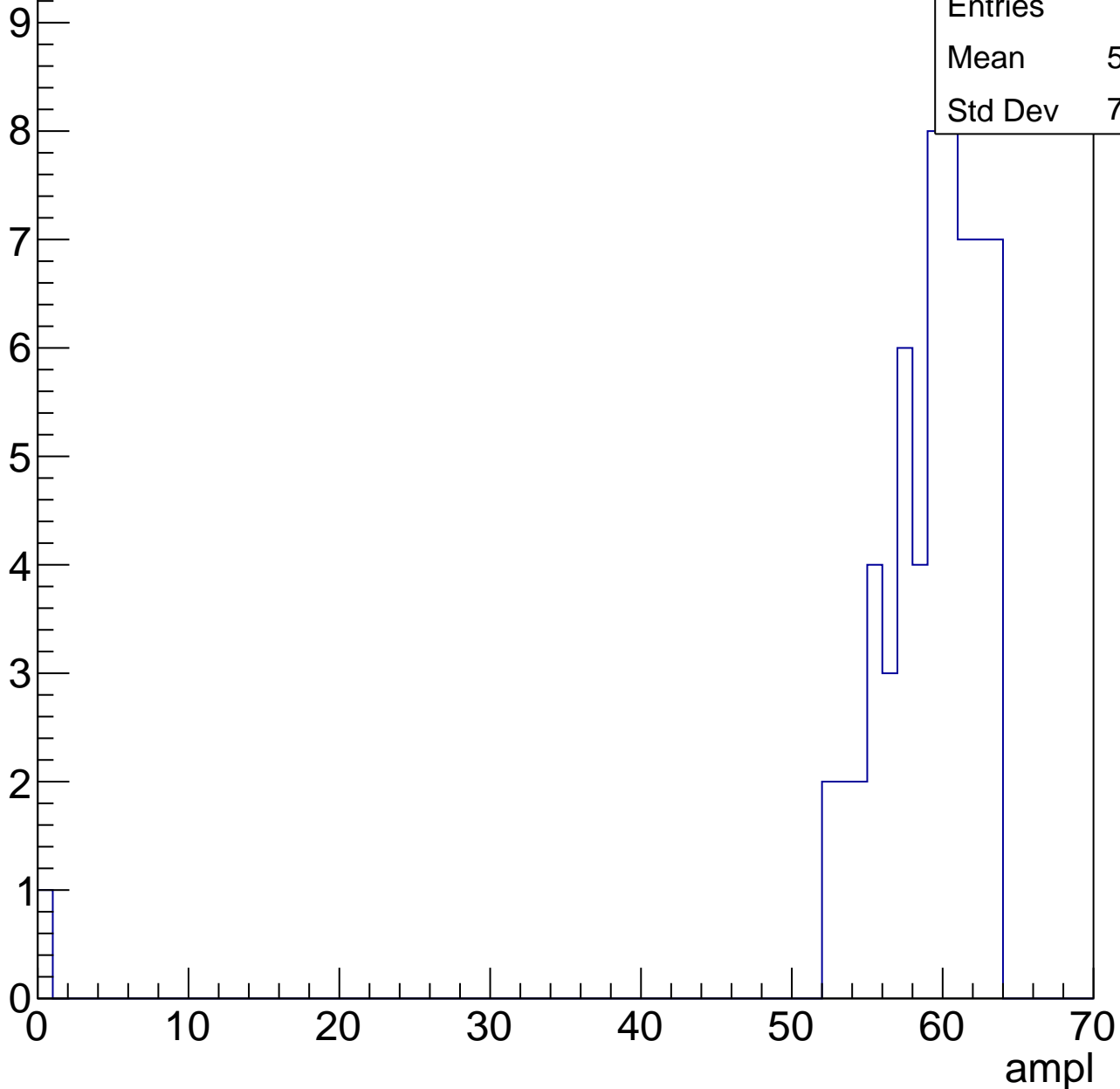


B0L102S, U12-ch90, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	57.97
Std Dev	7.998



B0L102S, U12-ch90, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.36
Std Dev	1.666

B0L102S, U12-ch90, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch91, adc0

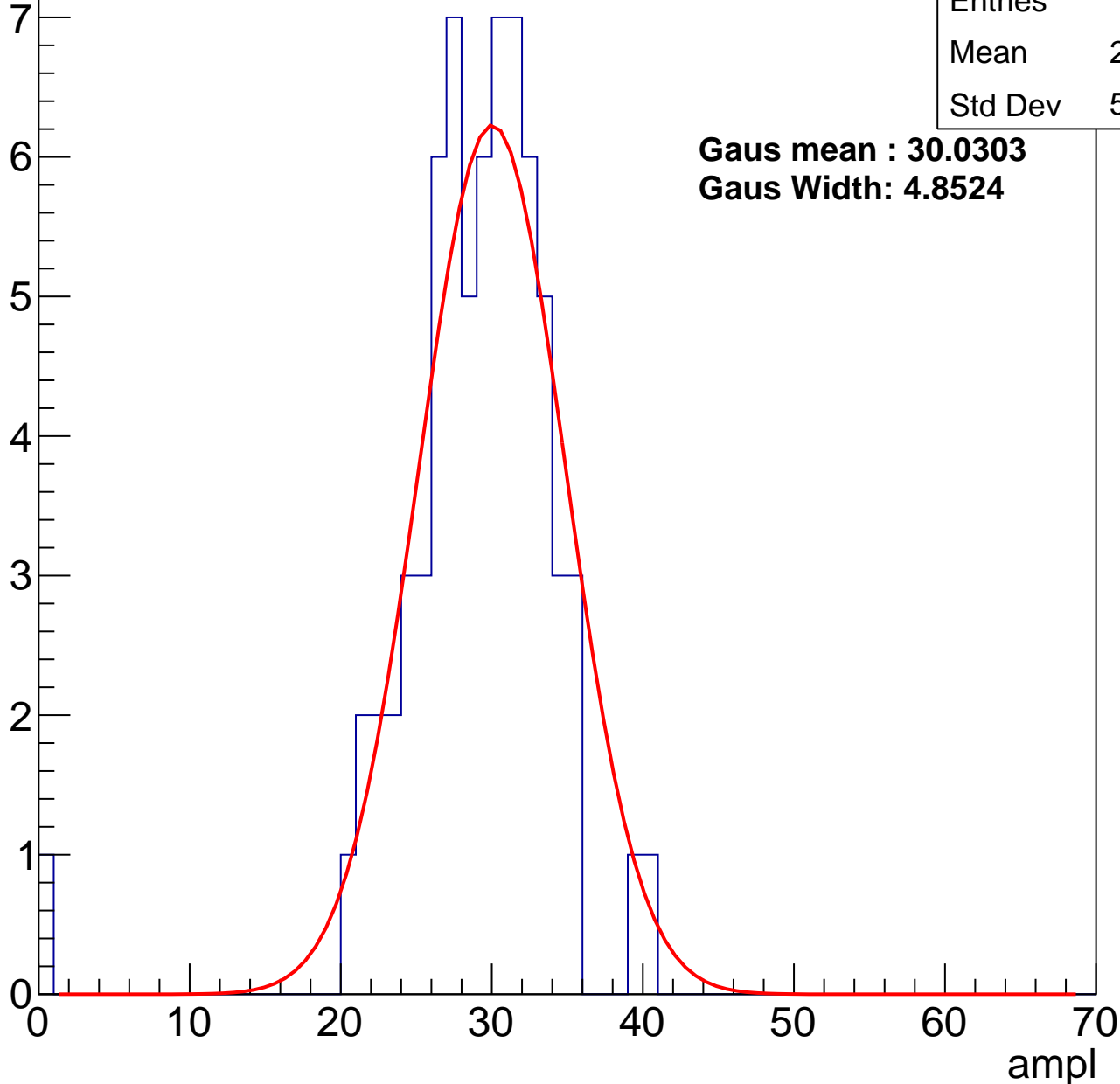
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	28.56
Std Dev	5.293

Gaus mean : 30.0303

Gaus Width: 4.8524

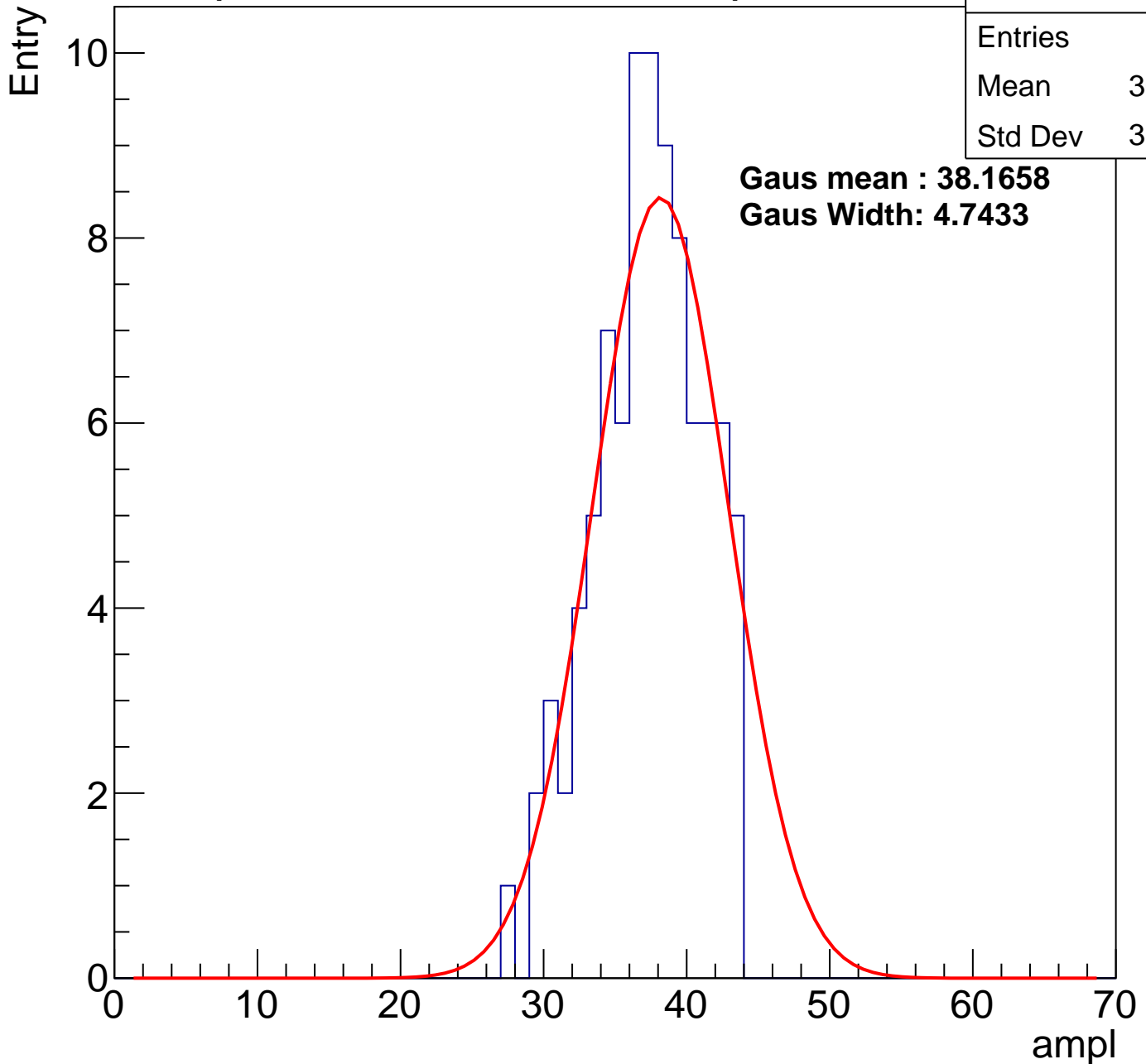


B0L102S, U12-ch91, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	90
Mean	36.83
Std Dev	3.716

Gaus mean : 38.1658
Gaus Width: 4.7433



B0L102S, U12-ch91, adc2

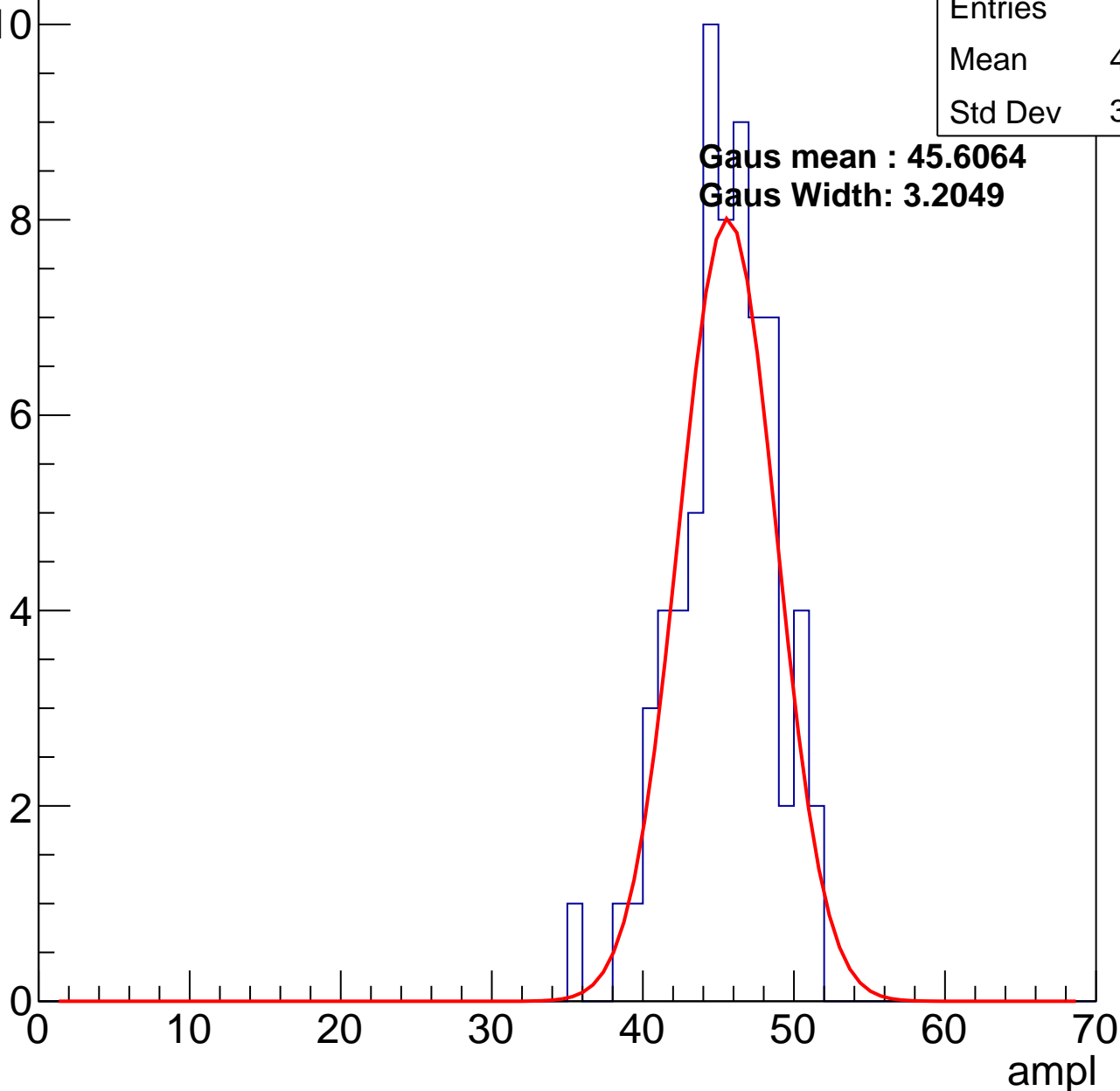
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	44.97
Std Dev	3.195

Gaus mean : 45.6064

Gaus Width: 3.2049

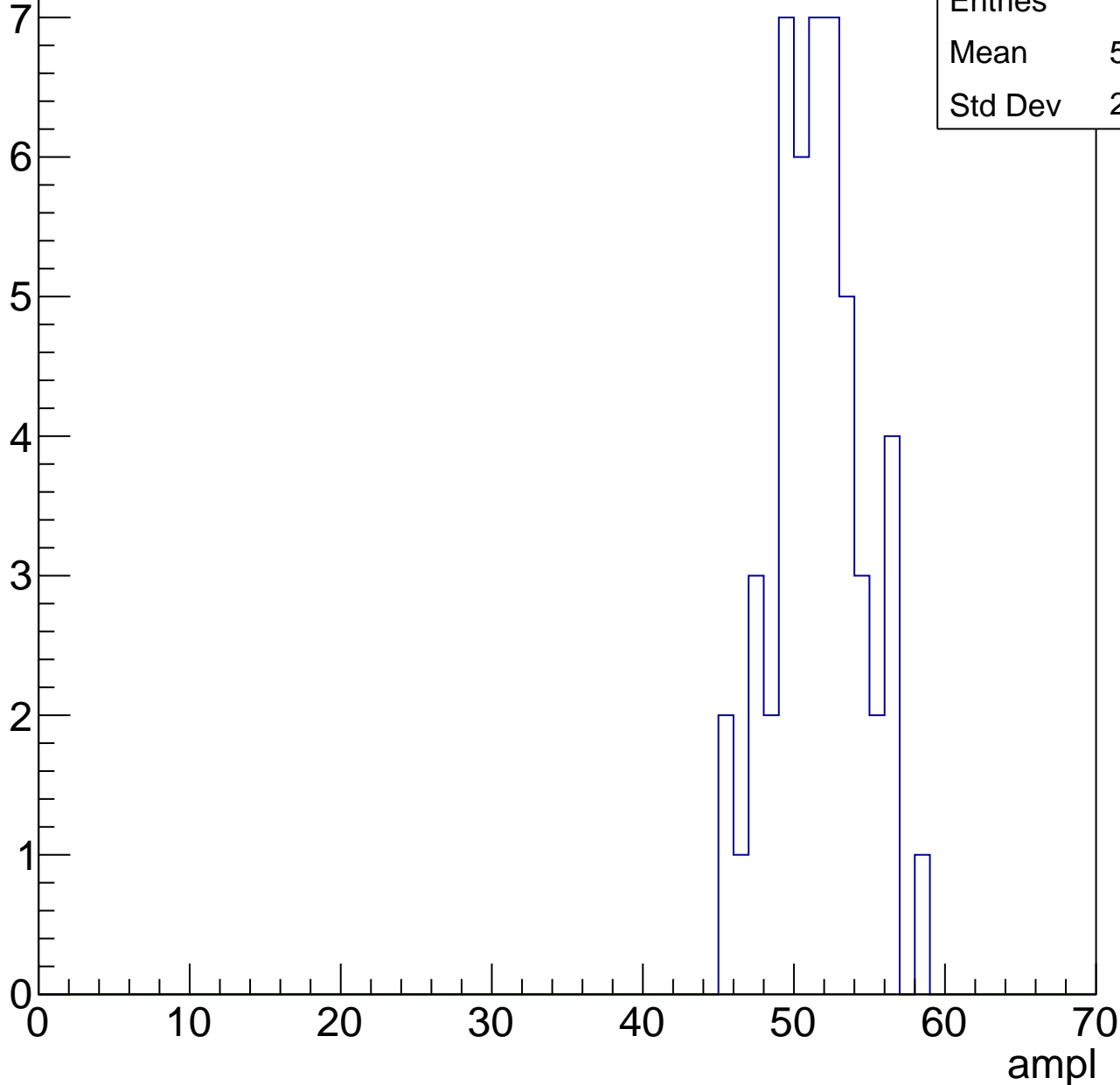


B0L102S, U12-ch91, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	51.12
Std Dev	2.937

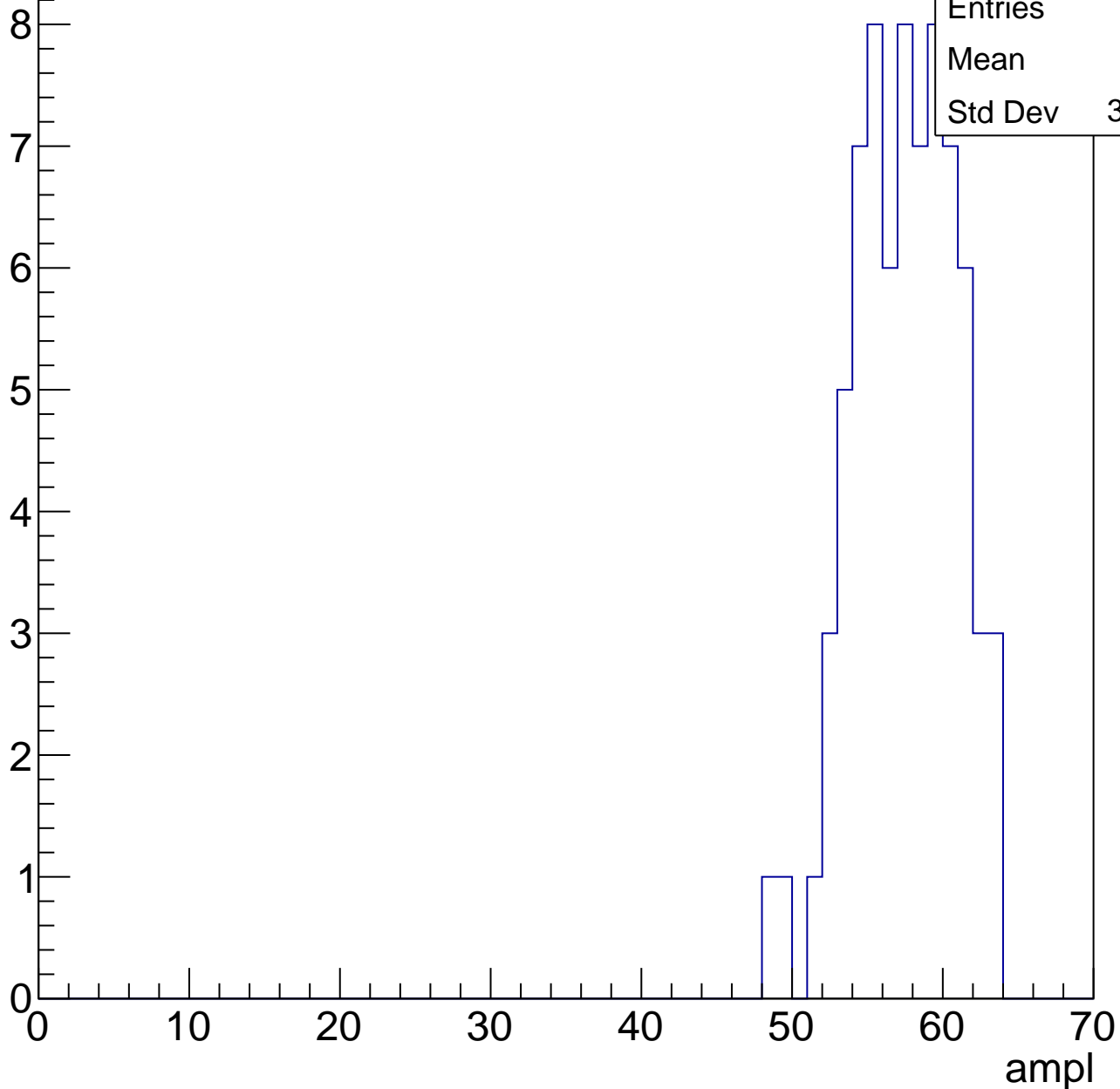


B0L102S, U12-ch91, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	57
Std Dev	3.325

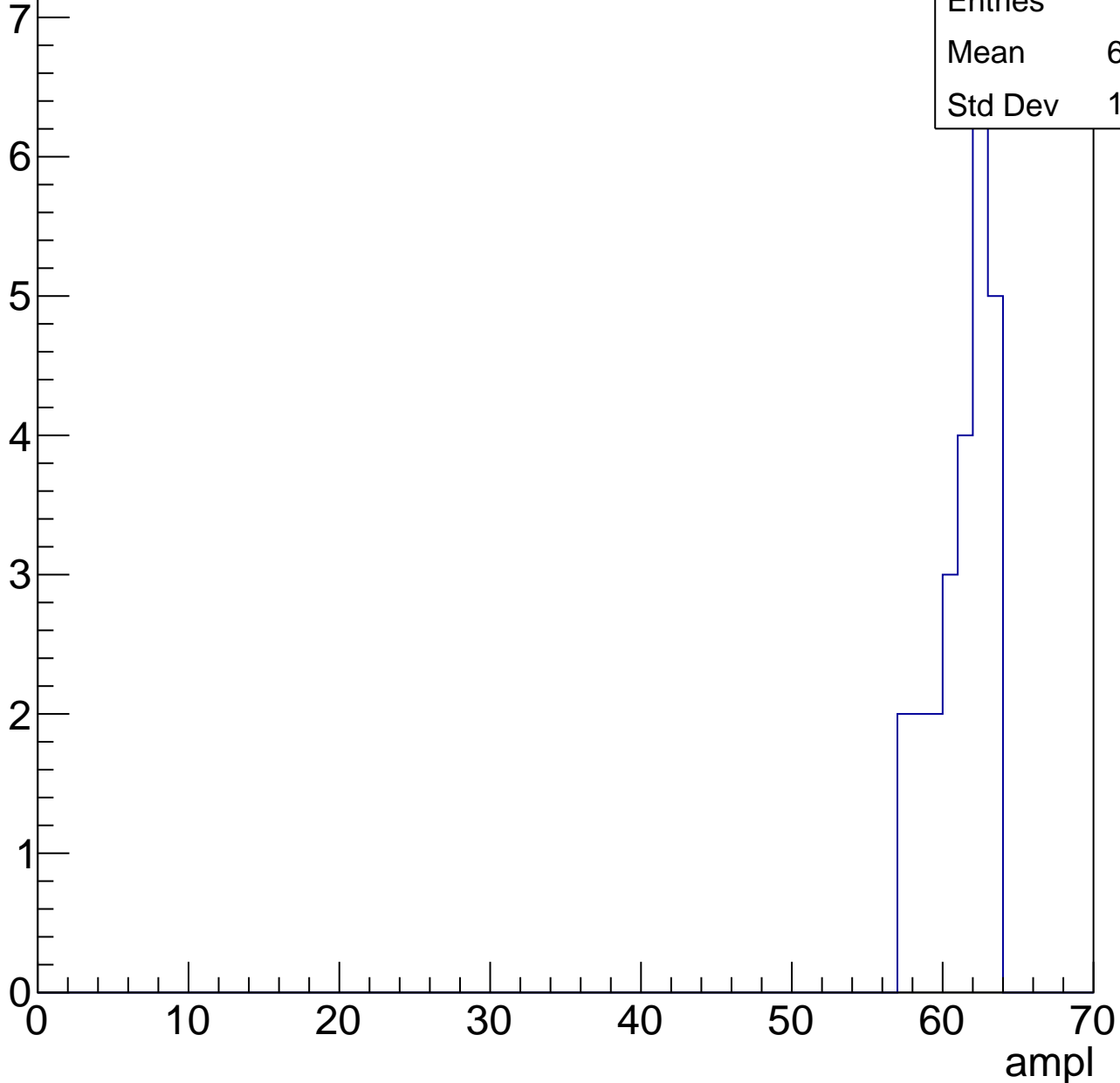


B0L102S, U12-ch91, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	25
Mean	60.84
Std Dev	1.869



B0L102S, U12-ch91, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch91, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch92, adc0

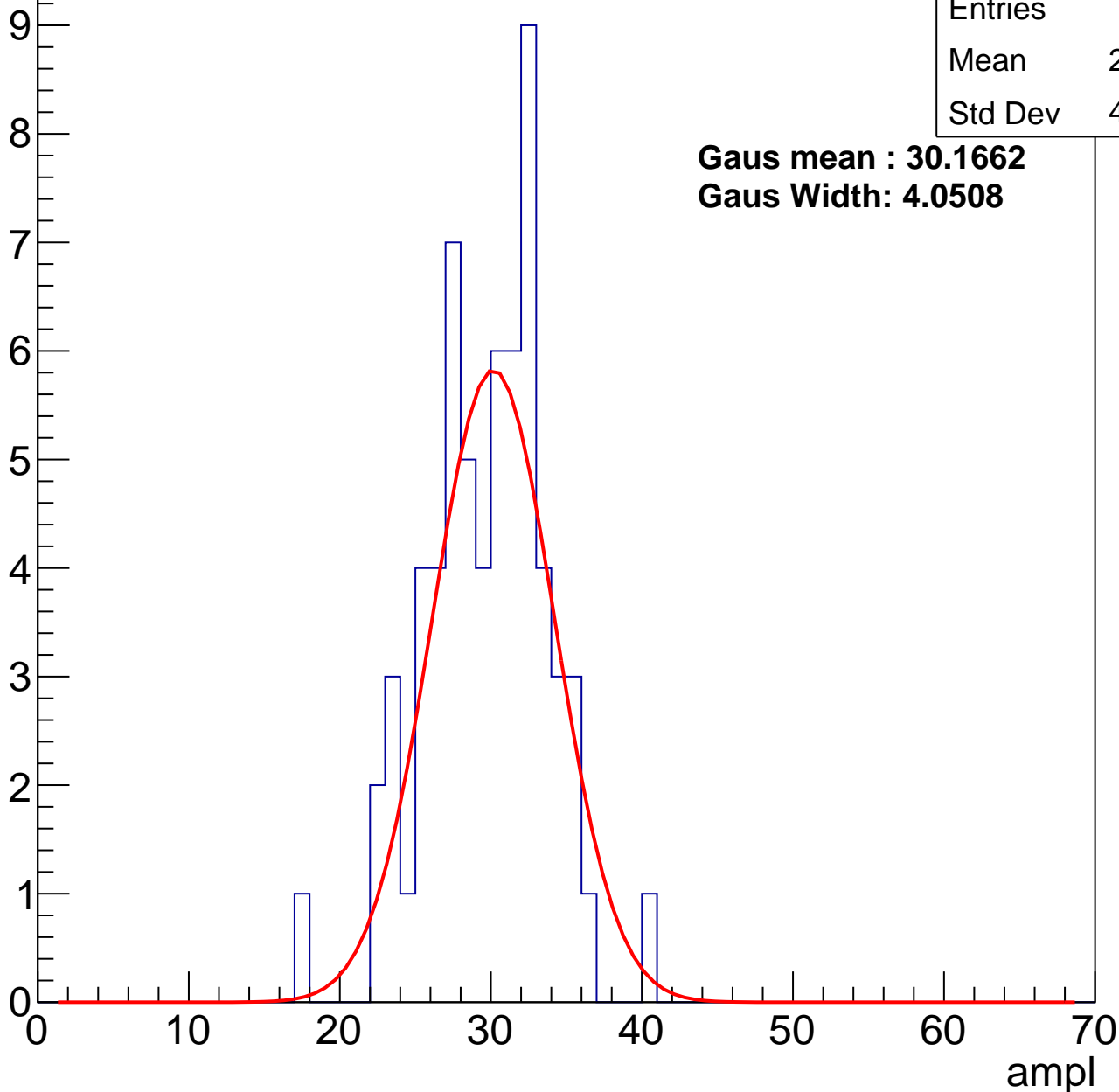
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.25
Std Dev	4.016

Gaus mean : 30.1662

Gaus Width: 4.0508



B0L102S, U12-ch92, adc1

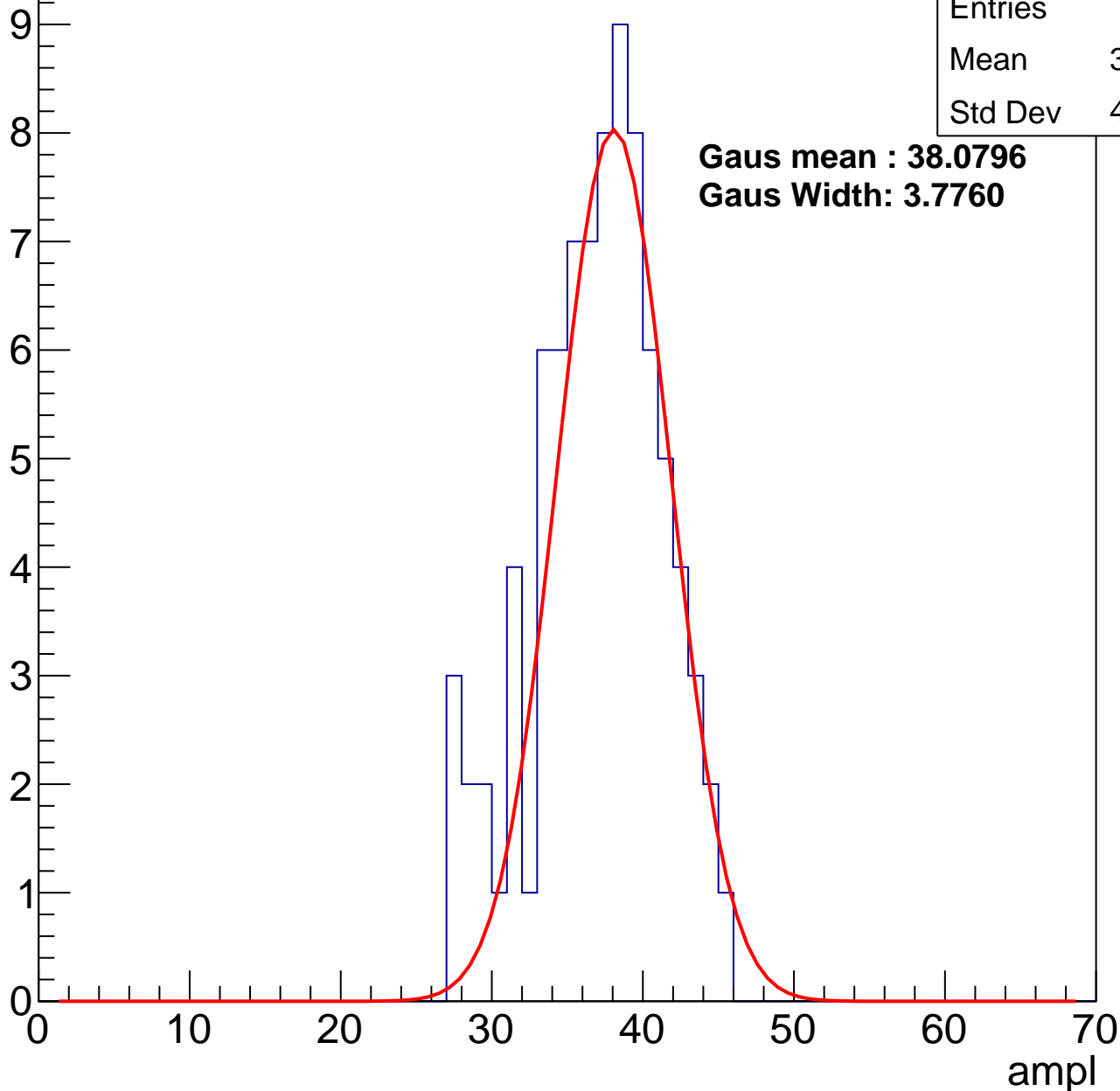
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	36.53
Std Dev	4.228

Gaus mean : 38.0796

Gaus Width: 3.7760

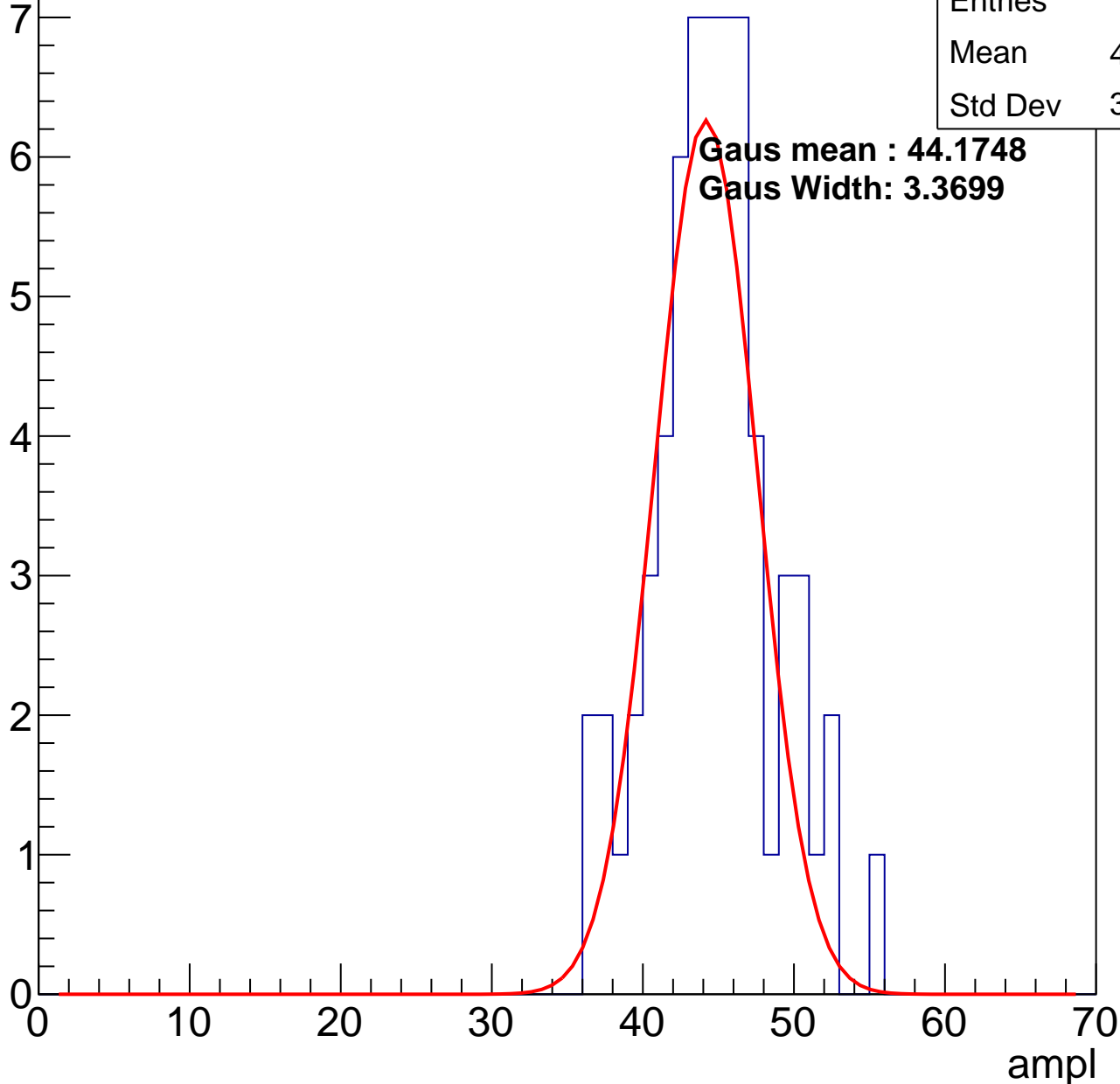


B0L102S, U12-ch92, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	44.24
Std Dev	3.967

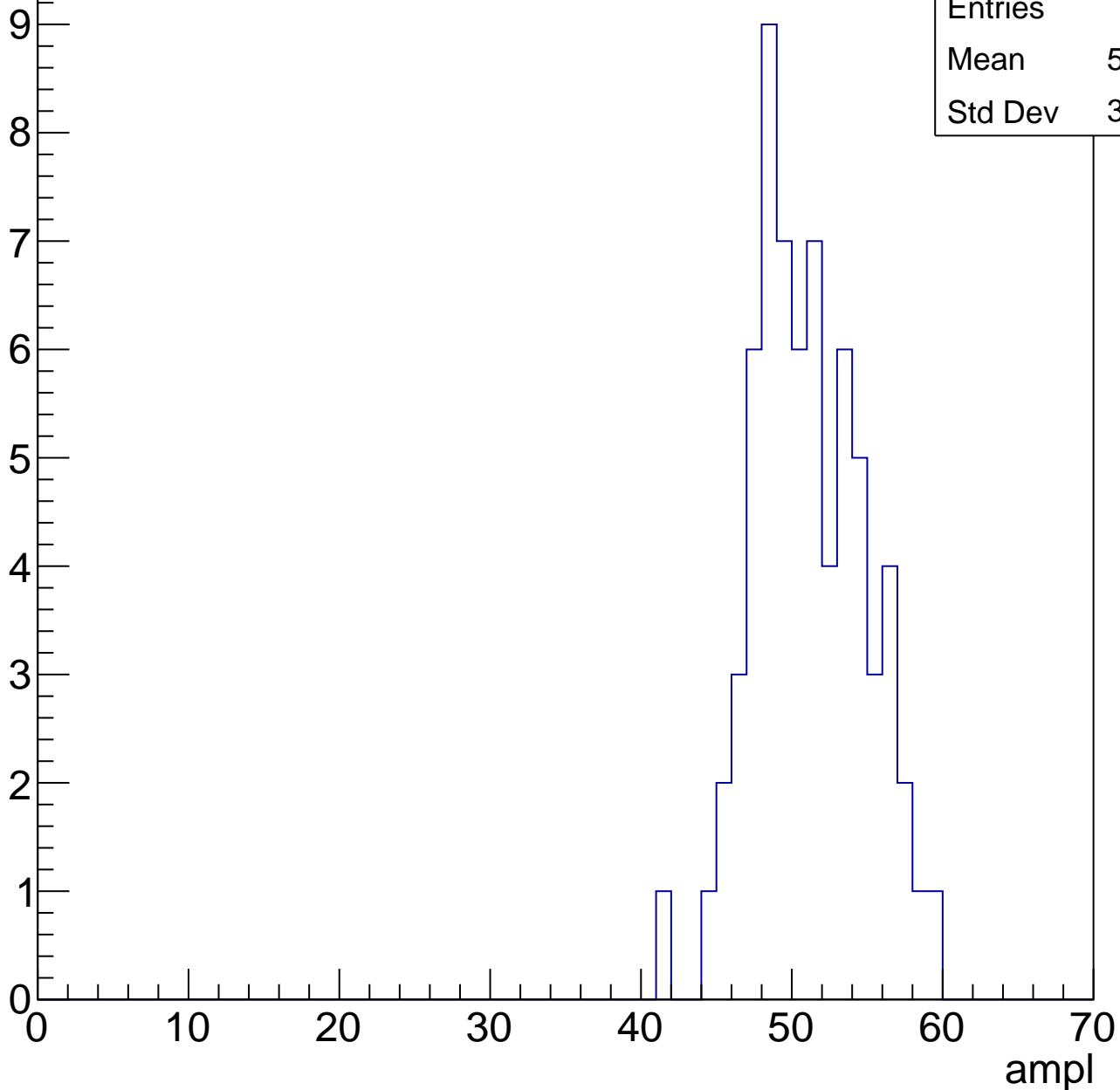


B0L102S, U12-ch92, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50.63
Std Dev	3.646

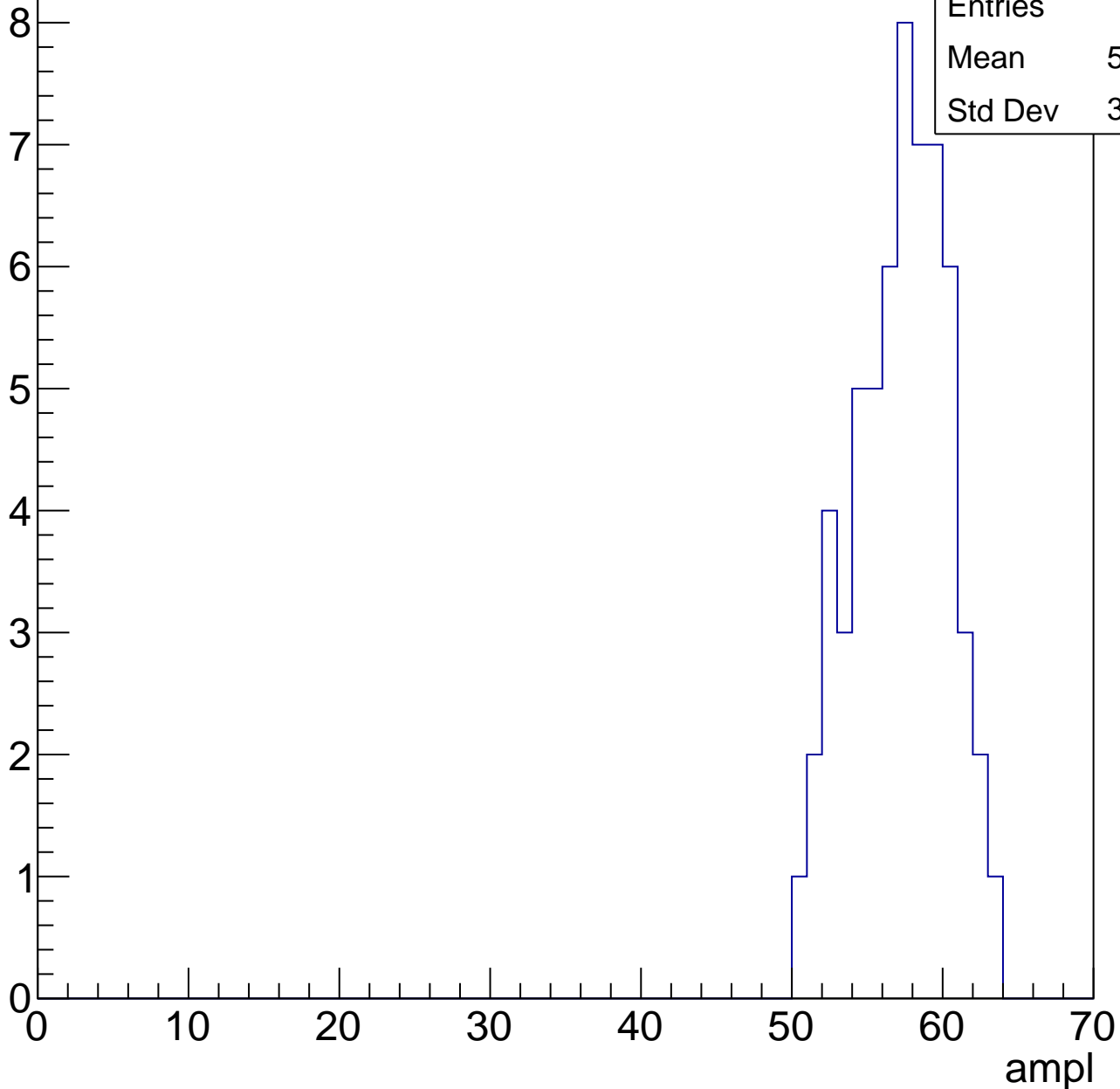


B0L102S, U12-ch92, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	56.75
Std Dev	3.053

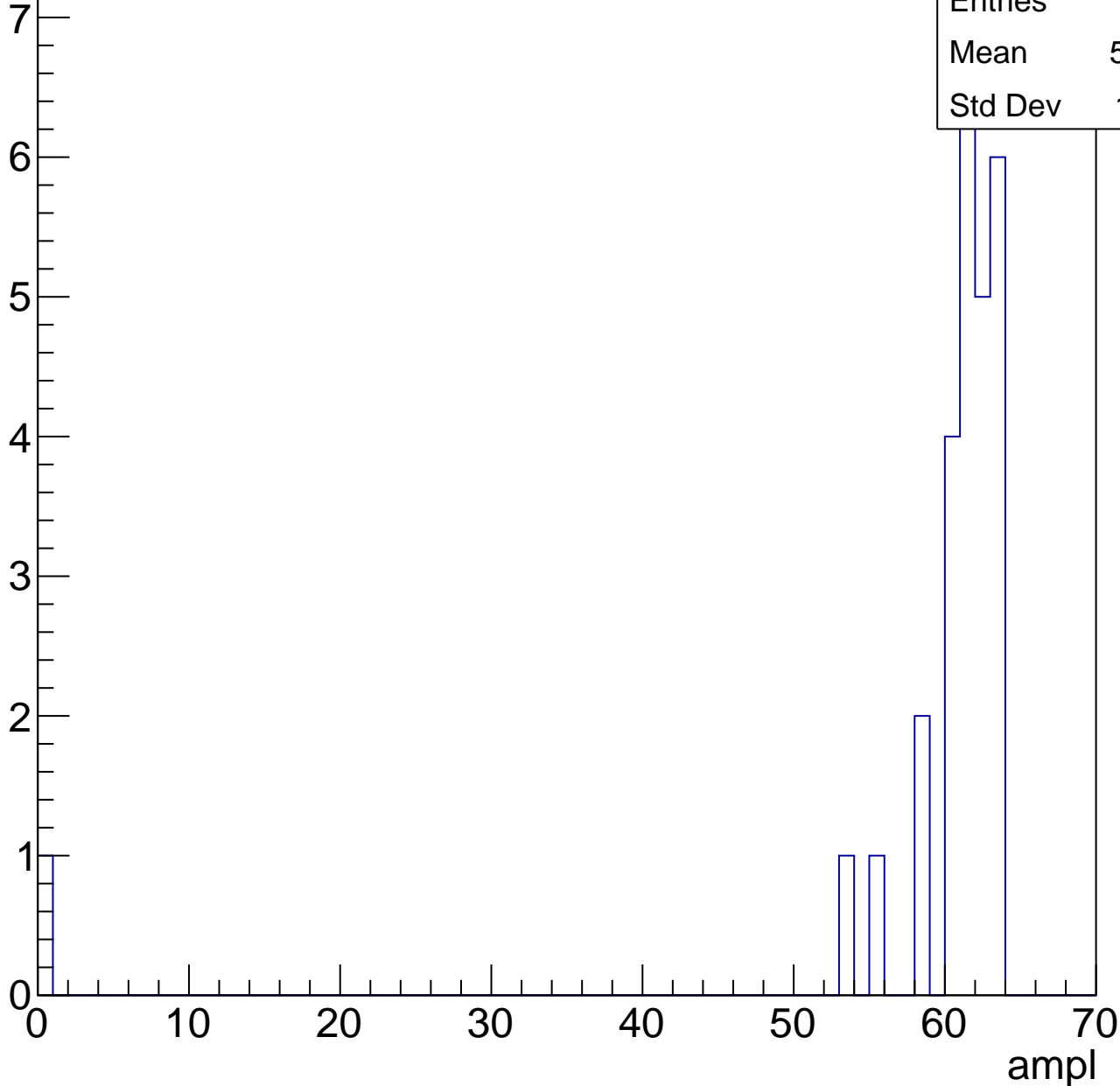


B0L102S, U12-ch92, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

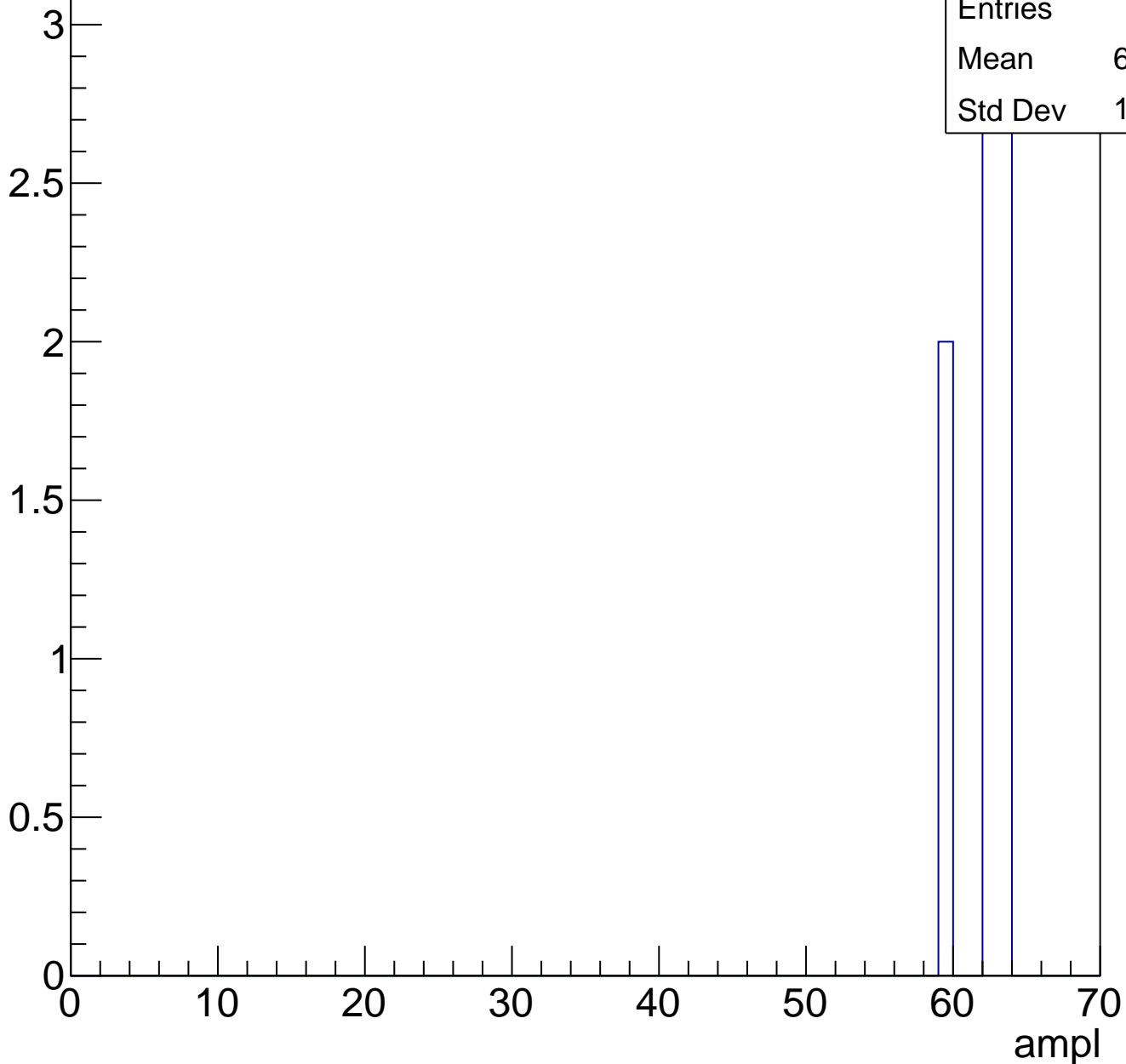
Entries	27
Mean	58.48
Std Dev	11.71



B0L102S, U12-ch92, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch92, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch93, adc0

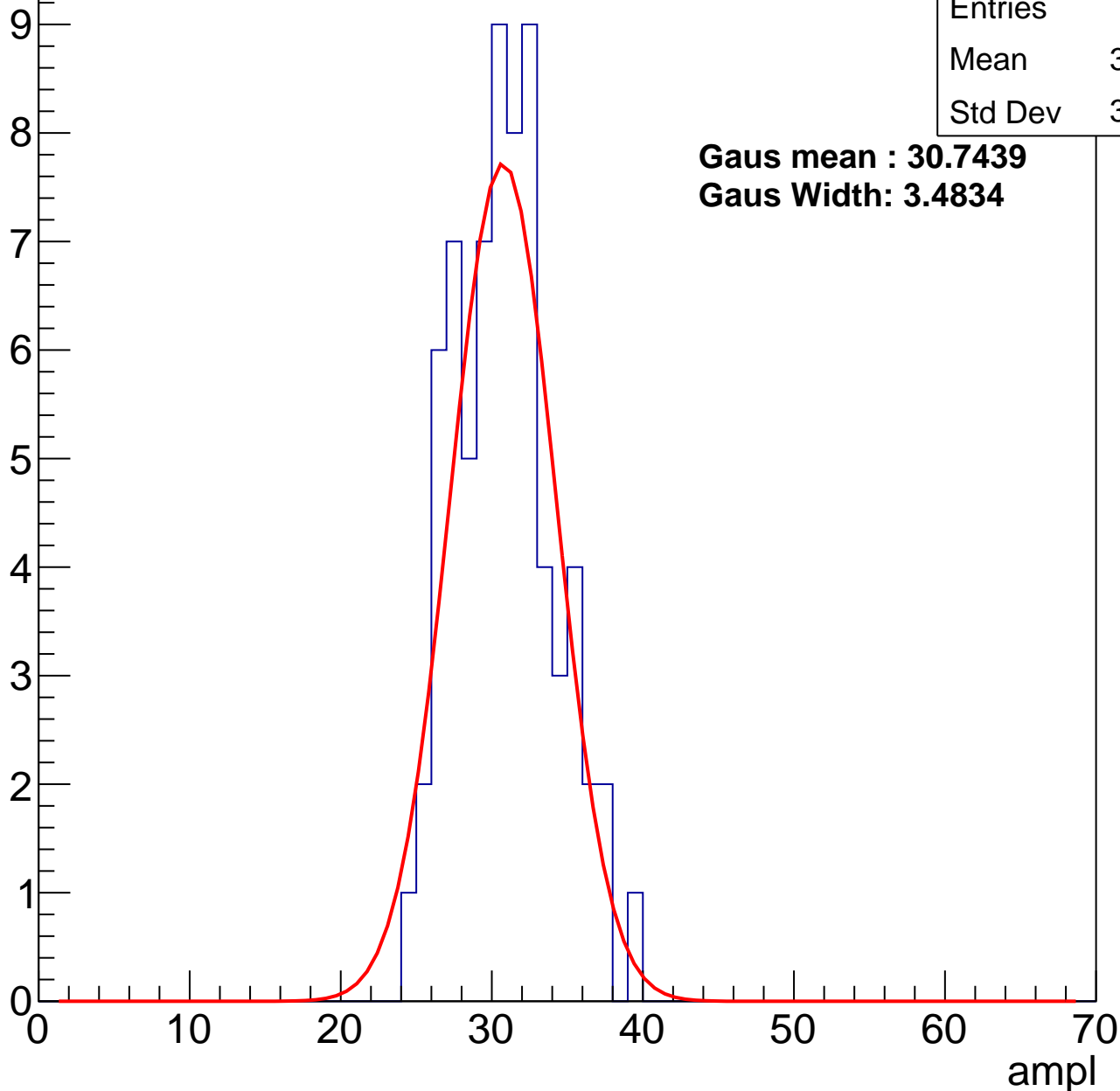
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	30.39
Std Dev	3.253

Gaus mean : 30.7439

Gaus Width: 3.4834



B0L102S, U12-ch93, adc1

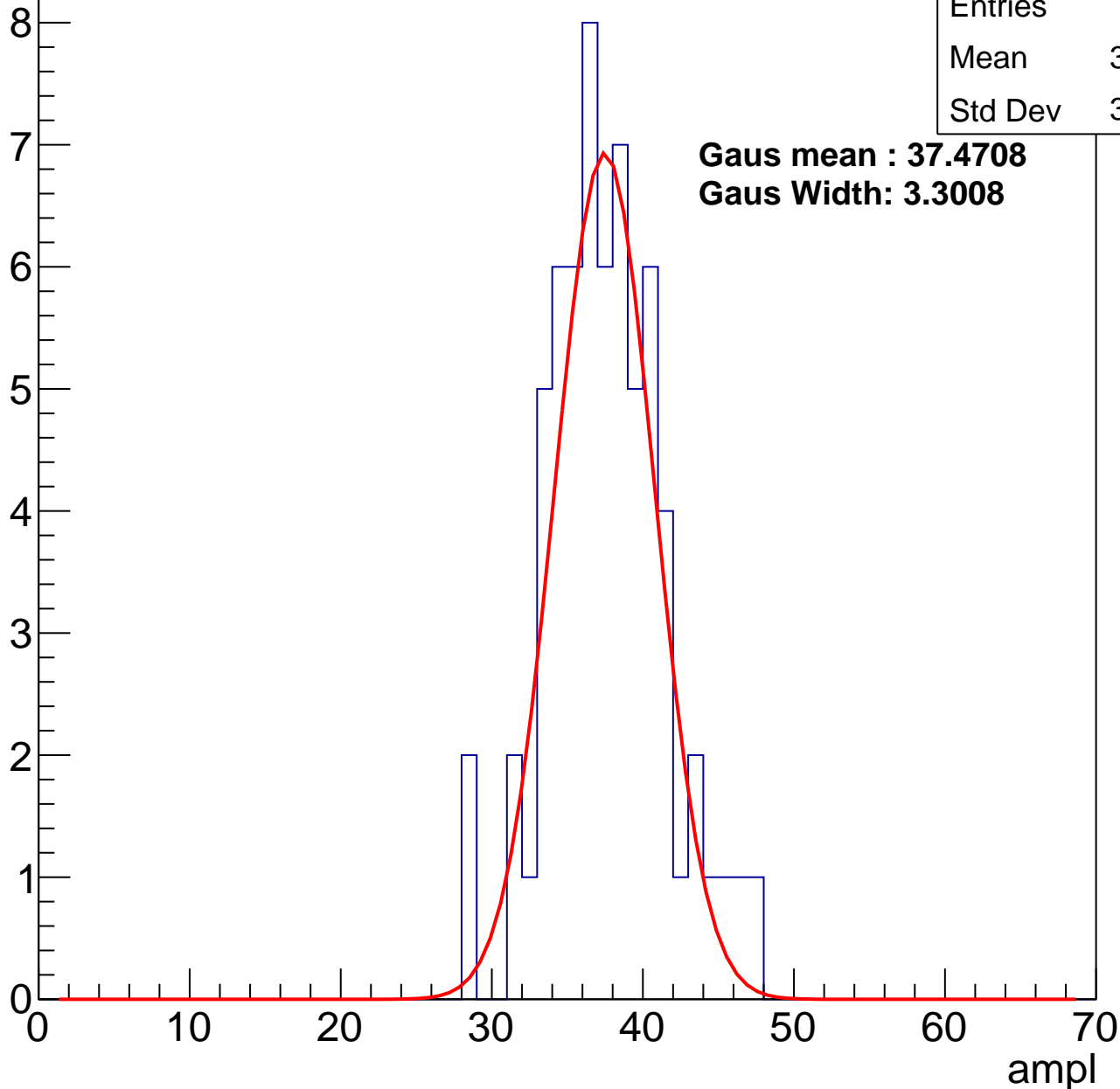
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.14
Std Dev	3.843

Gaus mean : 37.4708

Gaus Width: 3.3008



B0L102S, U12-ch93, adc2

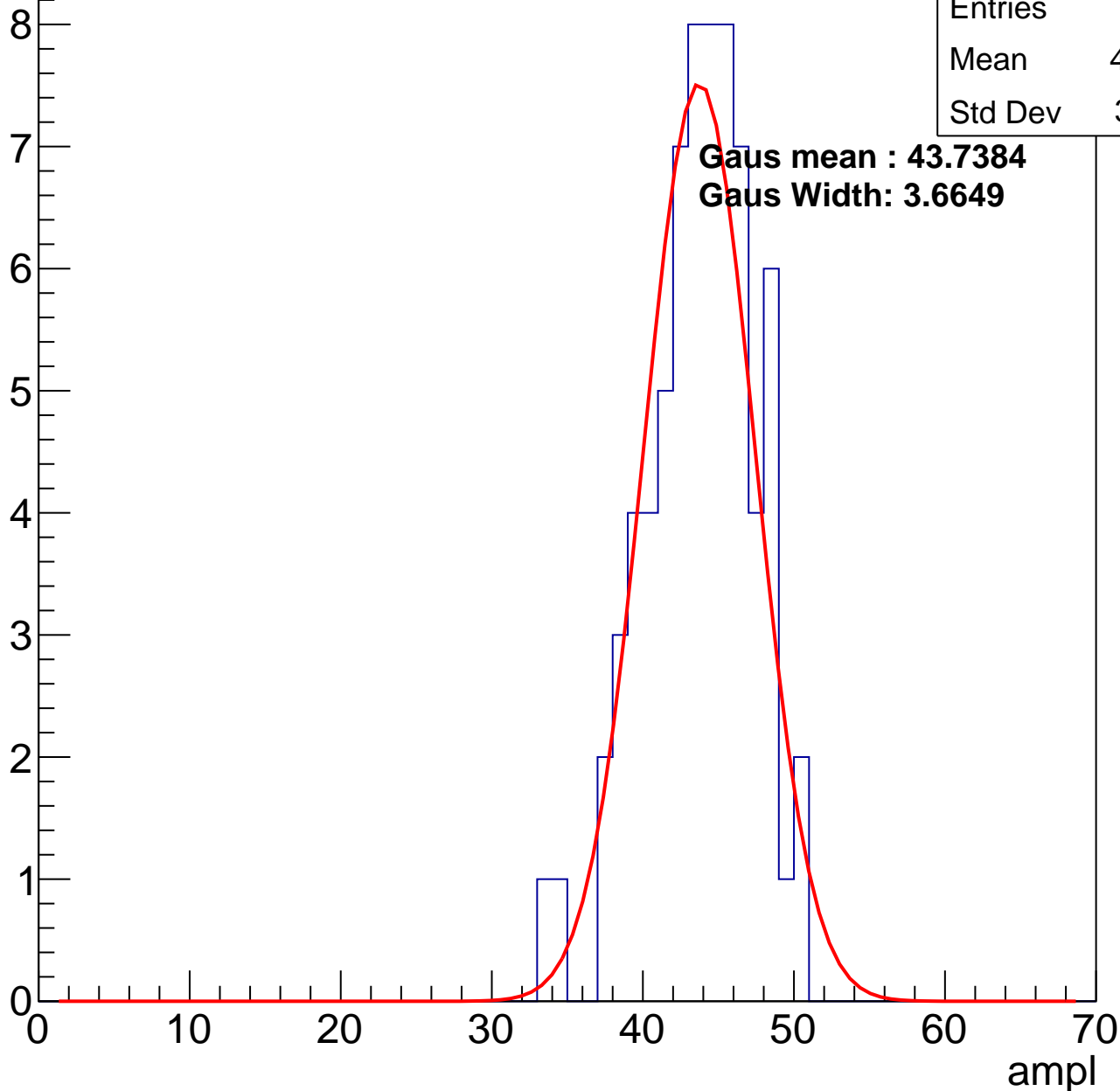
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	43.28
Std Dev	3.561

Gaus mean : 43.7384

Gaus Width: 3.6649

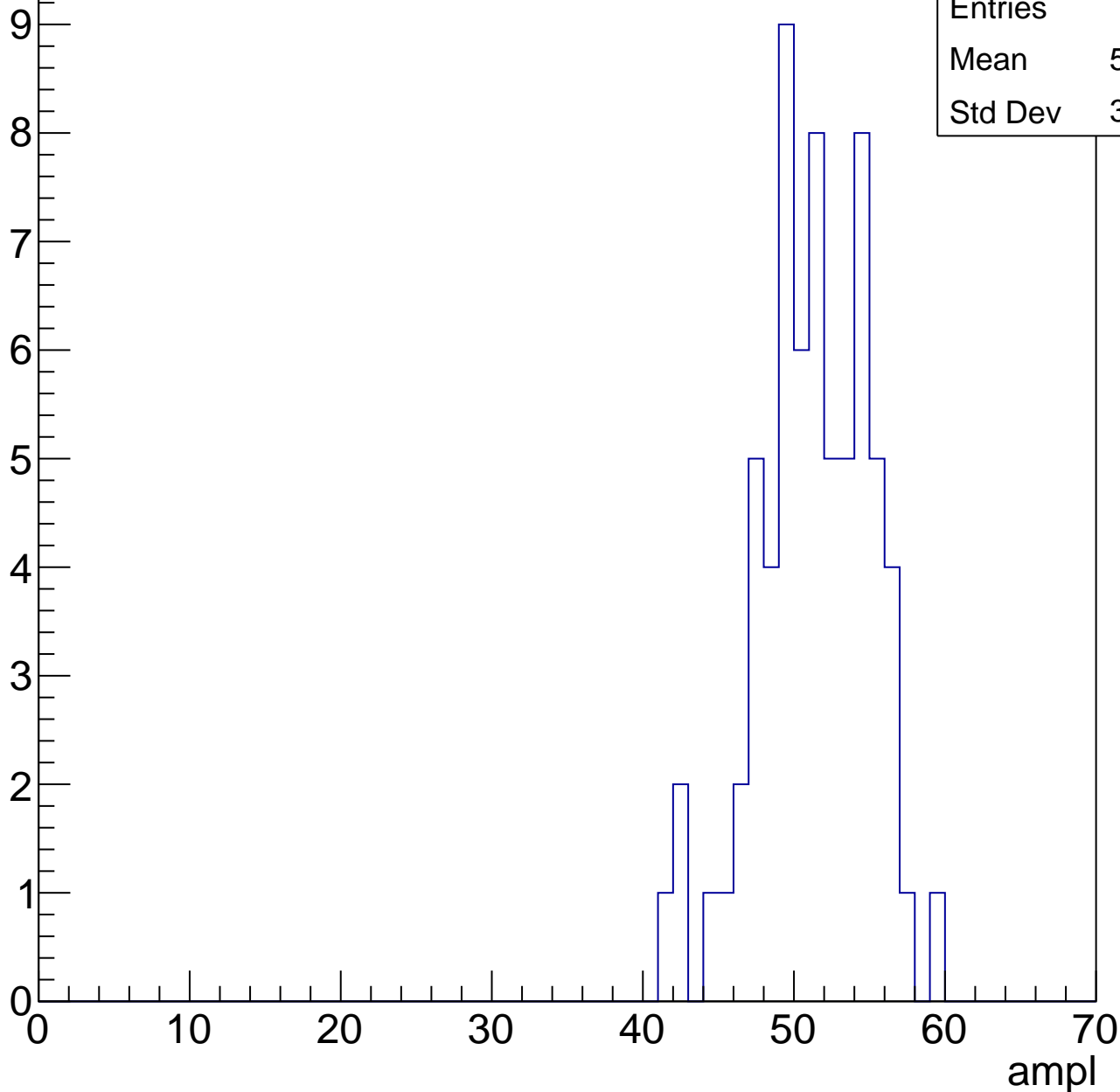


B0L102S, U12-ch93, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50.79
Std Dev	3.696

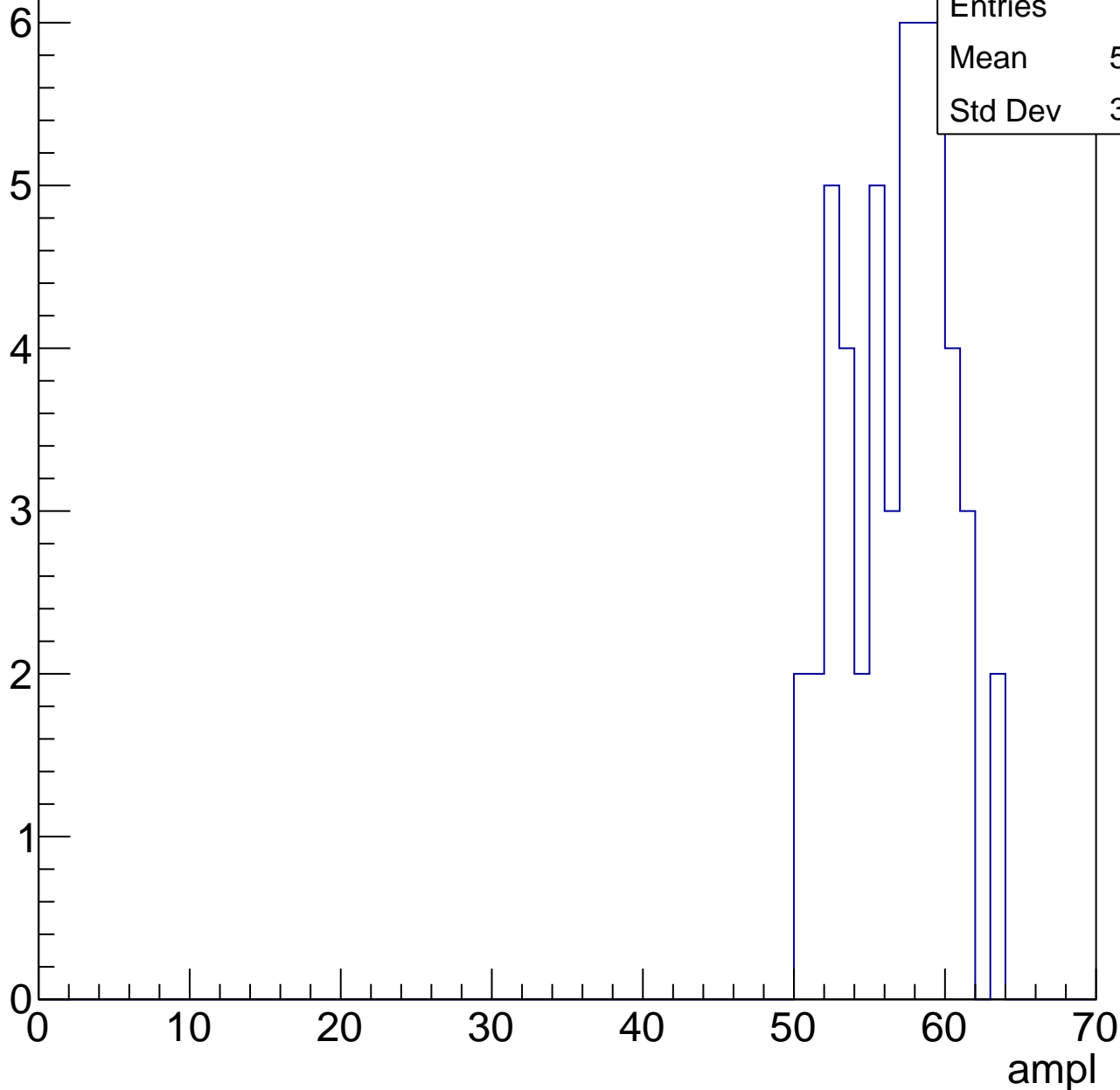


B0L102S, U12-ch93, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	56.36
Std Dev	3.363

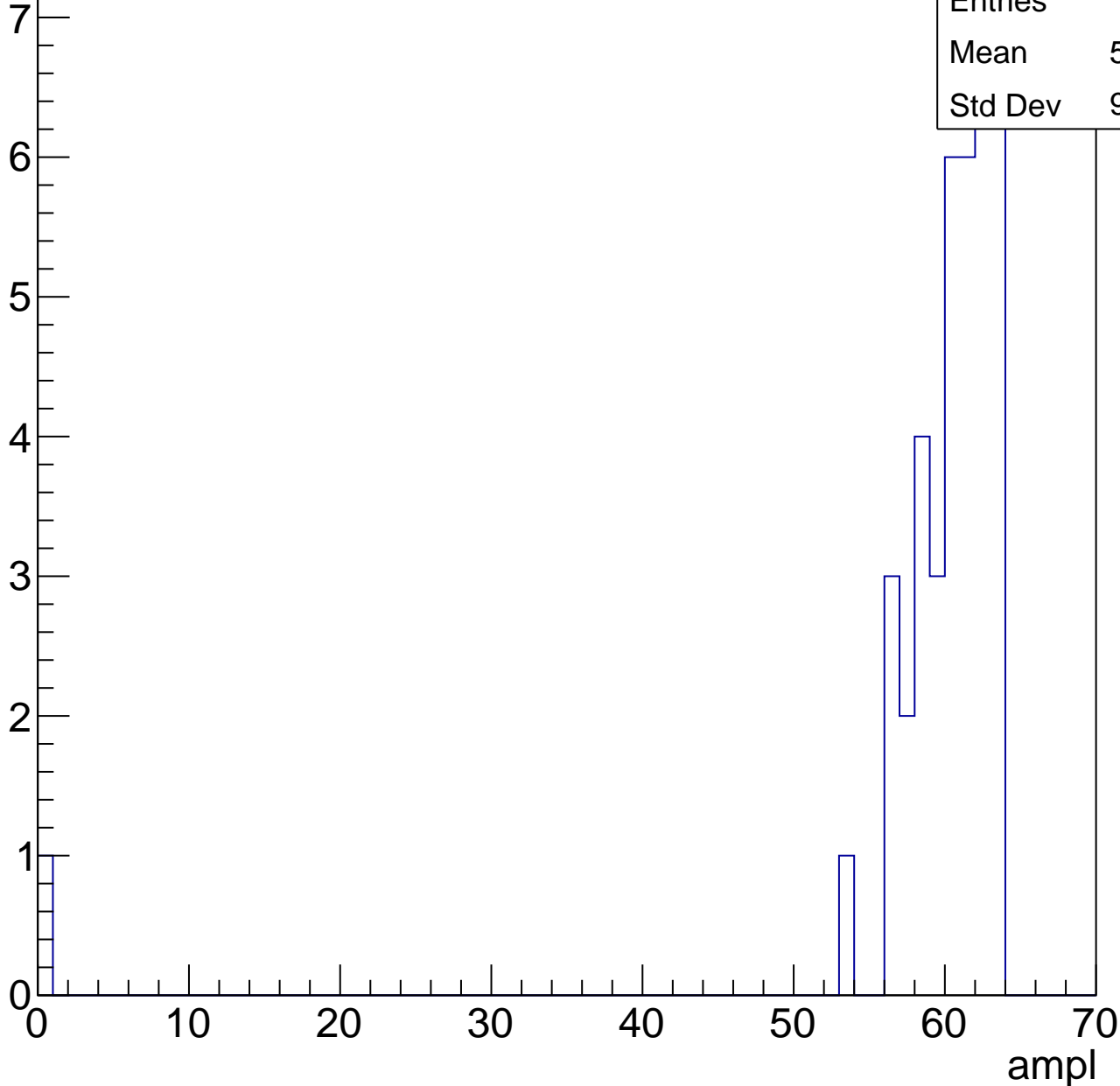


B0L102S, U12-ch93, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

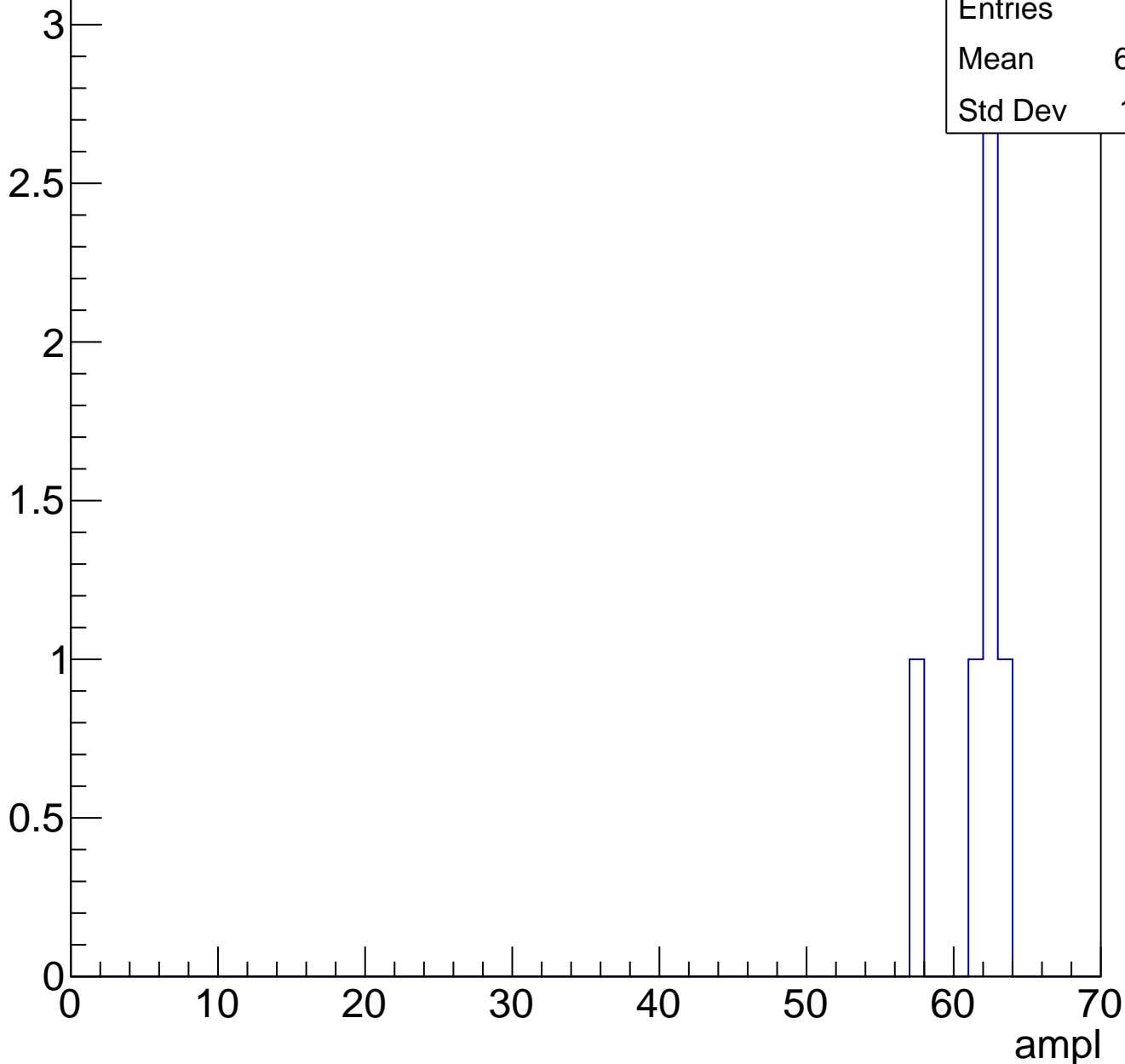
Entries	40
Mean	58.62
Std Dev	9.689



B0L102S, U12-ch93, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch93, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch94, adc0

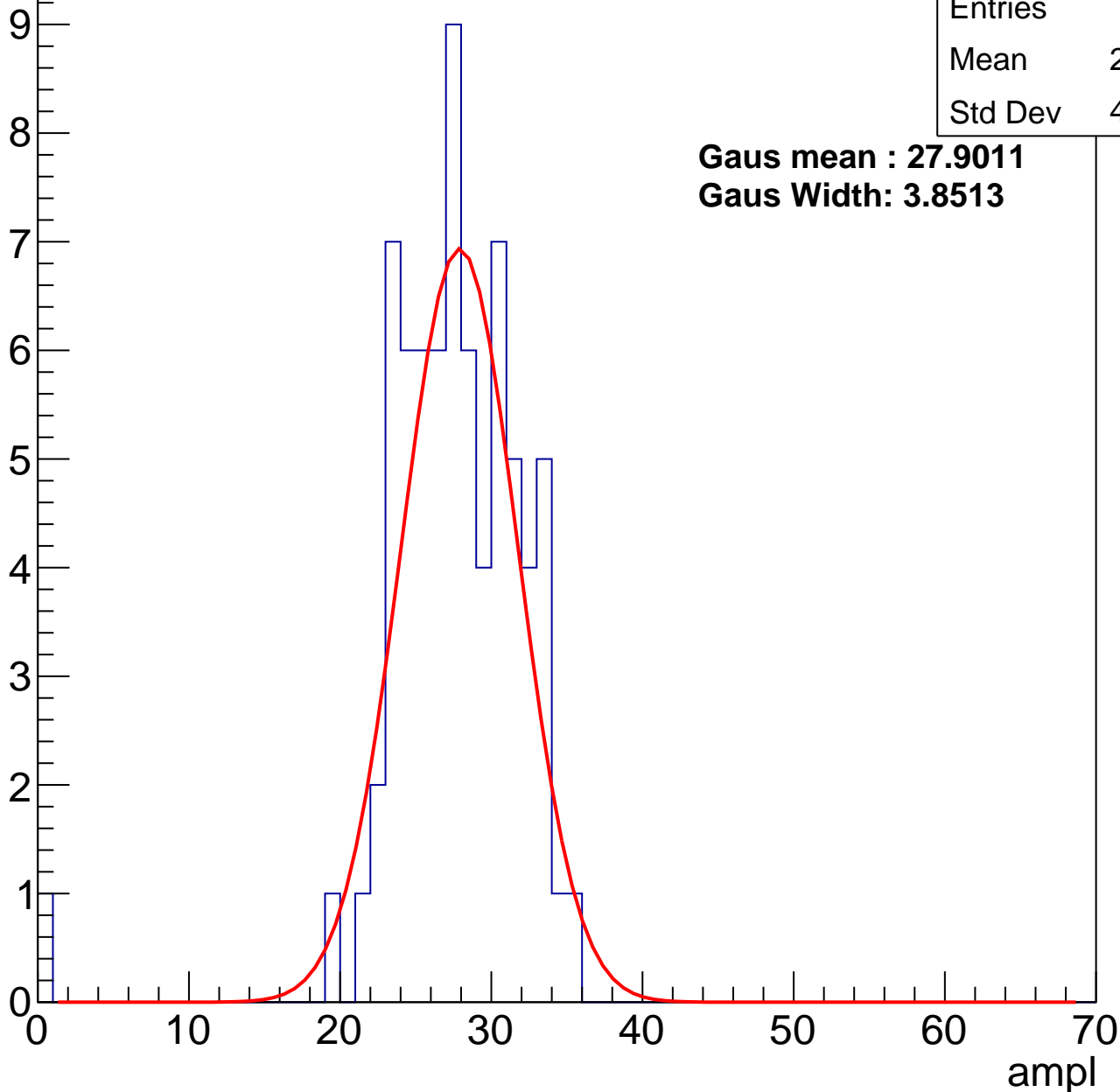
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	27.07
Std Dev	4.762

Gaus mean : 27.9011

Gaus Width: 3.8513



B0L102S, U12-ch94, adc1

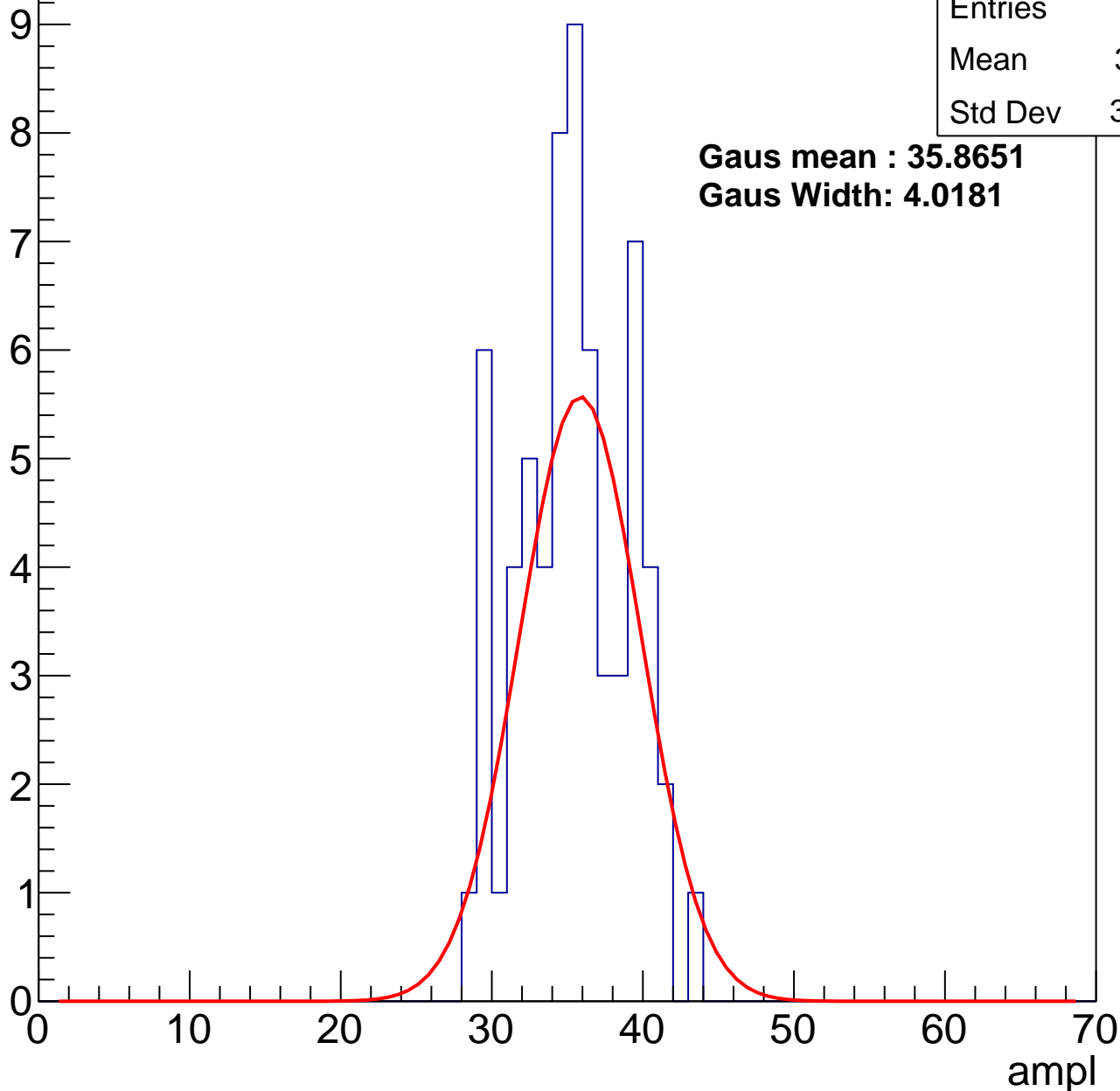
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	34.91
Std Dev	3.569

Gaus mean : 35.8651

Gaus Width: 4.0181



B0L102S, U12-ch94, adc2

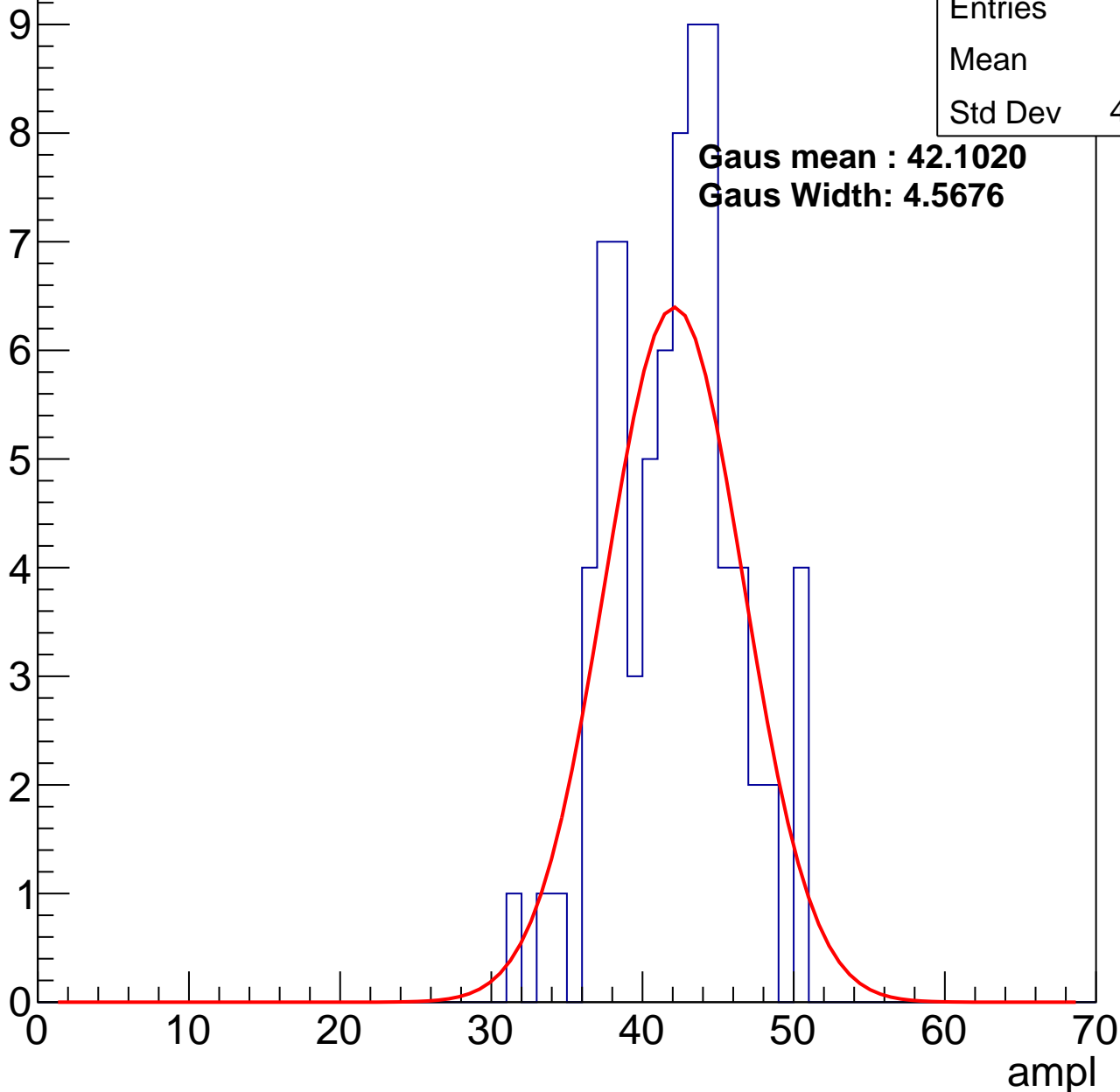
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	41.6
Std Dev	4.046

Gaus mean : 42.1020

Gaus Width: 4.5676

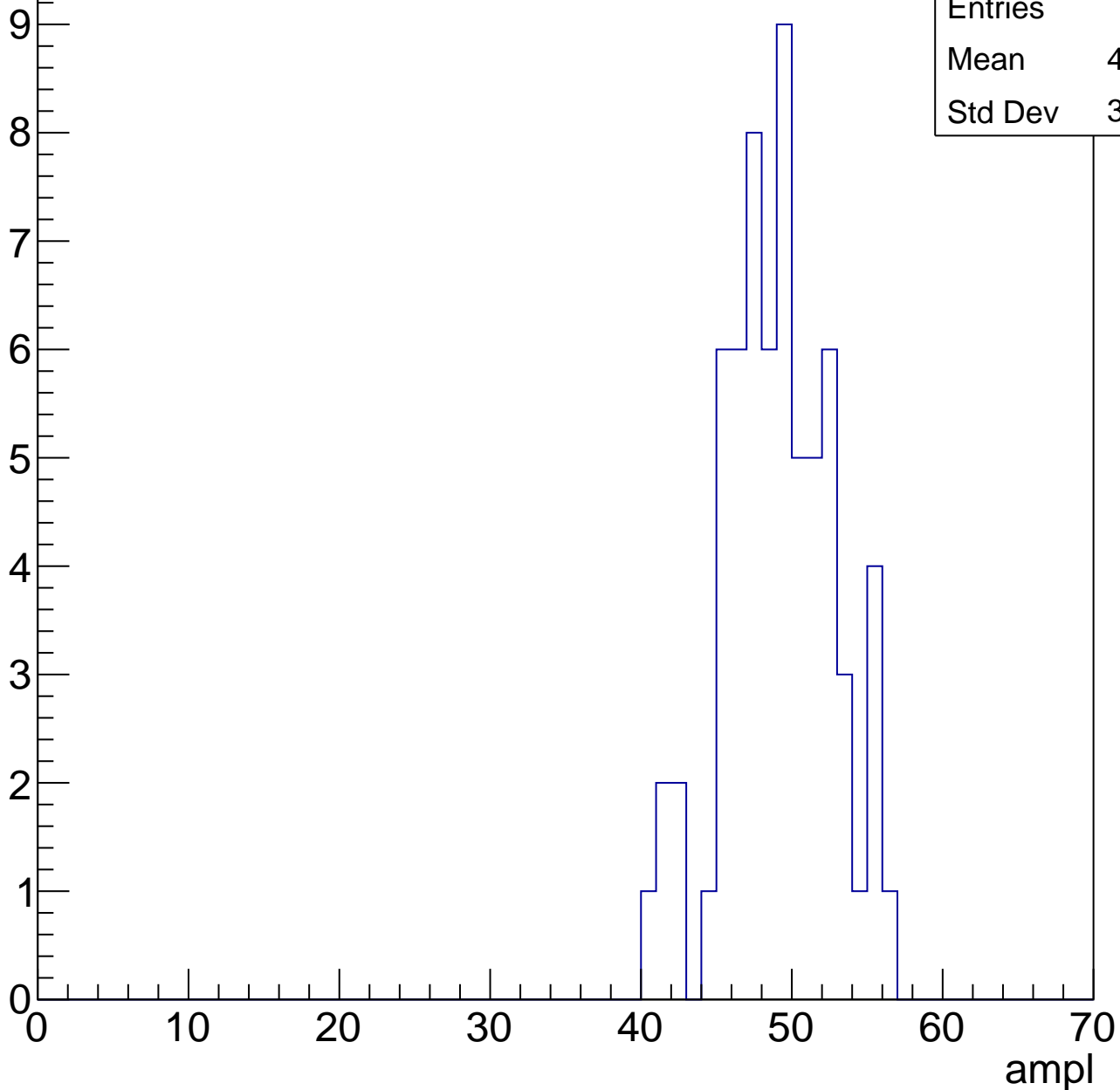


B0L102S, U12-ch94, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

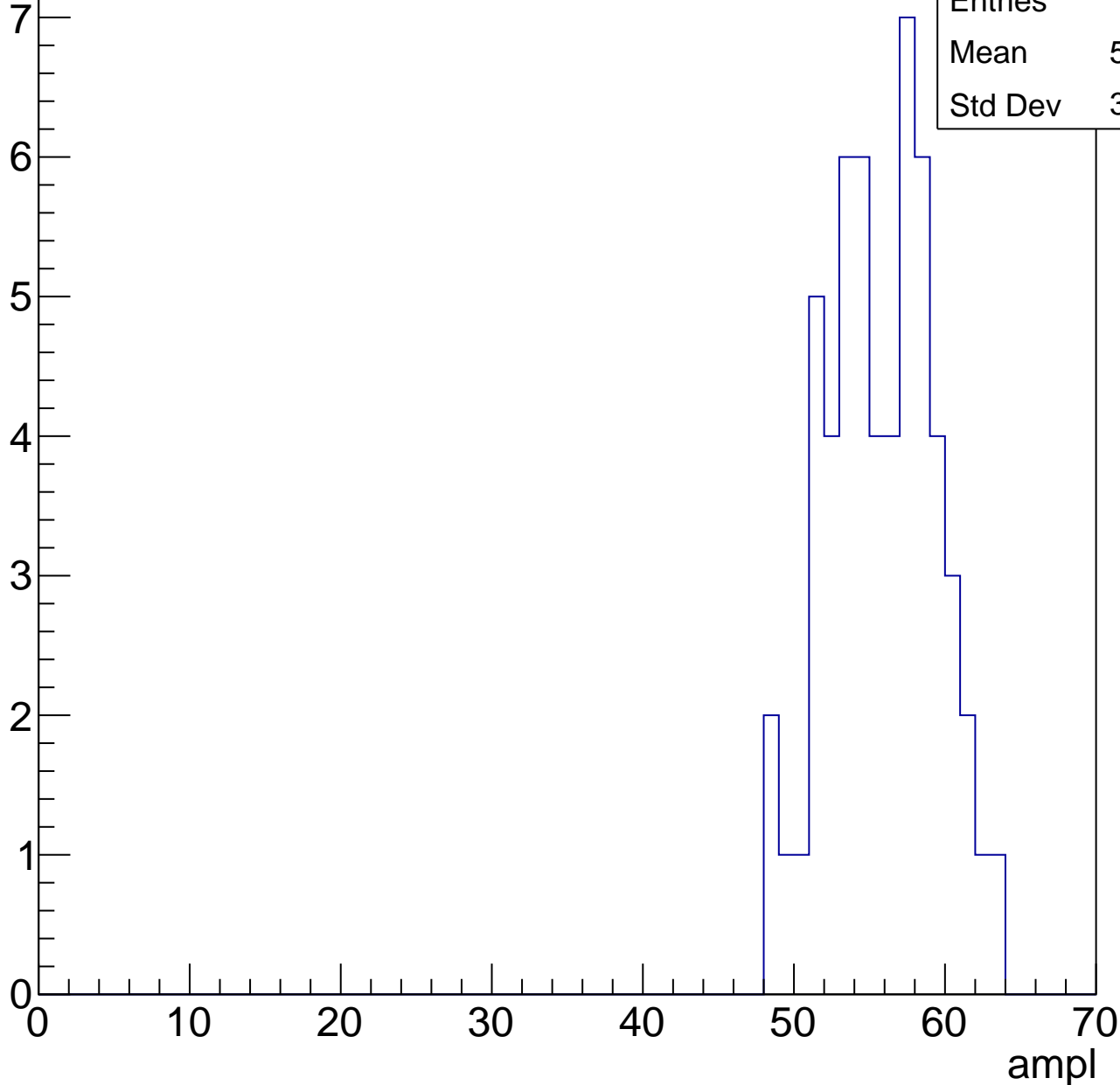
Entries	66
Mean	48.59
Std Dev	3.597



B0L102S, U12-ch94, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch94, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	57.79
Std Dev	8.939

ampl

0

10

20

30

40

50

60

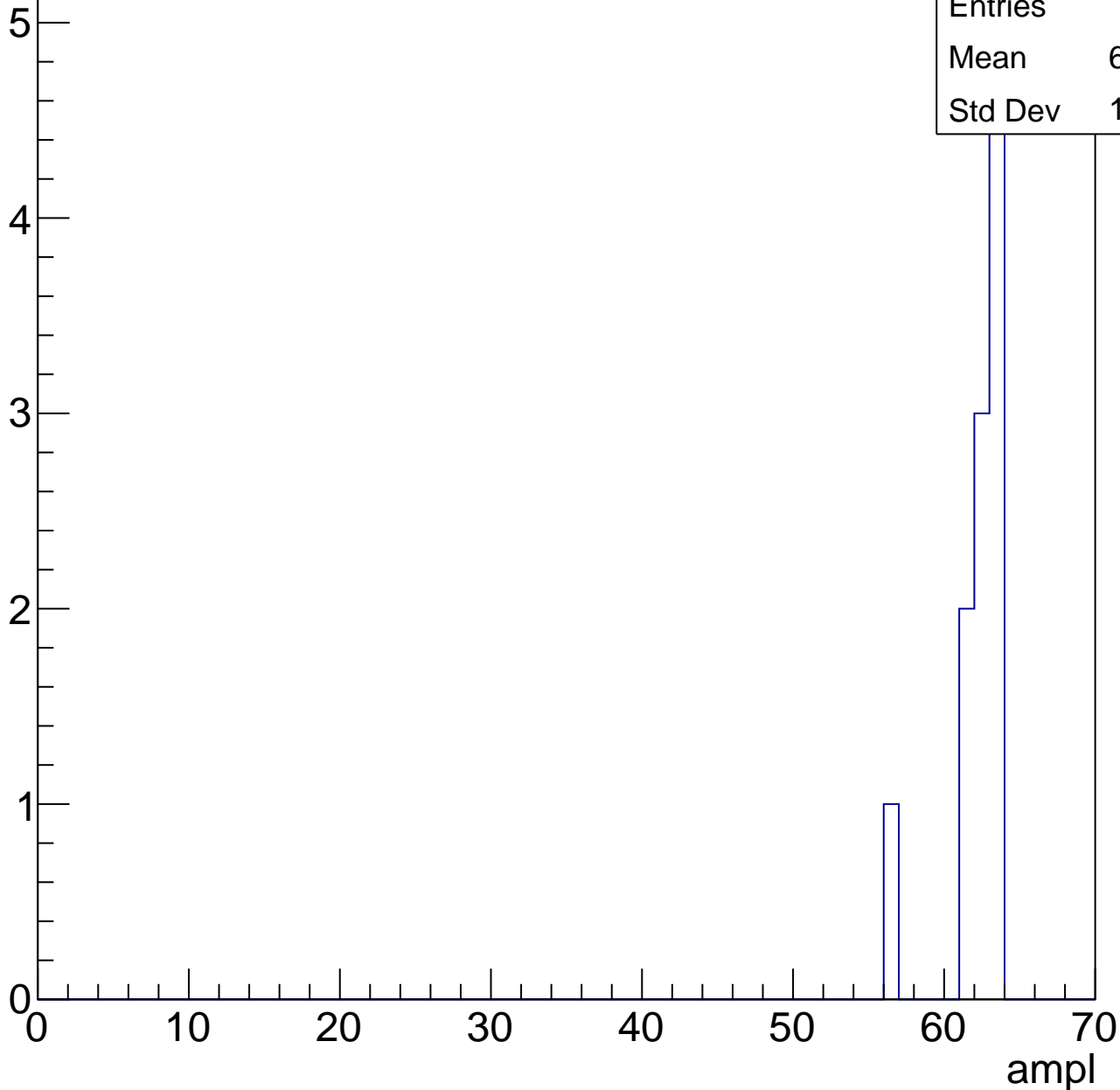
70

B0L102S, U12-ch94, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.73
Std Dev	1.958



B0L102S, U12-ch94, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch95, adc0

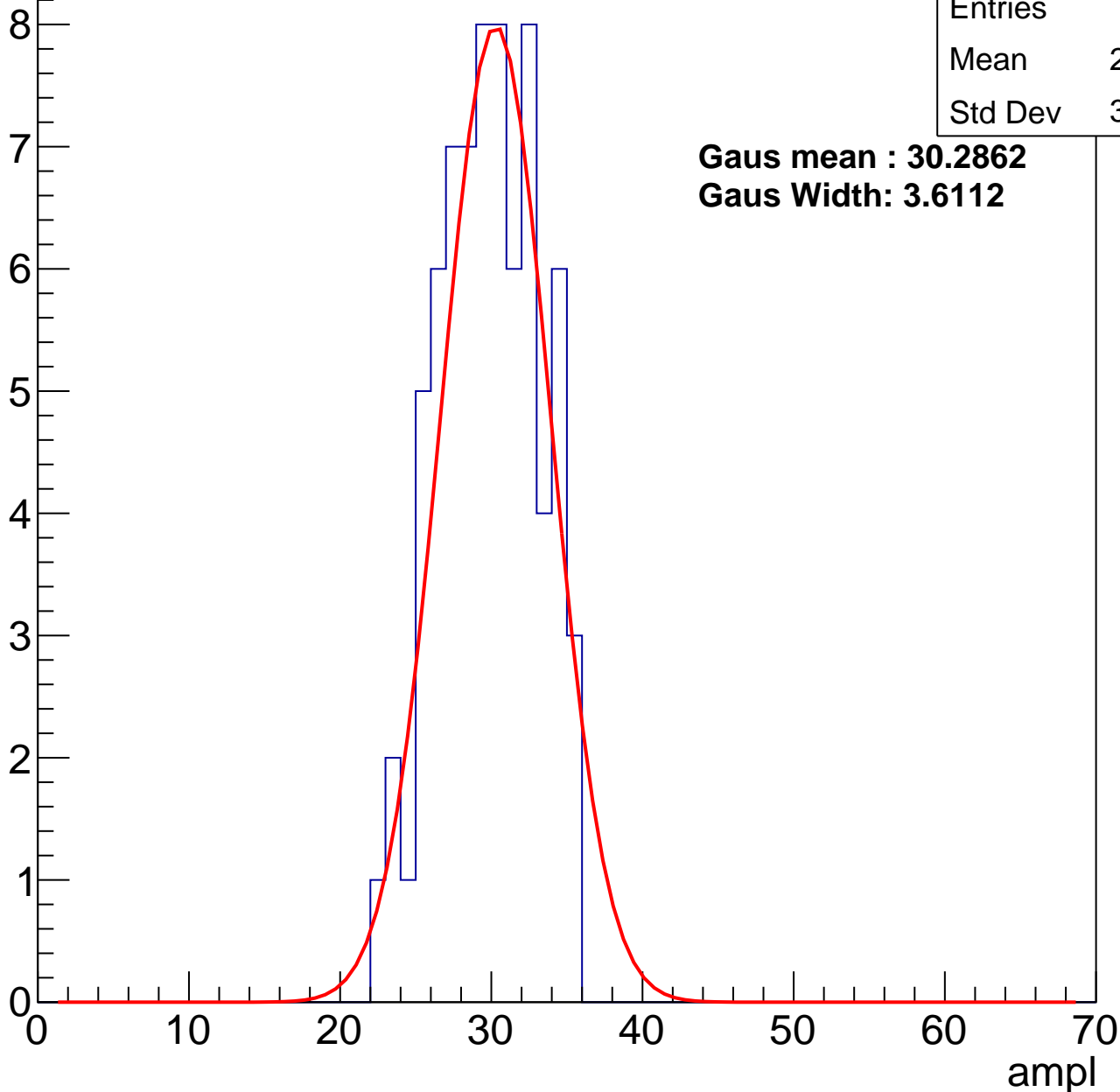
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	29.35
Std Dev	3.189

Gaus mean : 30.2862

Gaus Width: 3.6112



B0L102S, U12-ch95, adc1

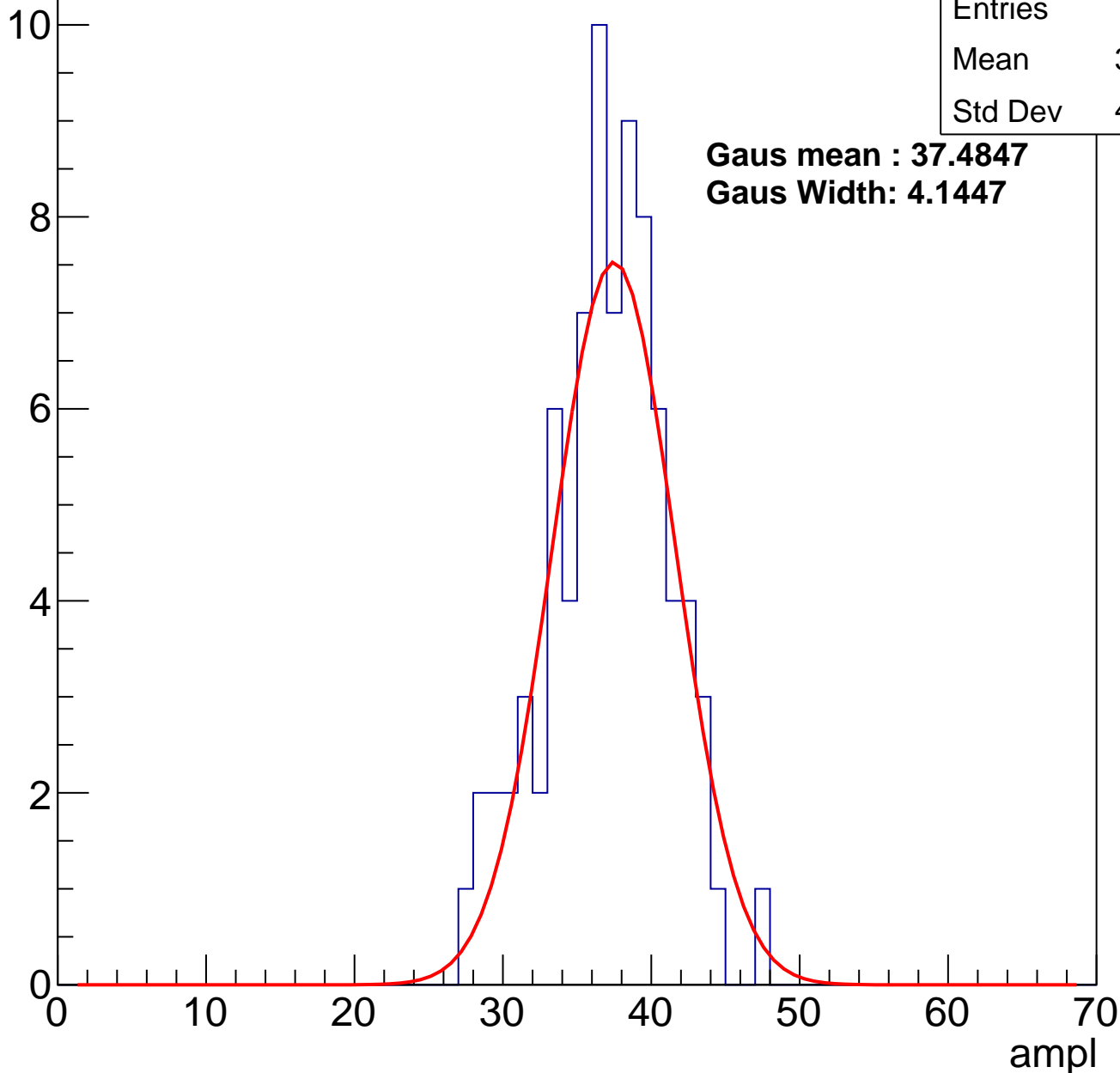
calib_packv5_042523_0143.root, FC#12, port B1

Entries	82
Mean	36.61
Std Dev	4.011

Gaus mean : 37.4847

Gaus Width: 4.1447

Entry



B0L102S, U12-ch95, adc2

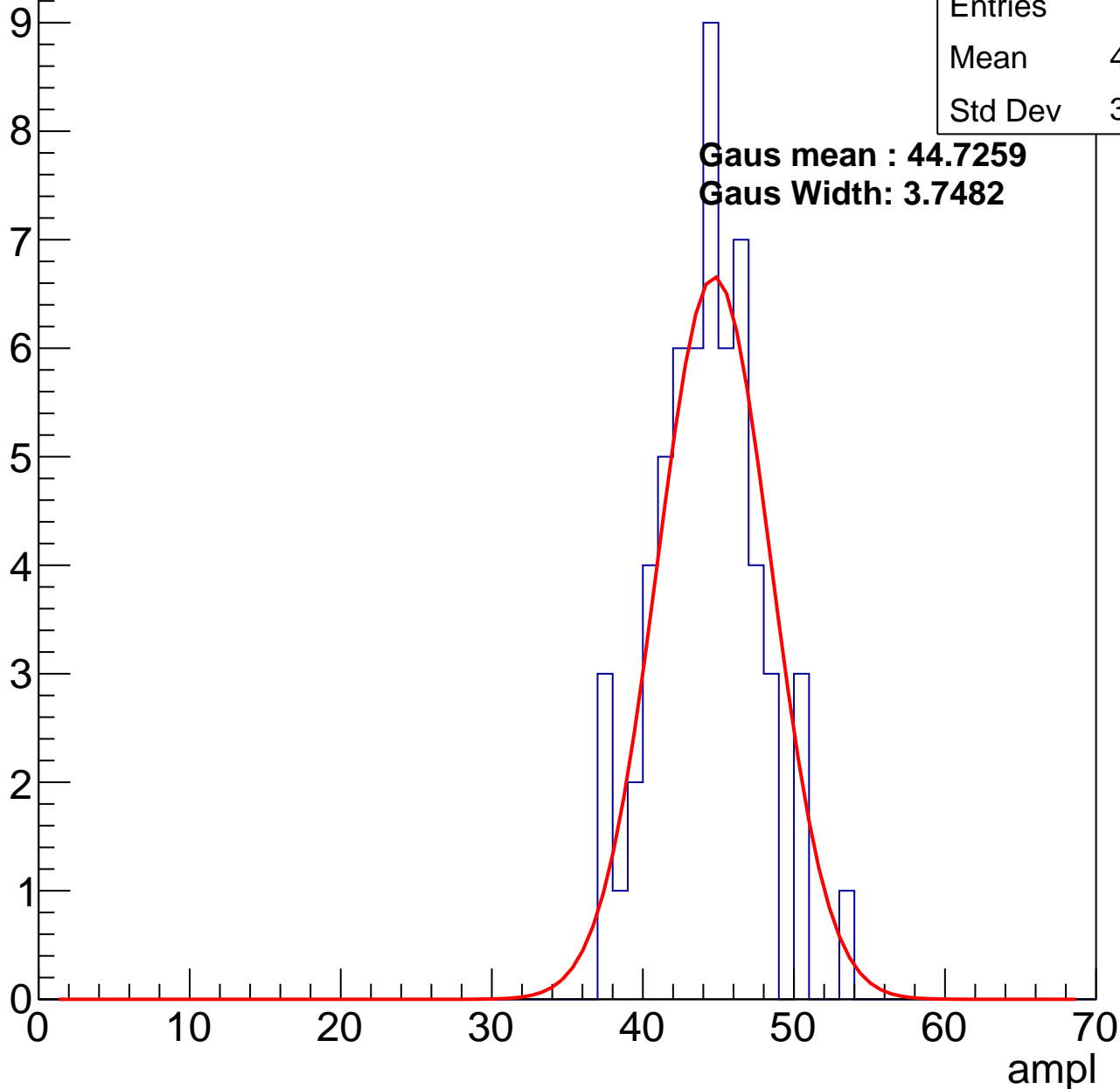
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	43.75
Std Dev	3.355

Gaus mean : 44.7259

Gaus Width: 3.7482

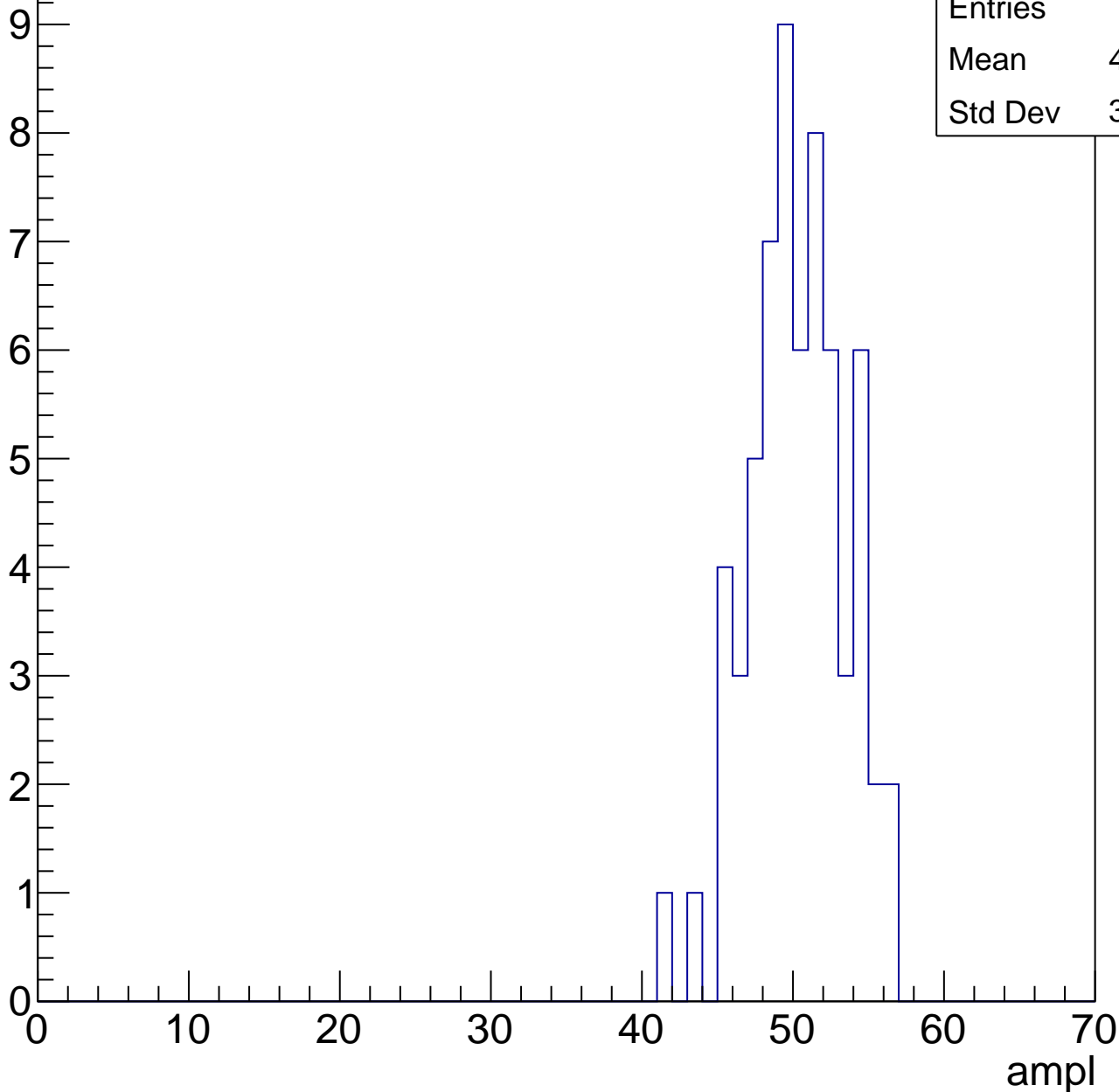


B0L102S, U12-ch95, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.83
Std Dev	3.175

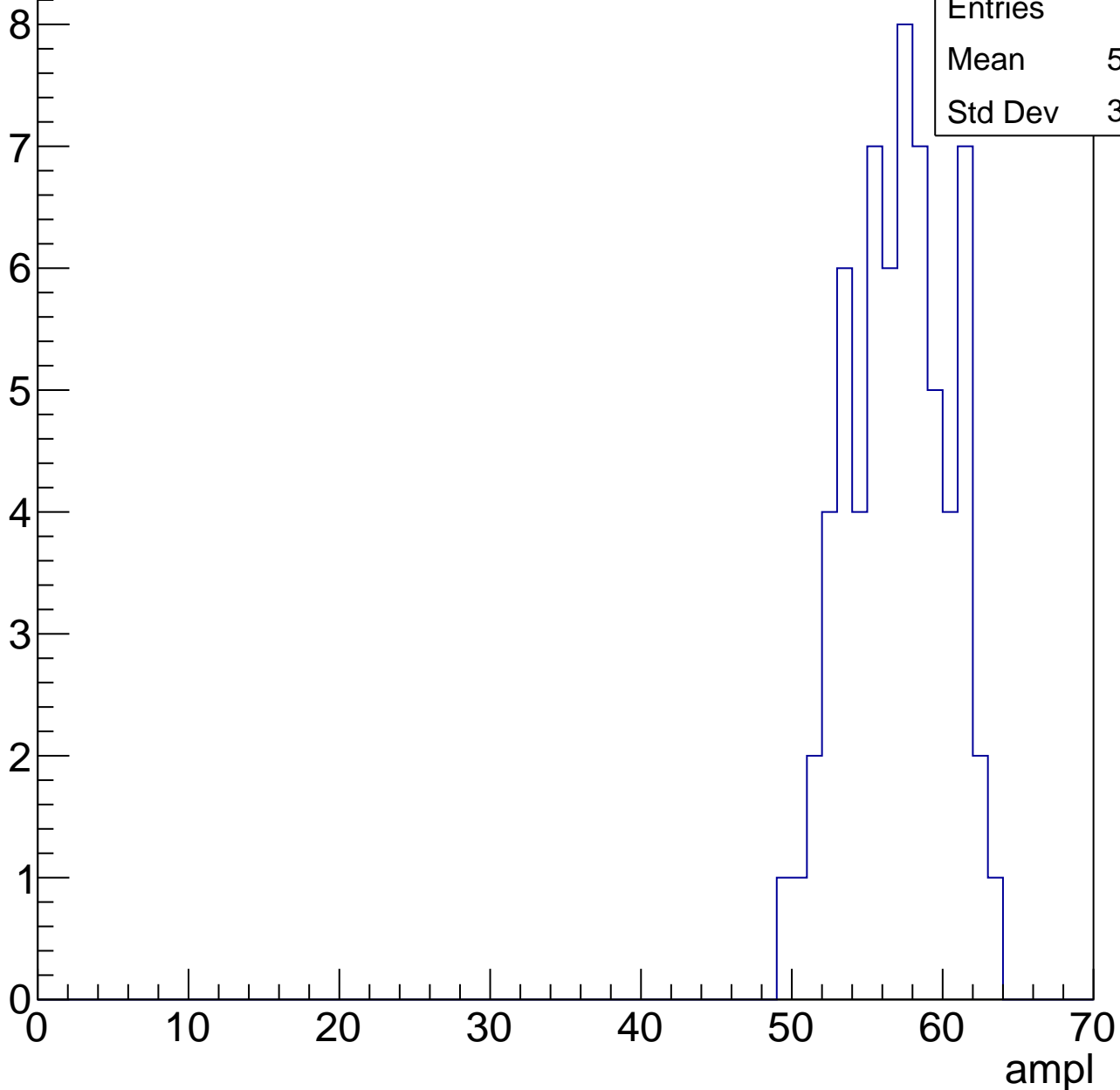


B0L102S, U12-ch95, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	56.54
Std Dev	3.277

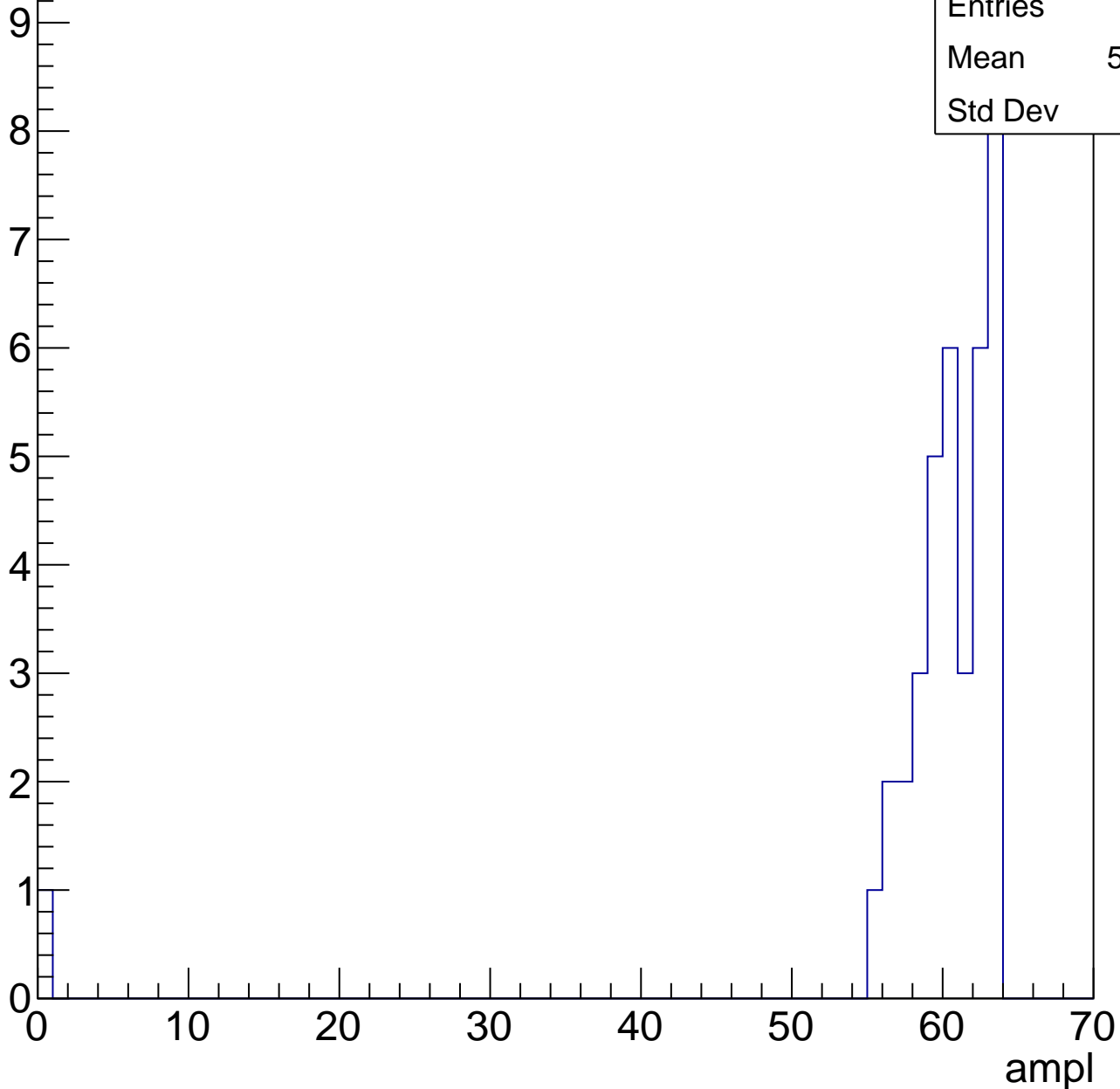


B0L102S, U12-ch95, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	38
Mean	58.74
Std Dev	9.92



B0L102S, U12-ch95, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch95, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch96, adc0

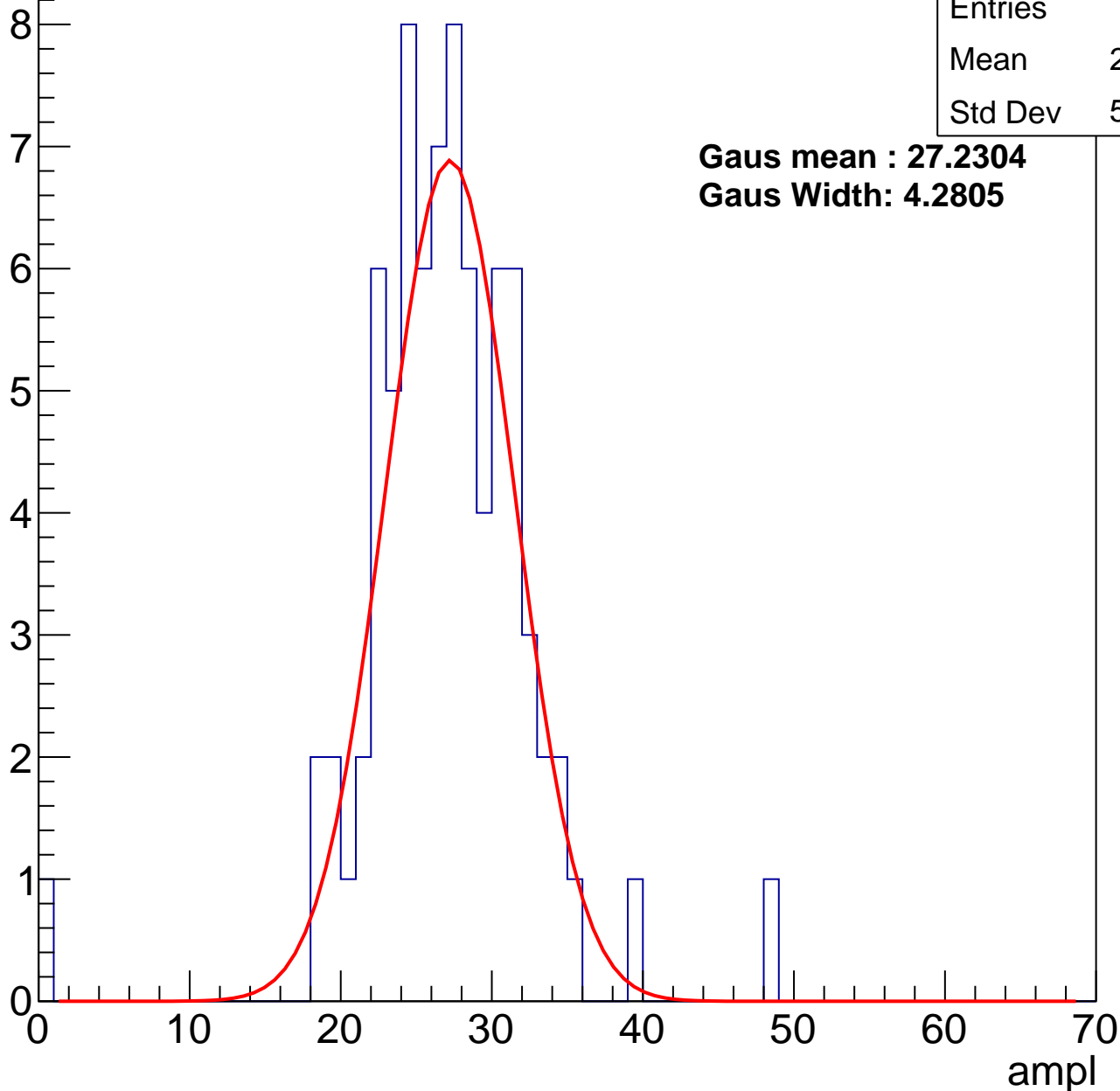
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	26.56
Std Dev	5.617

Gaus mean : 27.2304

Gaus Width: 4.2805



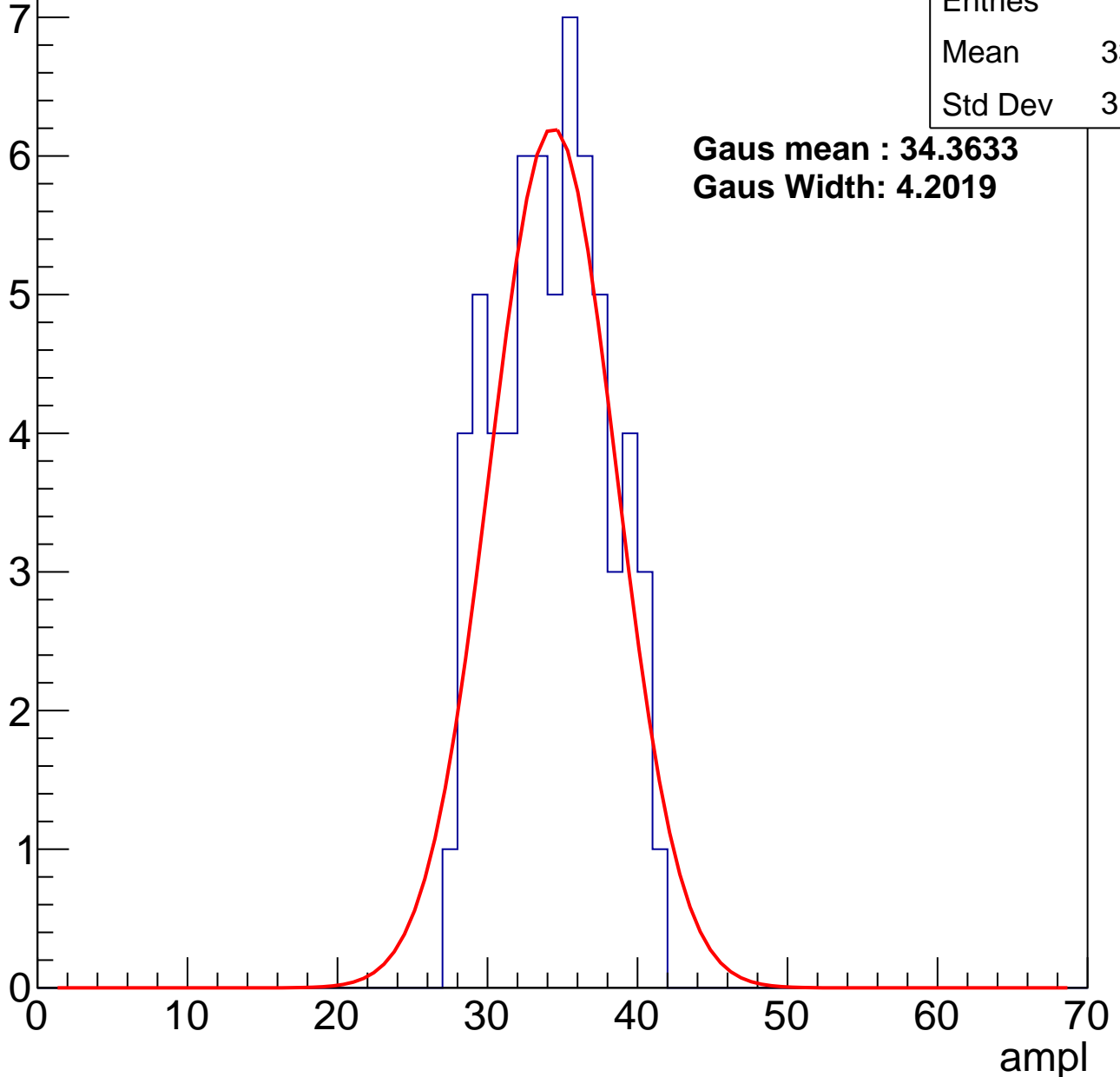
B0L102S, U12-ch96, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	33.83
Std Dev	3.595

Gaus mean : 34.3633
Gaus Width: 4.2019



B0L102S, U12-ch96, adc2

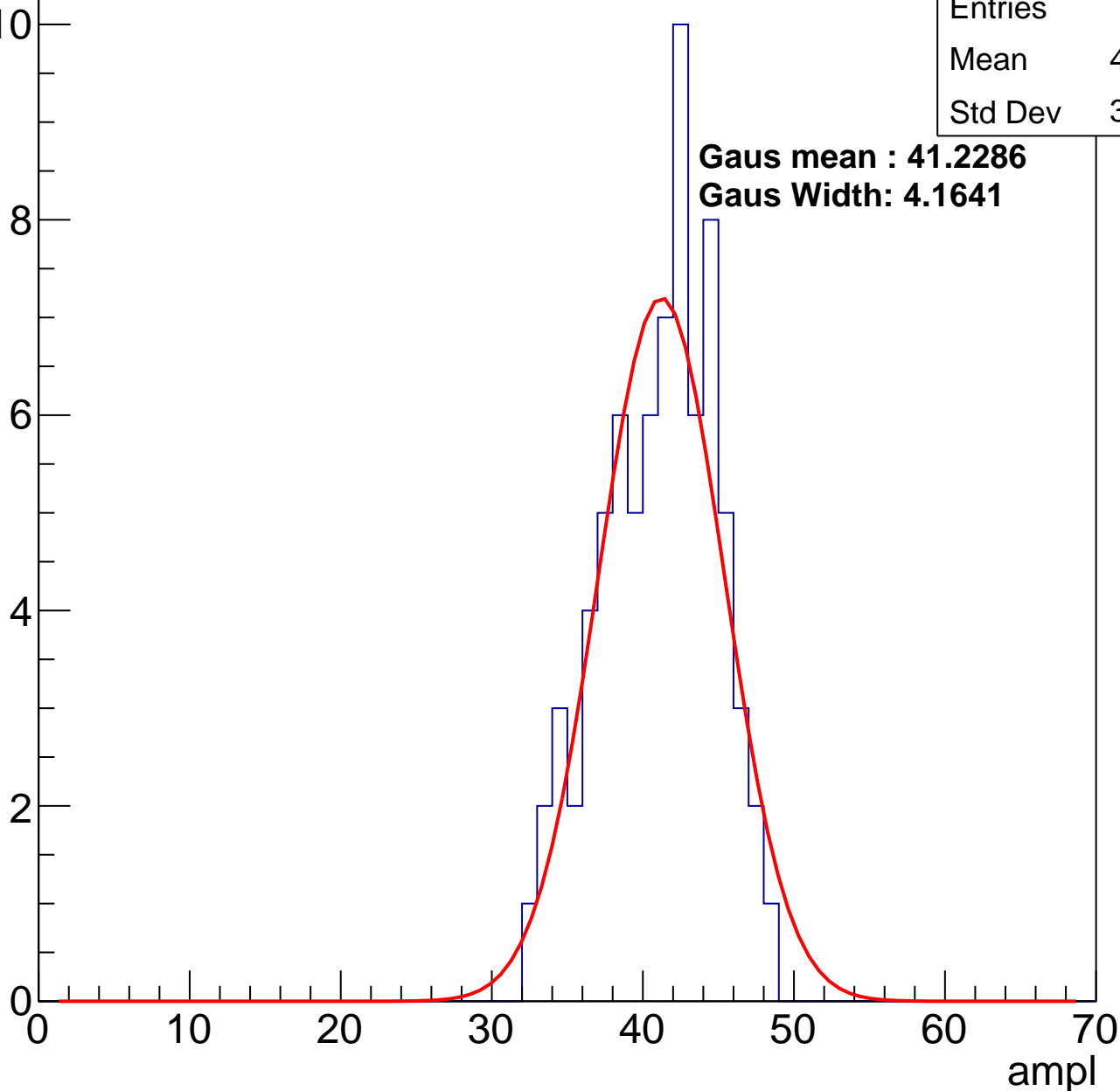
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	40.58
Std Dev	3.728

Gaus mean : 41.2286

Gaus Width: 4.1641

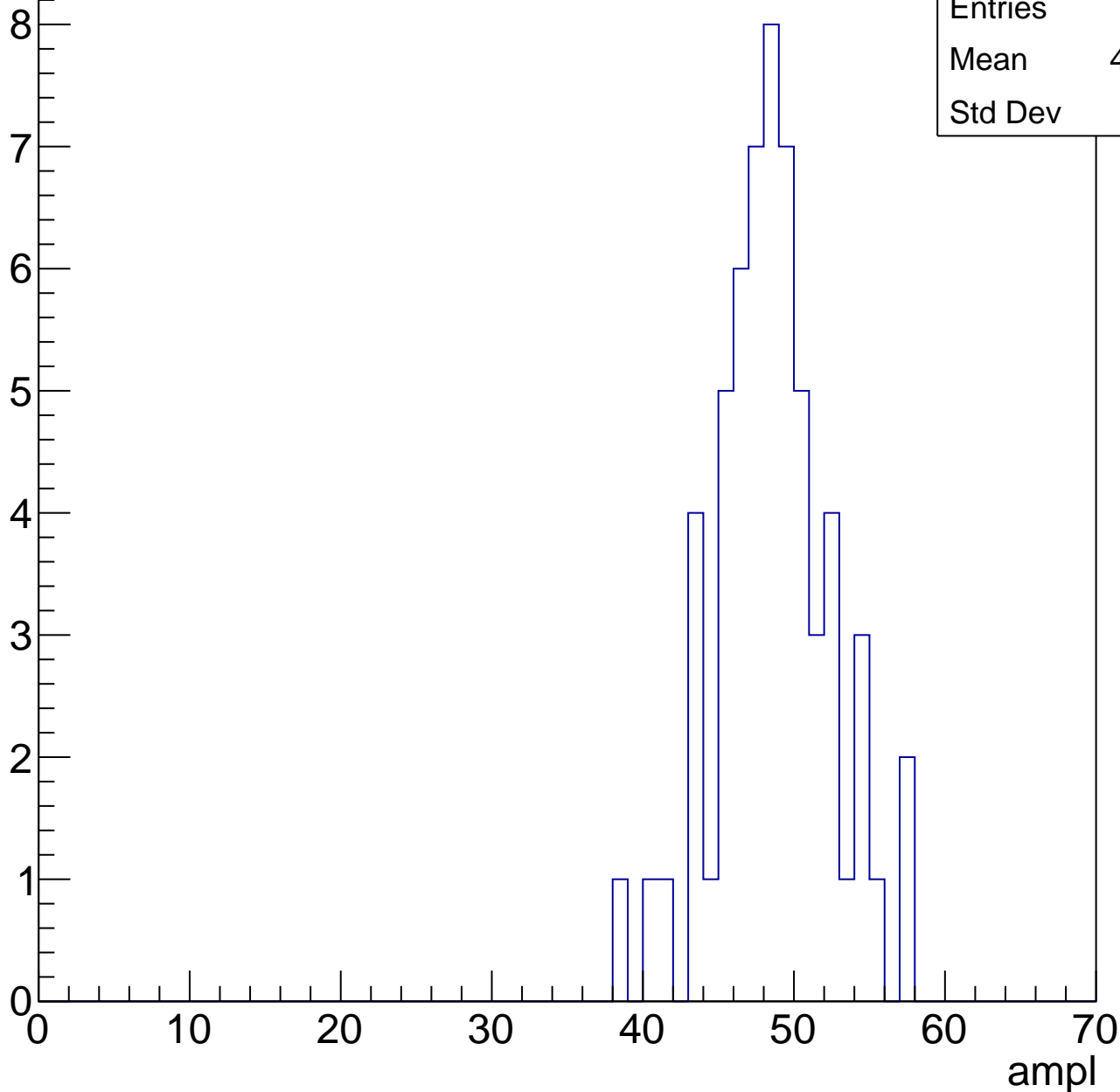


B0L102S, U12-ch96, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	48.12
Std Dev	3.8

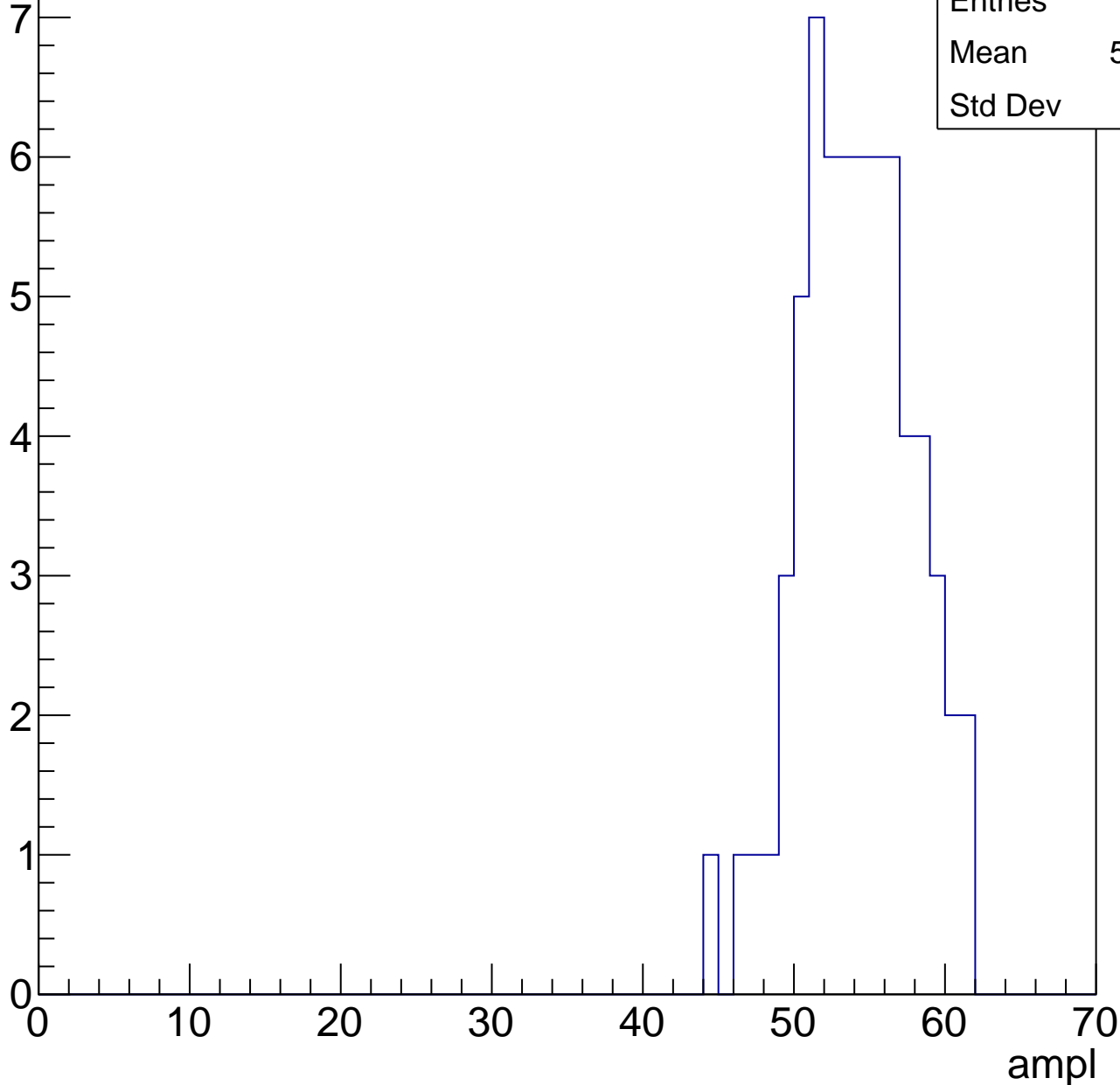


B0L102S, U12-ch96, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	53.72
Std Dev	3.68

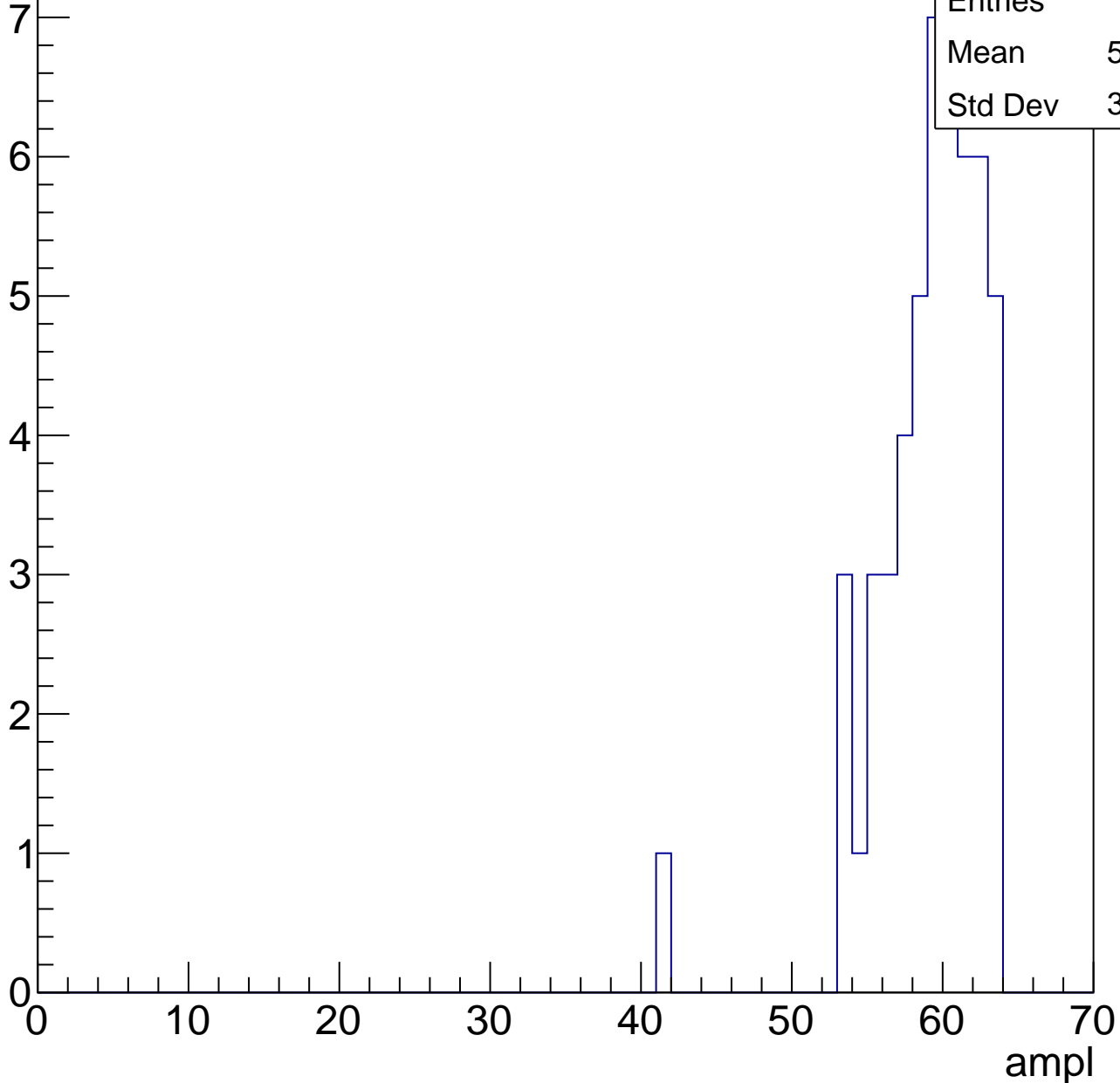


B0L102S, U12-ch96, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

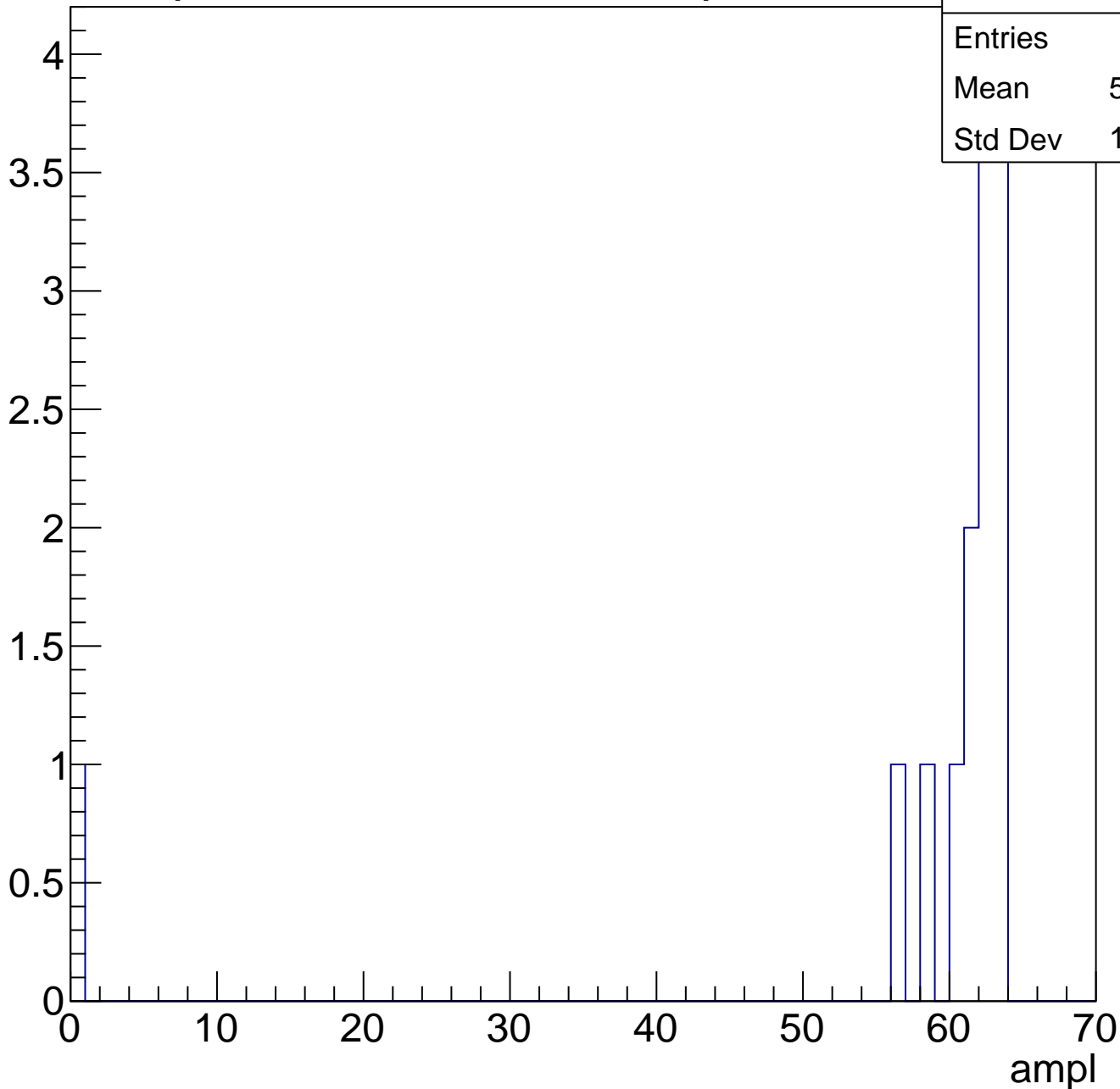
Entries	51
Mean	58.65
Std Dev	3.735



B0L102S, U12-ch96, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch96, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch97, adc0

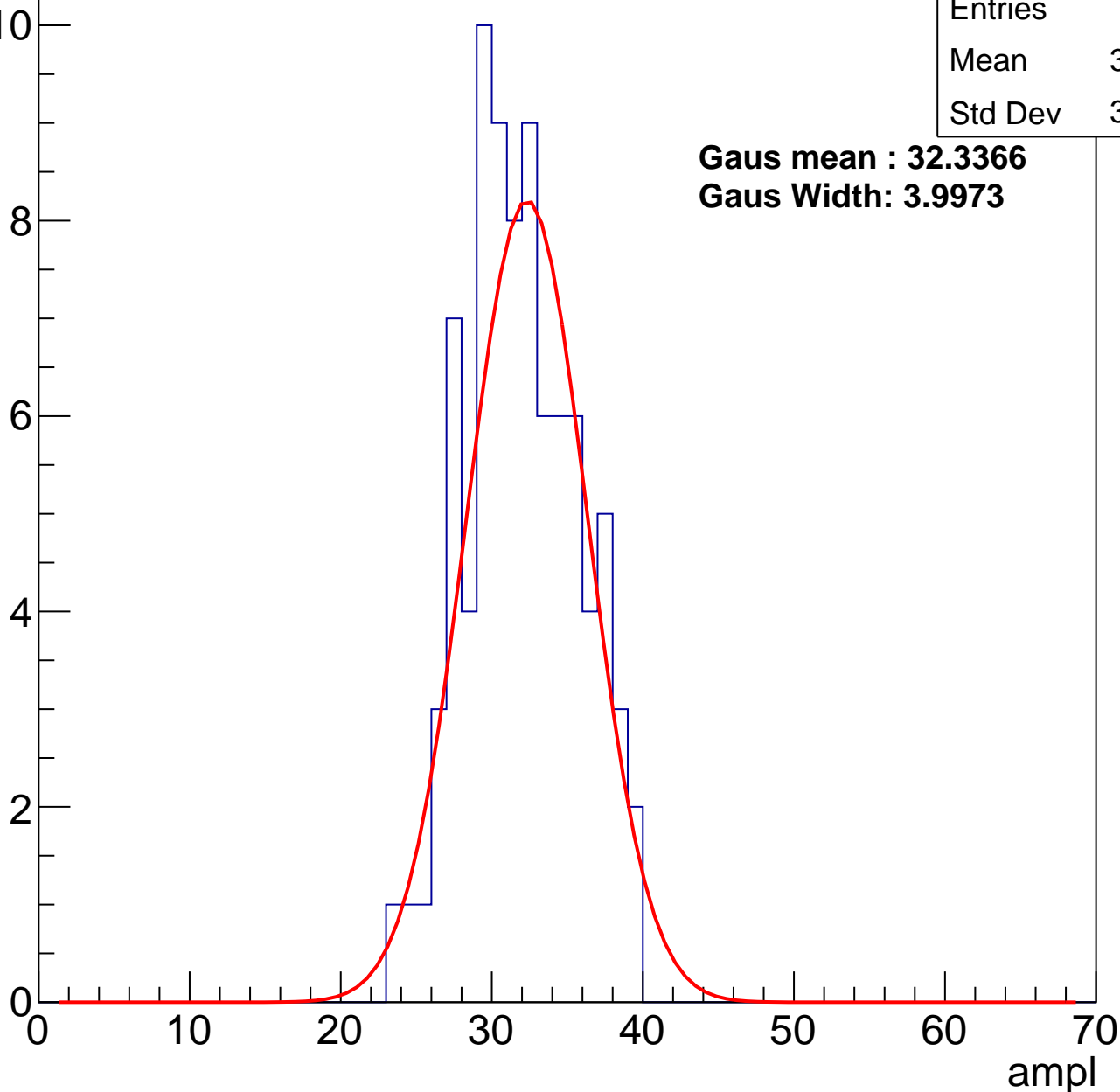
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	31.53
Std Dev	3.654

Gaus mean : 32.3366

Gaus Width: 3.9973



B0L102S, U12-ch97, adc1

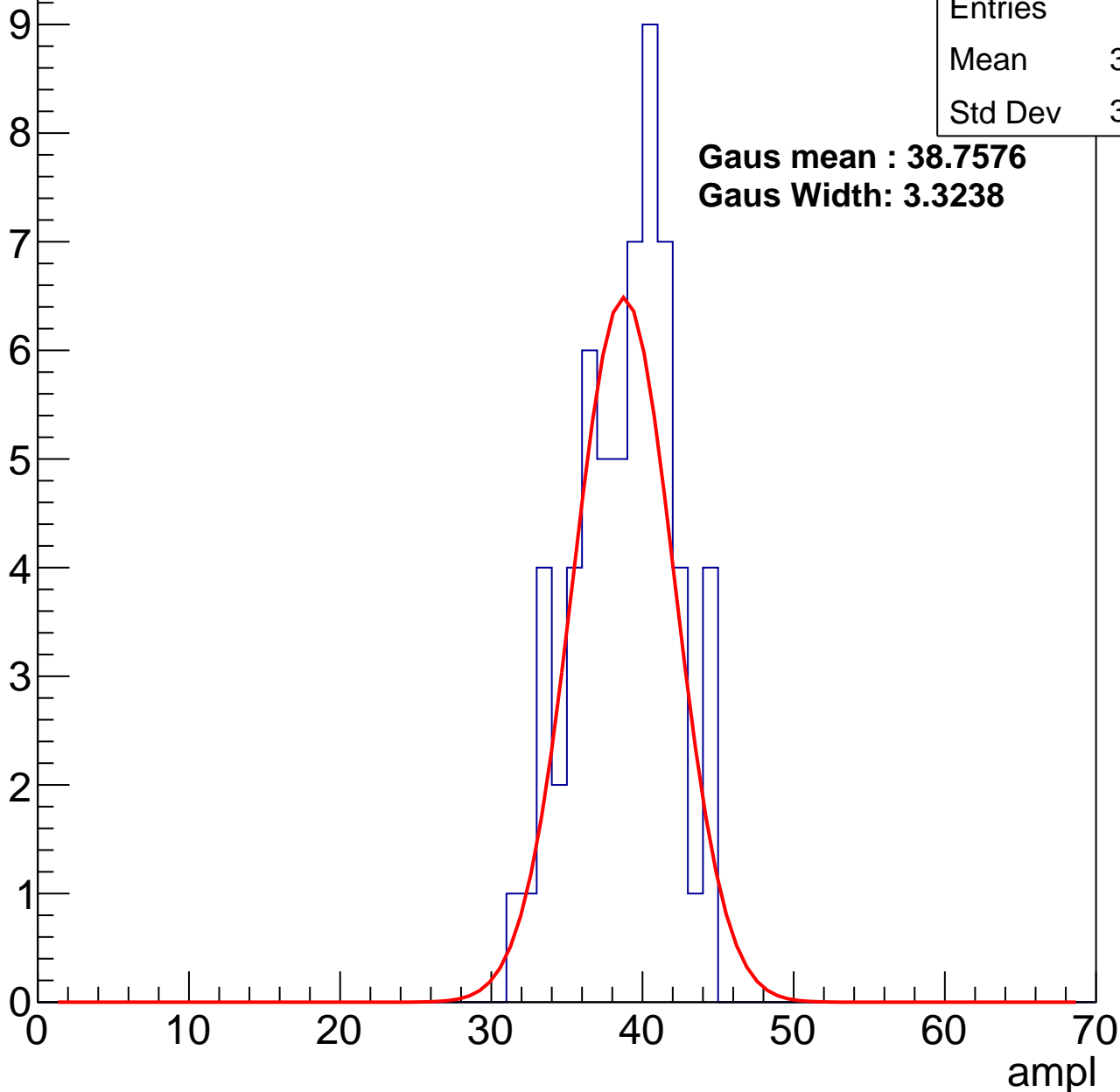
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	38.35
Std Dev	3.198

Gaus mean : 38.7576

Gaus Width: 3.3238



B0L102S, U12-ch97, adc2

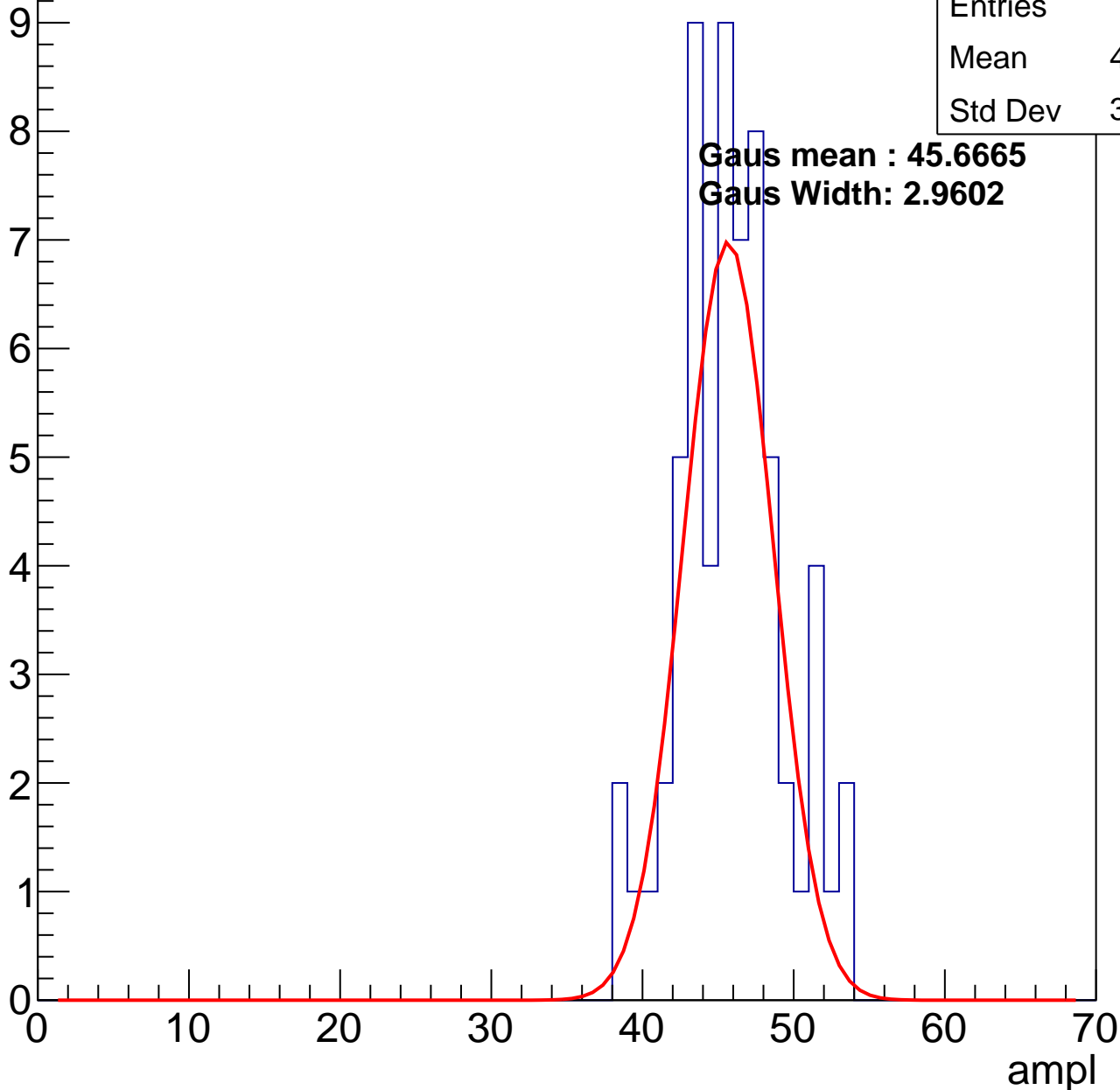
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	45.44
Std Dev	3.394

Gaus mean : 45.6665

Gaus Width: 2.9602

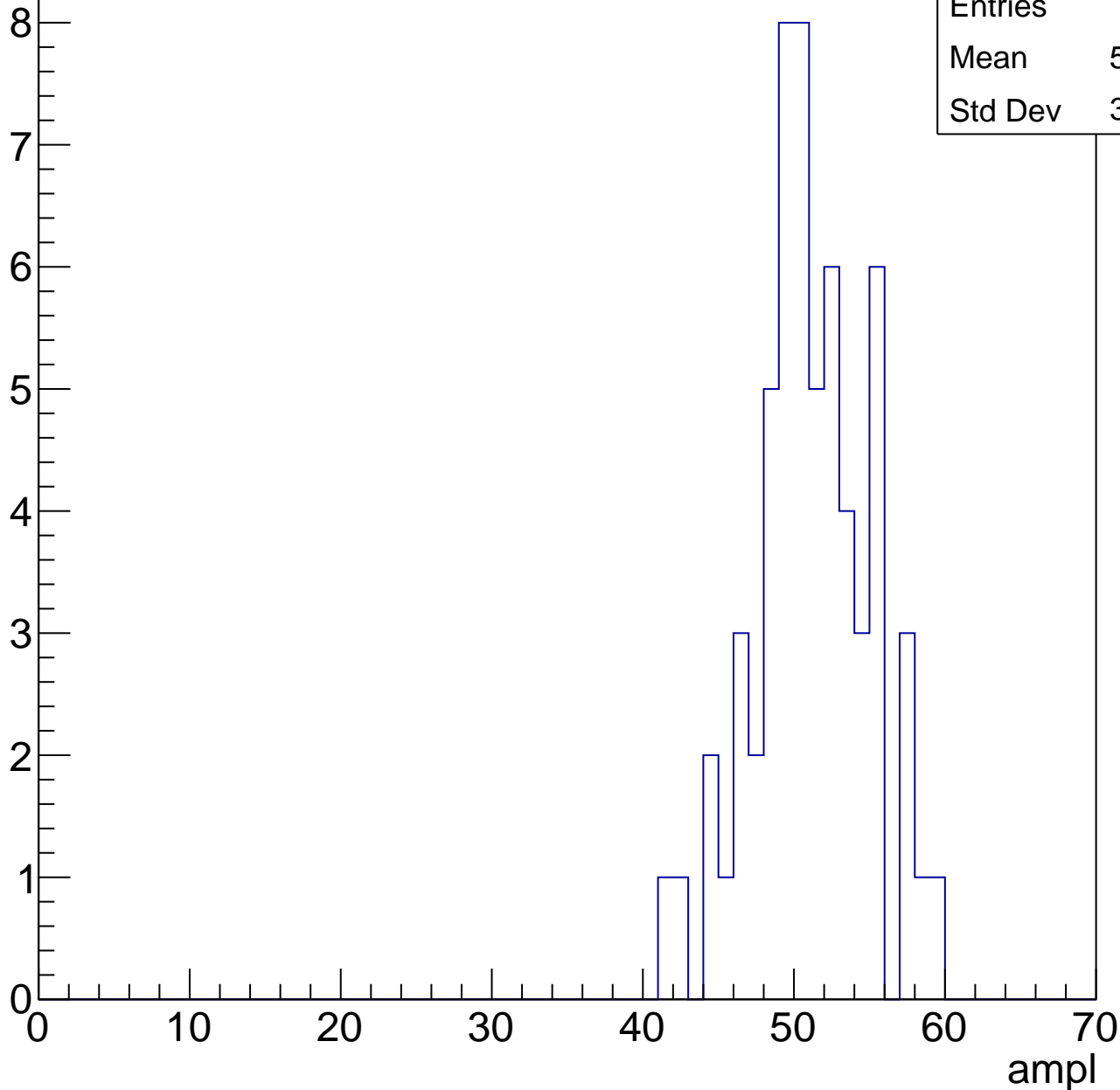


B0L102S, U12-ch97, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	50.65
Std Dev	3.798

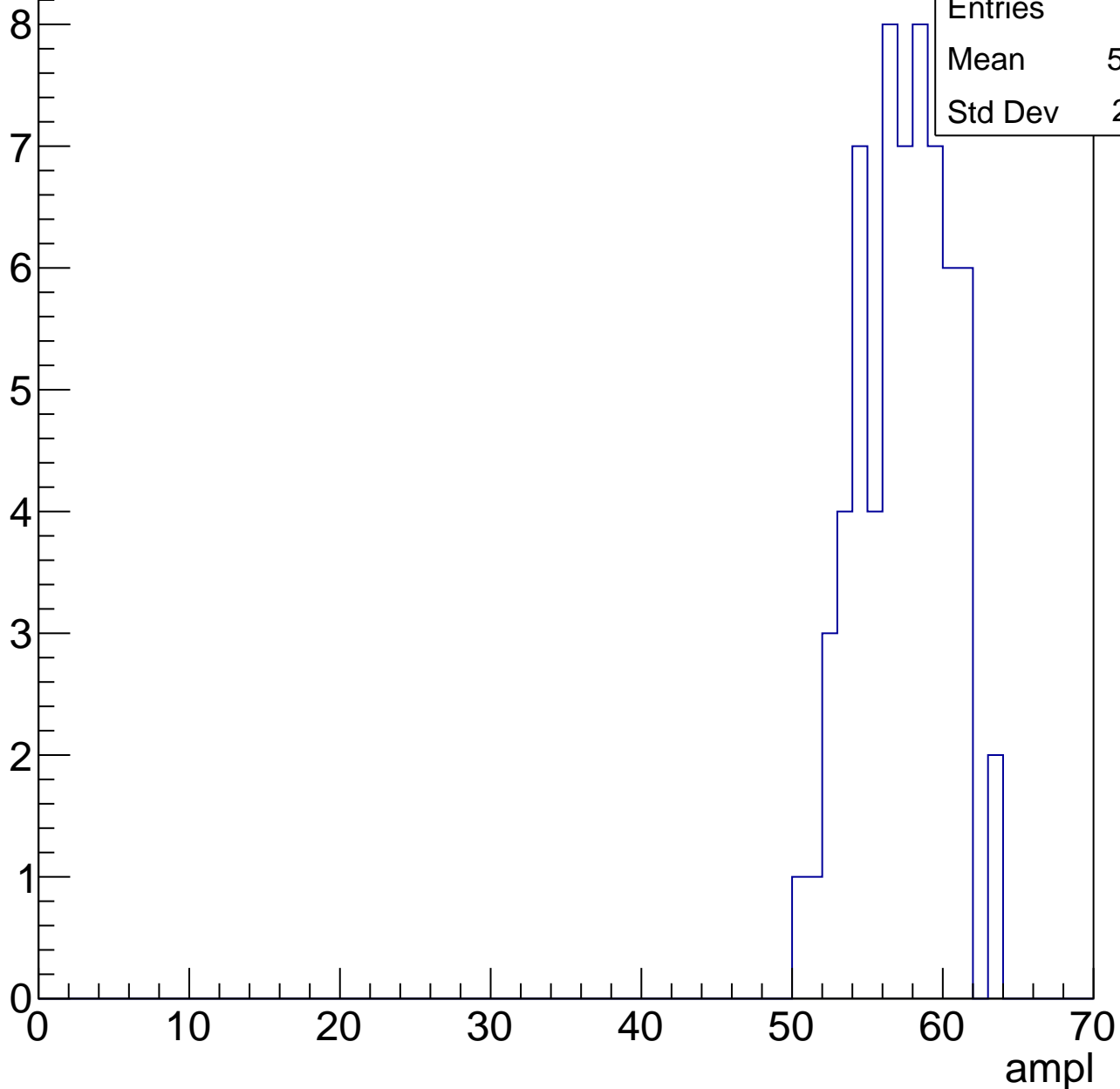


B0L102S, U12-ch97, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	56.92
Std Dev	2.981

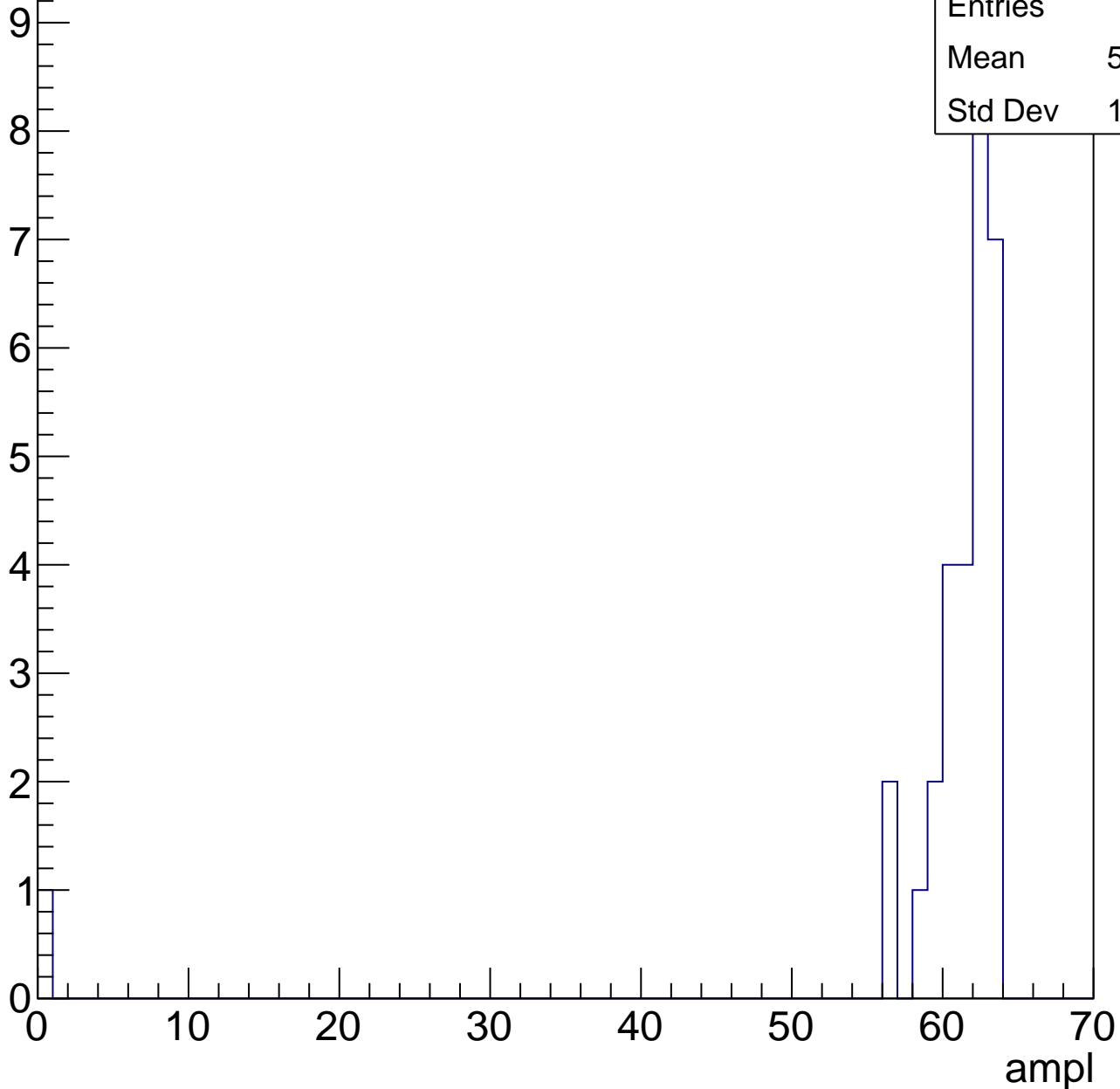


B0L102S, U12-ch97, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	30
Mean	59.03
Std Dev	11.12



B0L102S, U12-ch97, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch97, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch98, adc0

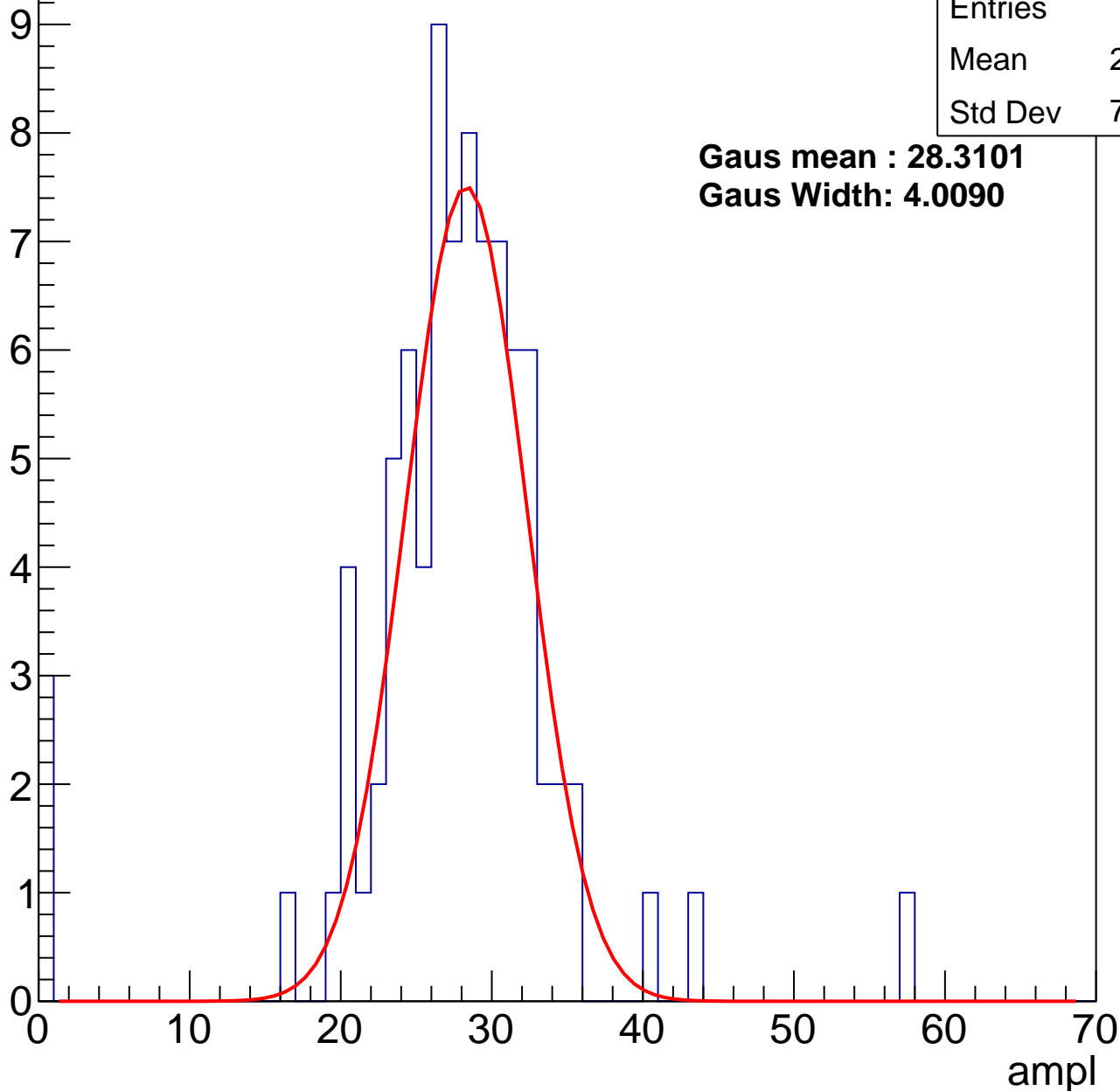
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	26.99
Std Dev	7.456

Gaus mean : 28.3101

Gaus Width: 4.0090



B0L102S, U12-ch98, adc1

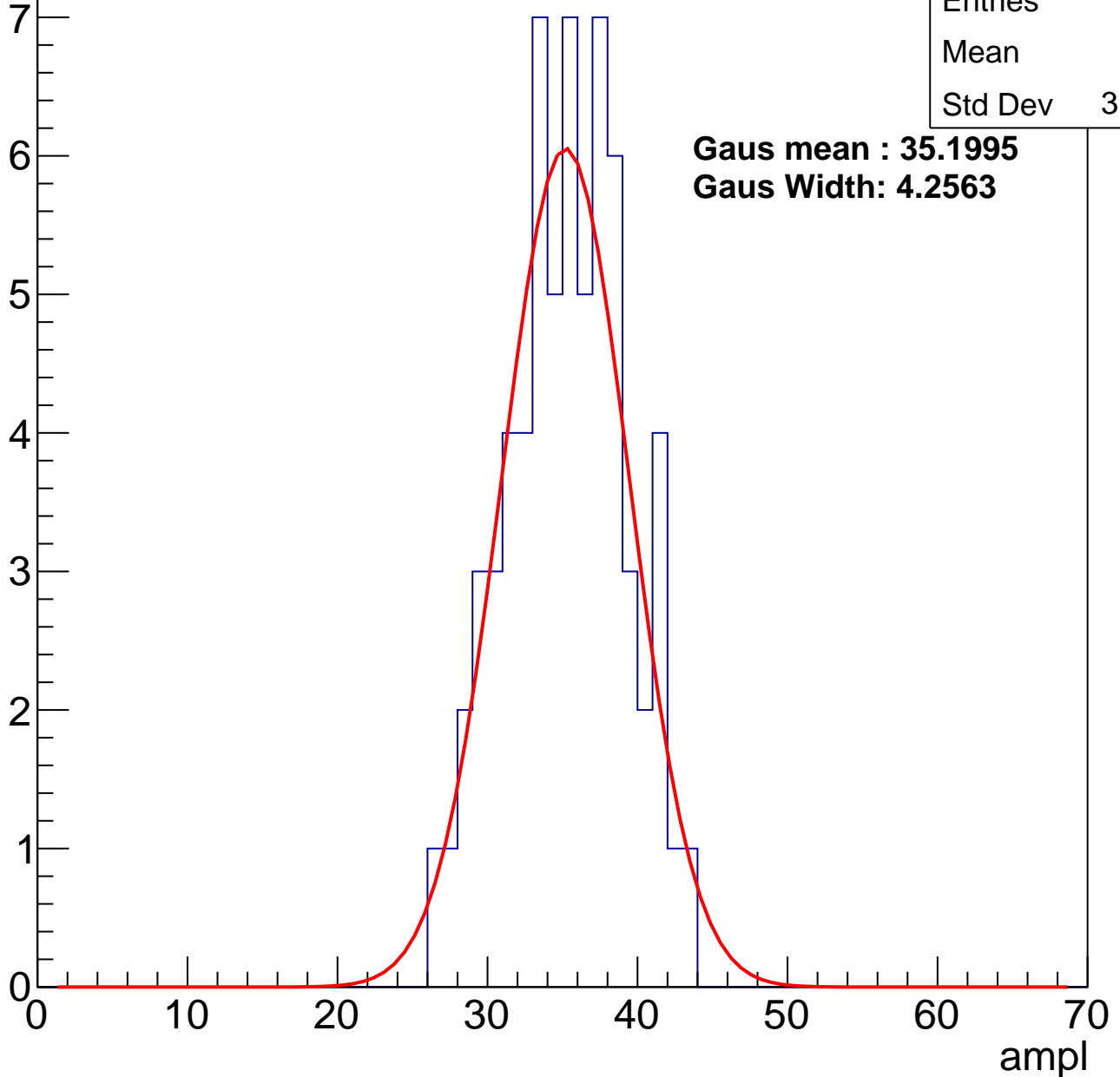
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.8
Std Dev	3.889

Gaus mean : 35.1995

Gaus Width: 4.2563



B0L102S, U12-ch98, adc2

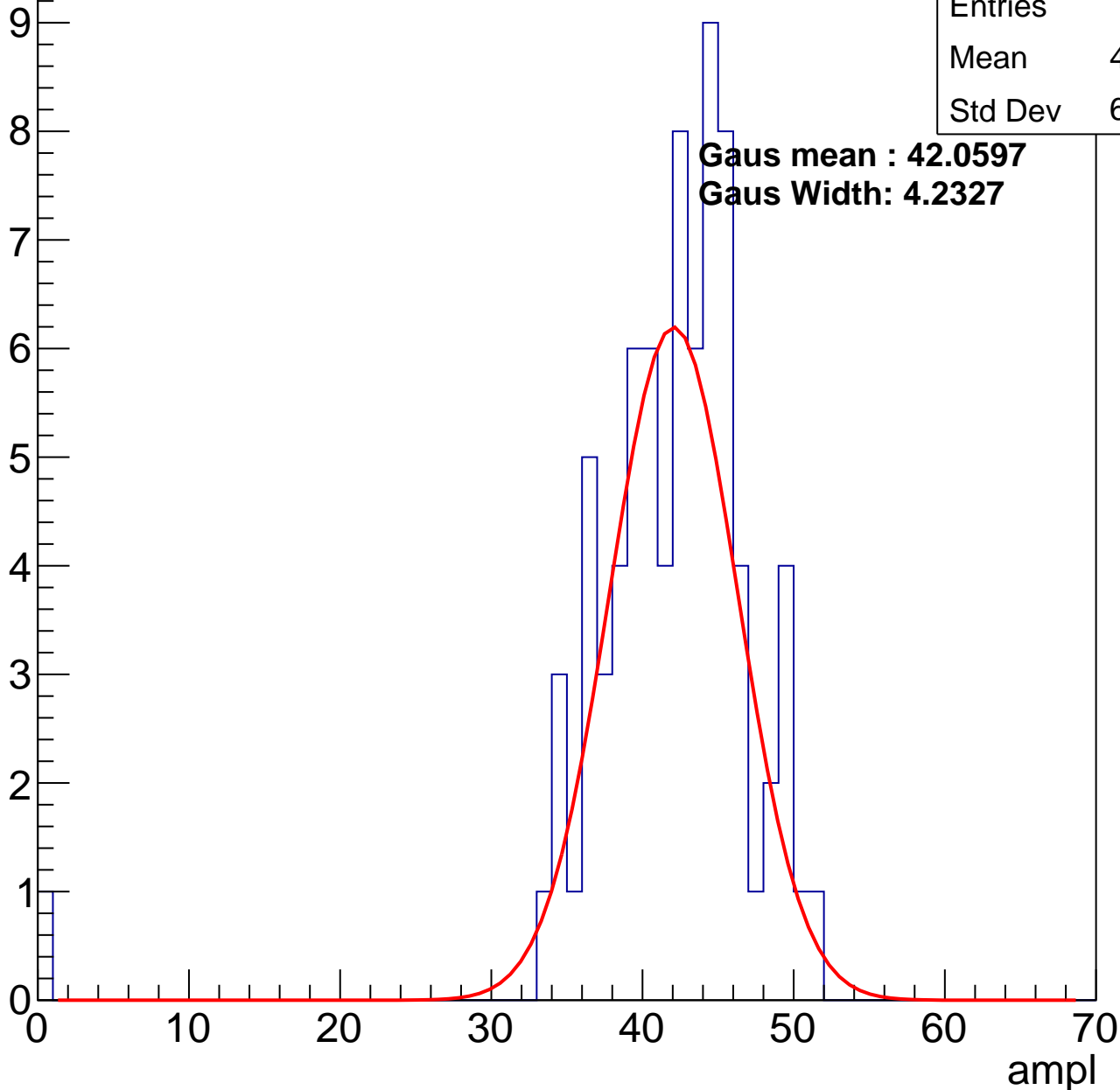
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	41.35
Std Dev	6.273

Gaus mean : 42.0597

Gaus Width: 4.2327

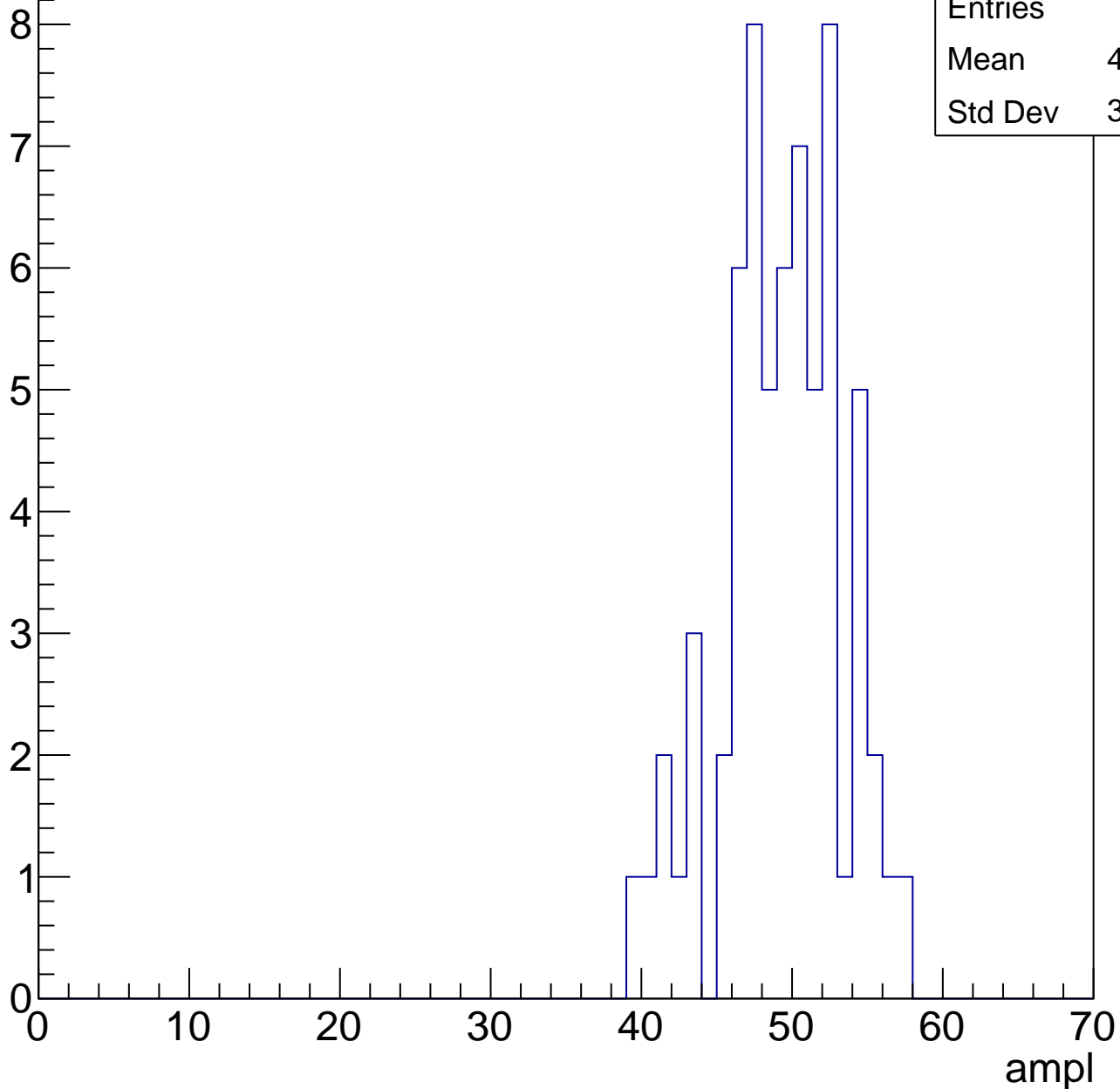


B0L102S, U12-ch98, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	48.85
Std Dev	3.947

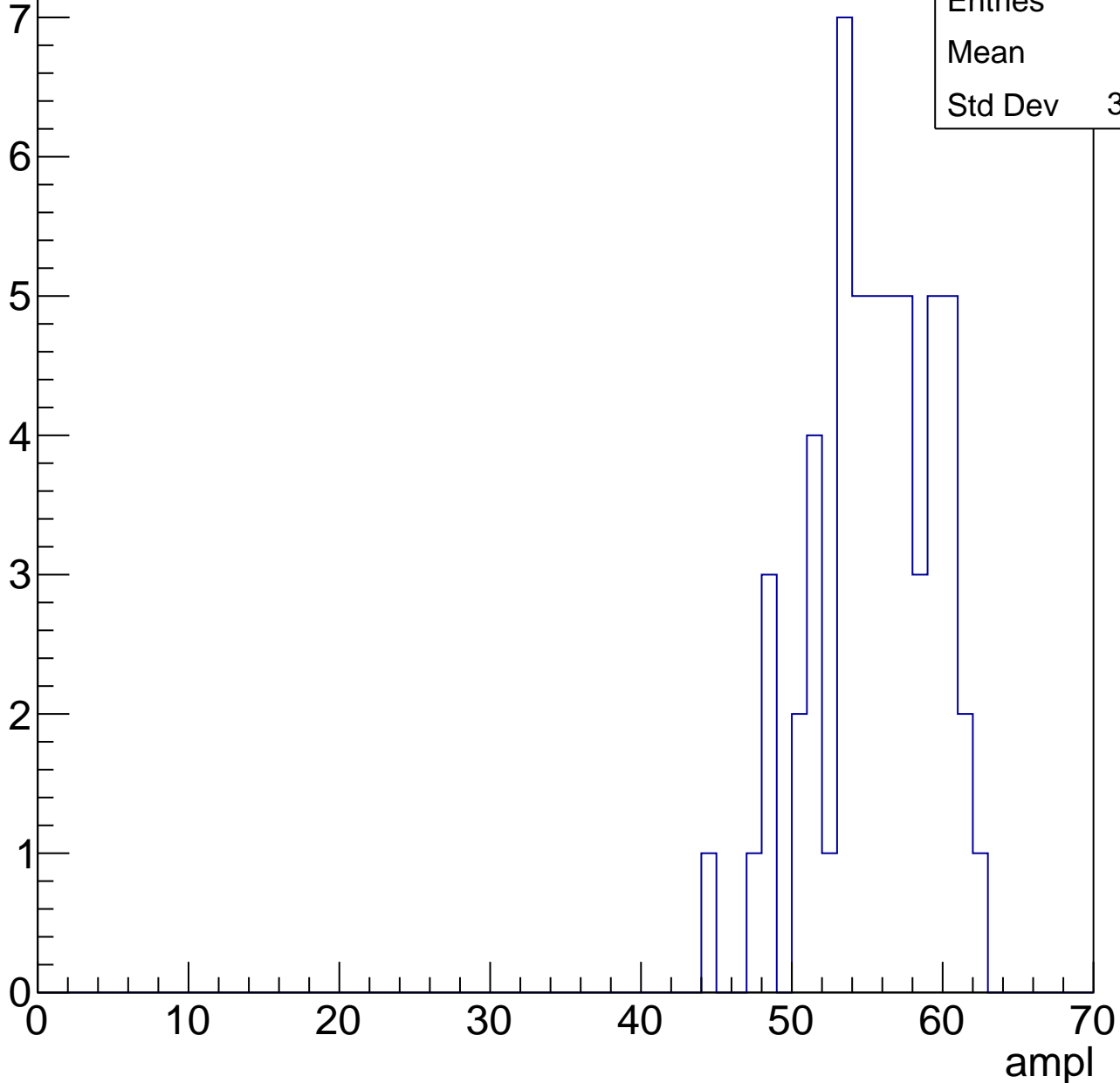


B0L102S, U12-ch98, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	55
Std Dev	3.968

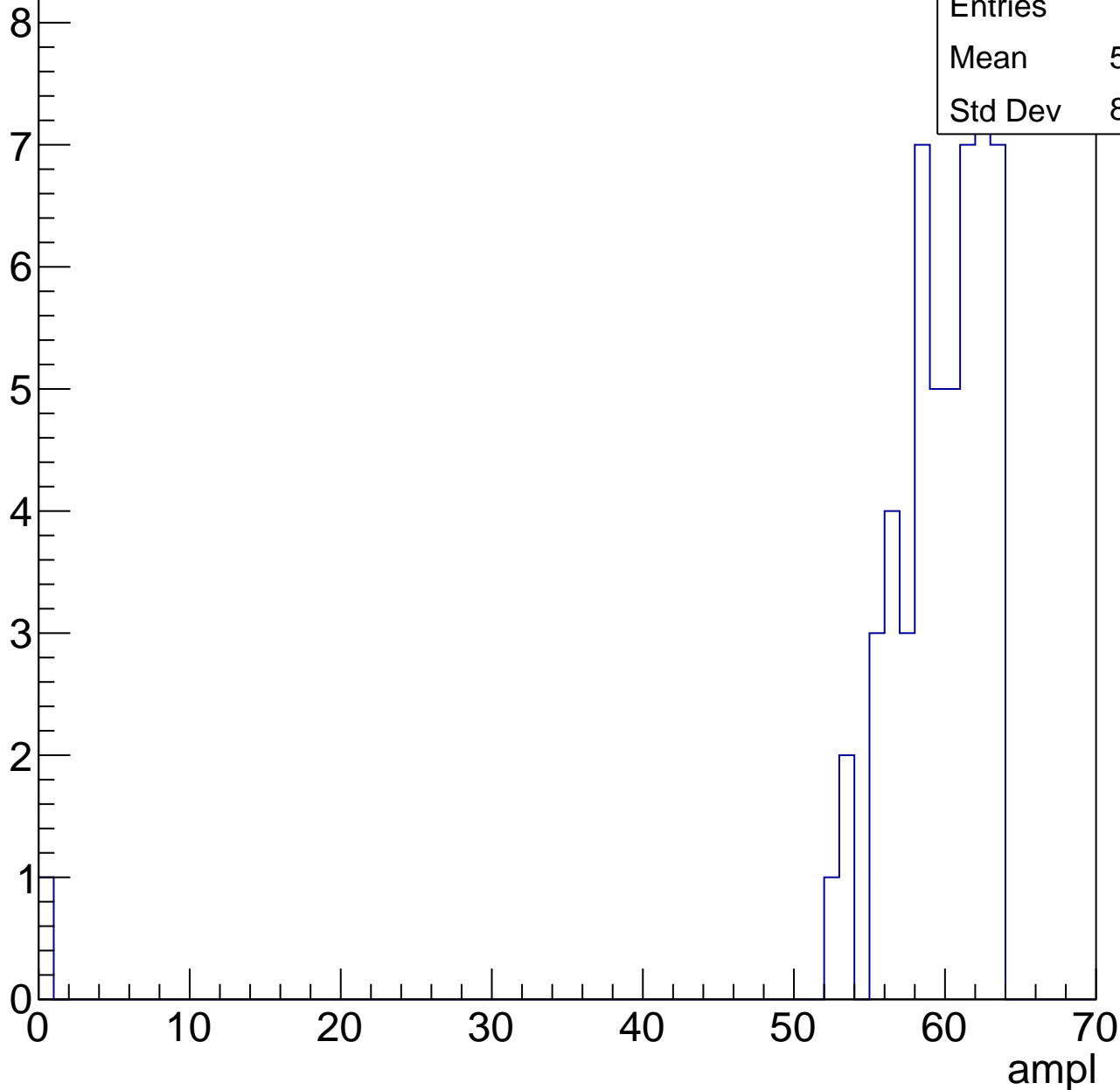


B0L102S, U12-ch98, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

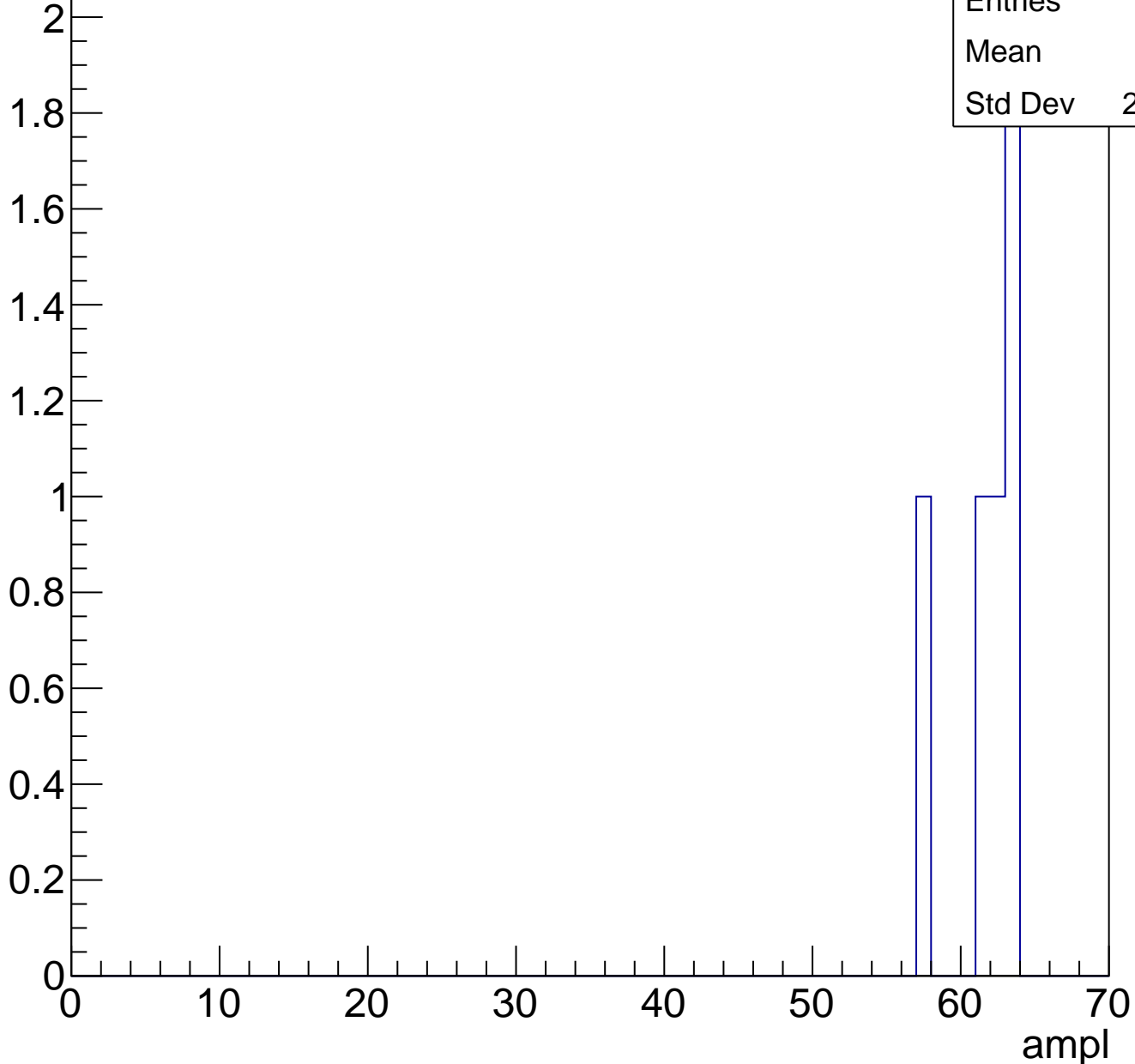
Entries	53
Mean	58.17
Std Dev	8.562



B0L102S, U12-ch98, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch98, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch99, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	93
Mean	29
Std Dev	5.346

Gaus mean : 29.7542

Gaus Width: 4.2089

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L102S, U12-ch99, adc1

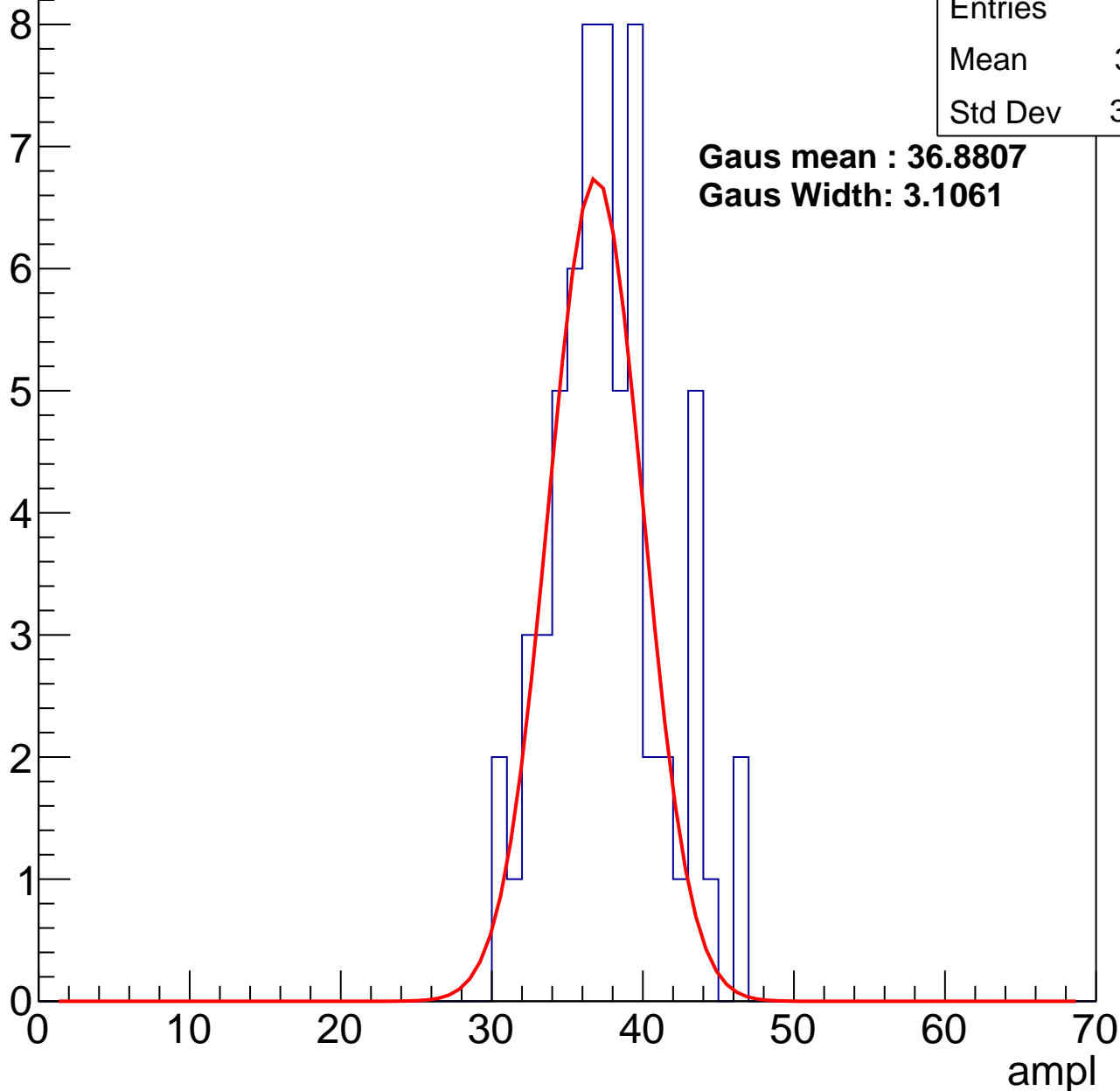
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	37.21
Std Dev	3.668

Gaus mean : 36.8807

Gaus Width: 3.1061



B0L102S, U12-ch99, adc2

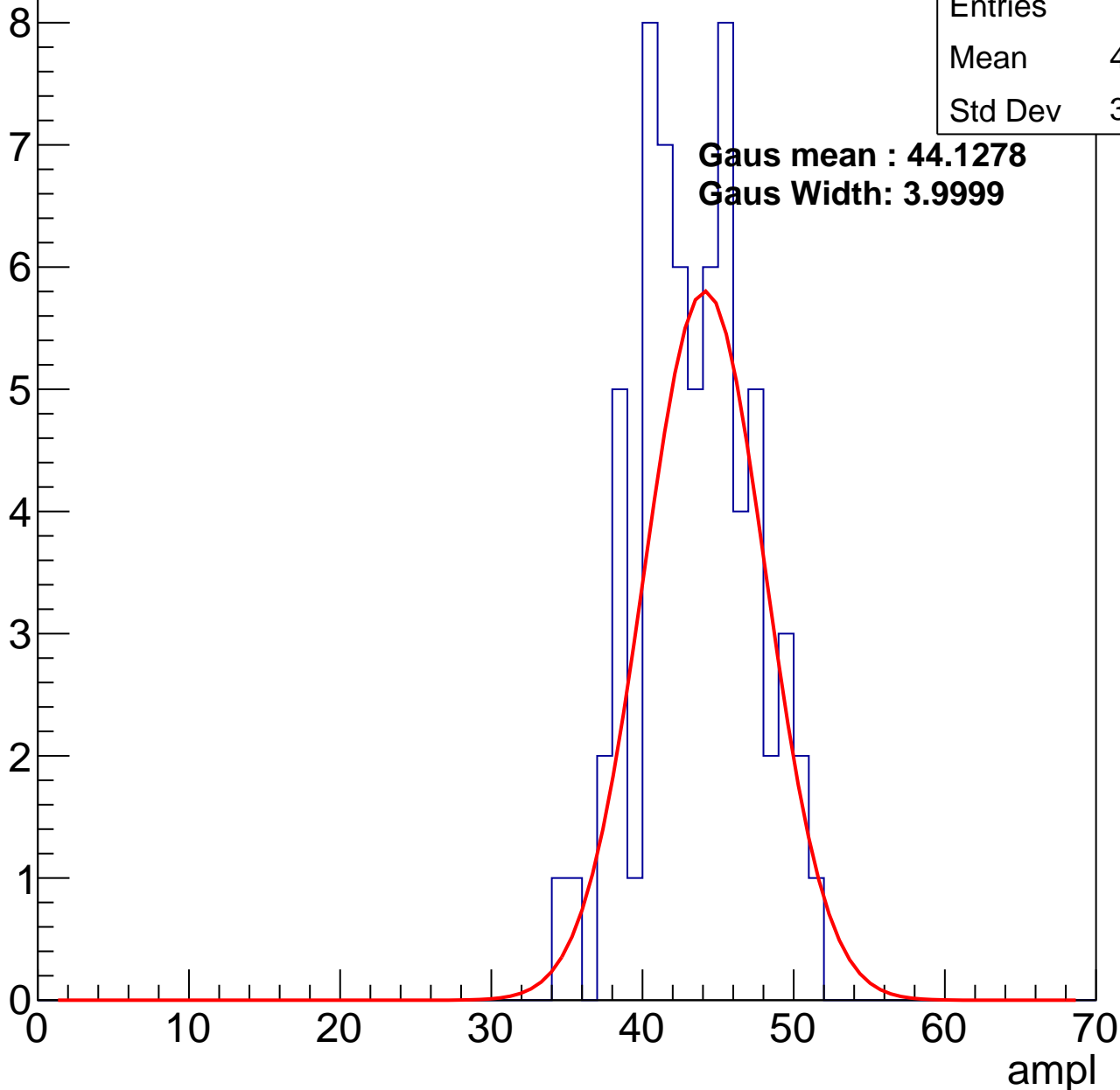
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.03
Std Dev	3.742

Gaus mean : 44.1278

Gaus Width: 3.9999

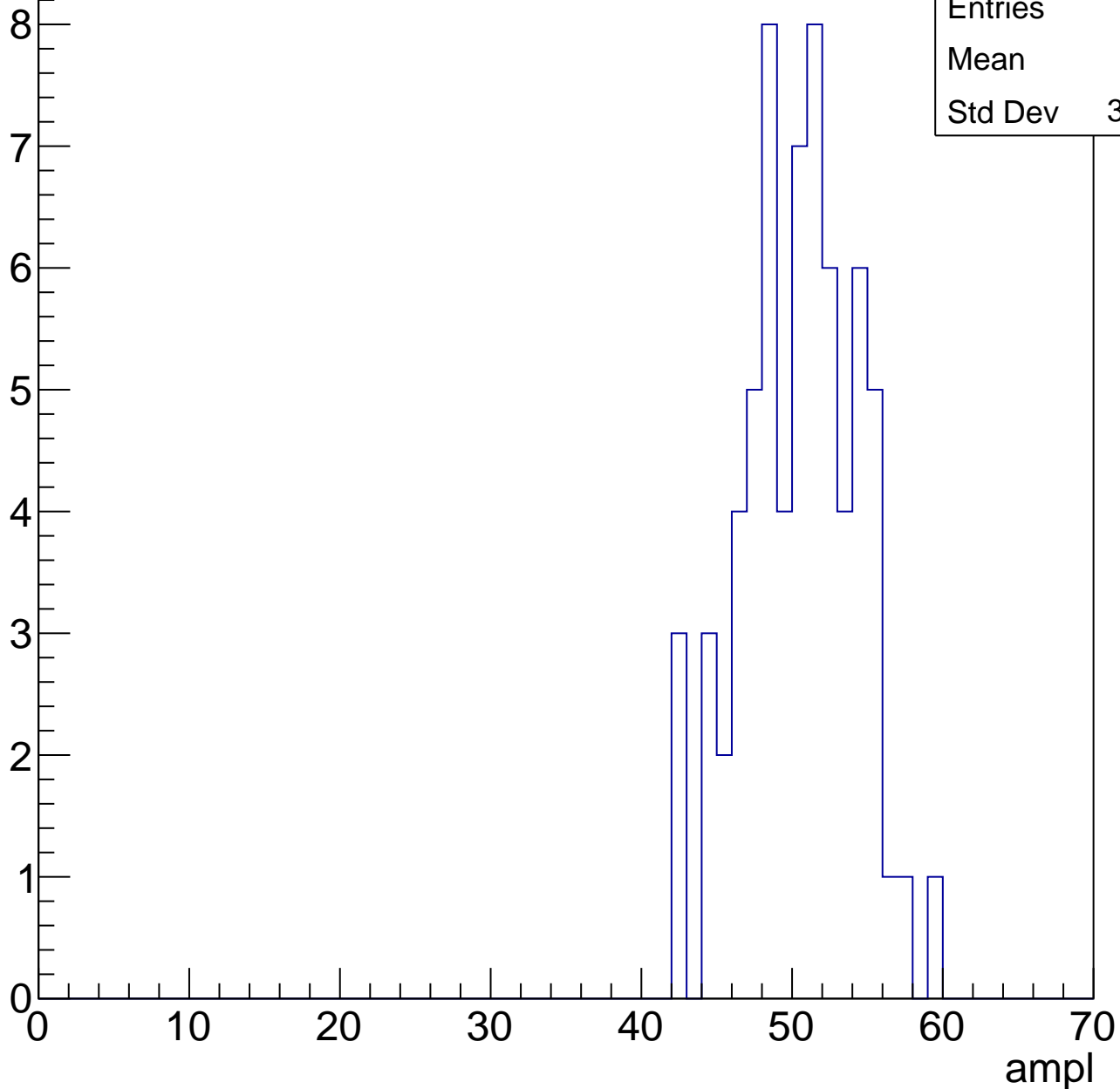


B0L102S, U12-ch99, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50
Std Dev	3.738

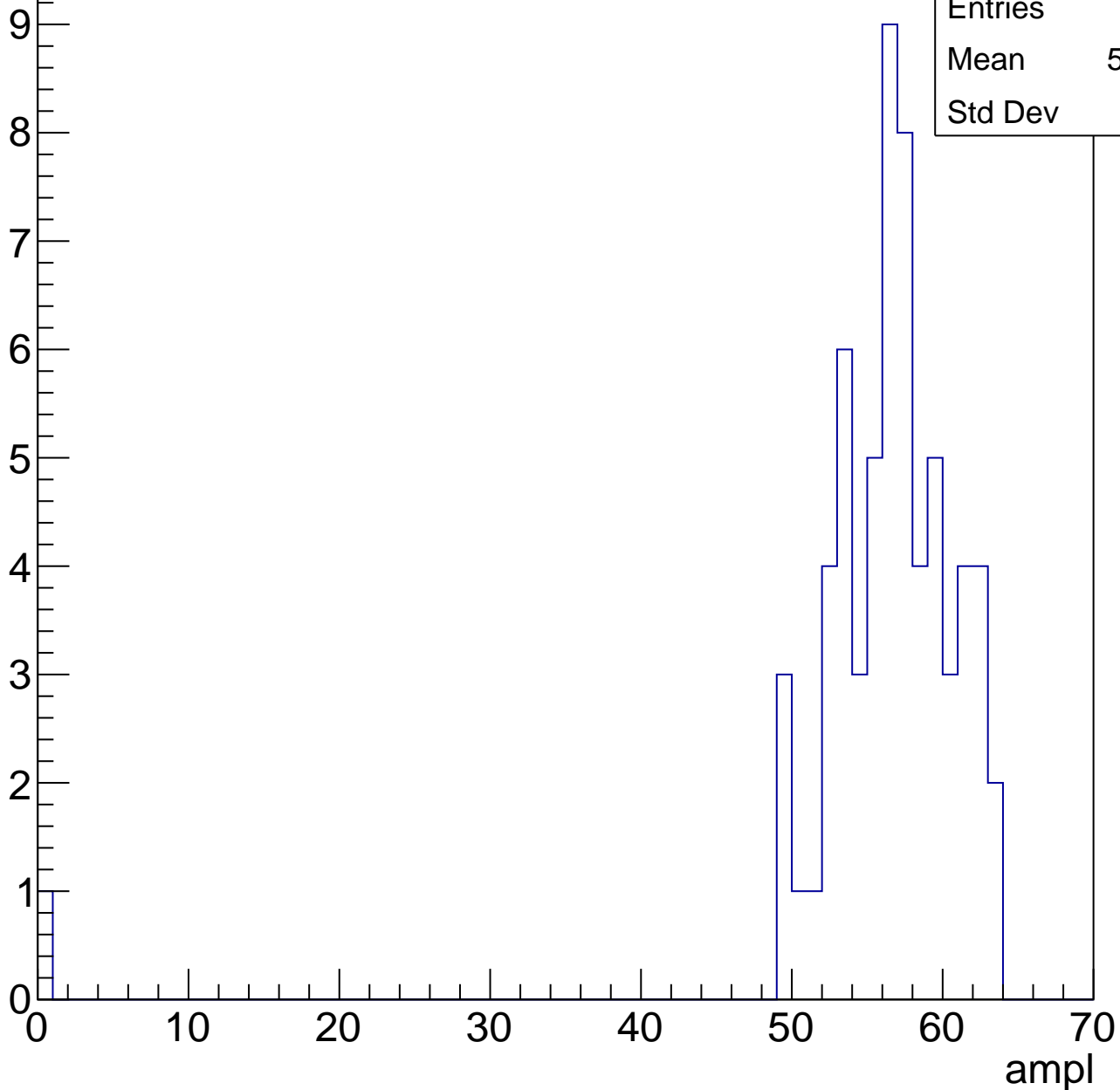


B0L102S, U12-ch99, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.49
Std Dev	7.89

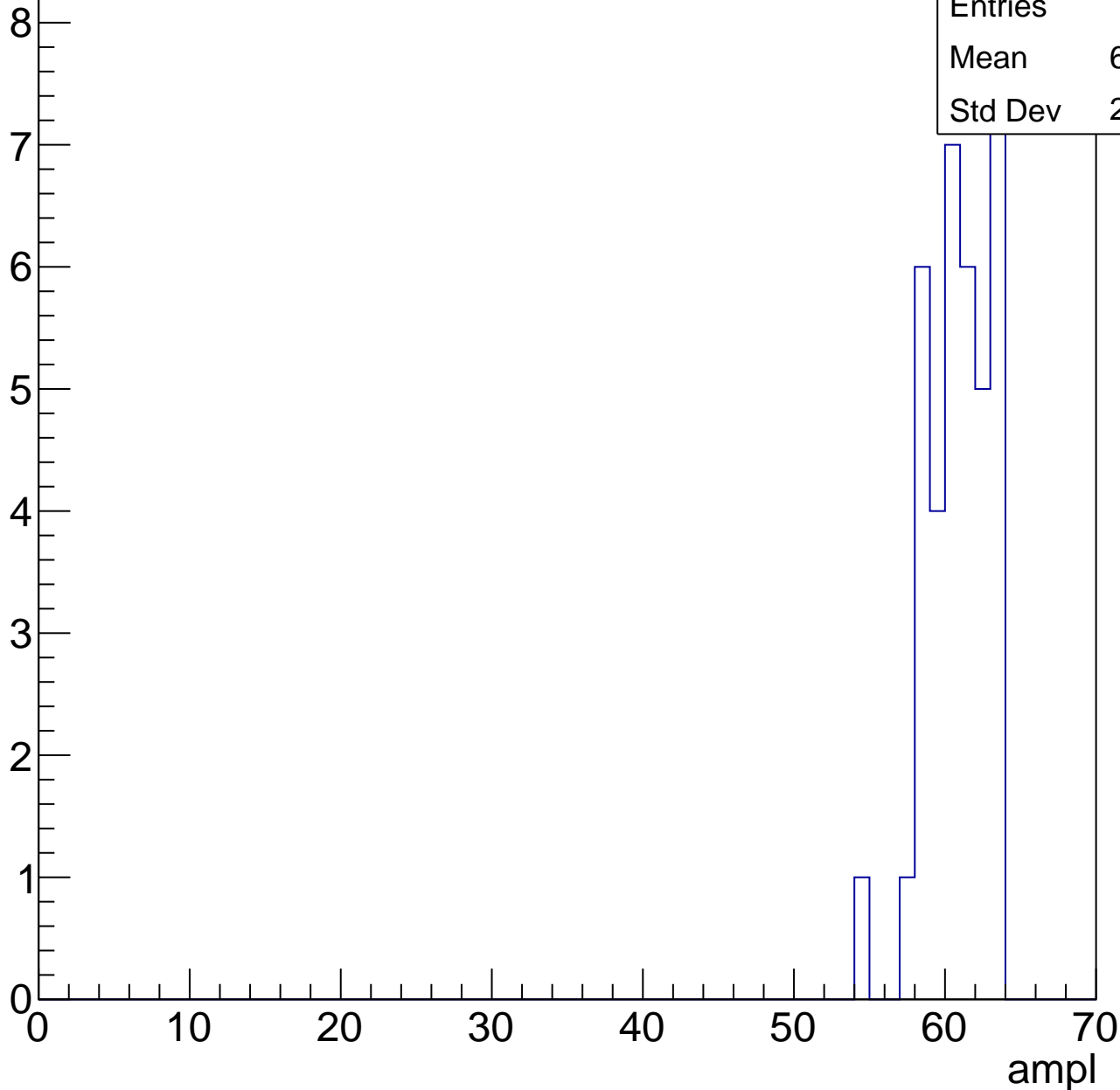


B0L102S, U12-ch99, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	38
Mean	60.39
Std Dev	2.084



B0L102S, U12-ch99, adc6

calib_packv5_042523_0143.root, FC#12, port B1

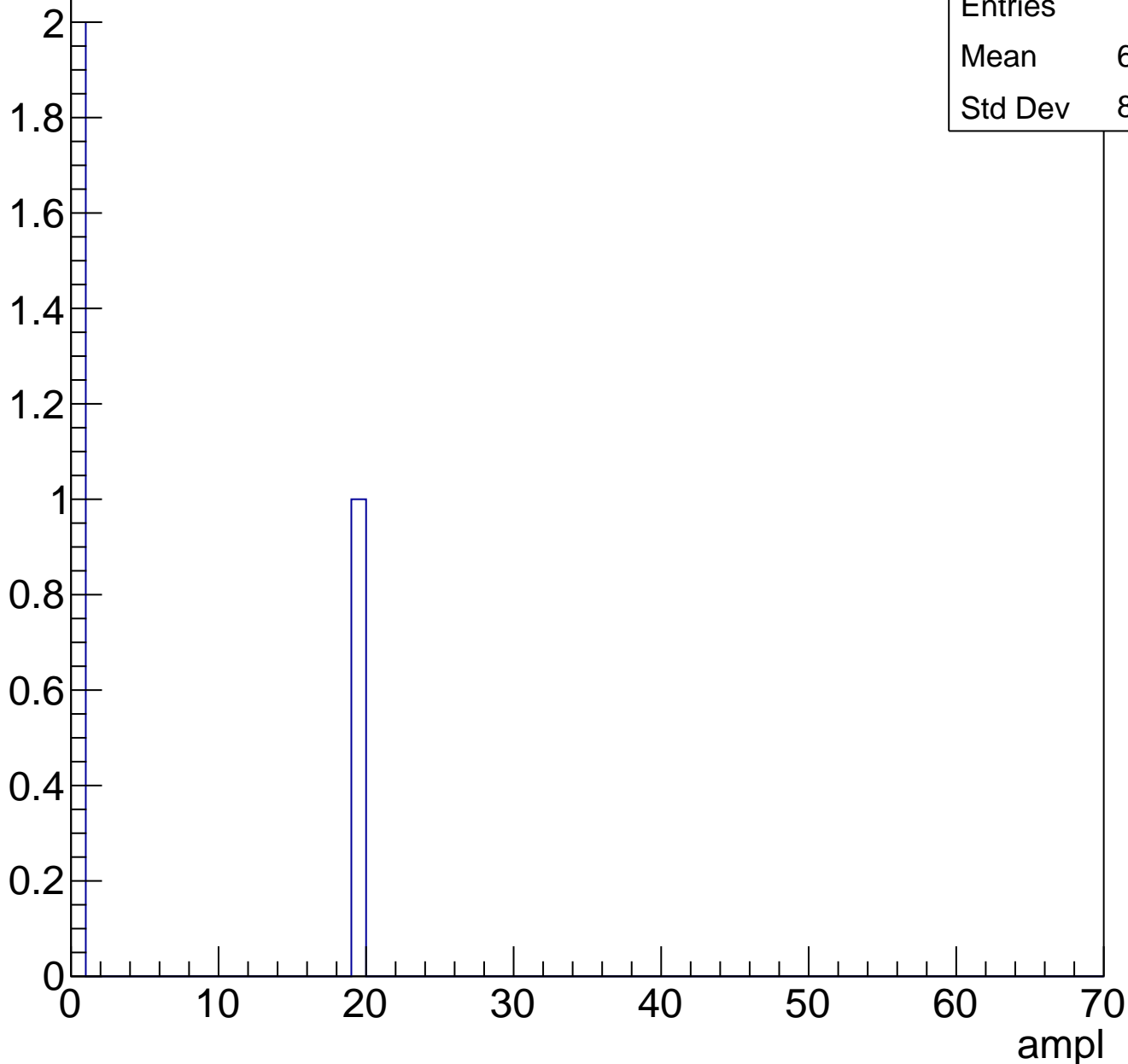
Entry



B0L102S, U12-ch99, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch100, adc0

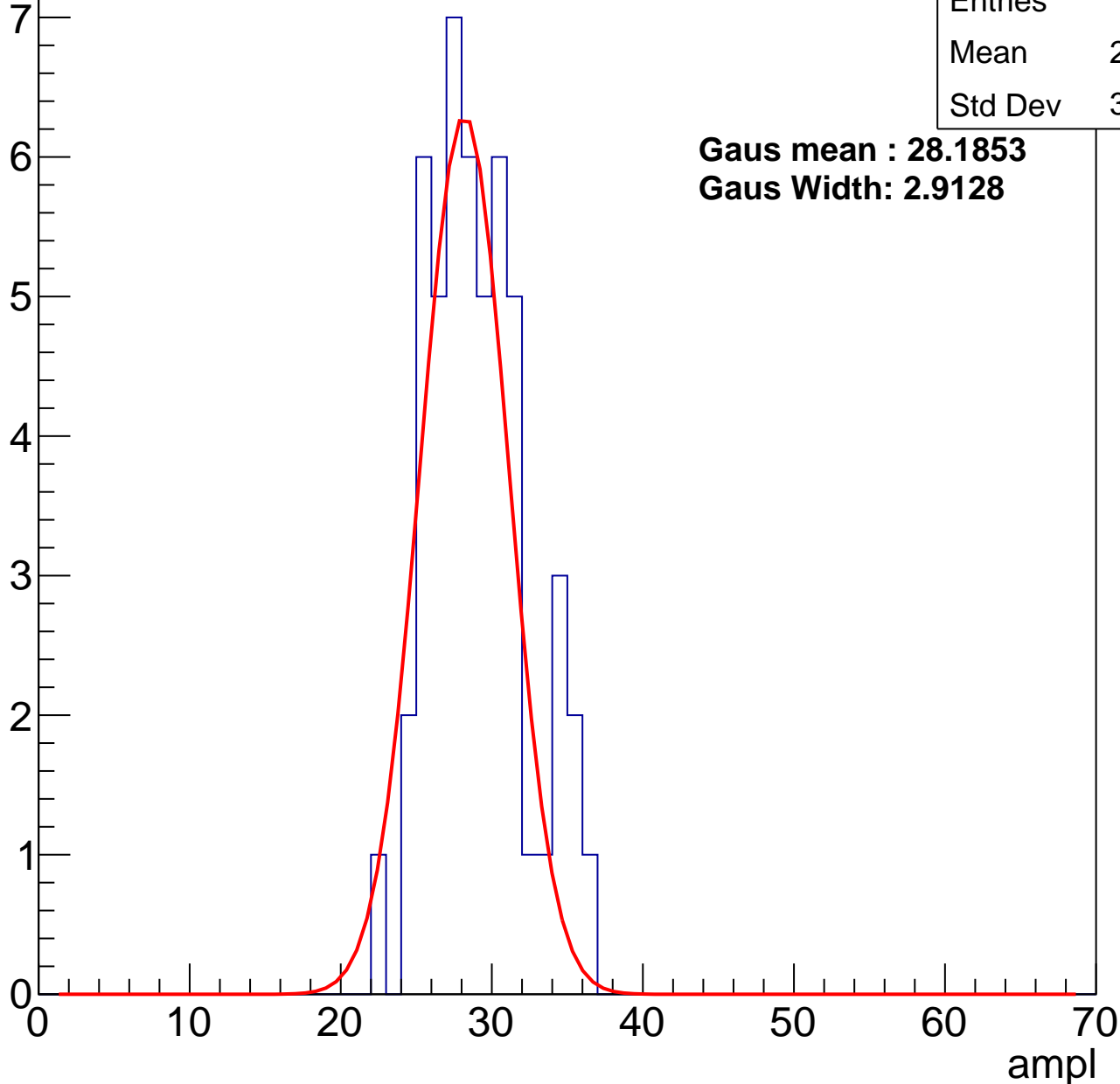
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	28.63
Std Dev	3.174

Gaus mean : 28.1853

Gaus Width: 2.9128



B0L102S, U12-ch100, adc1

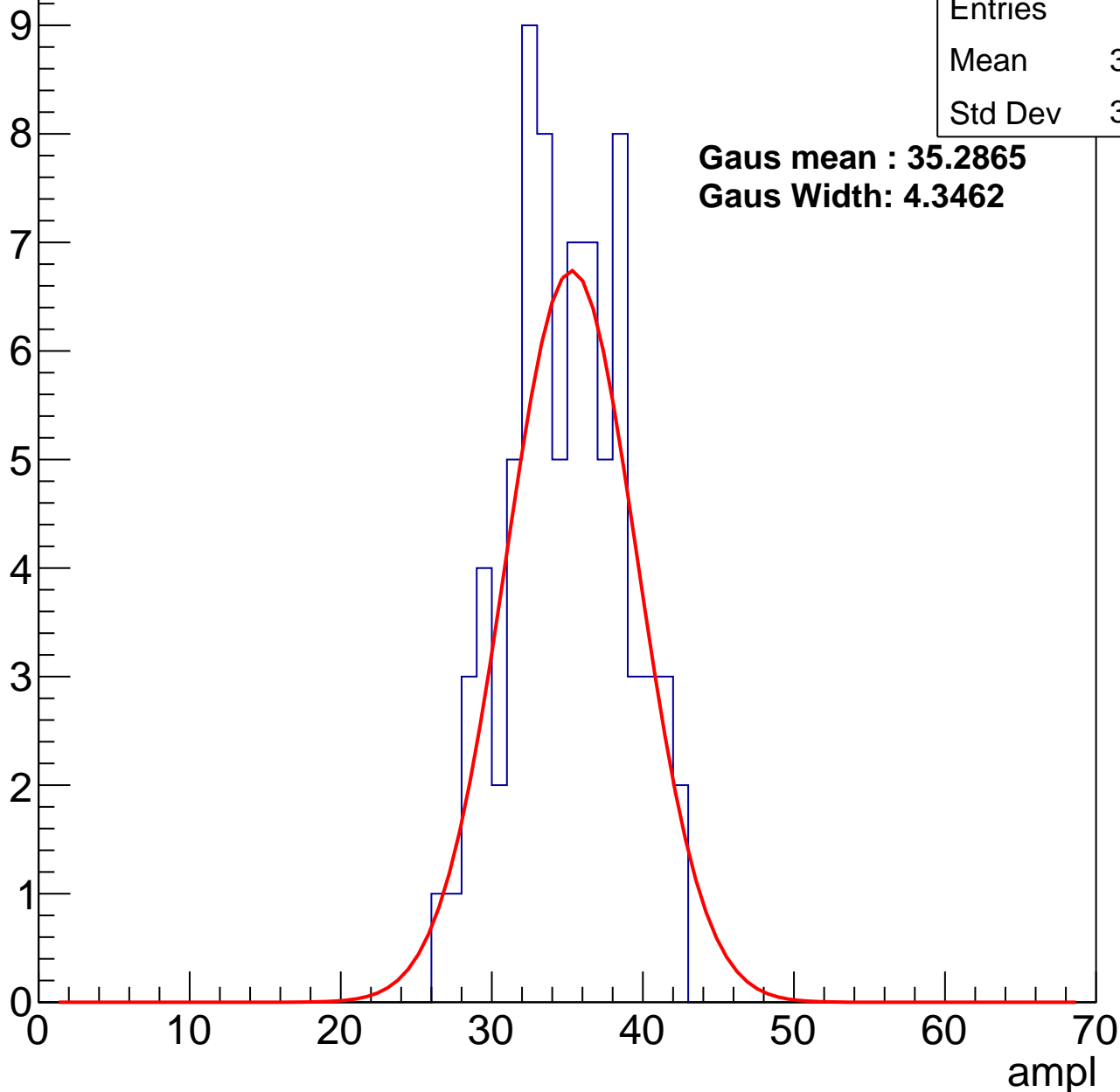
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	34.47
Std Dev	3.789

Gaus mean : 35.2865

Gaus Width: 4.3462



B0L102S, U12-ch100, adc2

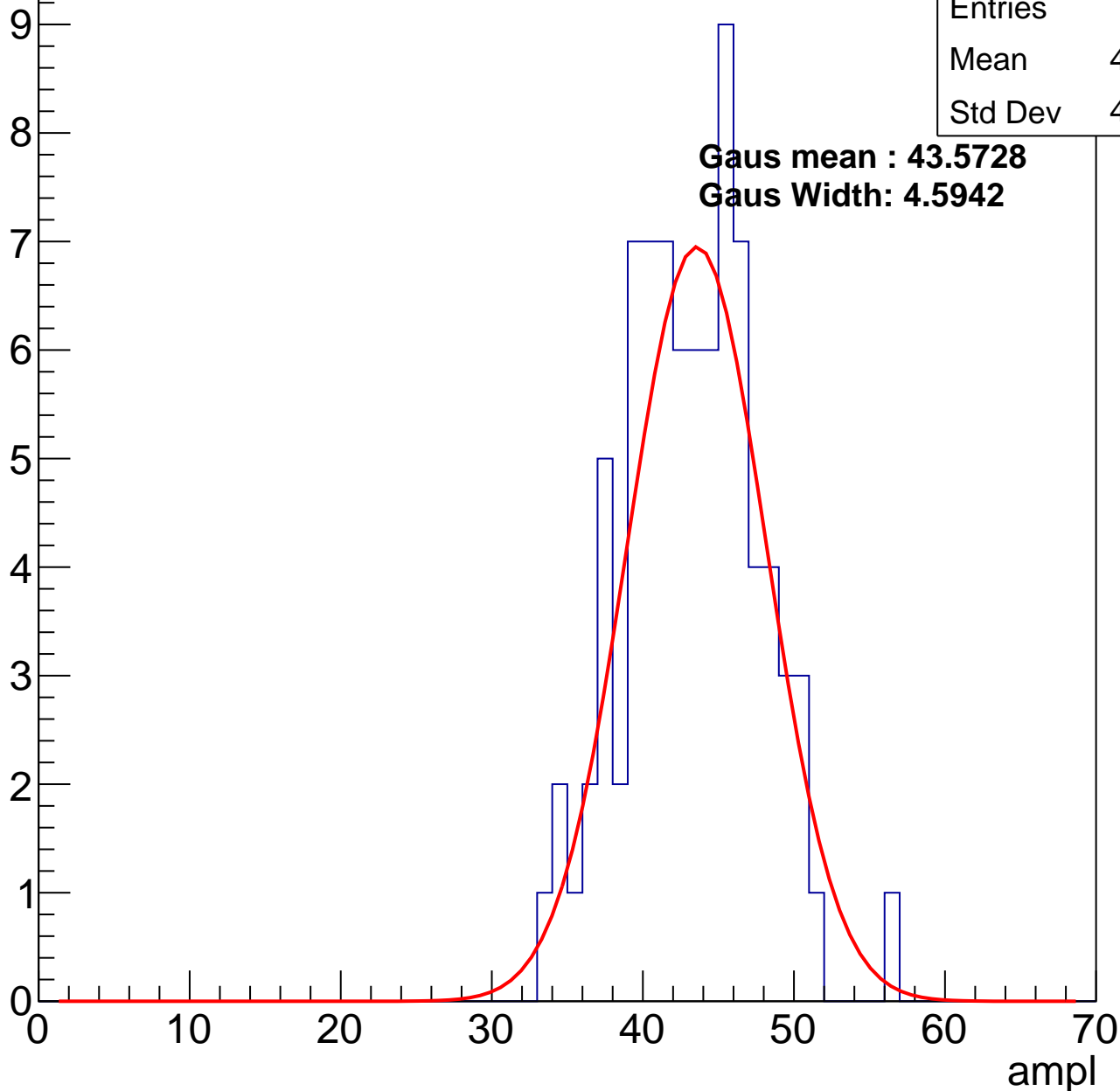
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	42.79
Std Dev	4.386

Gaus mean : 43.5728

Gaus Width: 4.5942

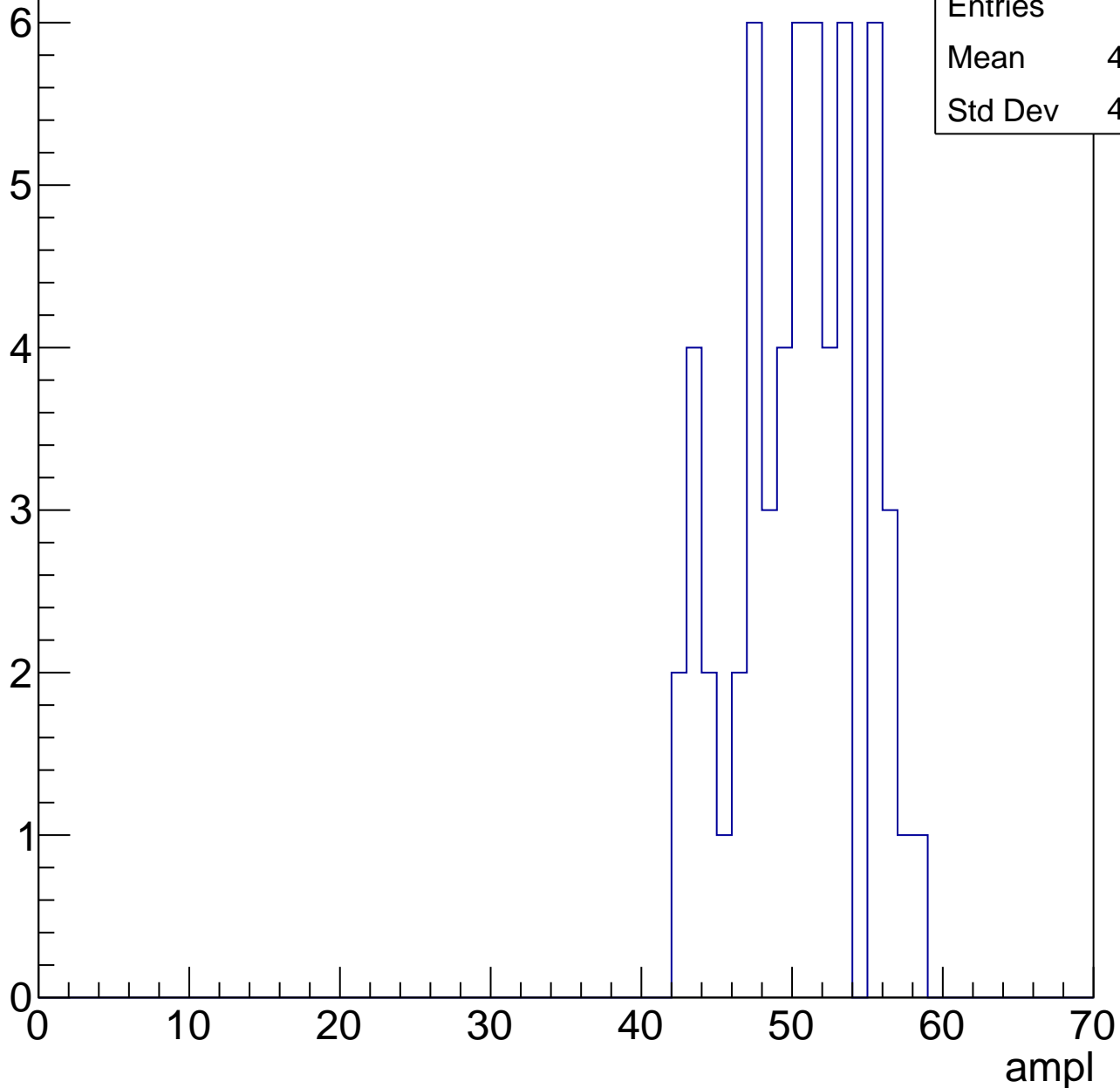


B0L102S, U12-ch100, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

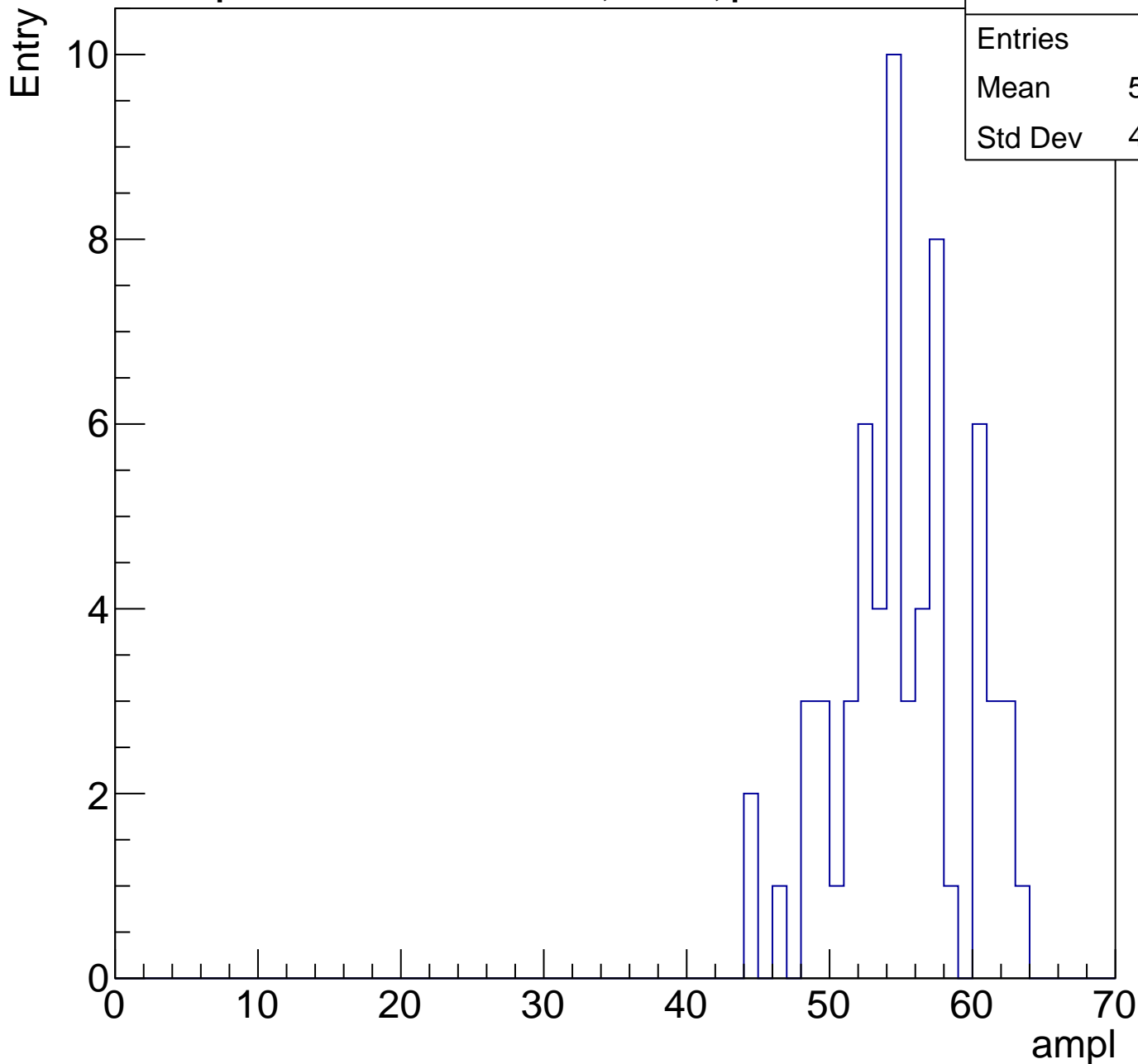
Entries	57
Mean	49.96
Std Dev	4.125



B0L102S, U12-ch100, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	62
Mean	54.63
Std Dev	4.466

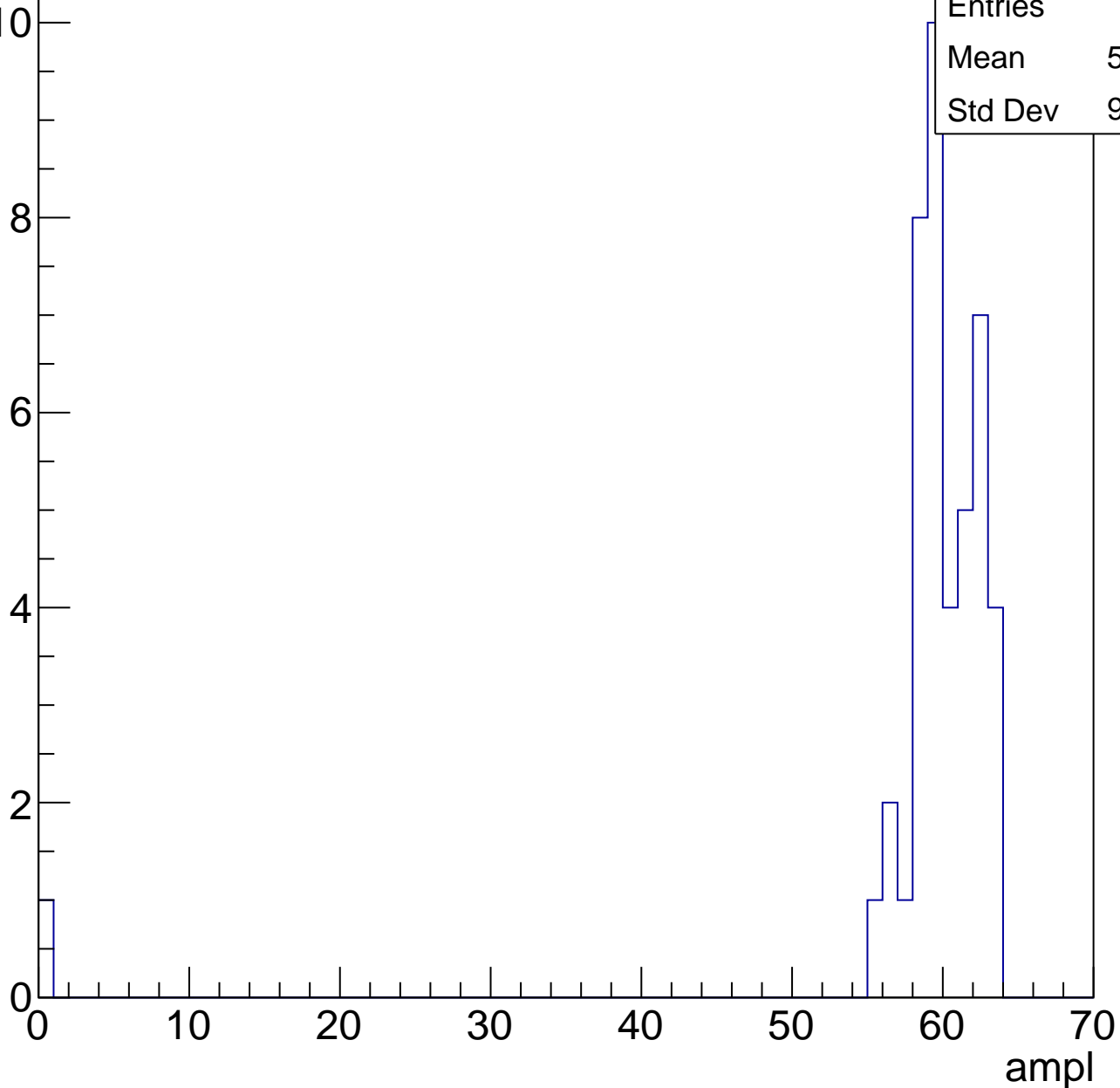


B0L102S, U12-ch100, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	58.35
Std Dev	9.226

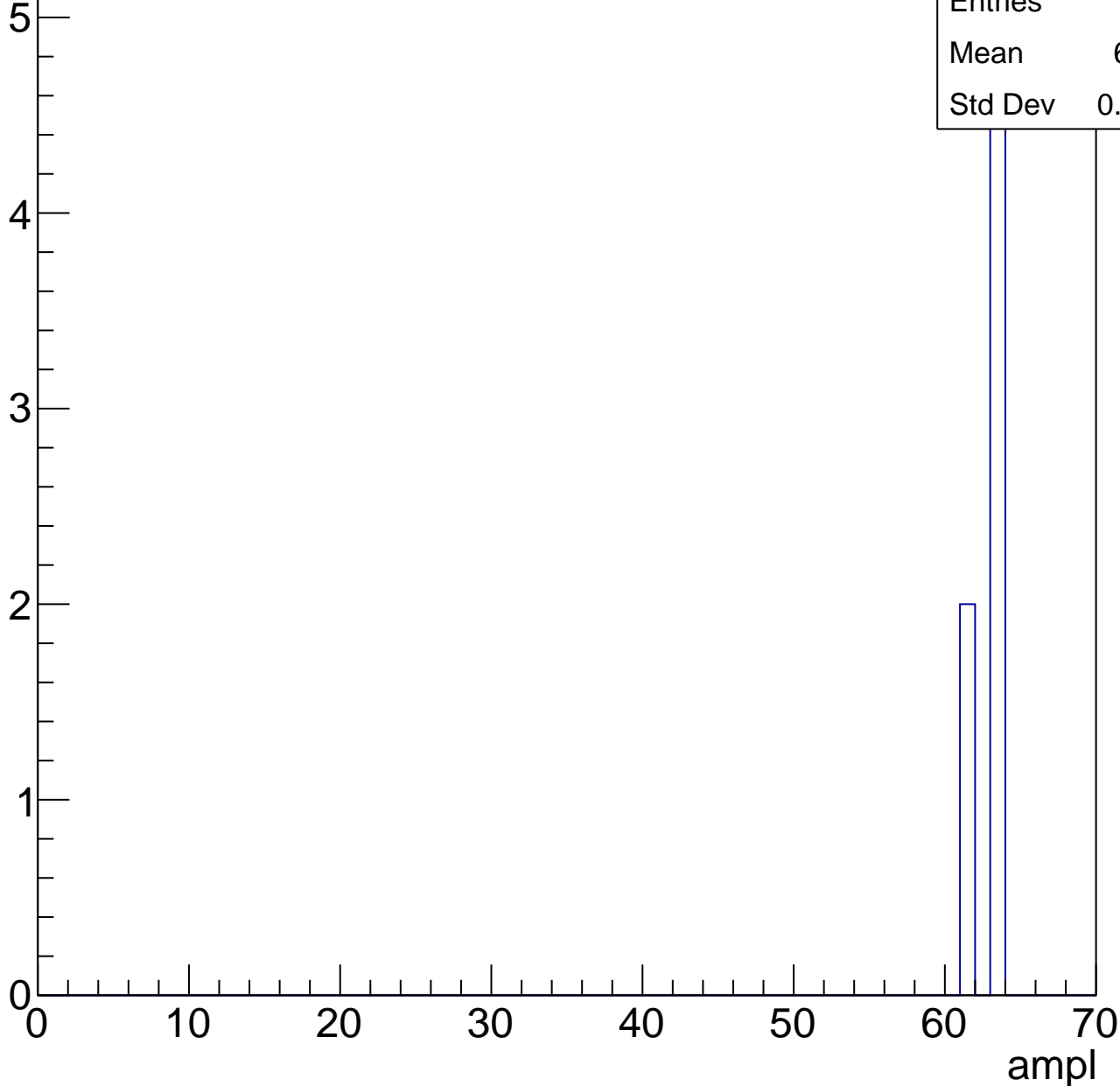


B0L102S, U12-ch100, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

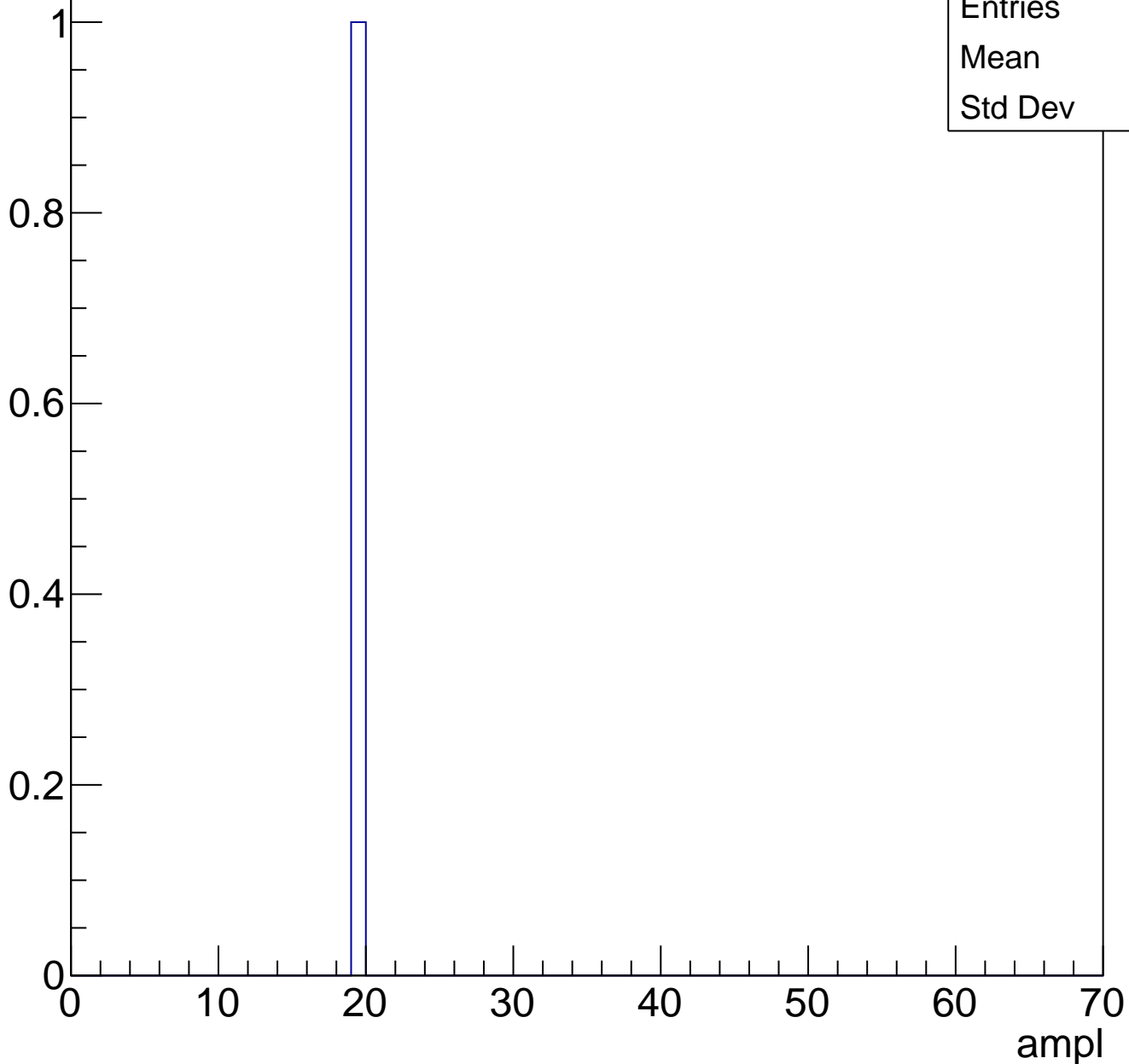
Entries	7
Mean	62.43
Std Dev	0.9035



B0L102S, U12-ch100, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch101, adc0

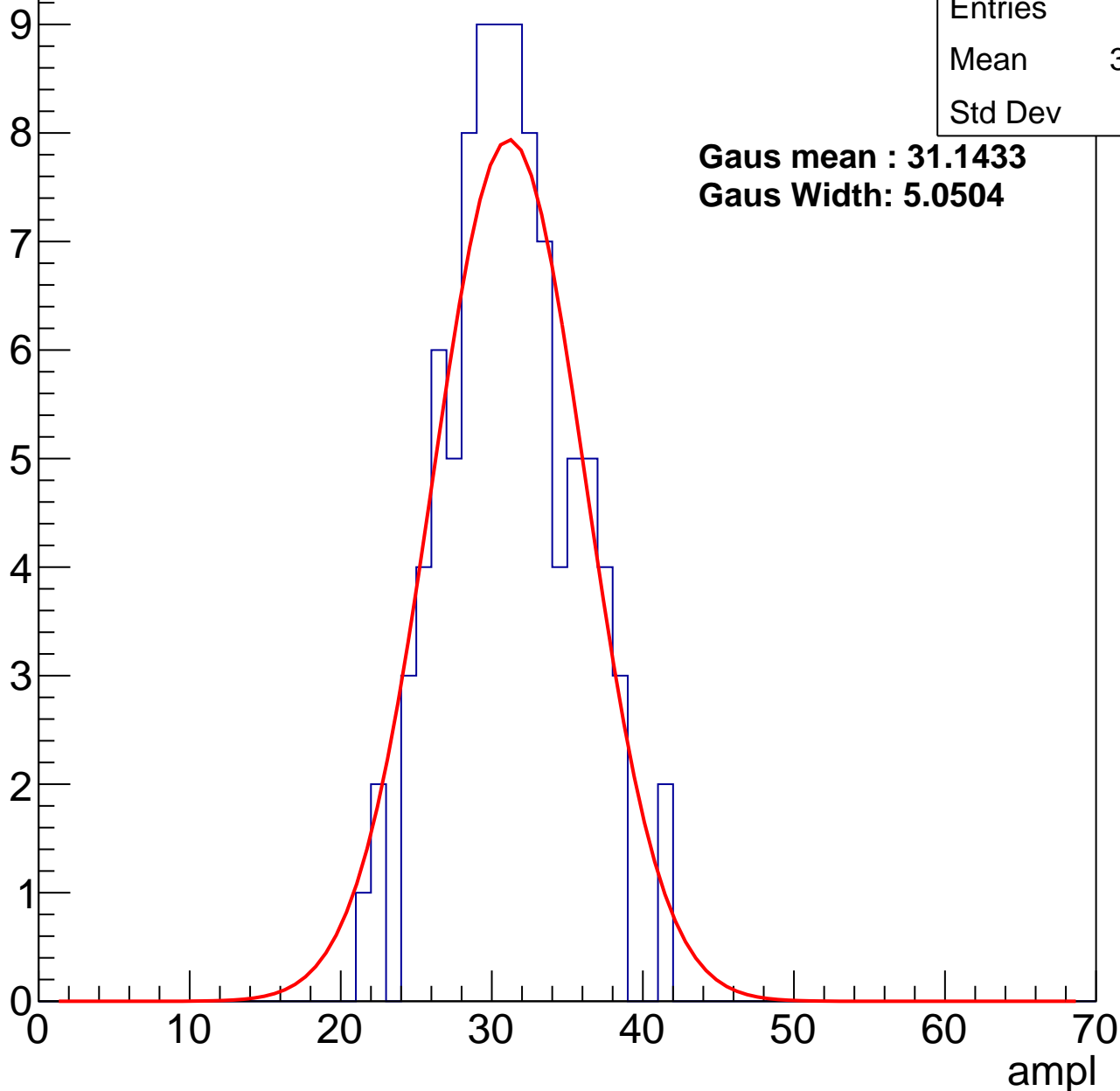
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	94
Mean	30.68
Std Dev	4.2

Gaus mean : 31.1433

Gaus Width: 5.0504



B0L102S, U12-ch101, adc1

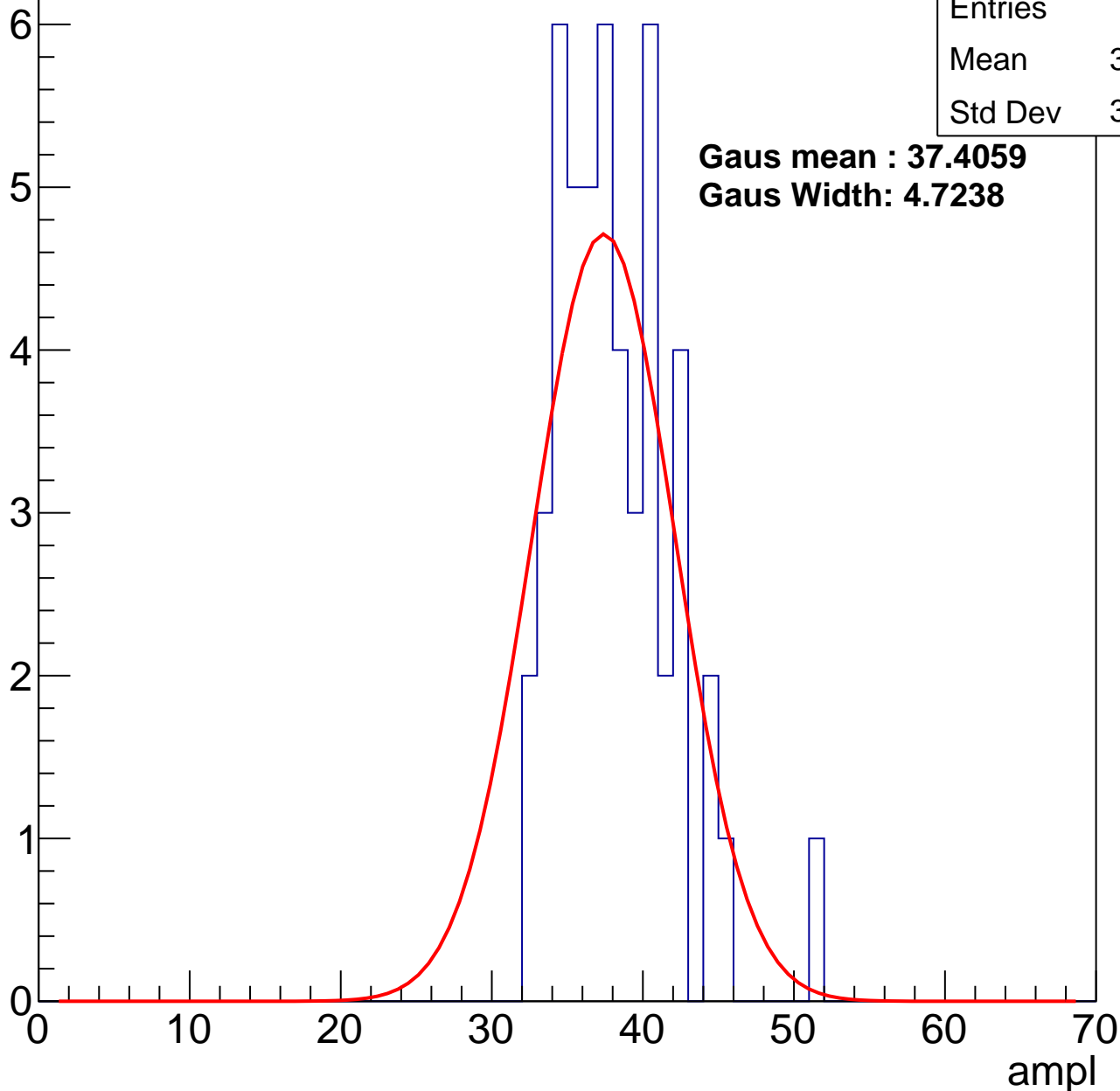
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	37.74
Std Dev	3.767

Gaus mean : 37.4059

Gaus Width: 4.7238



B0L102S, U12-ch101, adc2

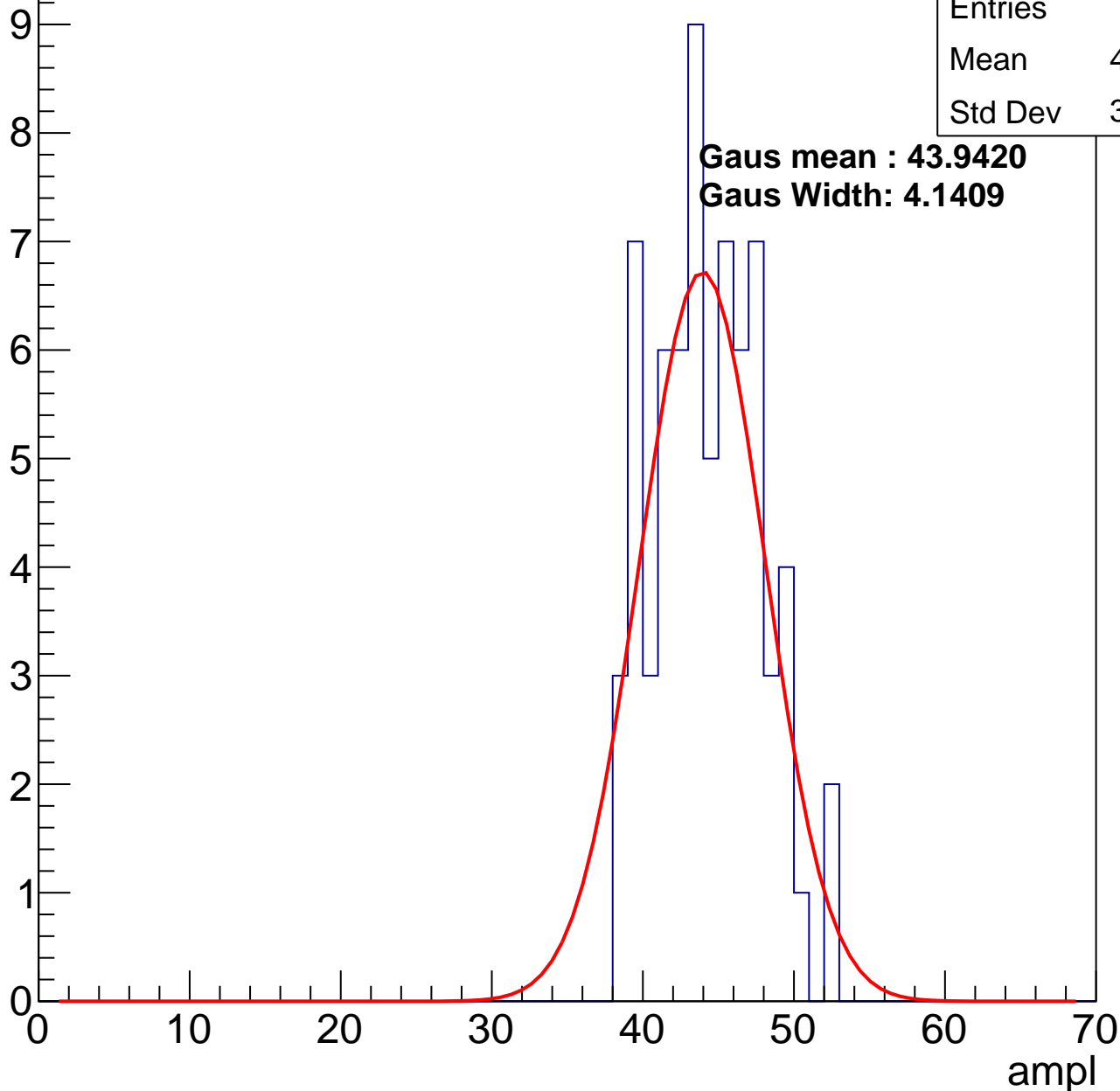
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	43.86
Std Dev	3.453

Gaus mean : 43.9420

Gaus Width: 4.1409

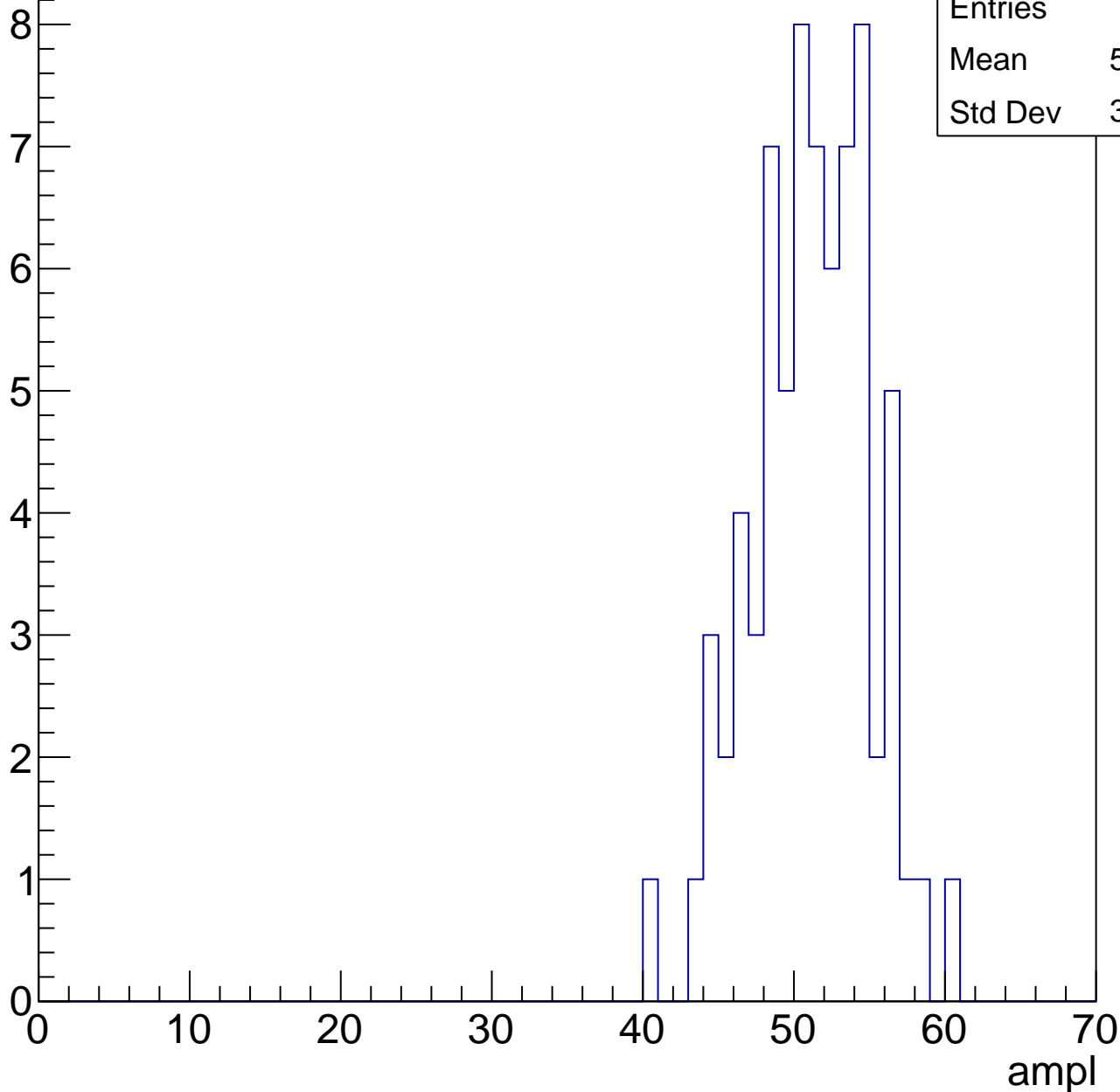


B0L102S, U12-ch101, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	50.67
Std Dev	3.844

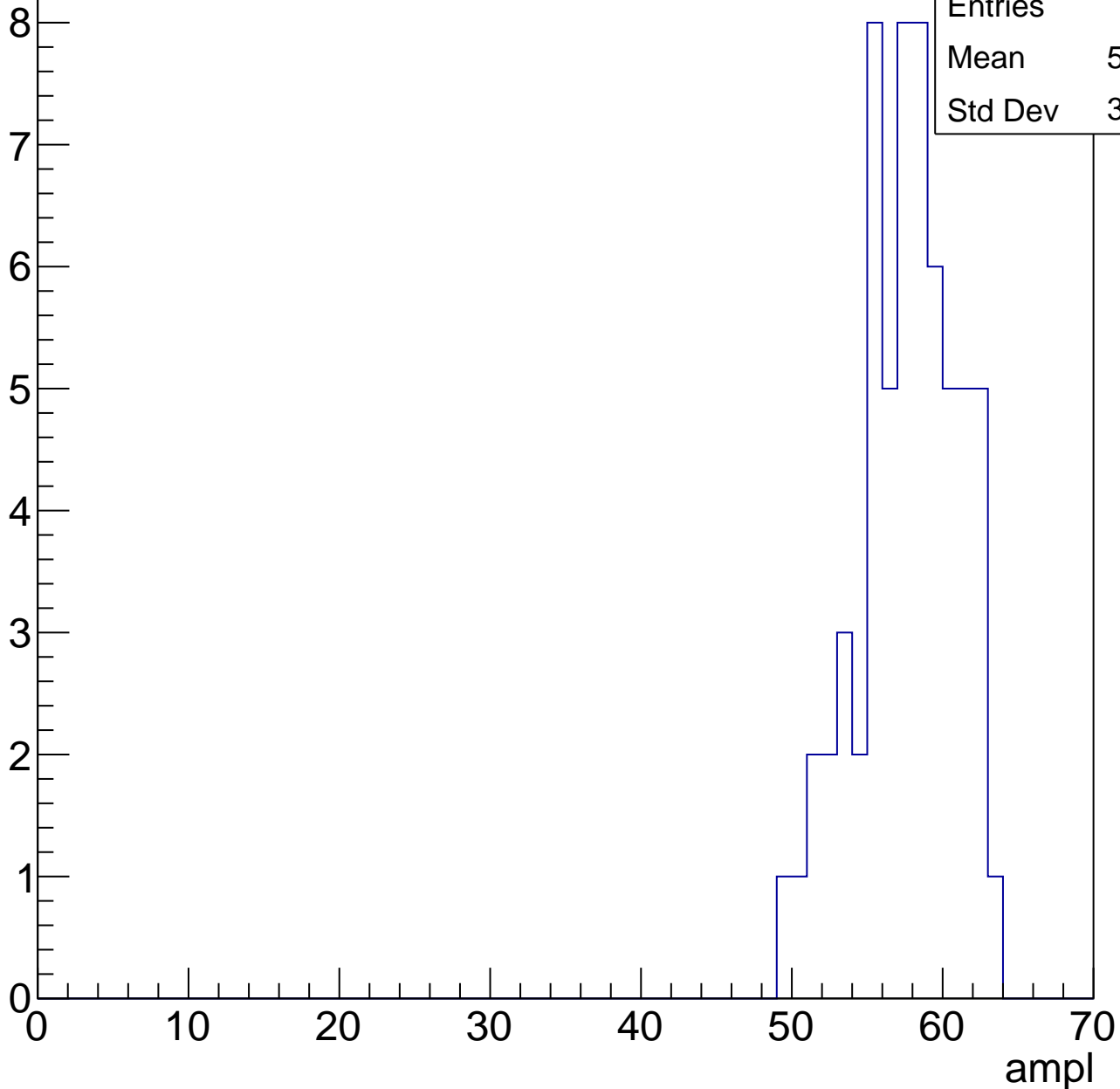


B0L102S, U12-ch101, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	57.16
Std Dev	3.249

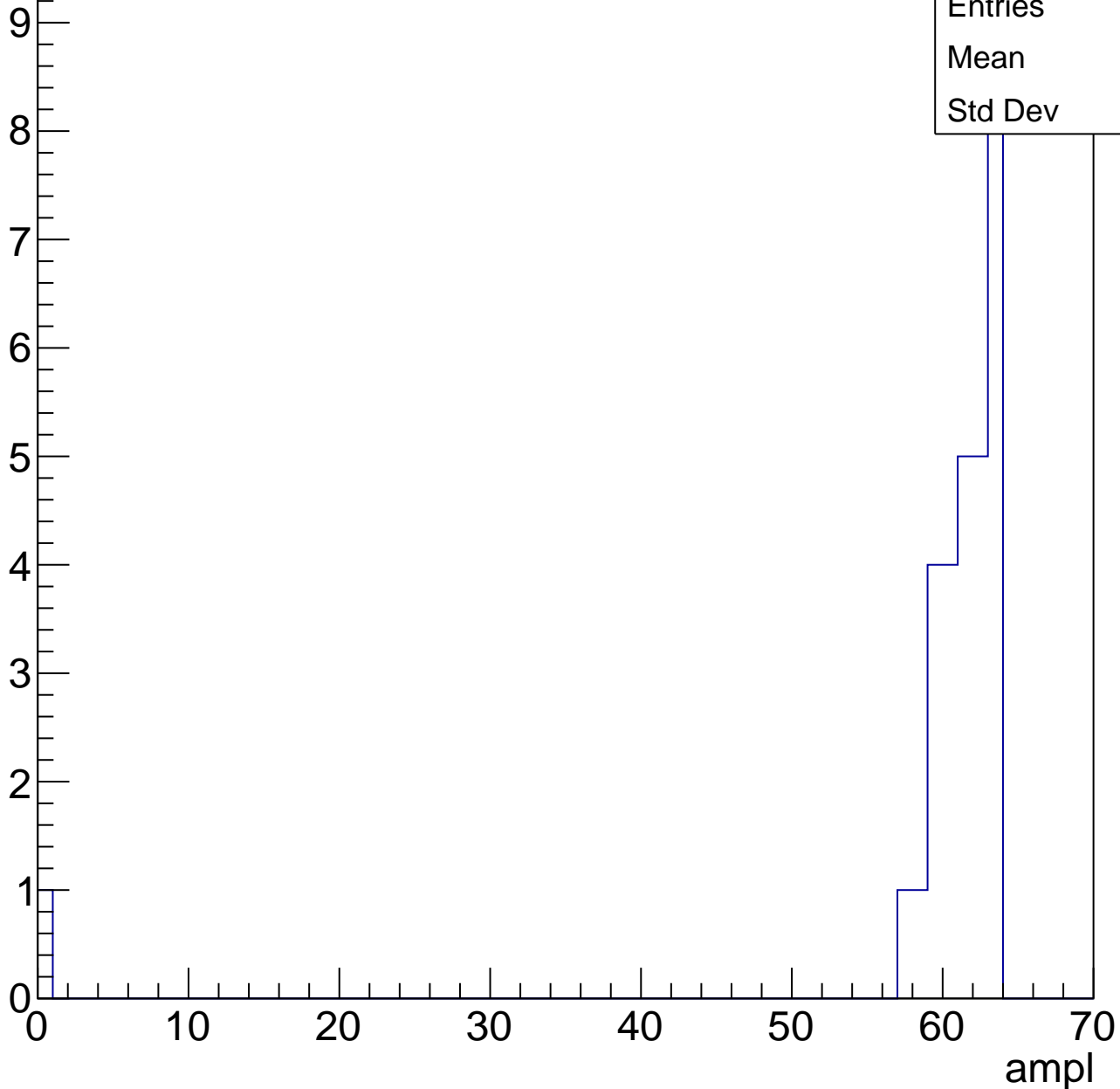


B0L102S, U12-ch101, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	30
Mean	59.1
Std Dev	11.1



B0L102S, U12-ch101, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch101, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch102, adc0

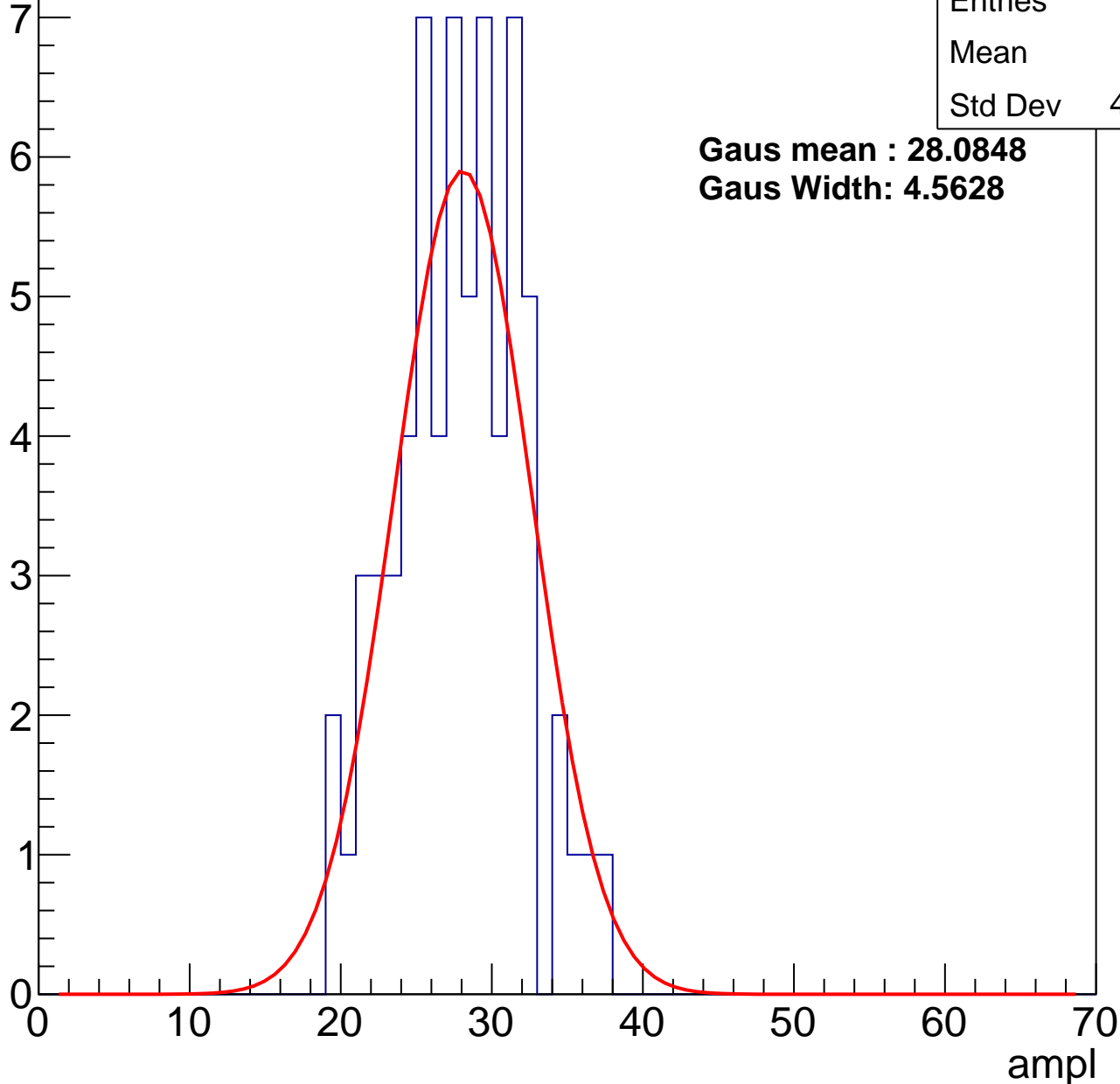
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	27.4
Std Dev	4.078

Gaus mean : 28.0848

Gaus Width: 4.5628



B0L102S, U12-ch102, adc1

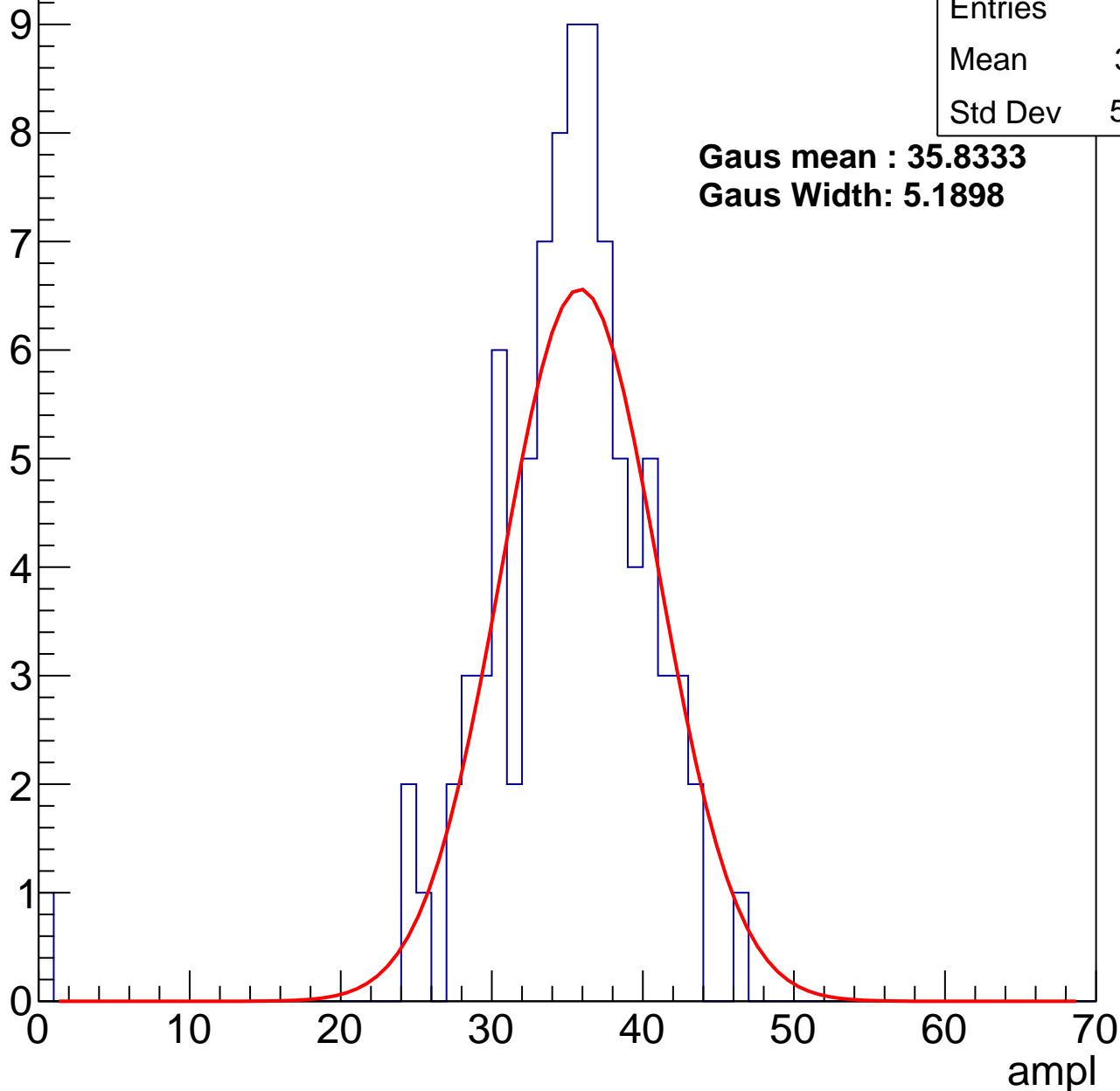
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	88
Mean	34.41
Std Dev	5.797

Gaus mean : 35.8333

Gaus Width: 5.1898



B0L102S, U12-ch102, adc2

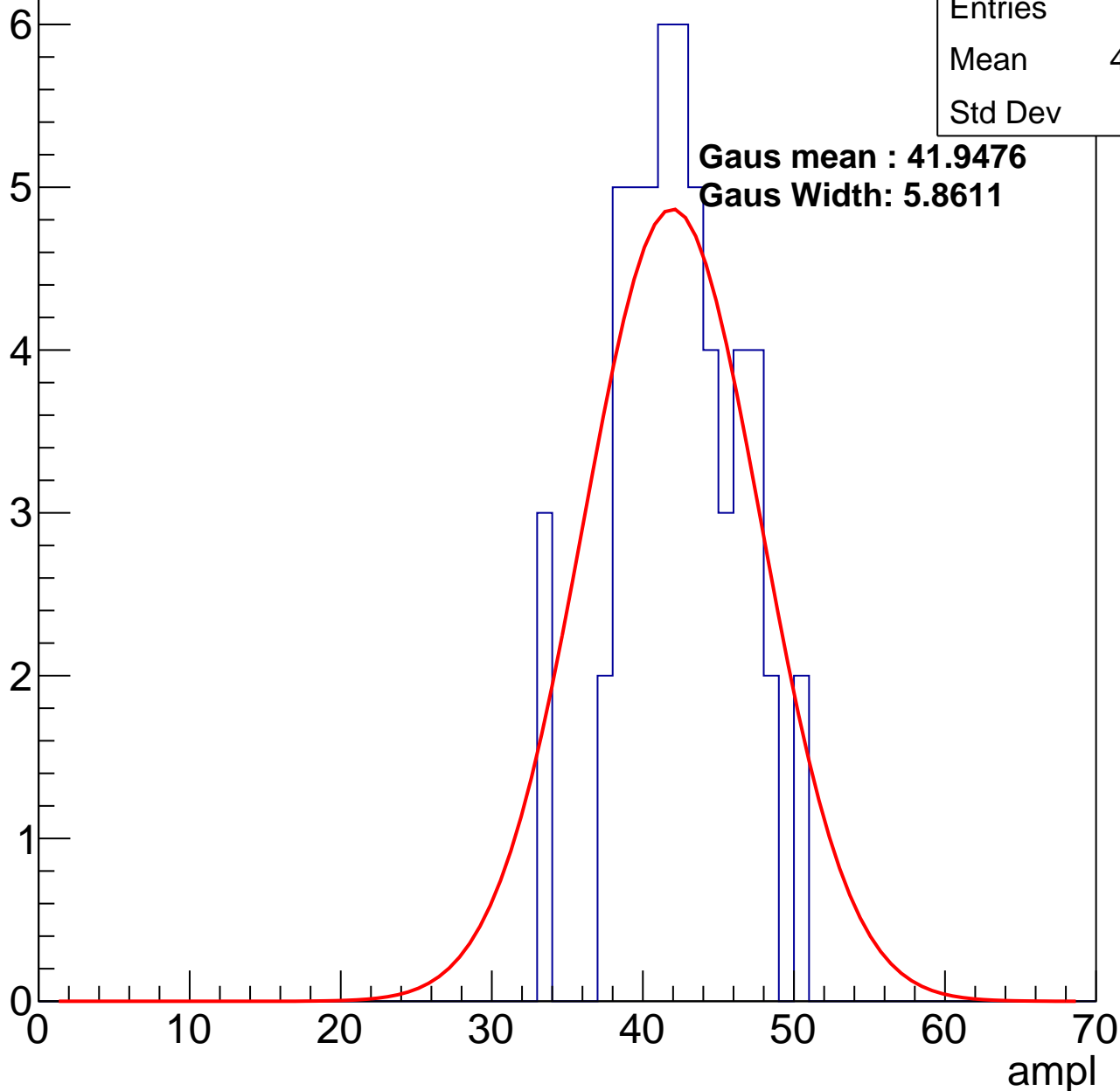
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	41.96
Std Dev	3.91

Gaus mean : 41.9476

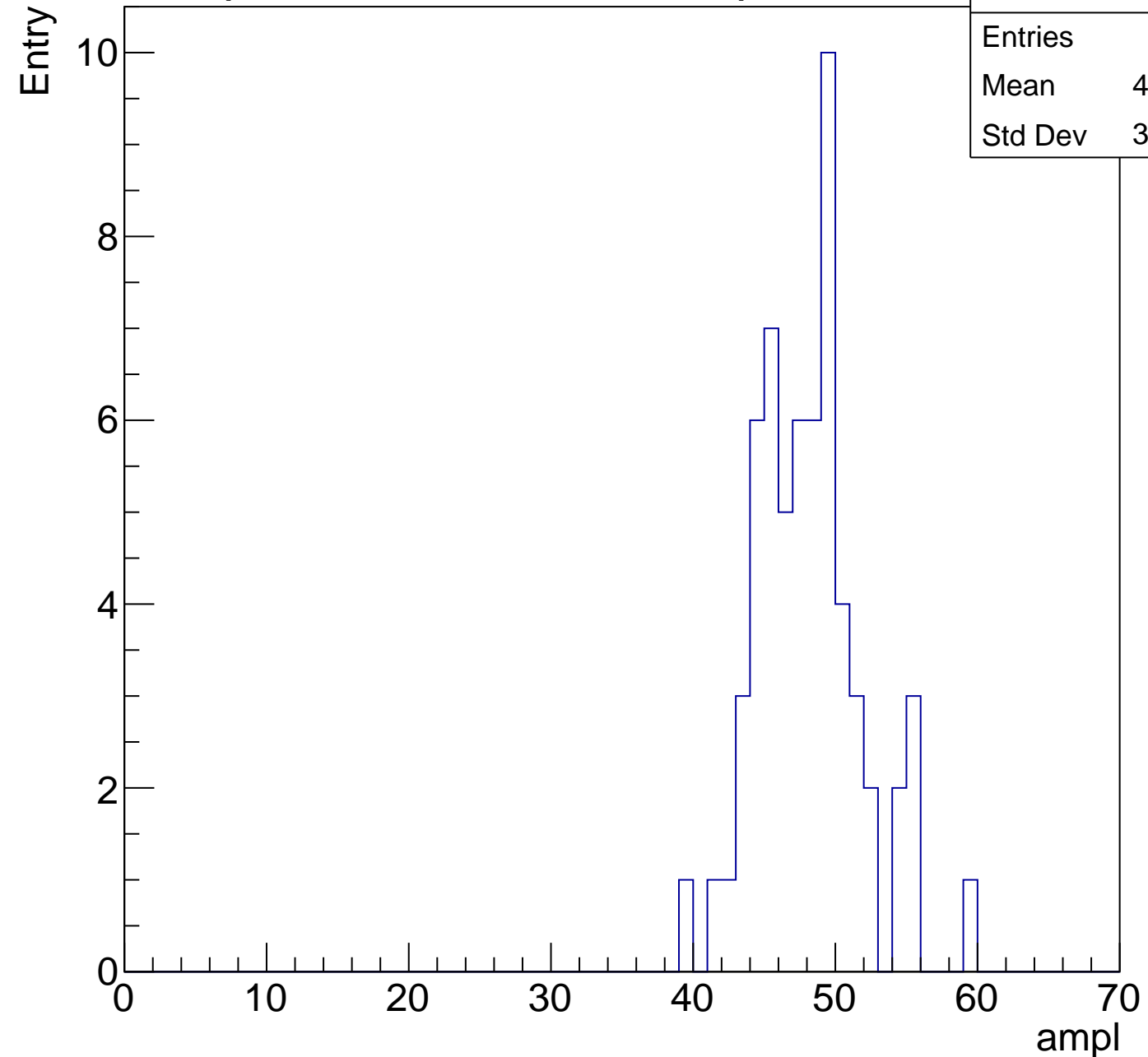
Gaus Width: 5.8611



B0L102S, U12-ch102, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	61
Mean	47.69
Std Dev	3.722

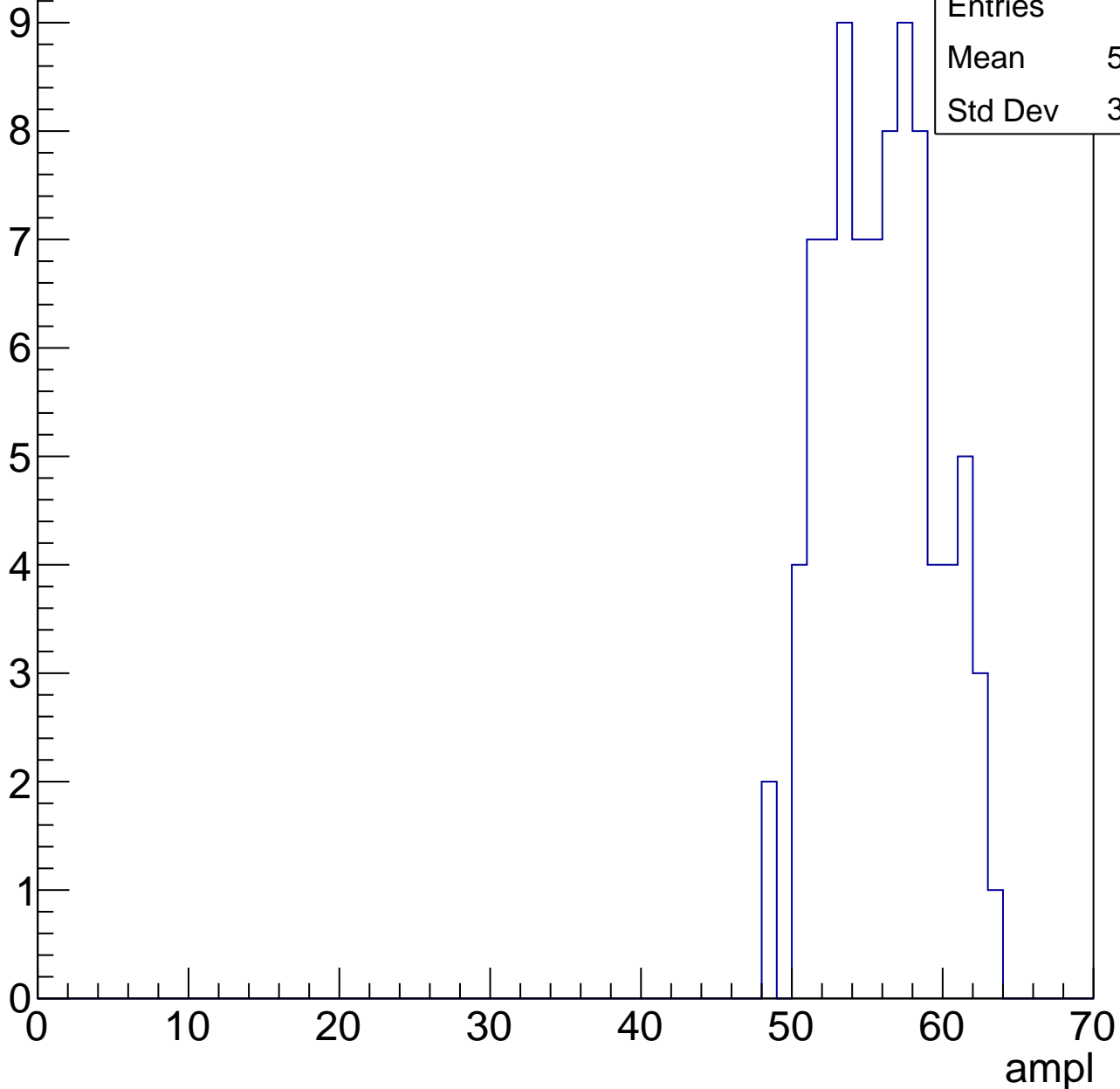


B0L102S, U12-ch102, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	55.44
Std Dev	3.549

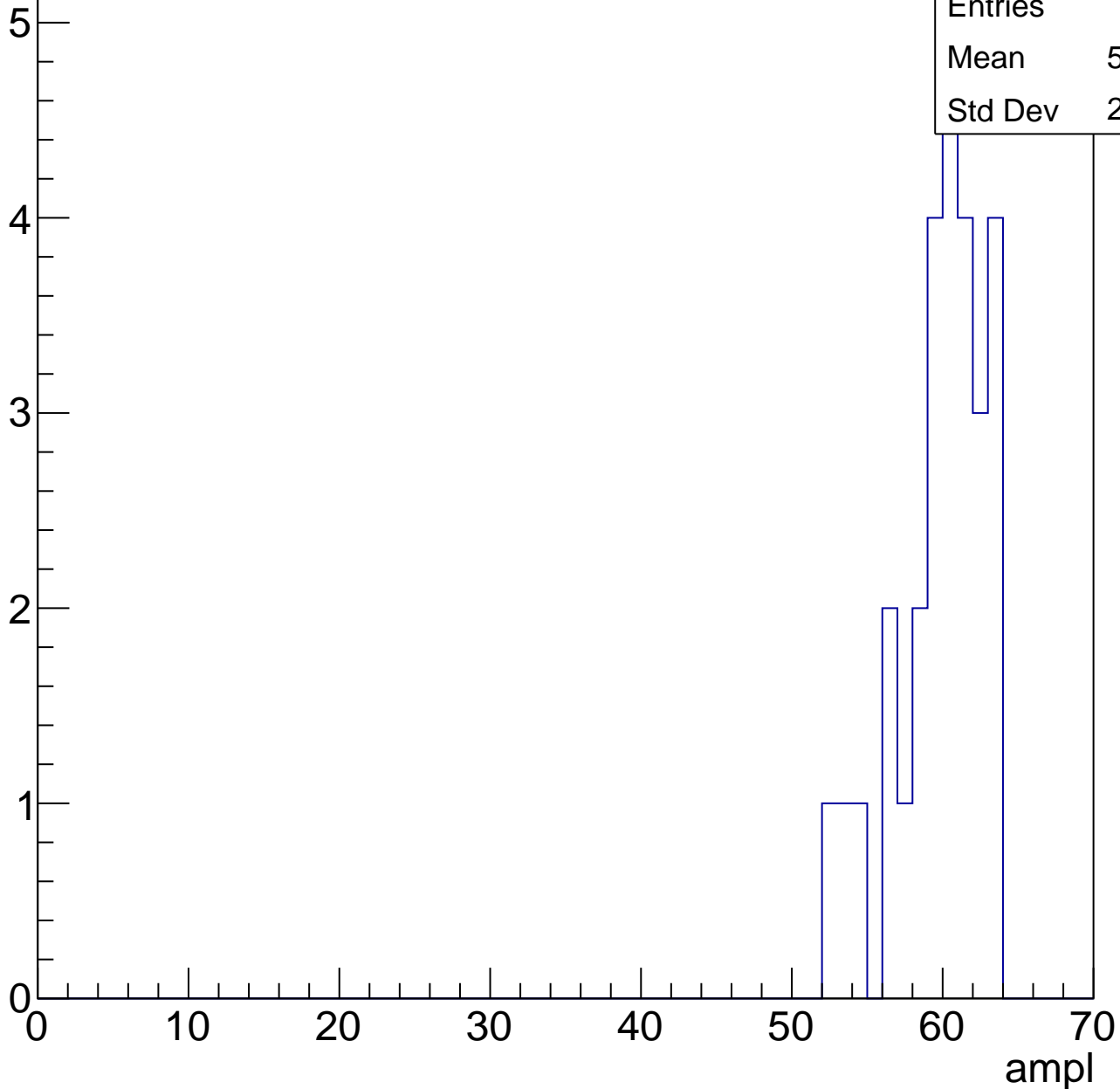


B0L102S, U12-ch102, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

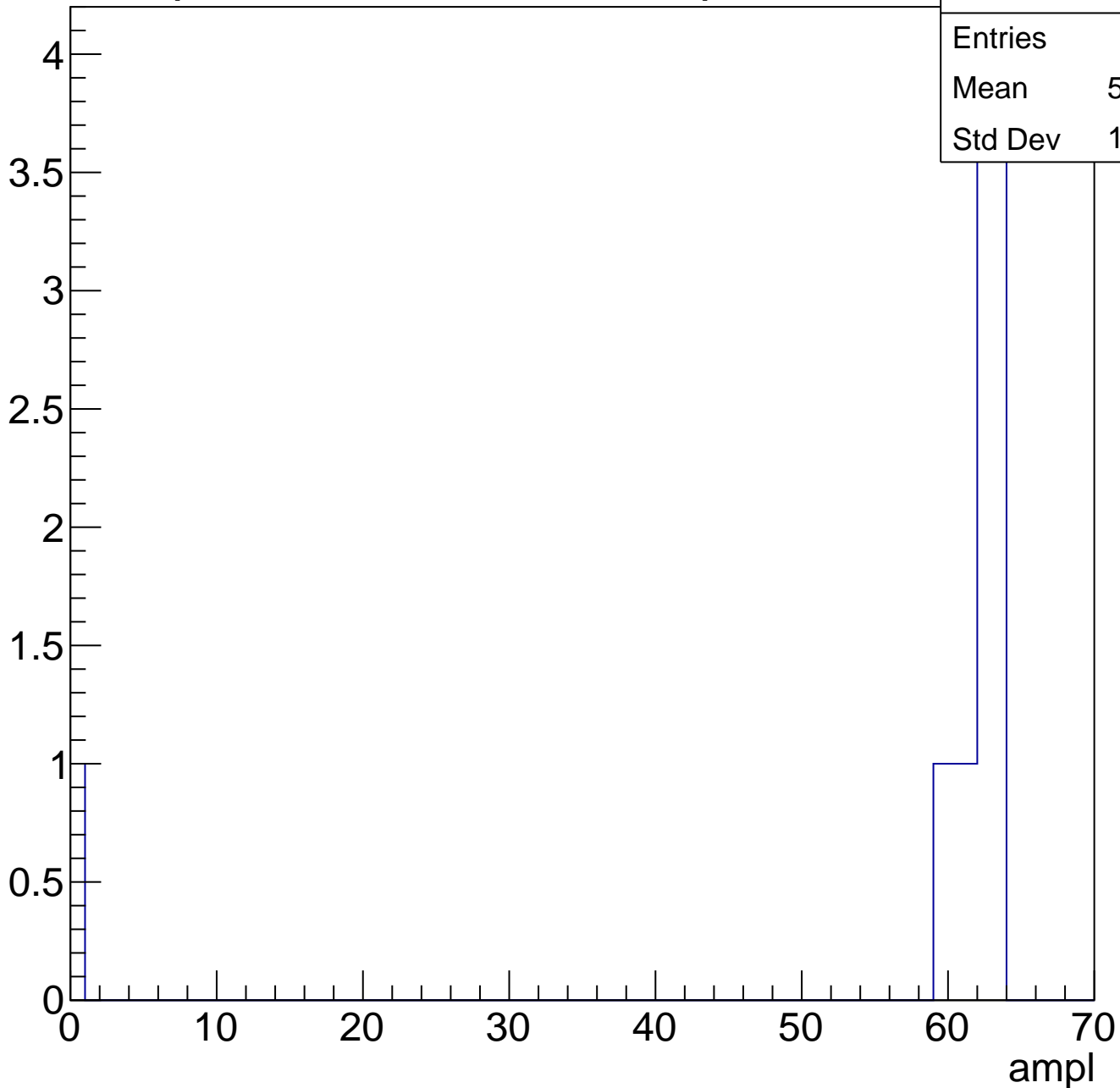
Entries	28
Mean	59.36
Std Dev	2.942



B0L102S, U12-ch102, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch102, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0

B0L102S, U12-ch103, adc0

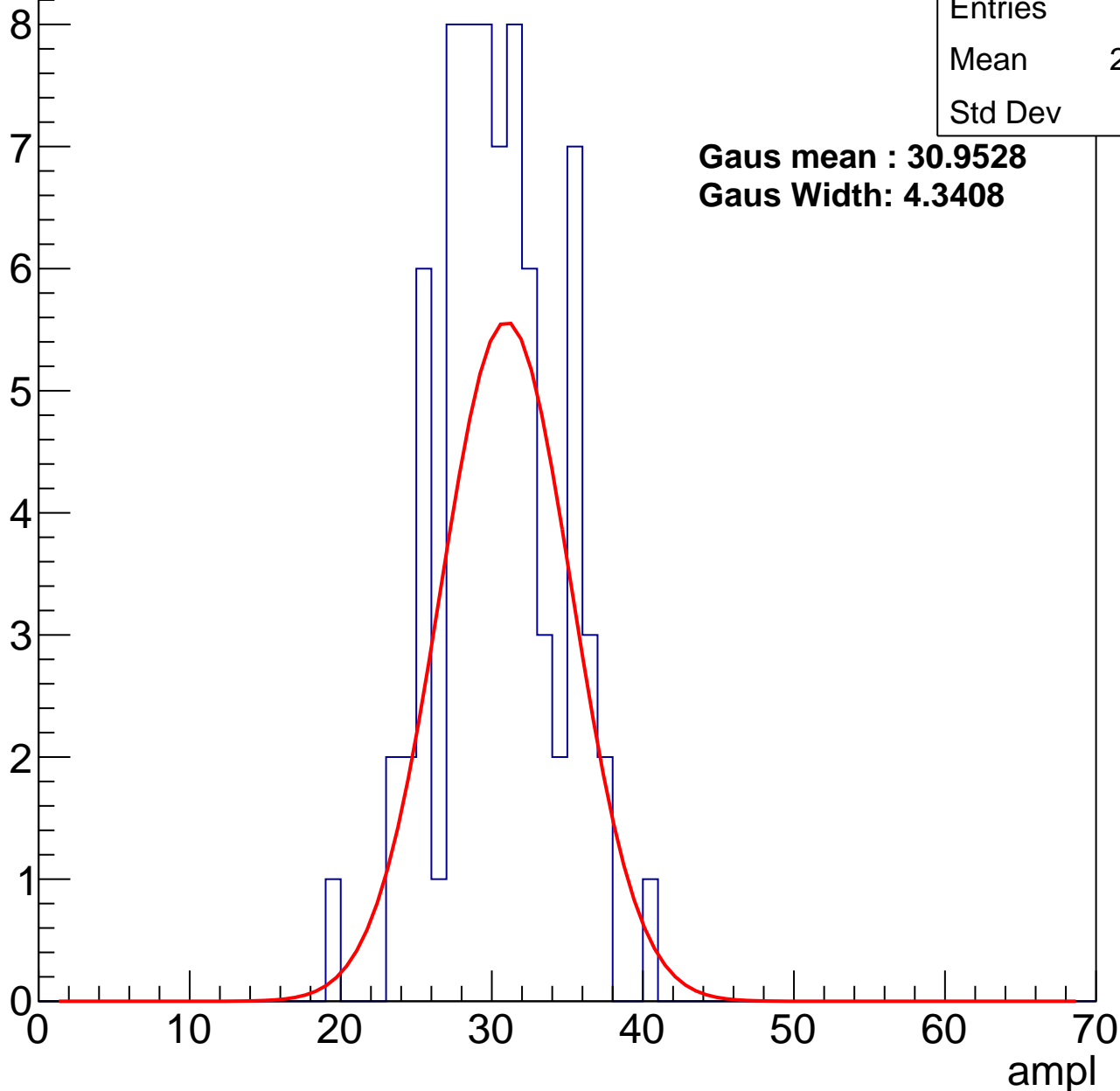
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	29.93
Std Dev	3.9

Gaus mean : 30.9528

Gaus Width: 4.3408



B0L102S, U12-ch103, adc1

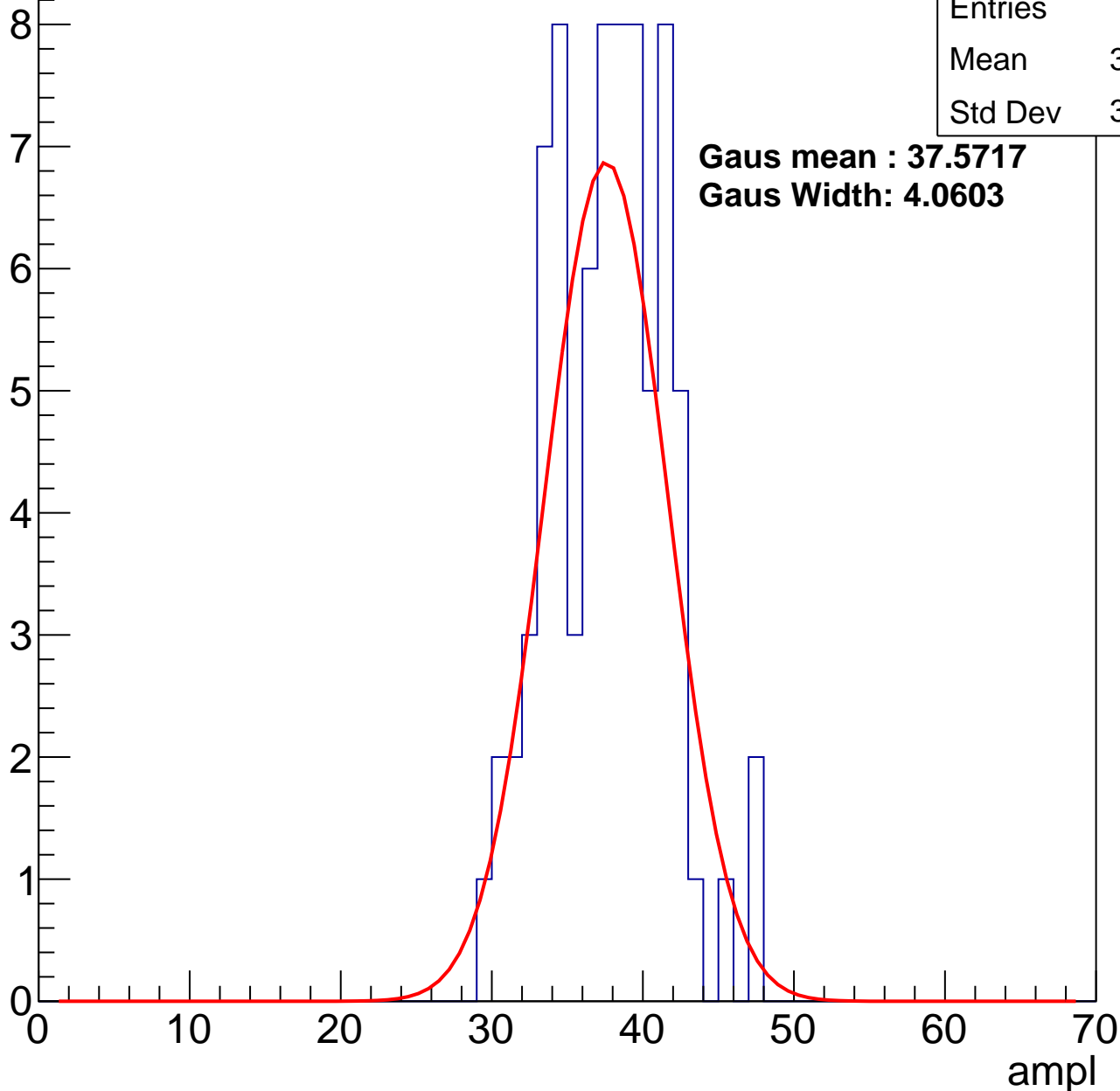
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	37.22
Std Dev	3.832

Gaus mean : 37.5717

Gaus Width: 4.0603



B0L102S, U12-ch103, adc2

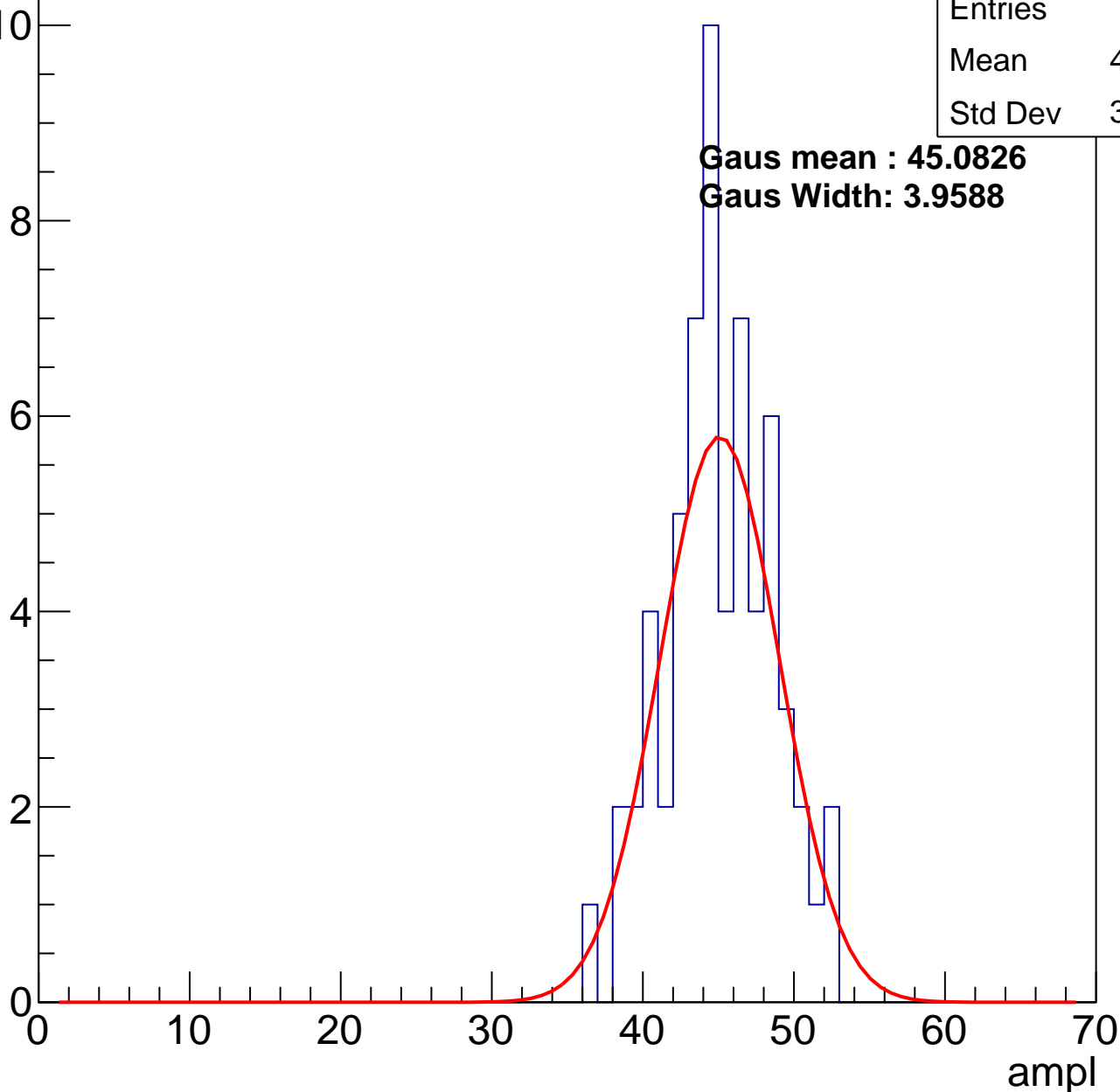
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	44.56
Std Dev	3.509

Gaus mean : 45.0826

Gaus Width: 3.9588

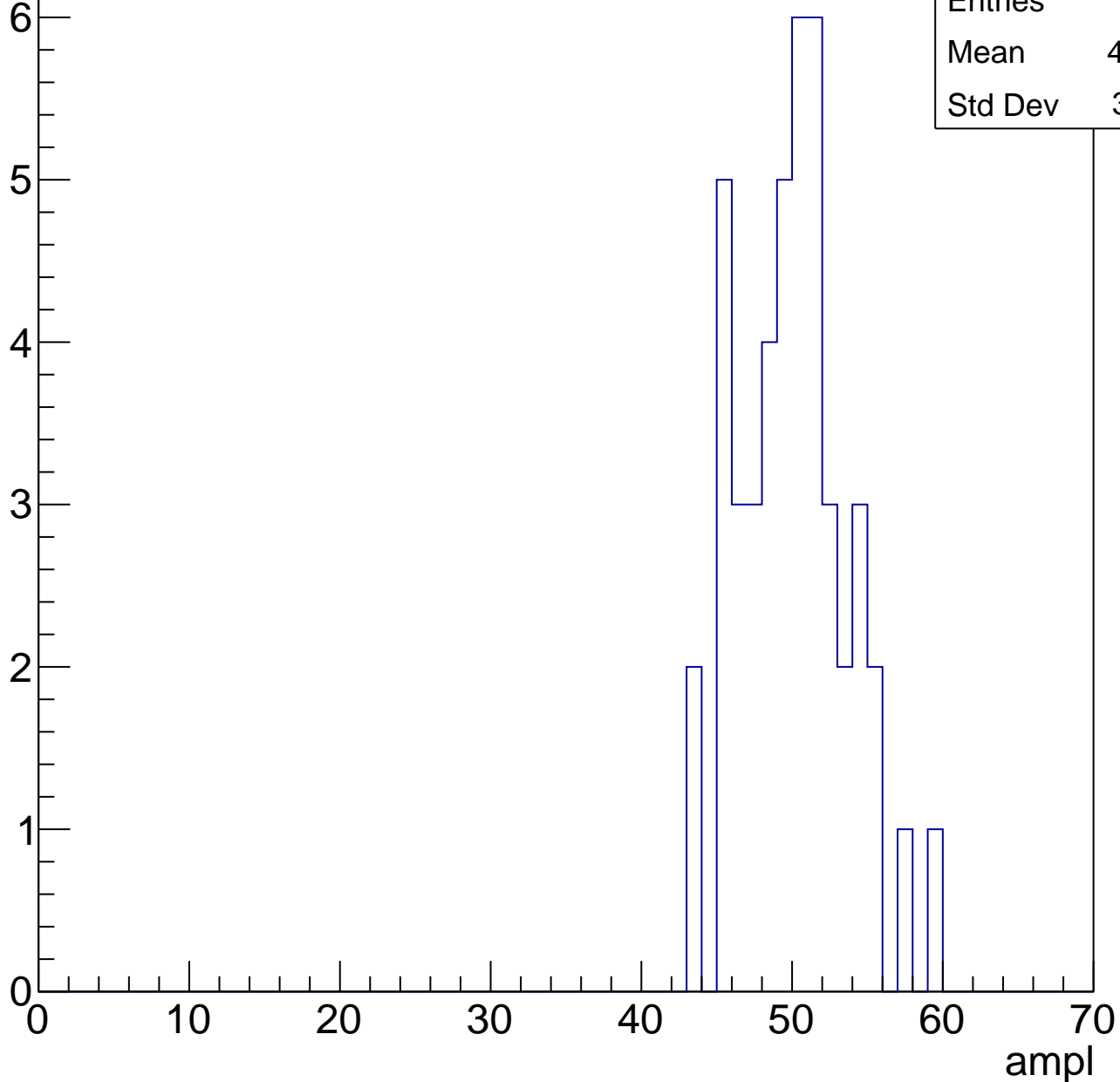


B0L102S, U12-ch103, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	49.63
Std Dev	3.541

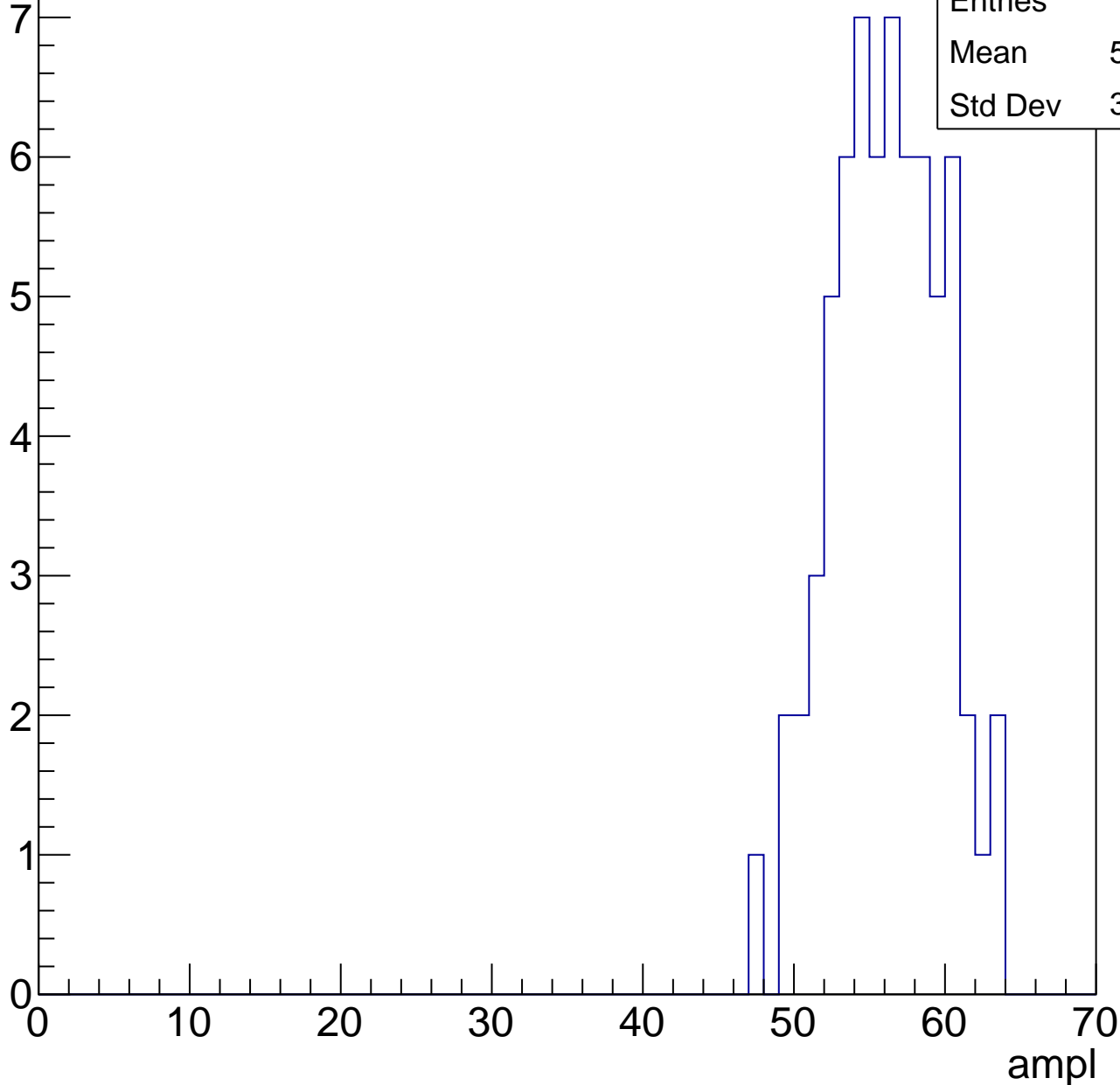


B0L102S, U12-ch103, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	55.69
Std Dev	3.546

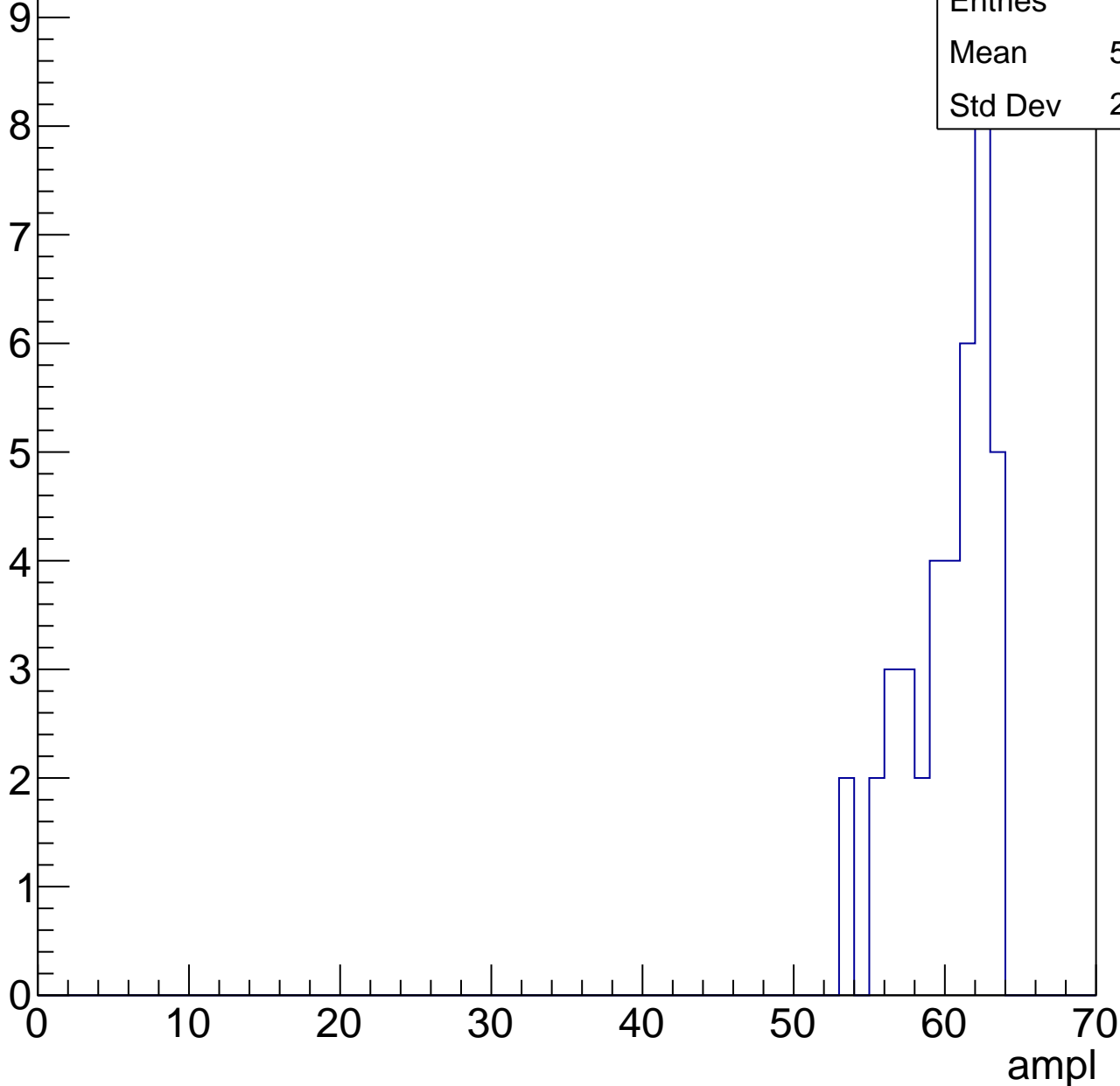


B0L102S, U12-ch103, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

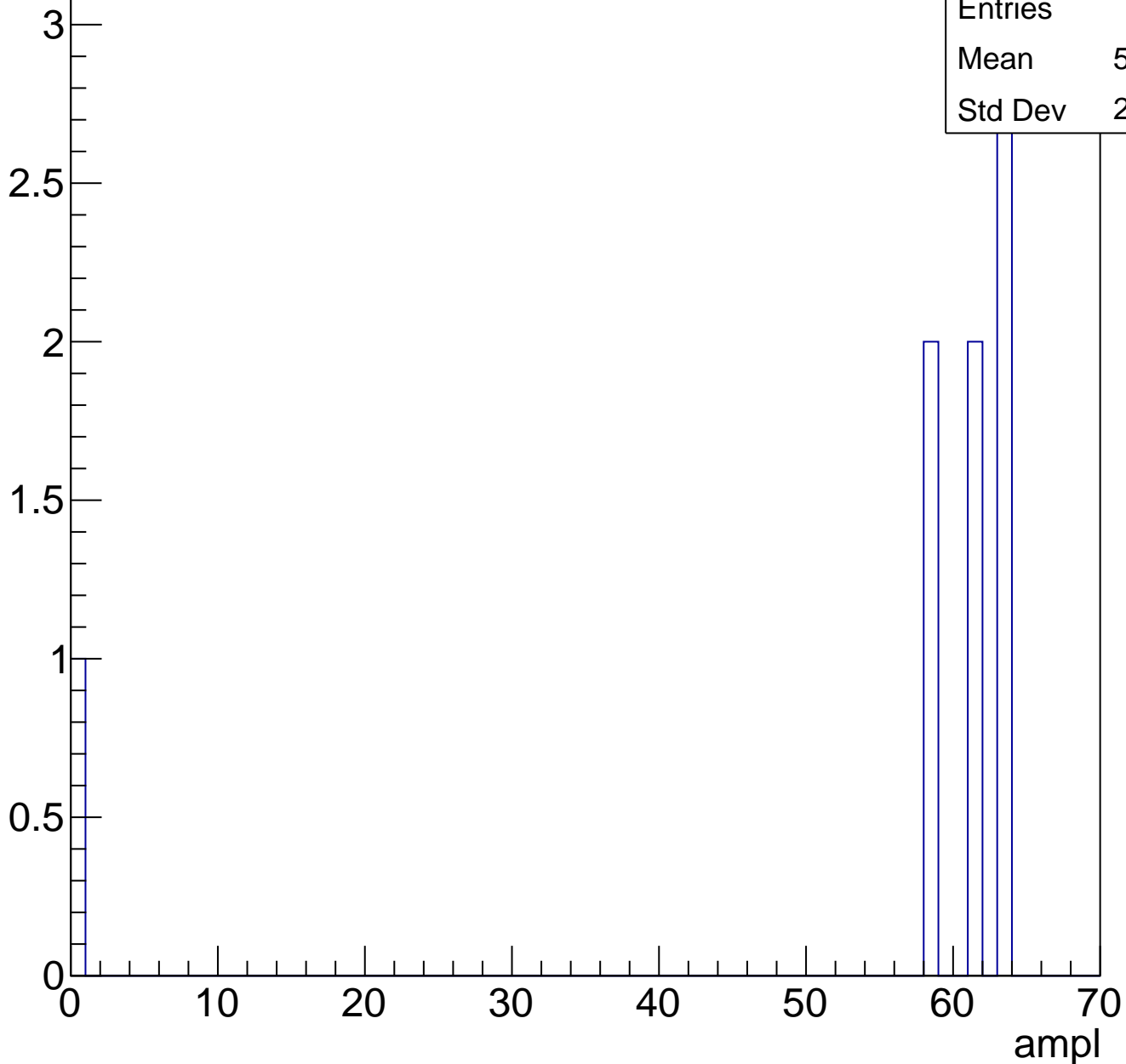
Entries	40
Mean	59.65
Std Dev	2.816



B0L102S, U12-ch103, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch103, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch104, adc0

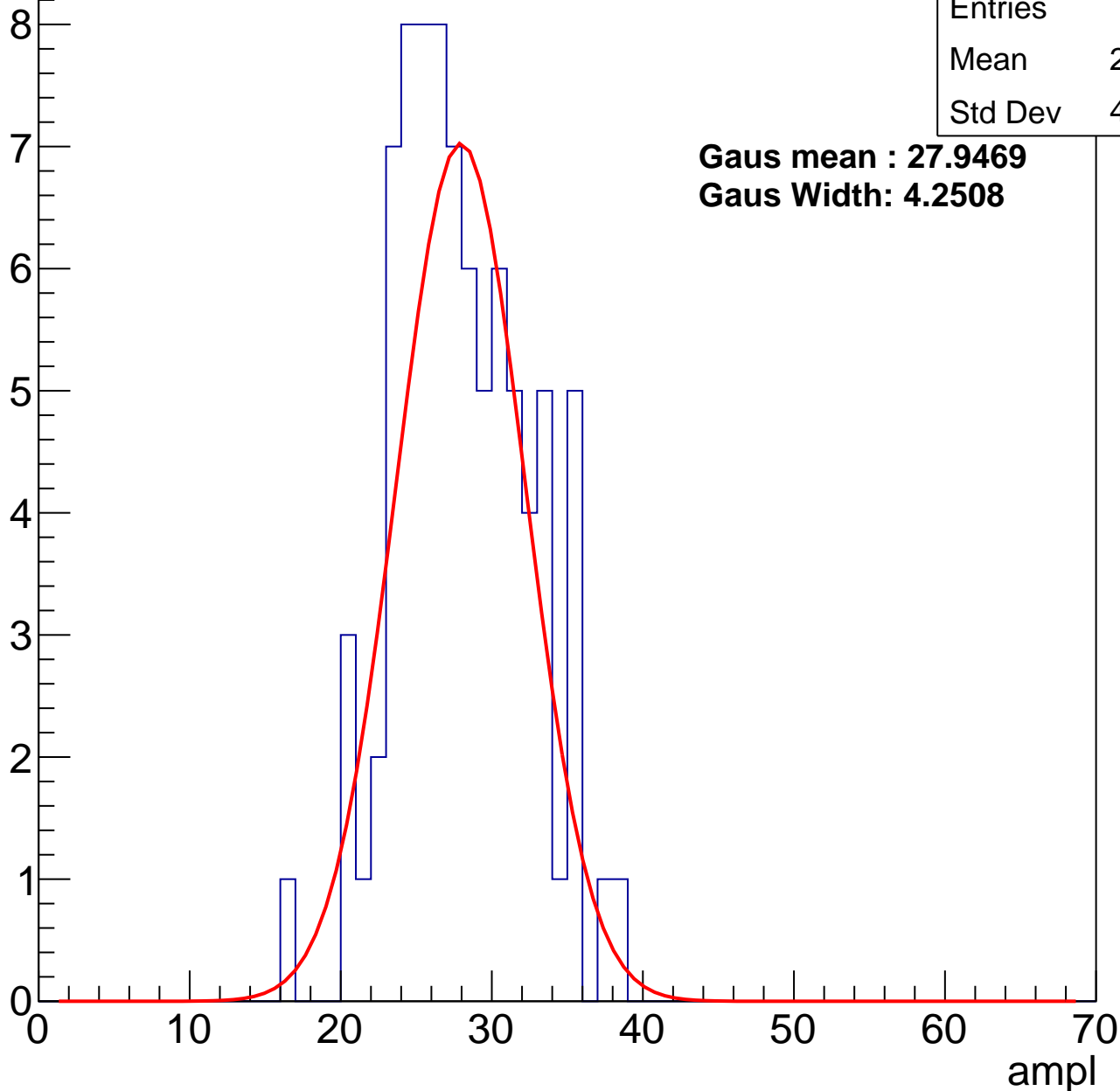
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	27.57
Std Dev	4.338

Gaus mean : 27.9469

Gaus Width: 4.2508



B0L102S, U12-ch104, adc1

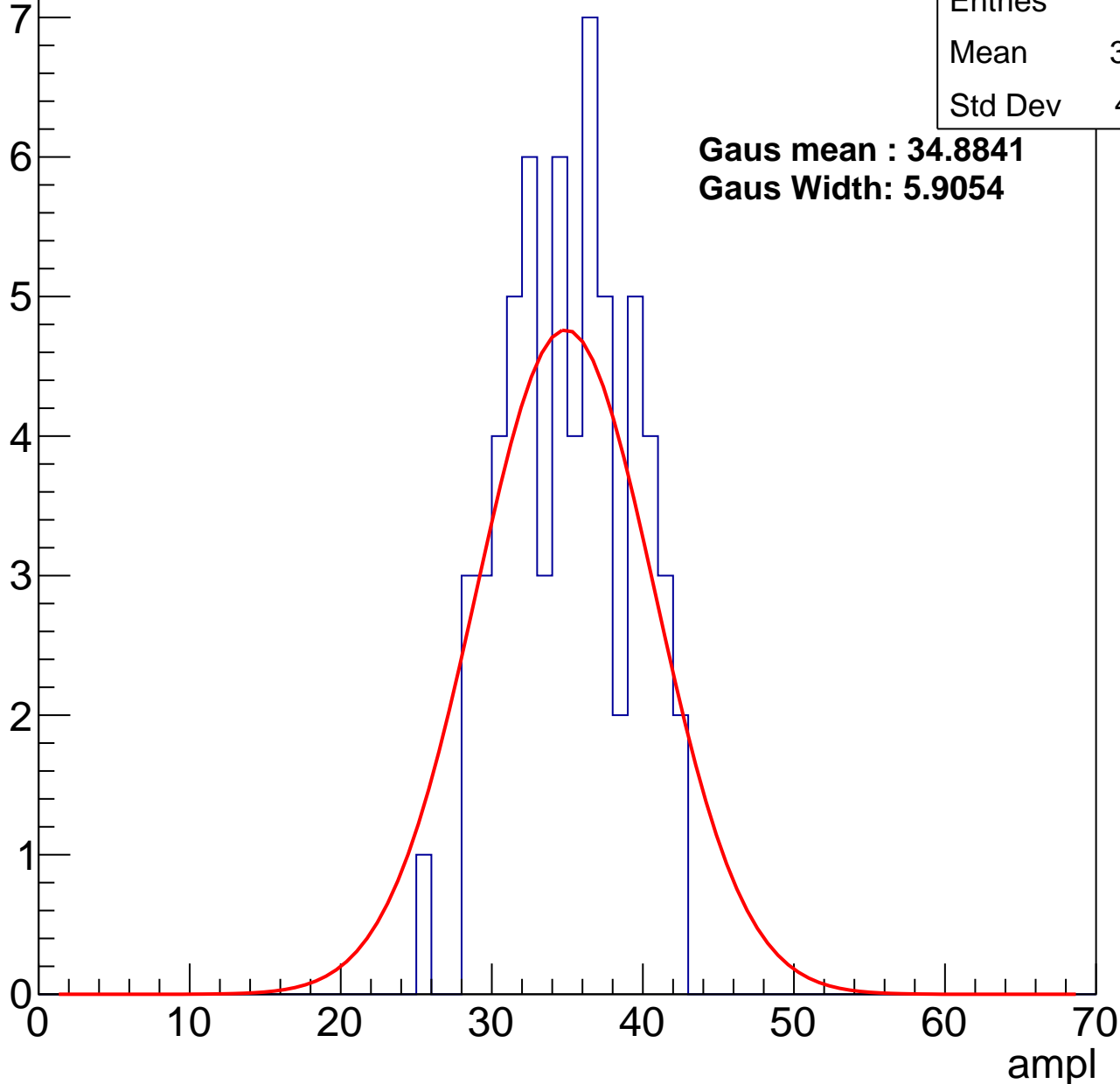
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	34.62
Std Dev	4.041

Gaus mean : 34.8841

Gaus Width: 5.9054



B0L102S, U12-ch104, adc2

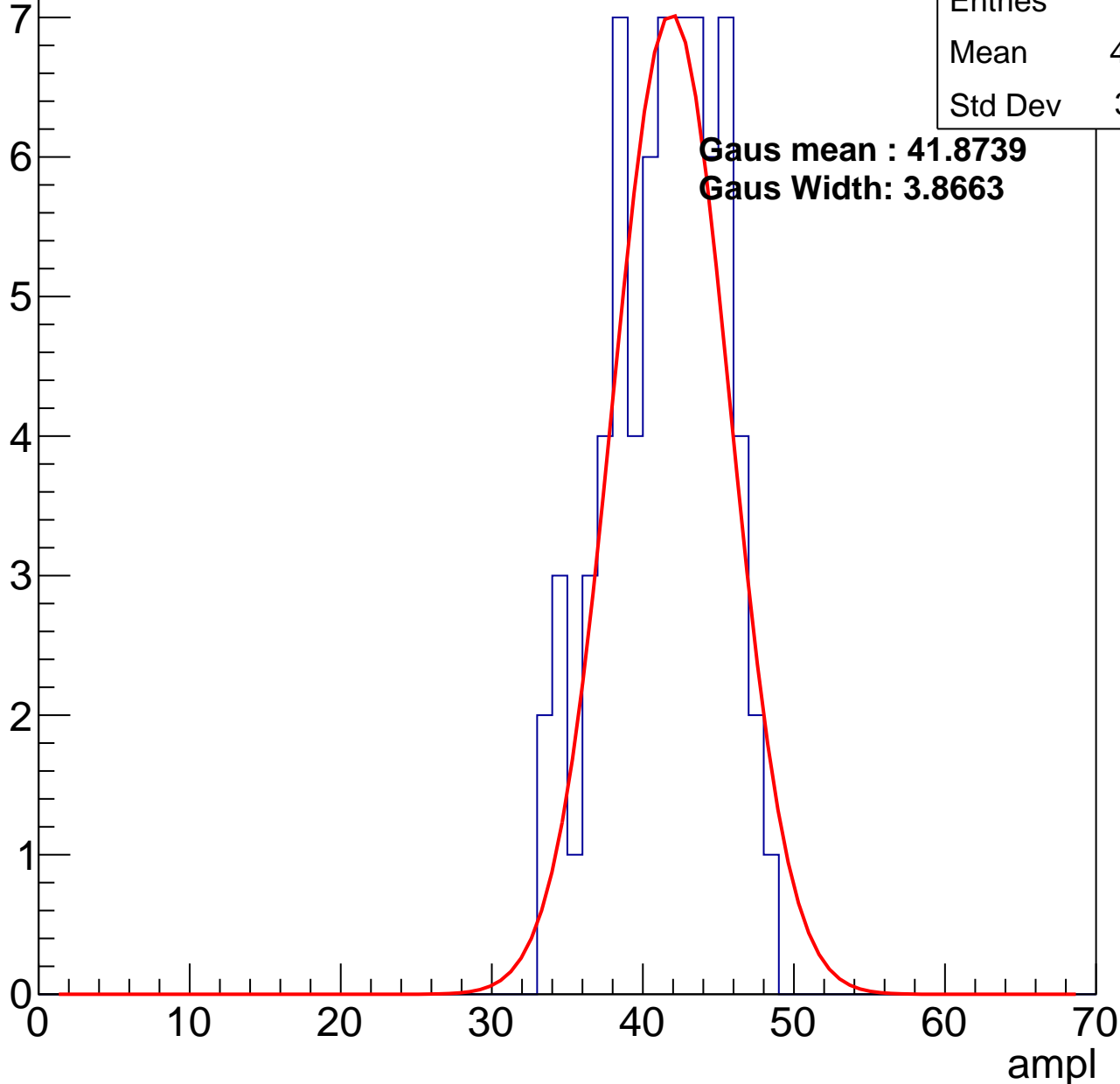
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	40.96
Std Dev	3.671

Gaus mean : 41.8739

Gaus Width: 3.8663

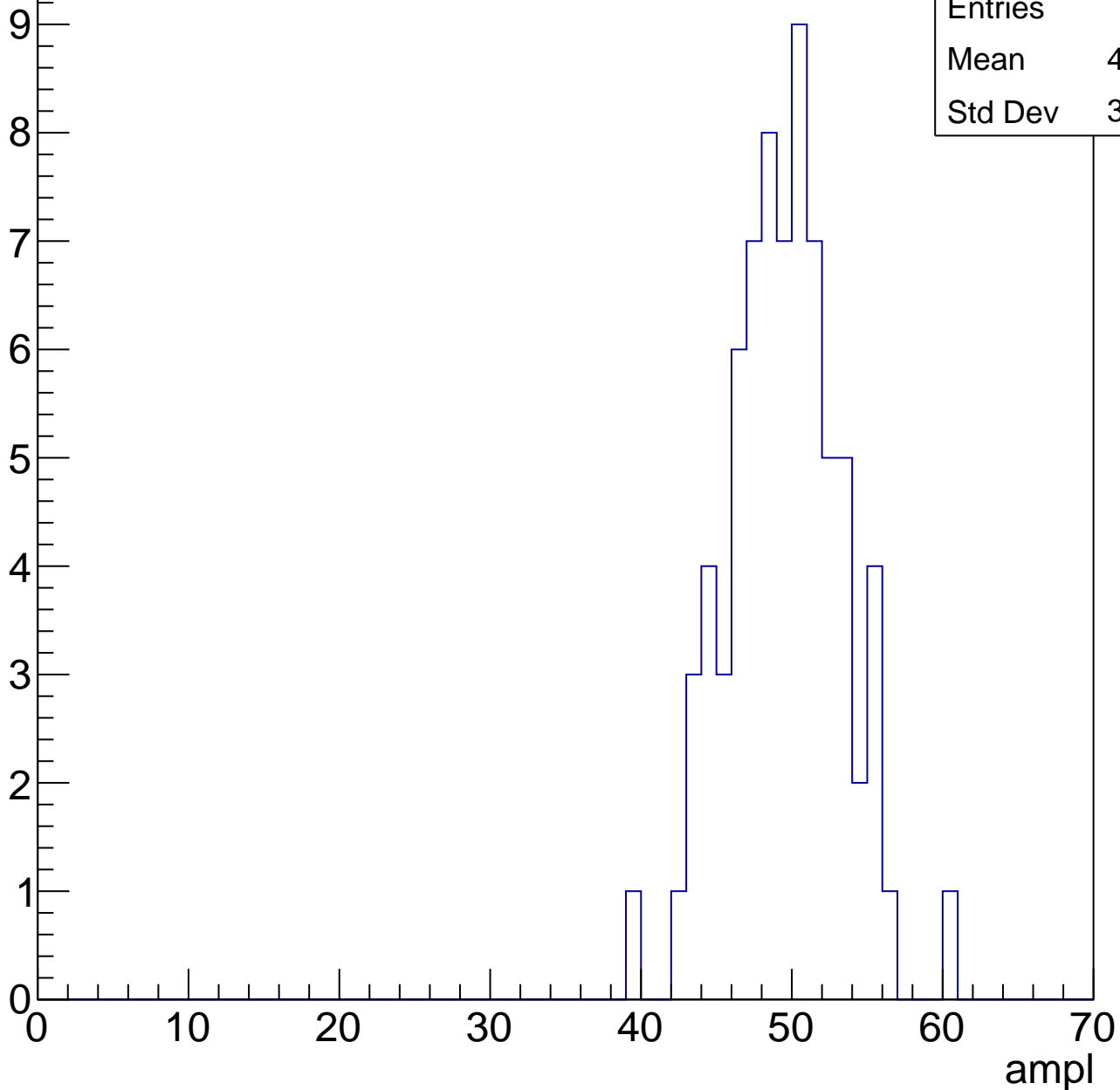


B0L102S, U12-ch104, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	49.04
Std Dev	3.714

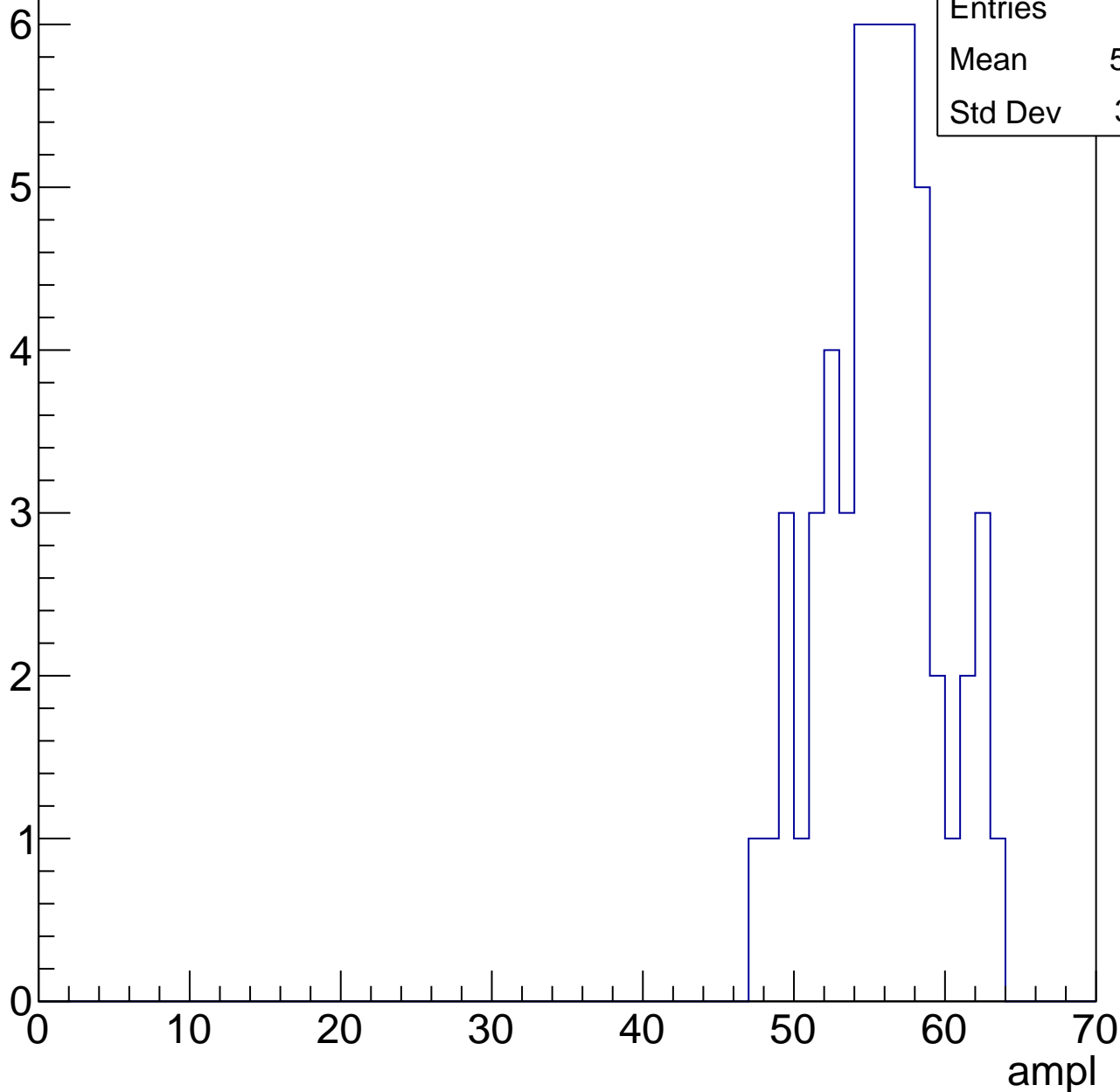


B0L102S, U12-ch104, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	55.24
Std Dev	3.751

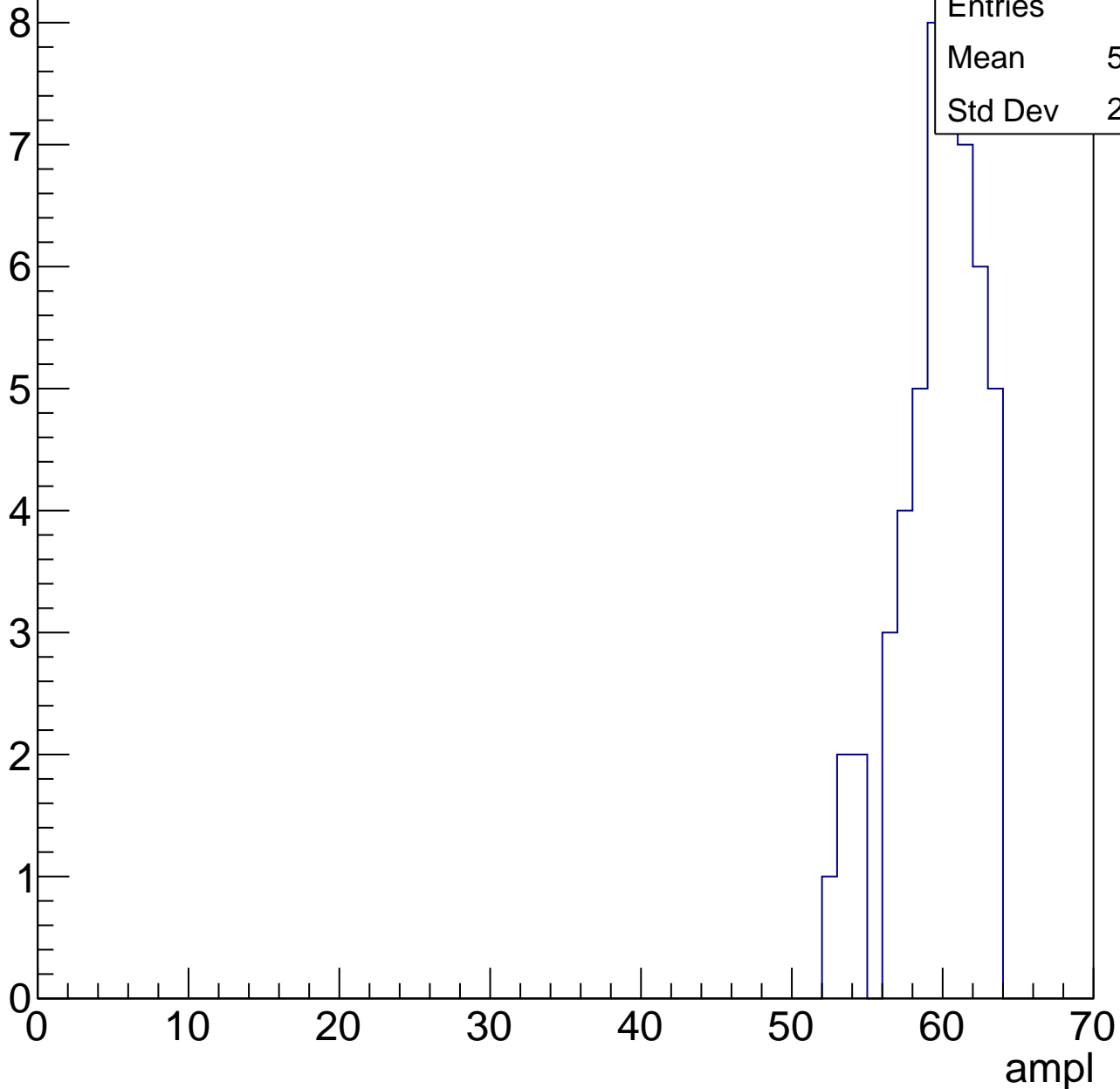


B0L102S, U12-ch104, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

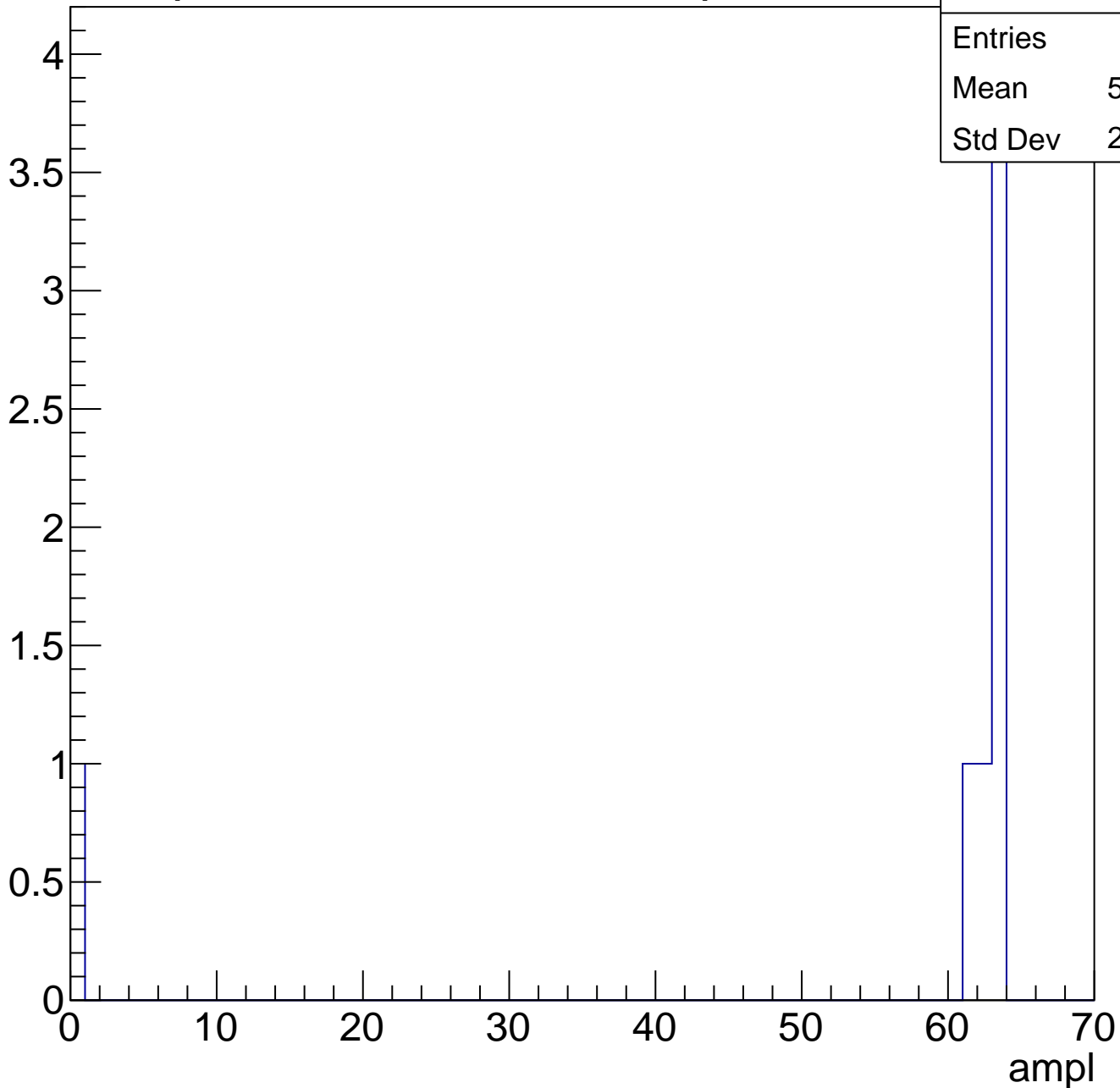
Entries	51
Mean	59.18
Std Dev	2.756



B0L102S, U12-ch104, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	7
Mean	53.57
Std Dev	21.88

B0L102S, U12-ch104, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch105, adc0

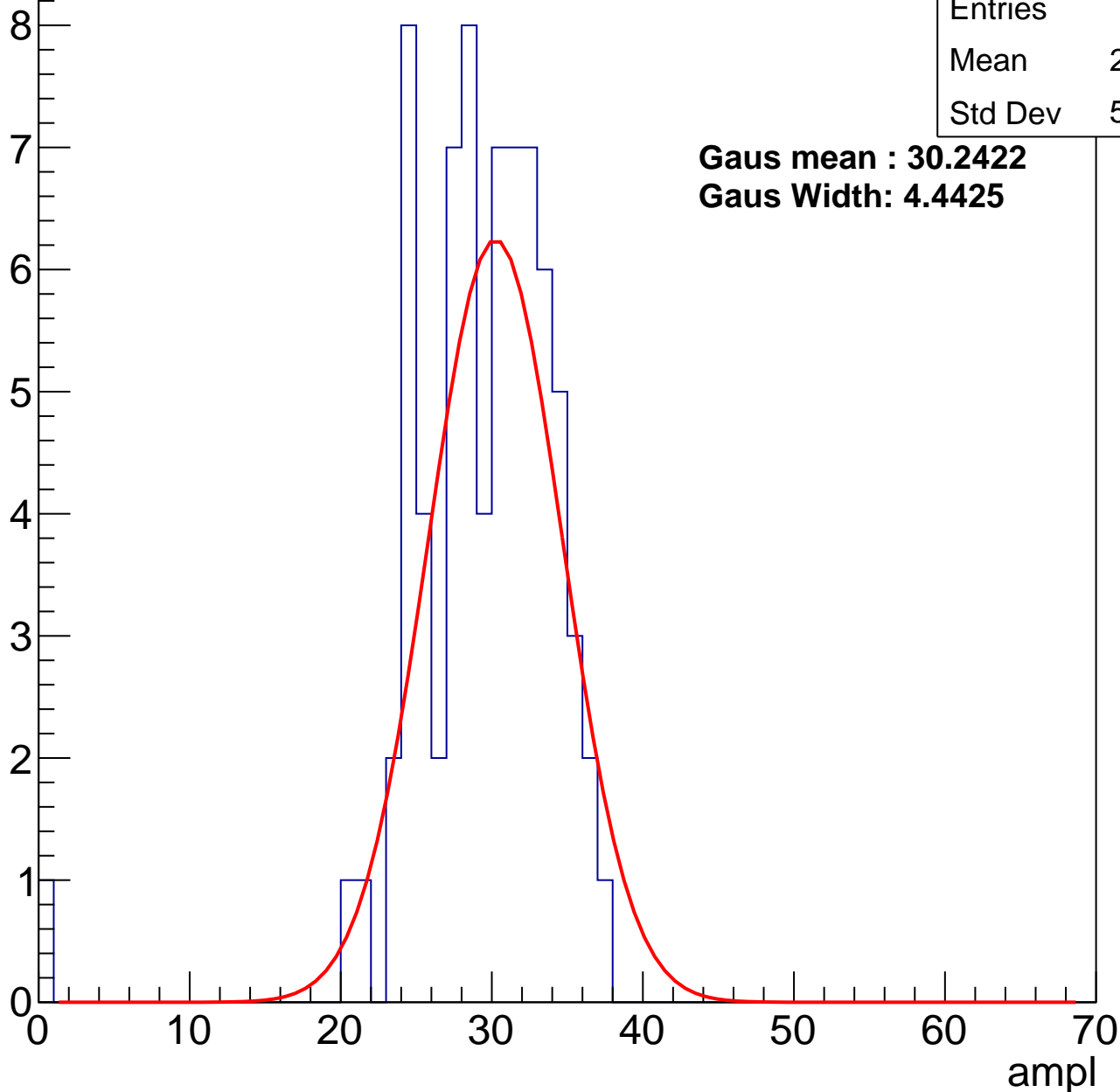
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	28.86
Std Dev	5.083

Gaus mean : 30.2422

Gaus Width: 4.4425



B0L102S, U12-ch105, adc1

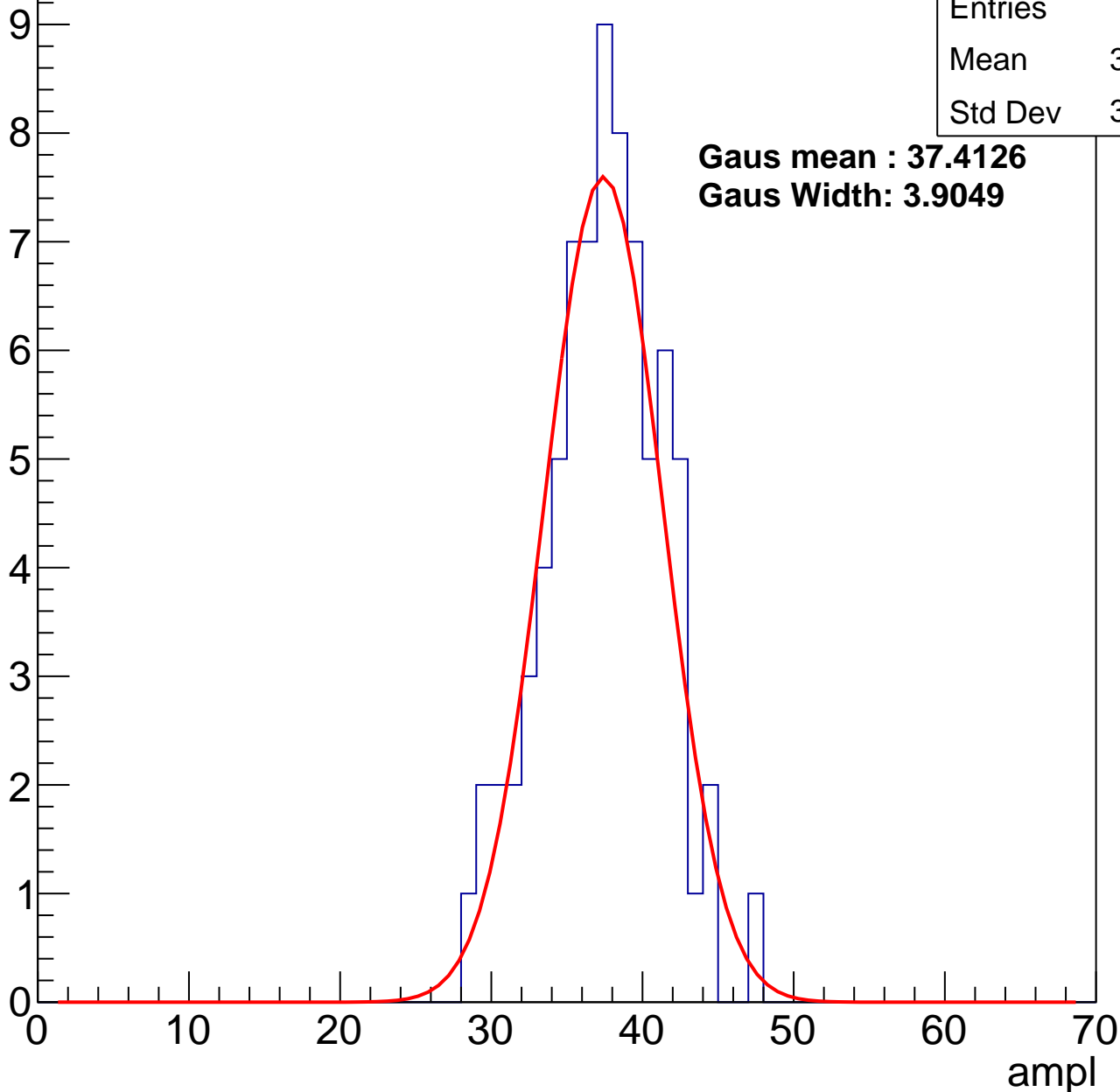
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	36.97
Std Dev	3.838

Gaus mean : 37.4126

Gaus Width: 3.9049



B0L102S, U12-ch105, adc2

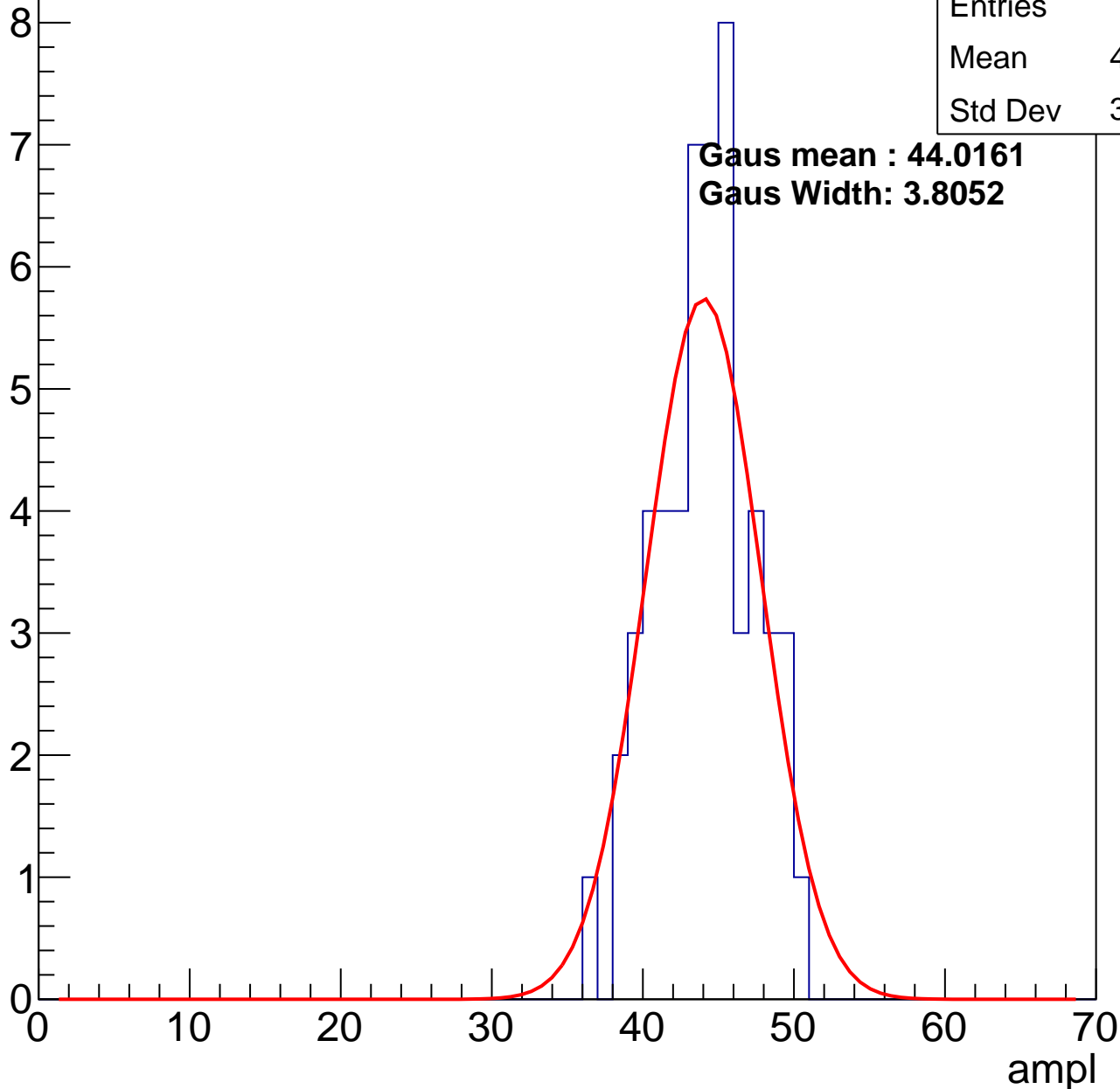
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	43.65
Std Dev	3.169

Gaus mean : 44.0161

Gaus Width: 3.8052

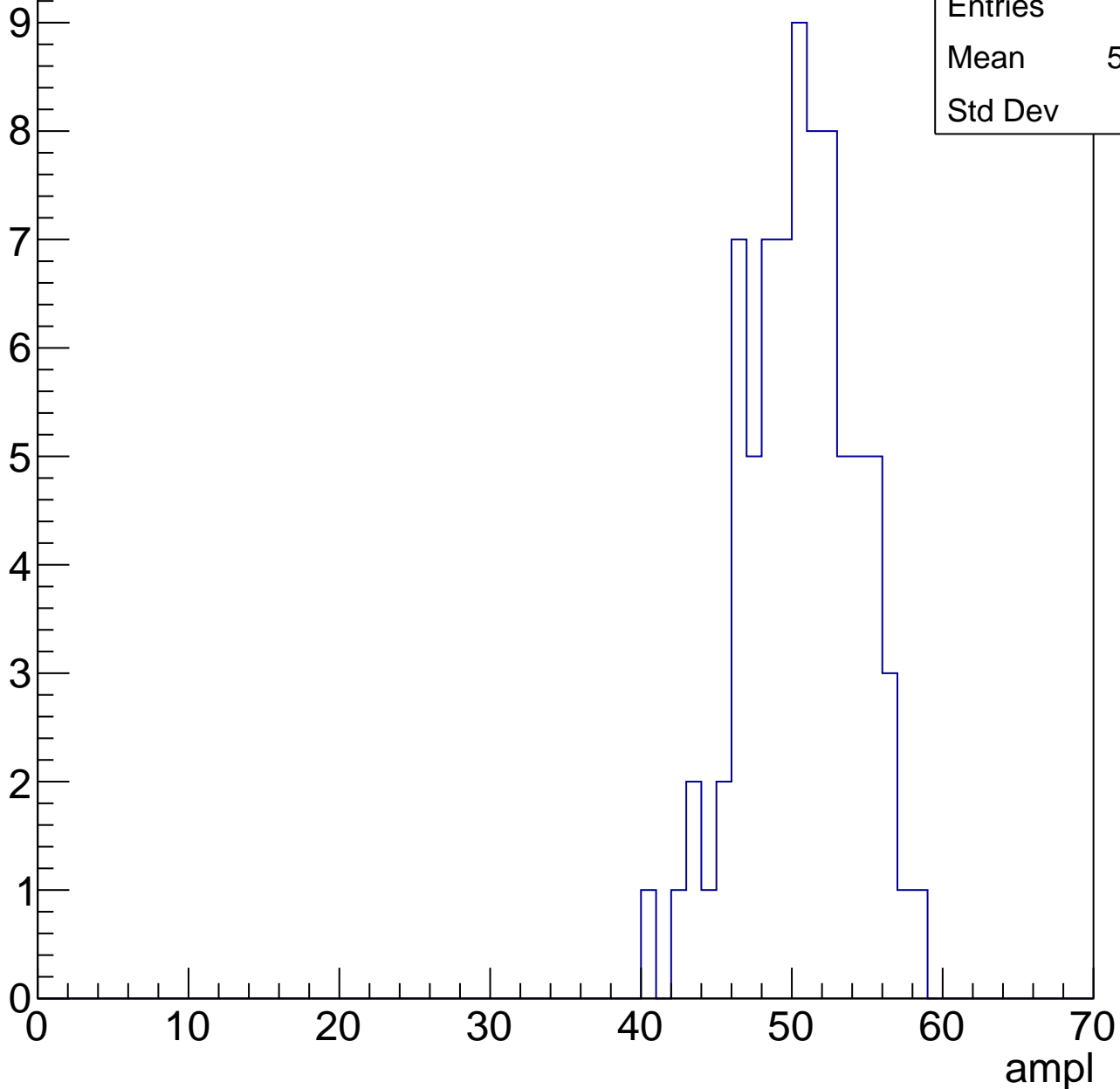


B0L102S, U12-ch105, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	50.06
Std Dev	3.67

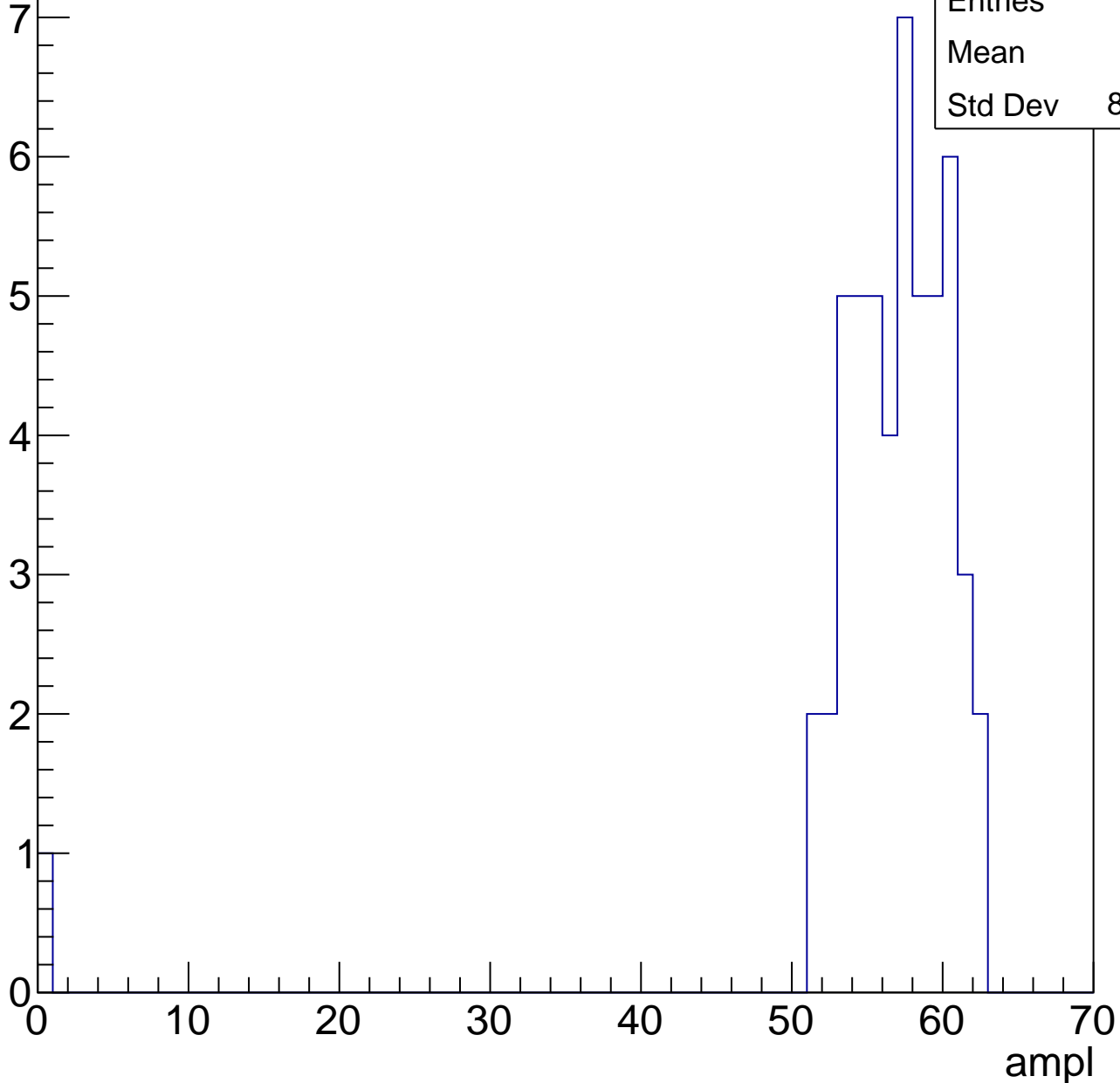


B0L102S, U12-ch105, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55.6
Std Dev	8.314

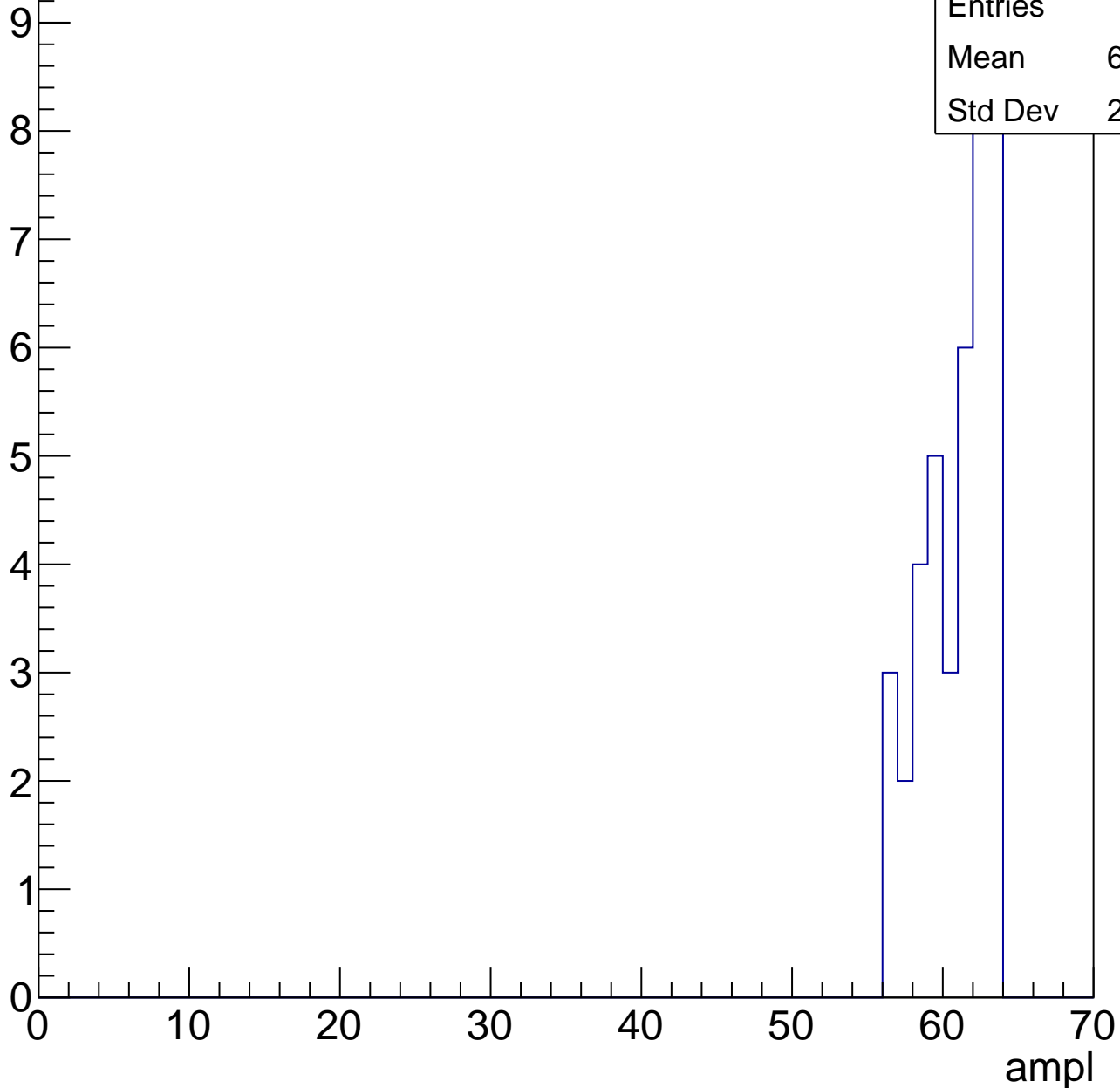


B0L102S, U12-ch105, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

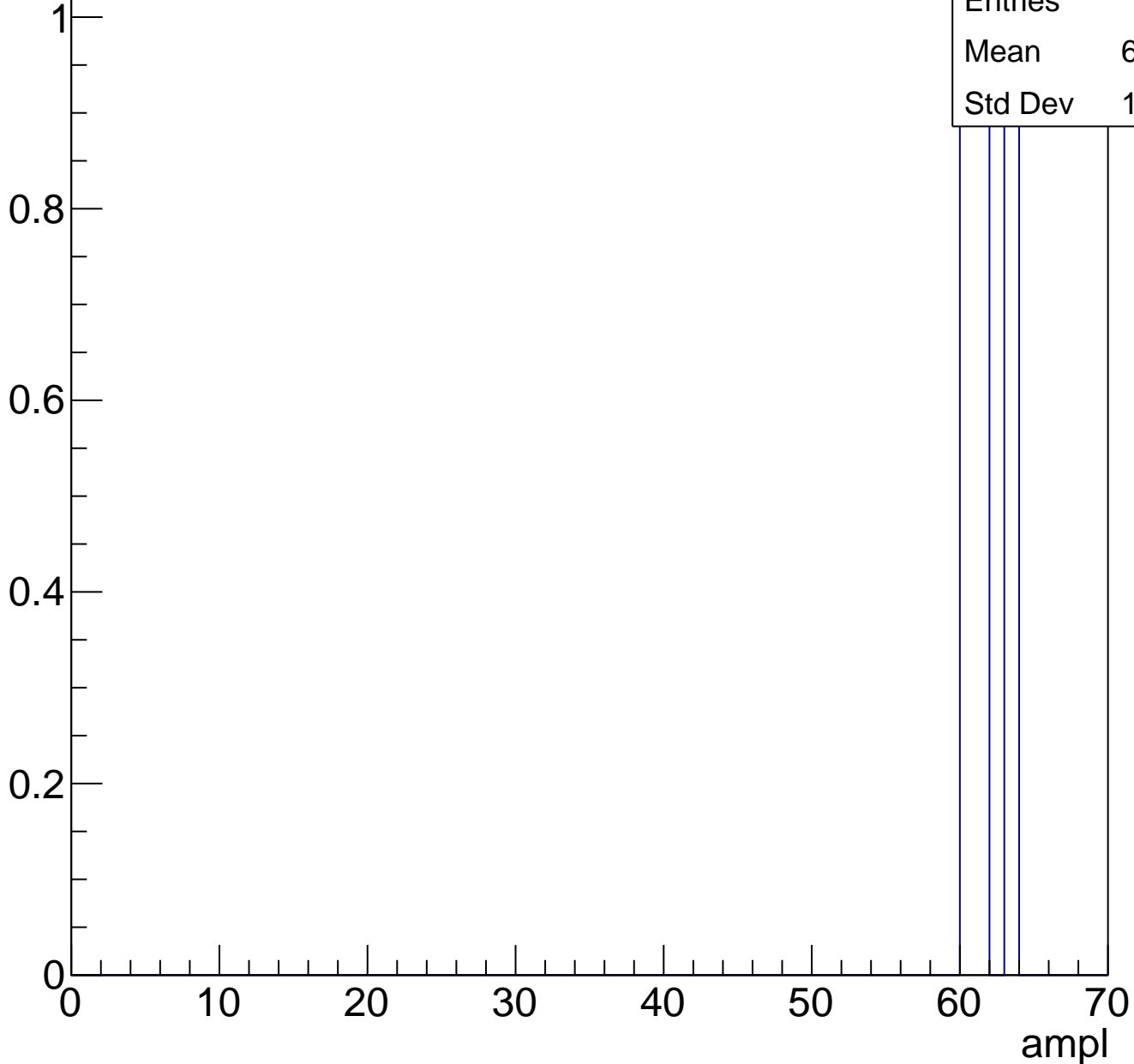
Entries	40
Mean	60.45
Std Dev	2.224



B0L102S, U12-ch105, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch105, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch106, adc0

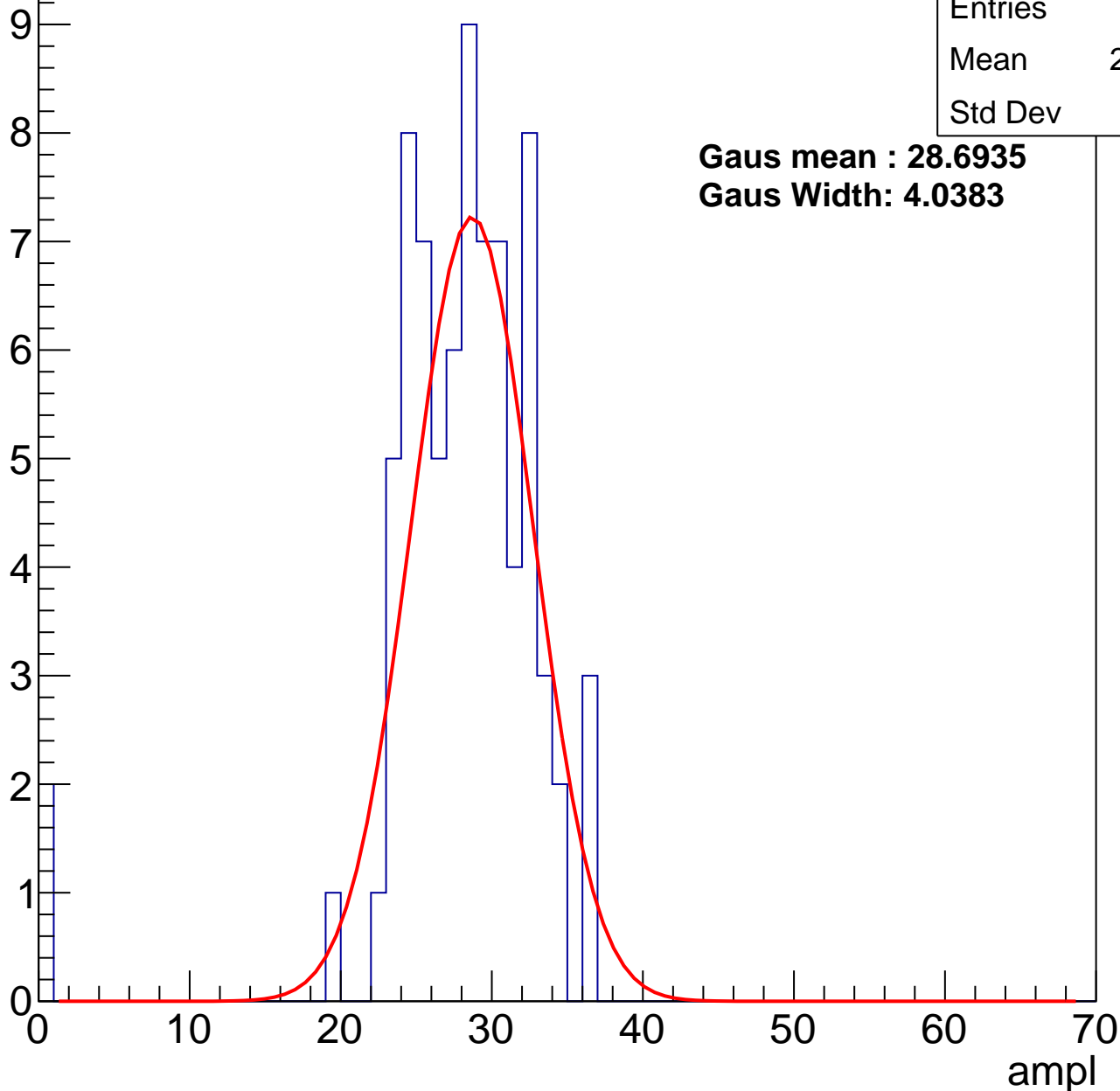
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	27.37
Std Dev	5.7

Gaus mean : 28.6935

Gaus Width: 4.0383



B0L102S, U12-ch106, adc1

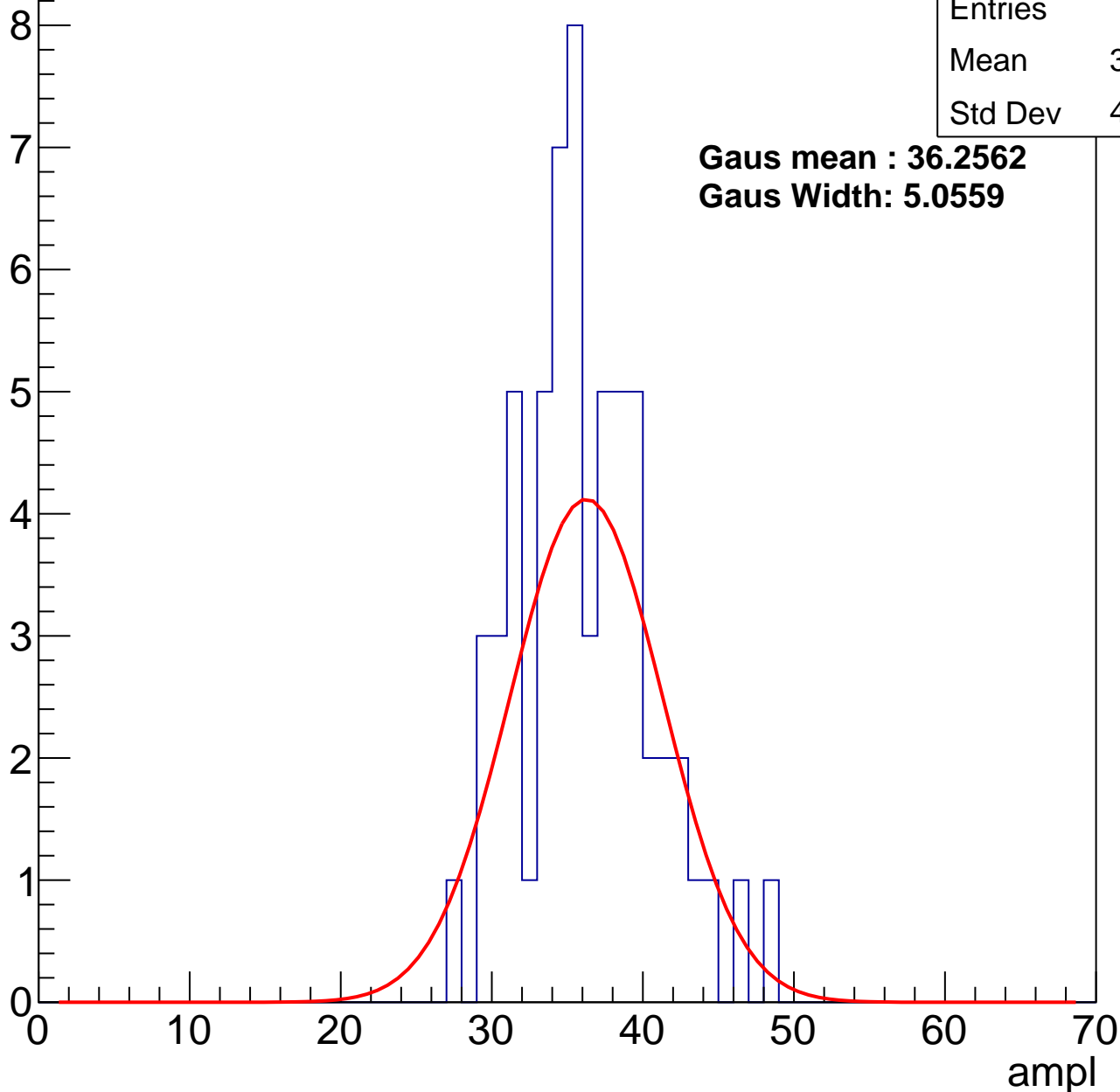
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	35.72
Std Dev	4.312

Gaus mean : 36.2562

Gaus Width: 5.0559



B0L102S, U12-ch106, adc2

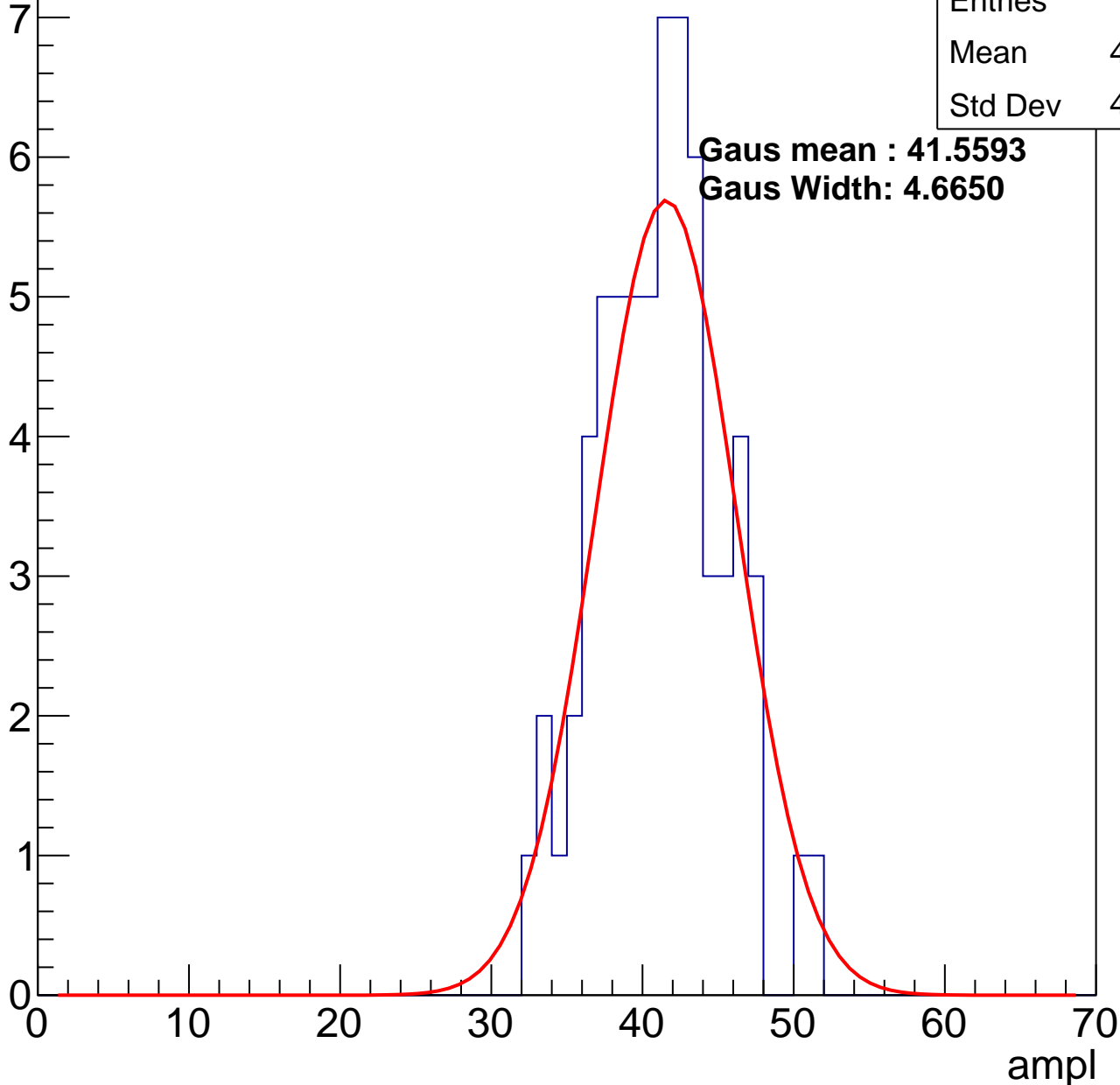
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	40.74
Std Dev	4.058

Gaus mean : 41.5593

Gaus Width: 4.6650

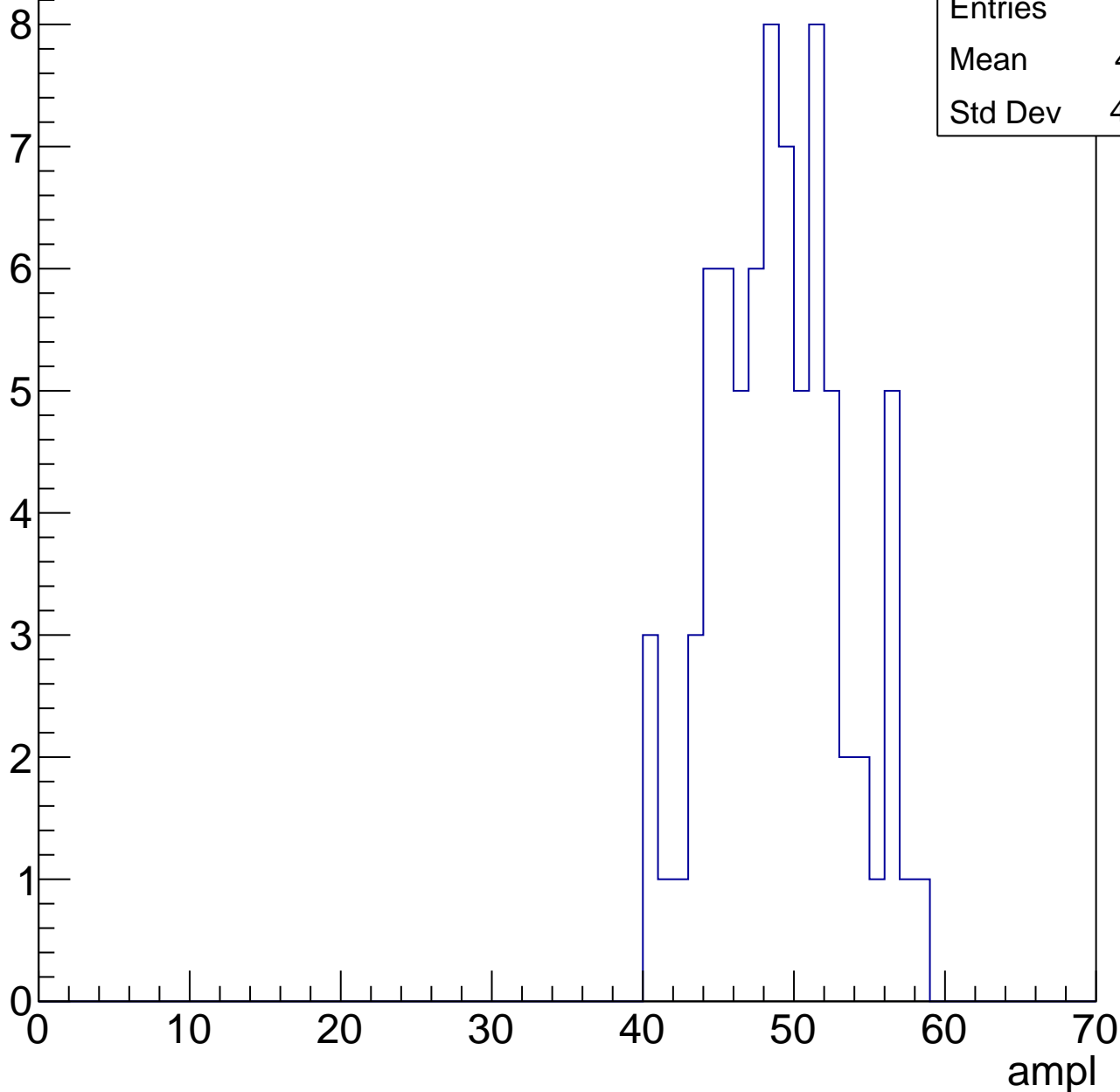


B0L102S, U12-ch106, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	48.51
Std Dev	4.229

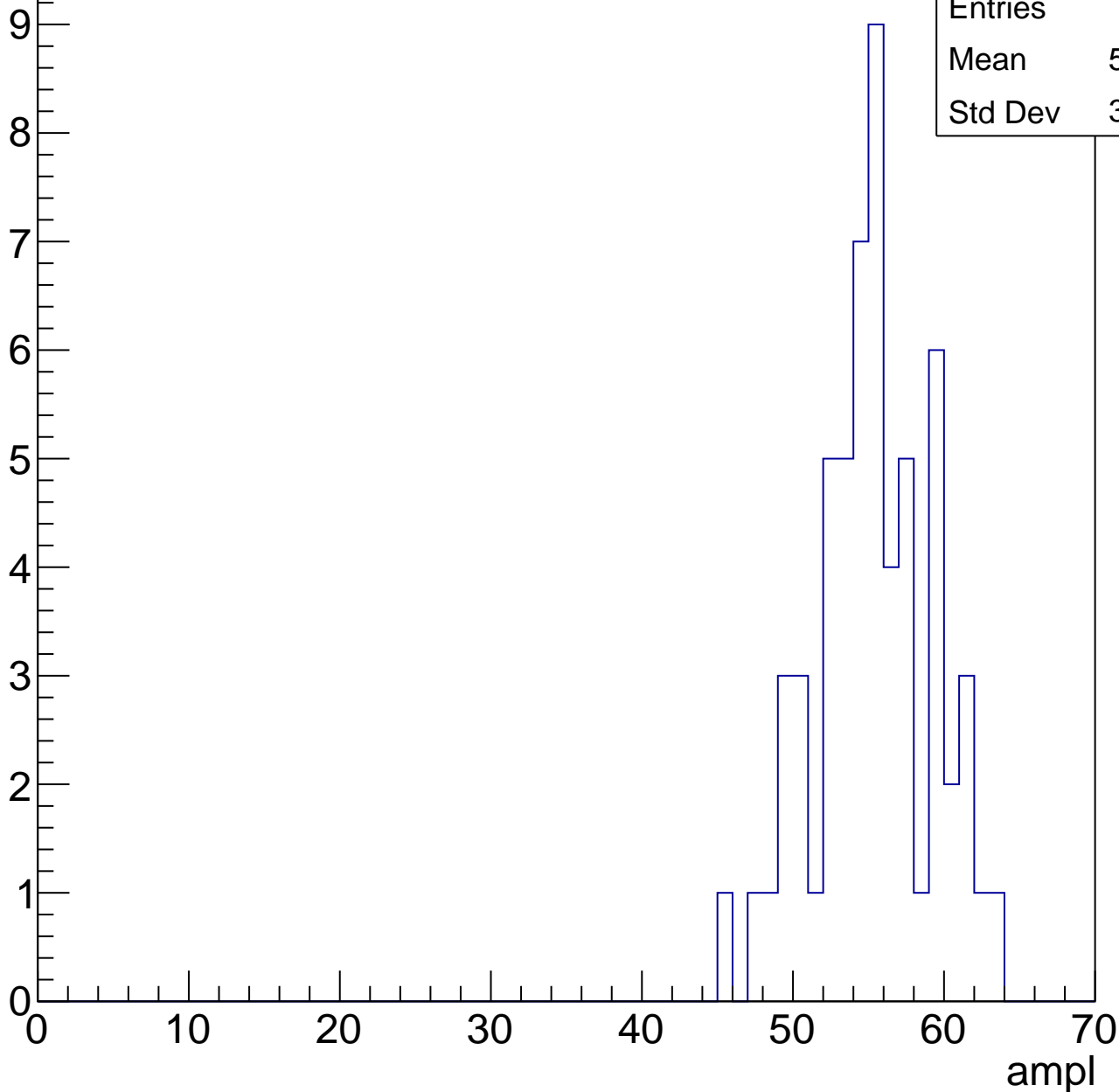


B0L102S, U12-ch106, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.83
Std Dev	3.867

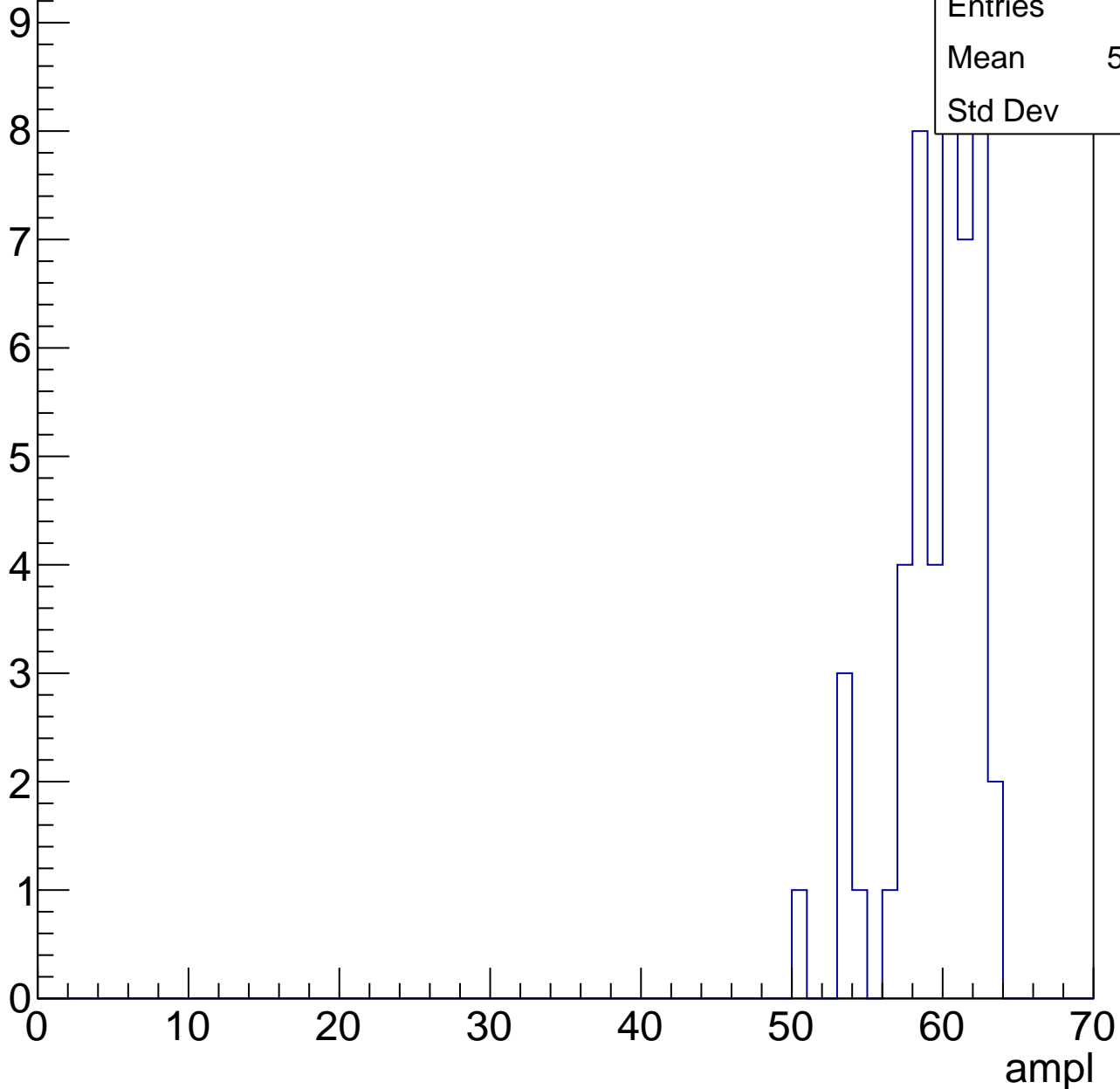


B0L102S, U12-ch106, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	59.12
Std Dev	2.87

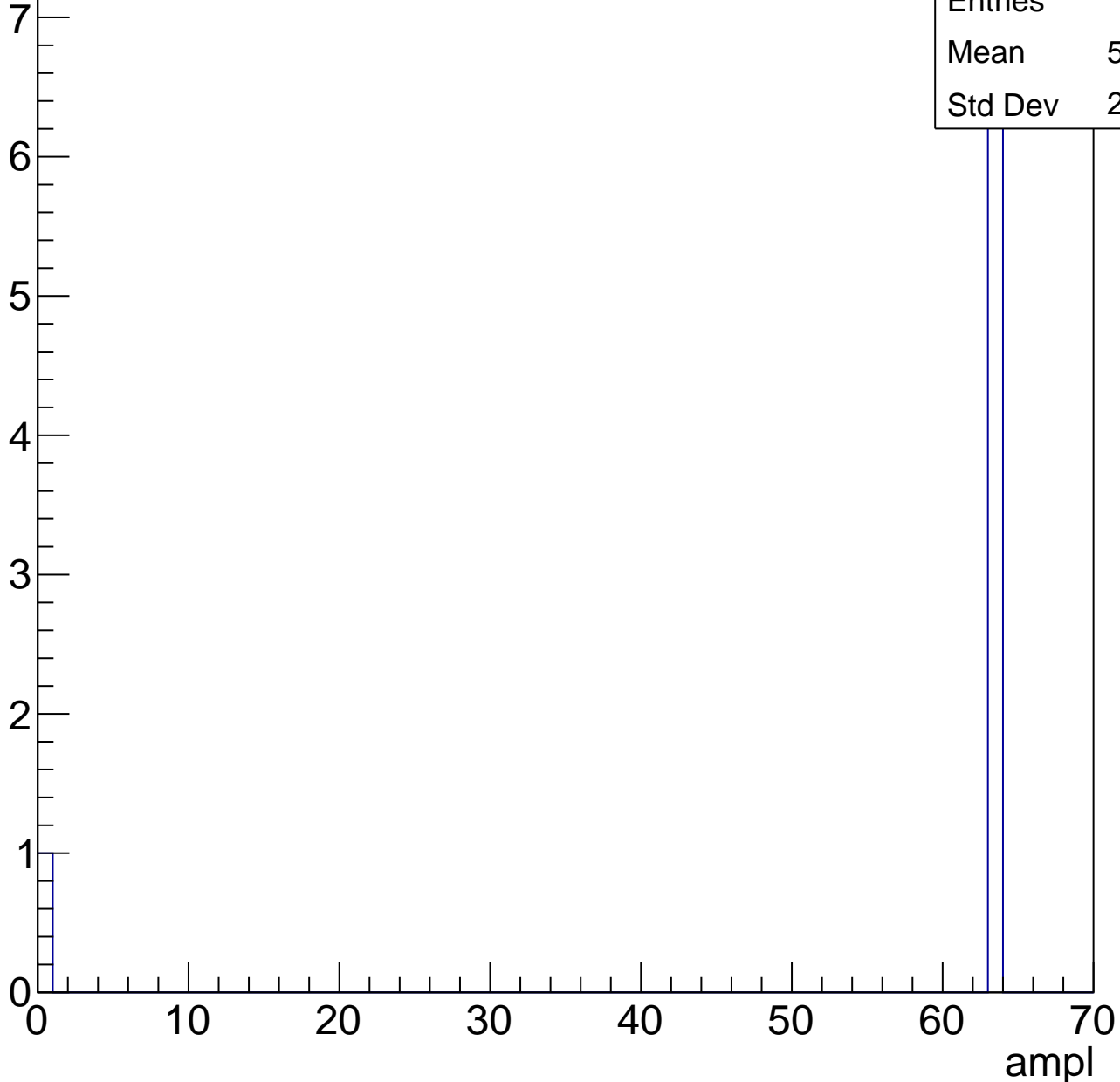


B0L102S, U12-ch106, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

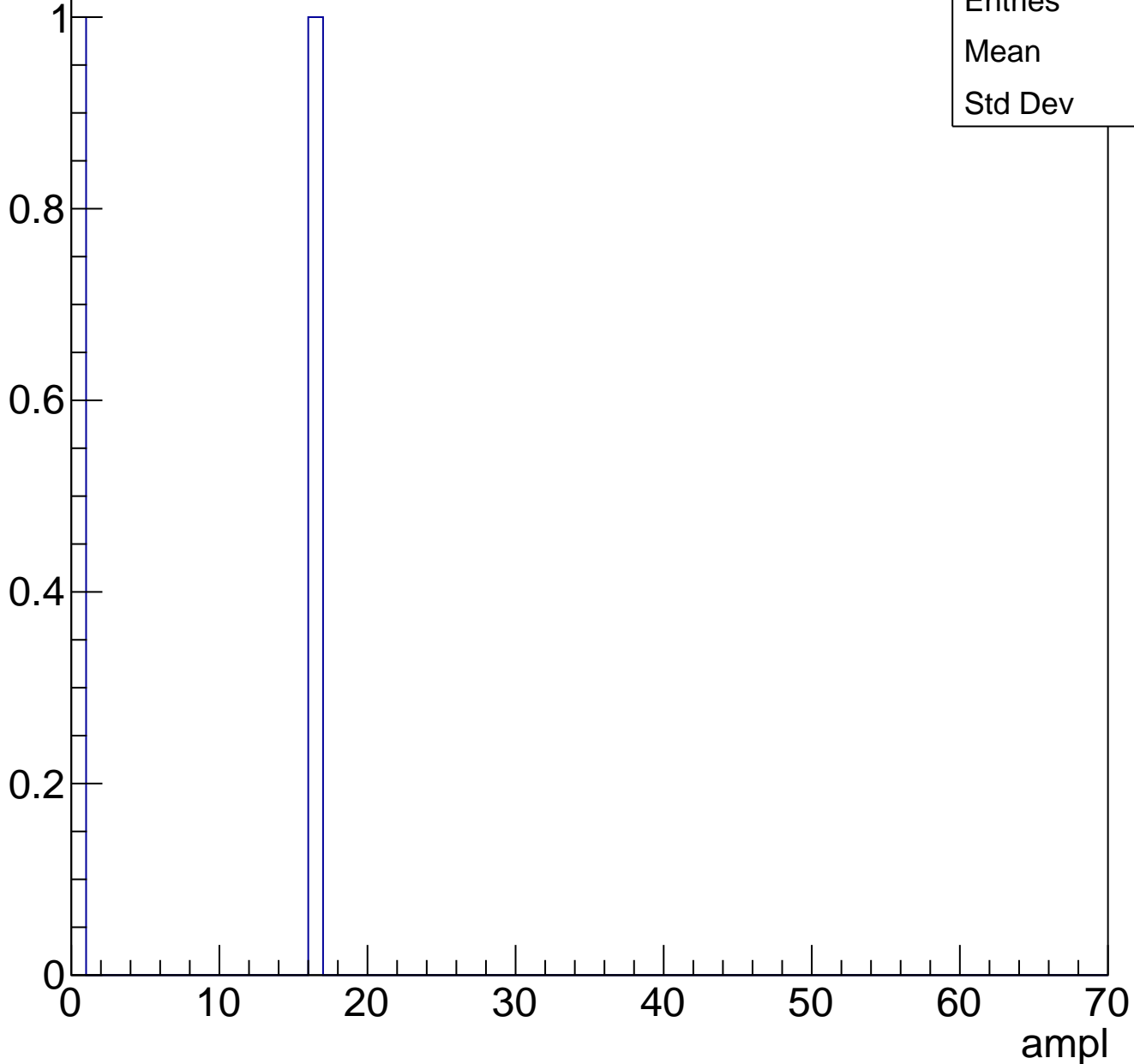
Entries	8
Mean	55.12
Std Dev	20.84



B0L102S, U12-ch106, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	8
Std Dev	8

B0L102S, U12-ch107, adc0

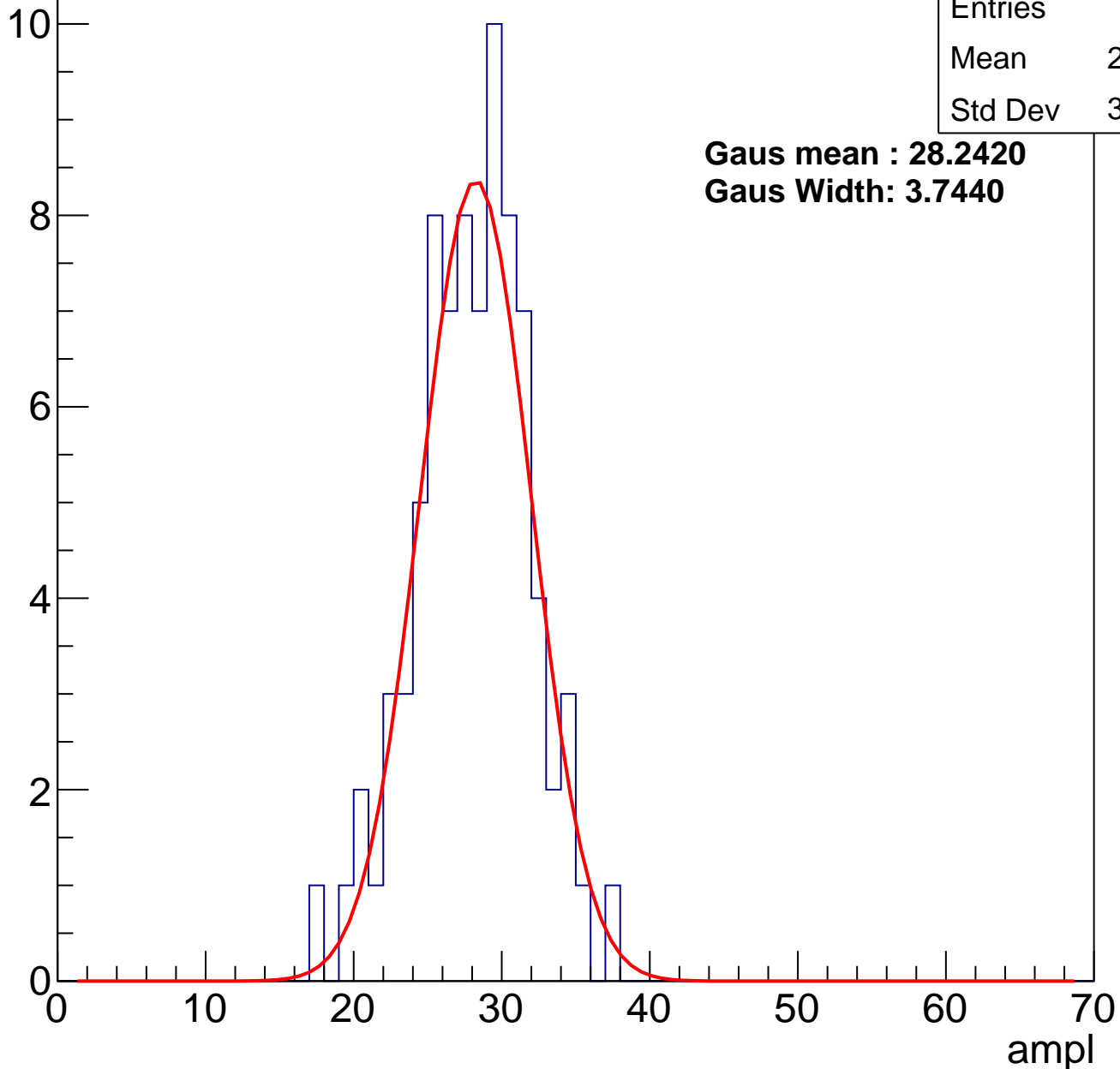
calib_packv5_042523_0143.root, FC#12, port B1

Entries	82
Mean	27.57
Std Dev	3.813

Gaus mean : 28.2420

Gaus Width: 3.7440

Entry



B0L102S, U12-ch107, adc1

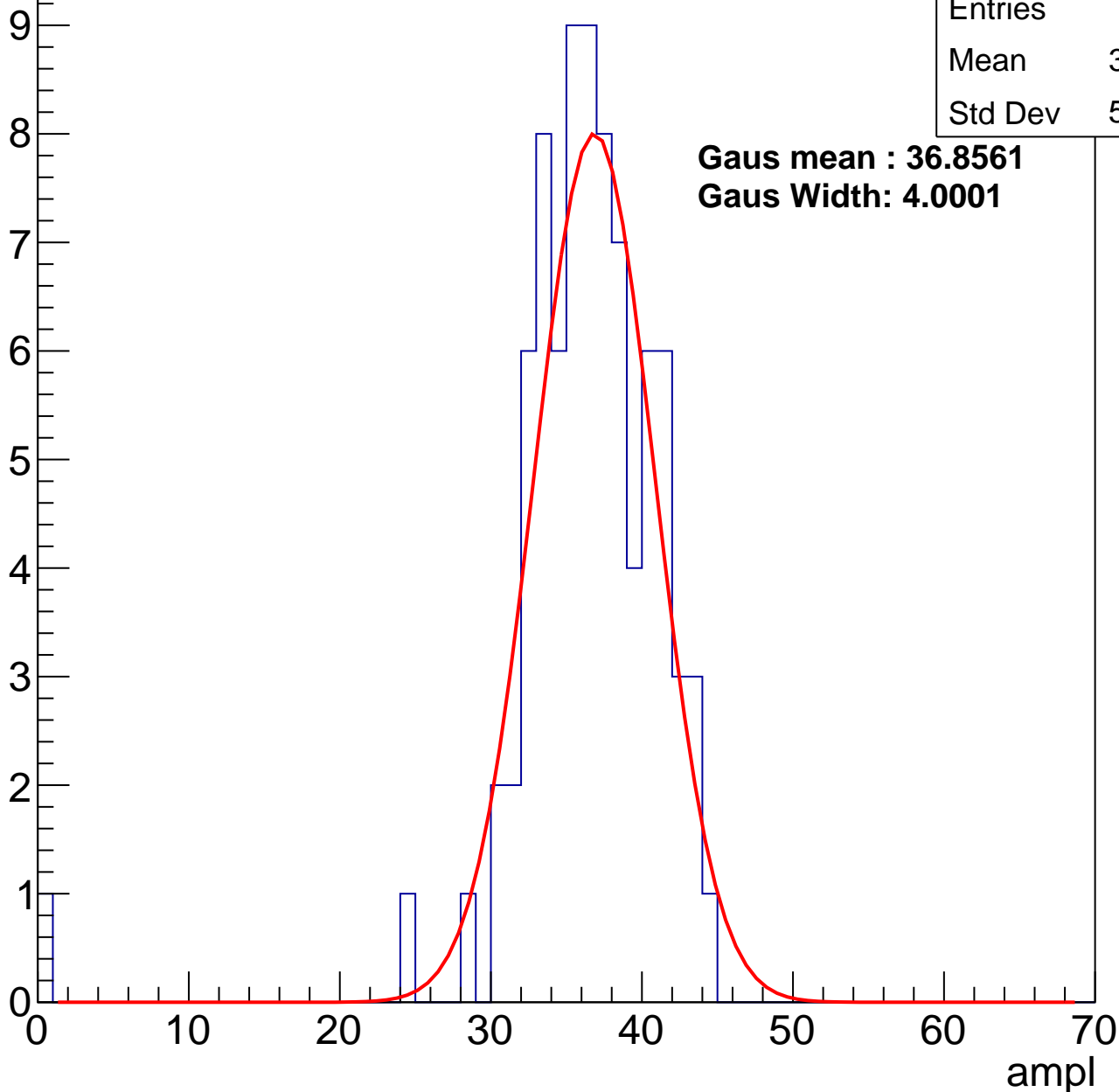
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	35.86
Std Dev	5.447

Gaus mean : 36.8561

Gaus Width: 4.0001



B0L102S, U12-ch107, adc2

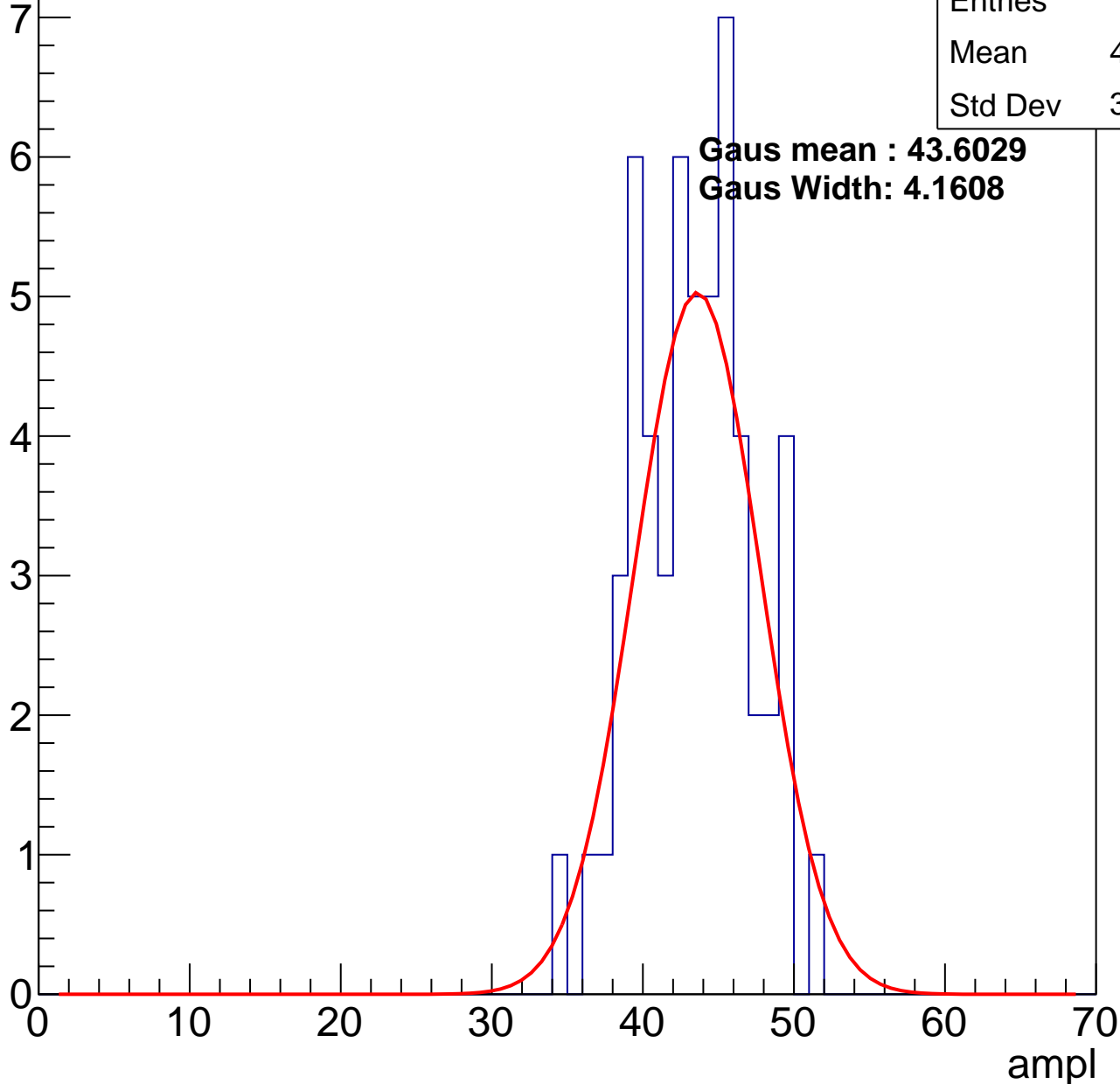
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	42.93
Std Dev	3.692

Gaus mean : 43.6029

Gaus Width: 4.1608

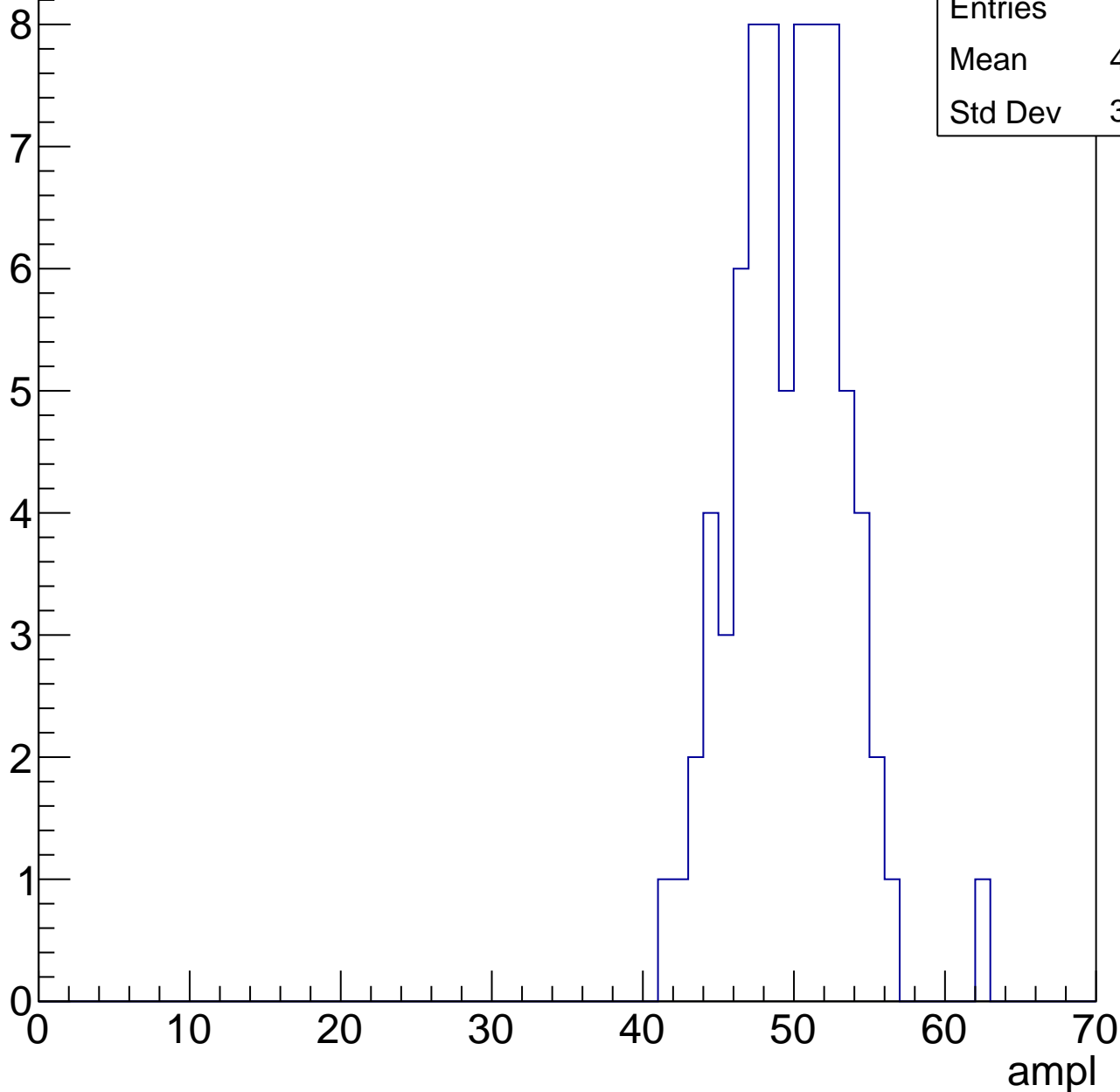


B0L102S, U12-ch107, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	49.25
Std Dev	3.659

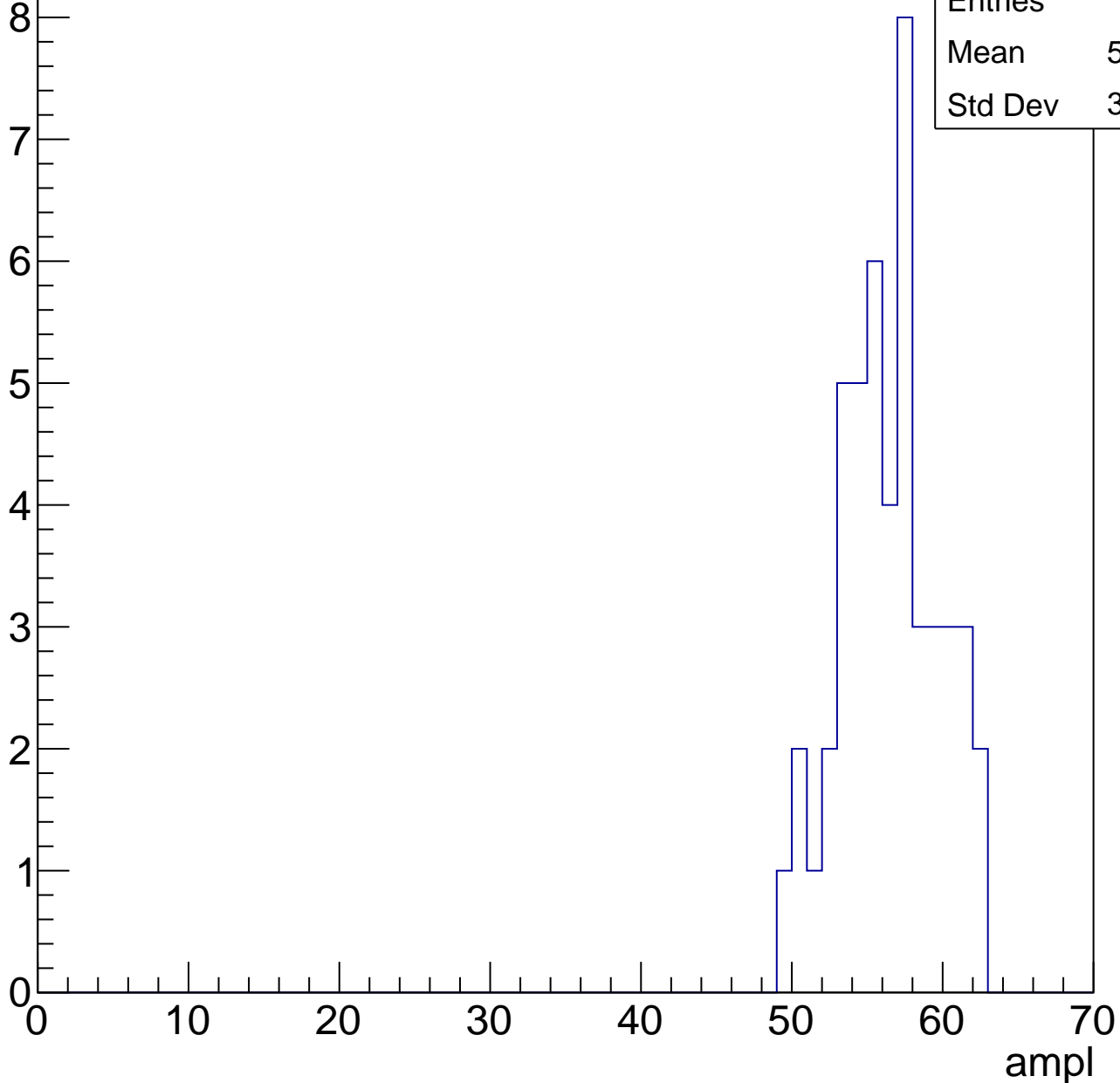


B0L102S, U12-ch107, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	55.98
Std Dev	3.198

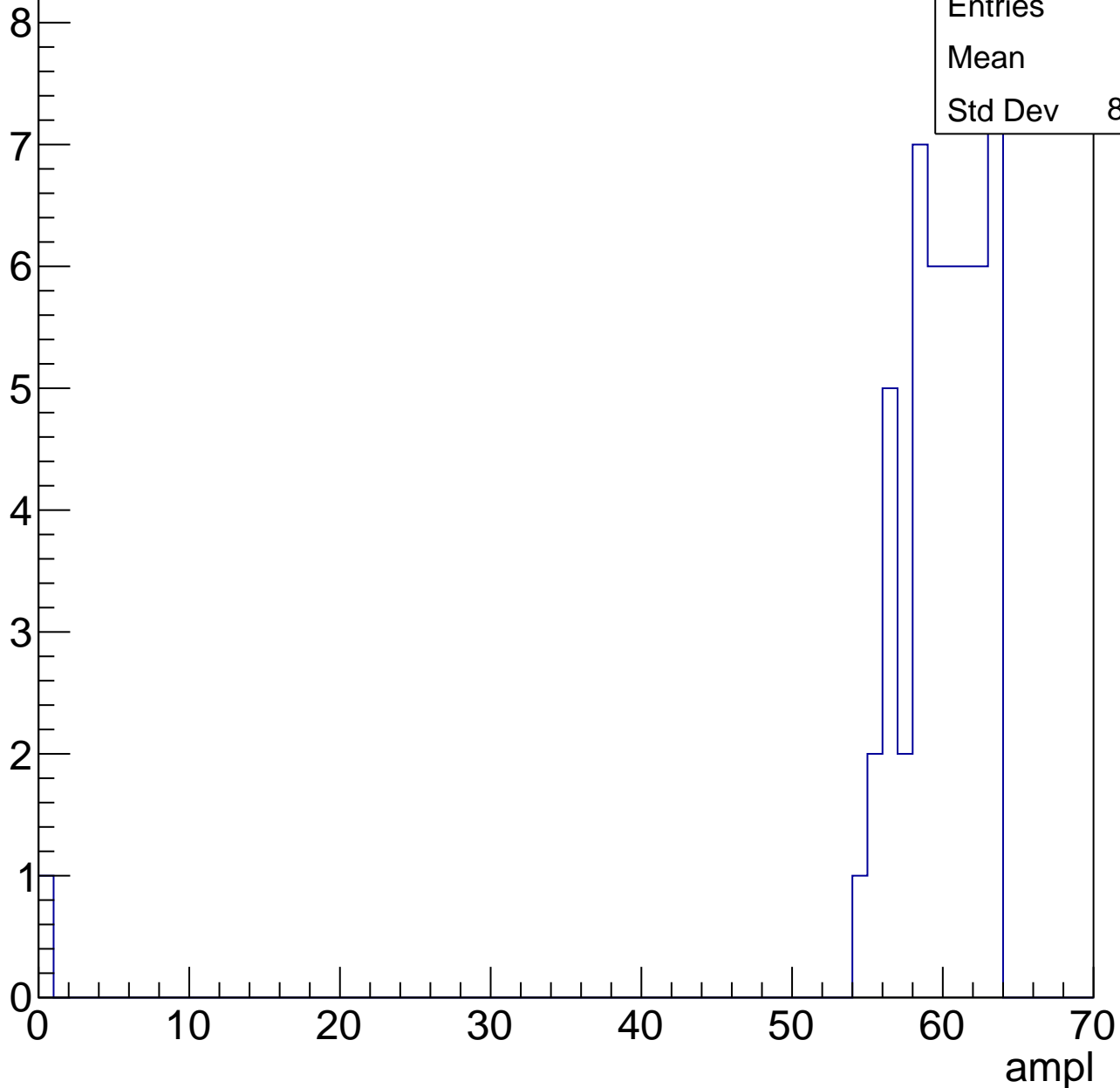


B0L102S, U12-ch107, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.4
Std Dev	8.709



B0L102S, U12-ch107, adc6

calib_packv5_042523_0143.root, FC#12, port B1

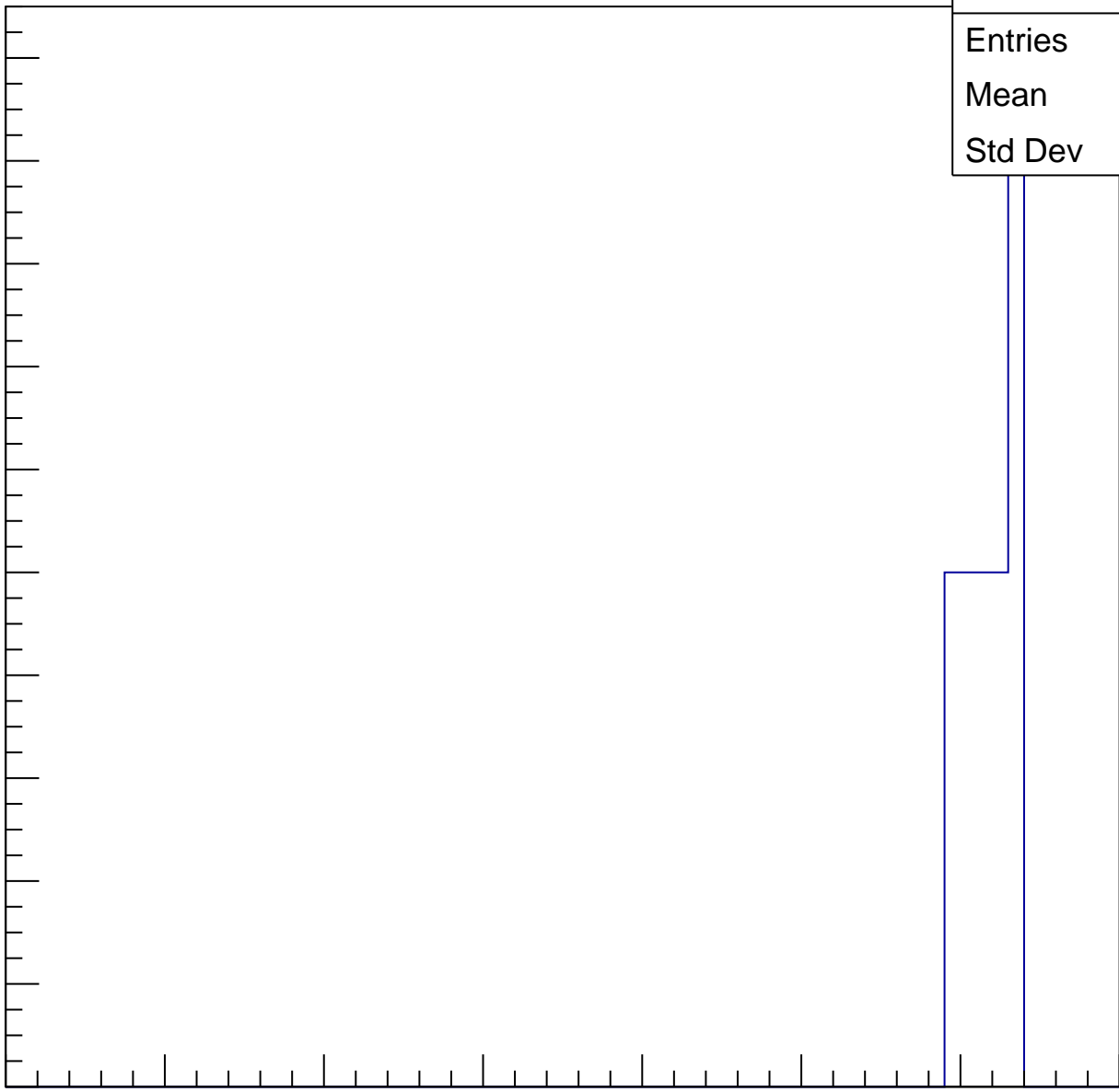
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl



B0L102S, U12-ch107, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch108, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	64
Mean	29.98
Std Dev	6.465

Gaus mean : 31.1560

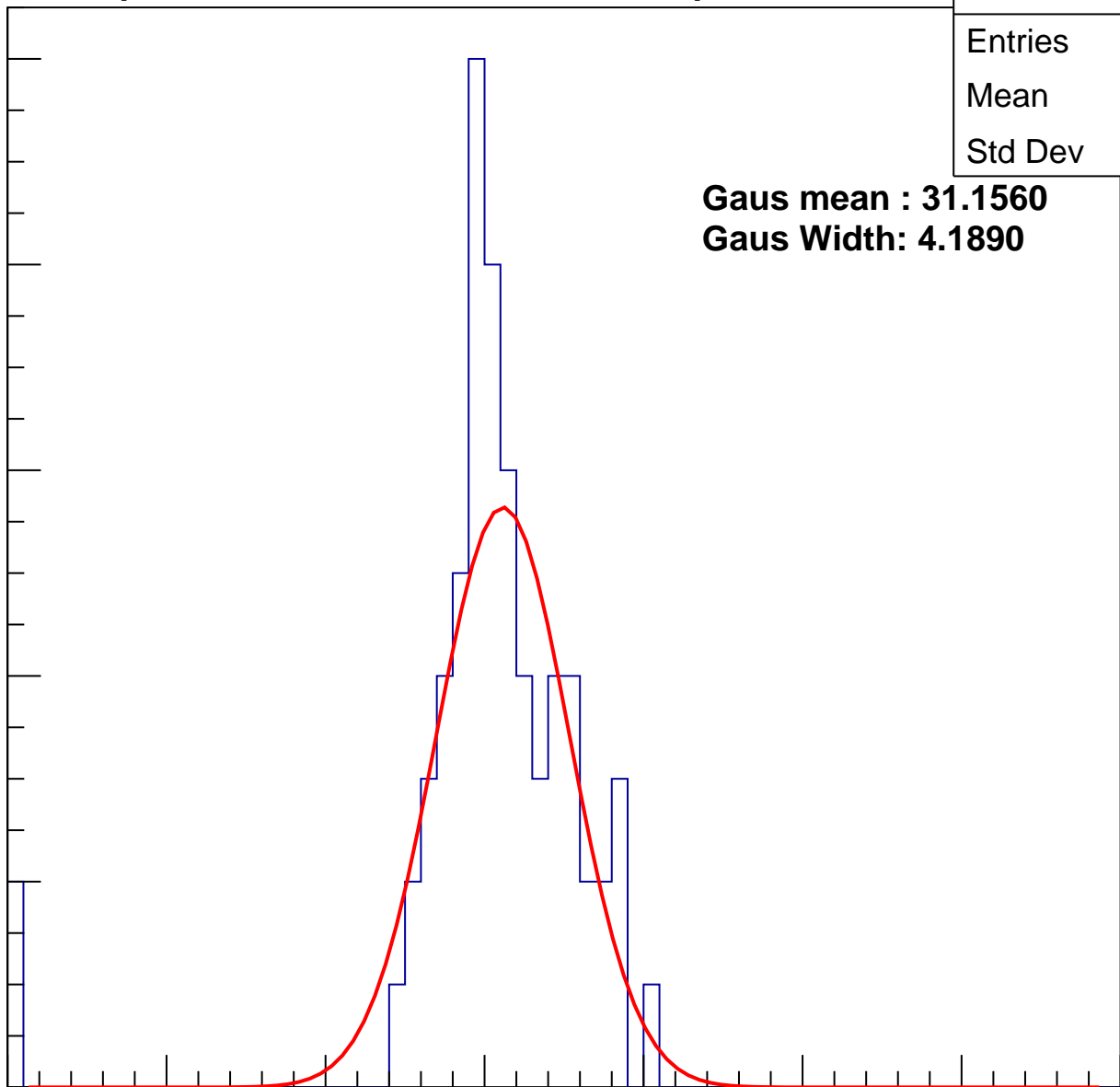
Gaus Width: 4.1890

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch108, adc1

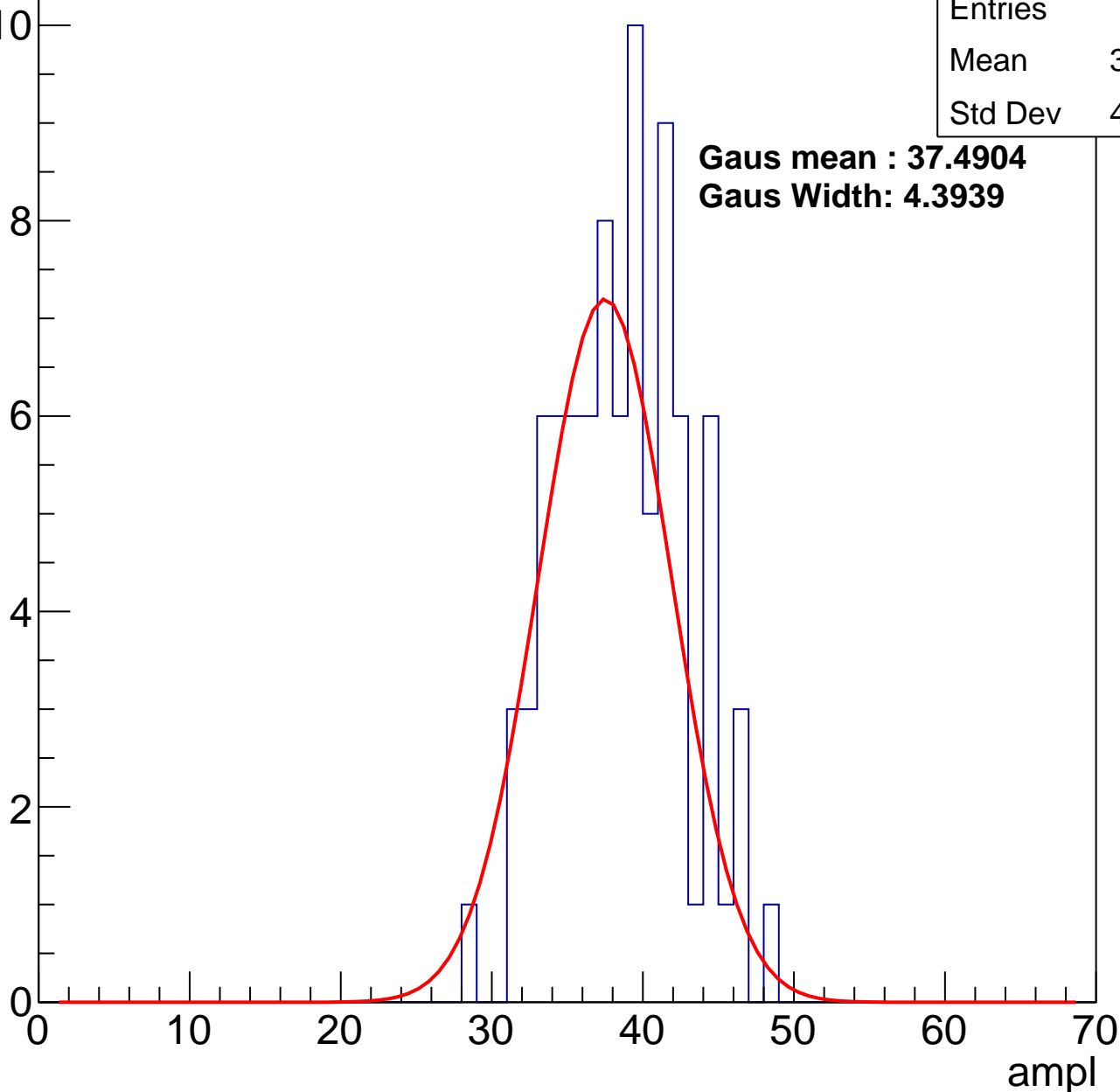
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	38.14
Std Dev	4.117

Gaus mean : 37.4904

Gaus Width: 4.3939



B0L102S, U12-ch108, adc2

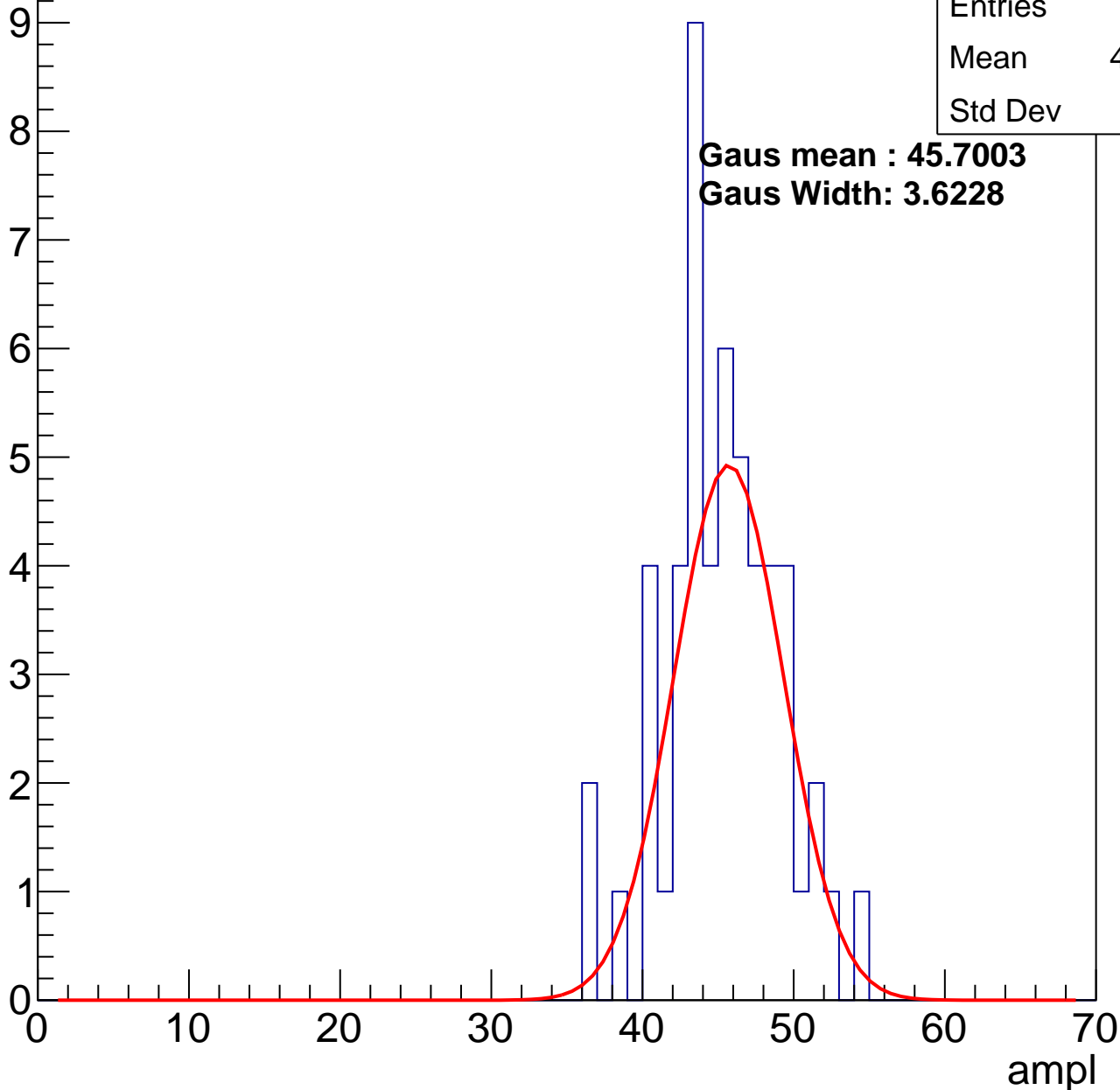
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	44.83
Std Dev	3.76

Gaus mean : 45.7003

Gaus Width: 3.6228

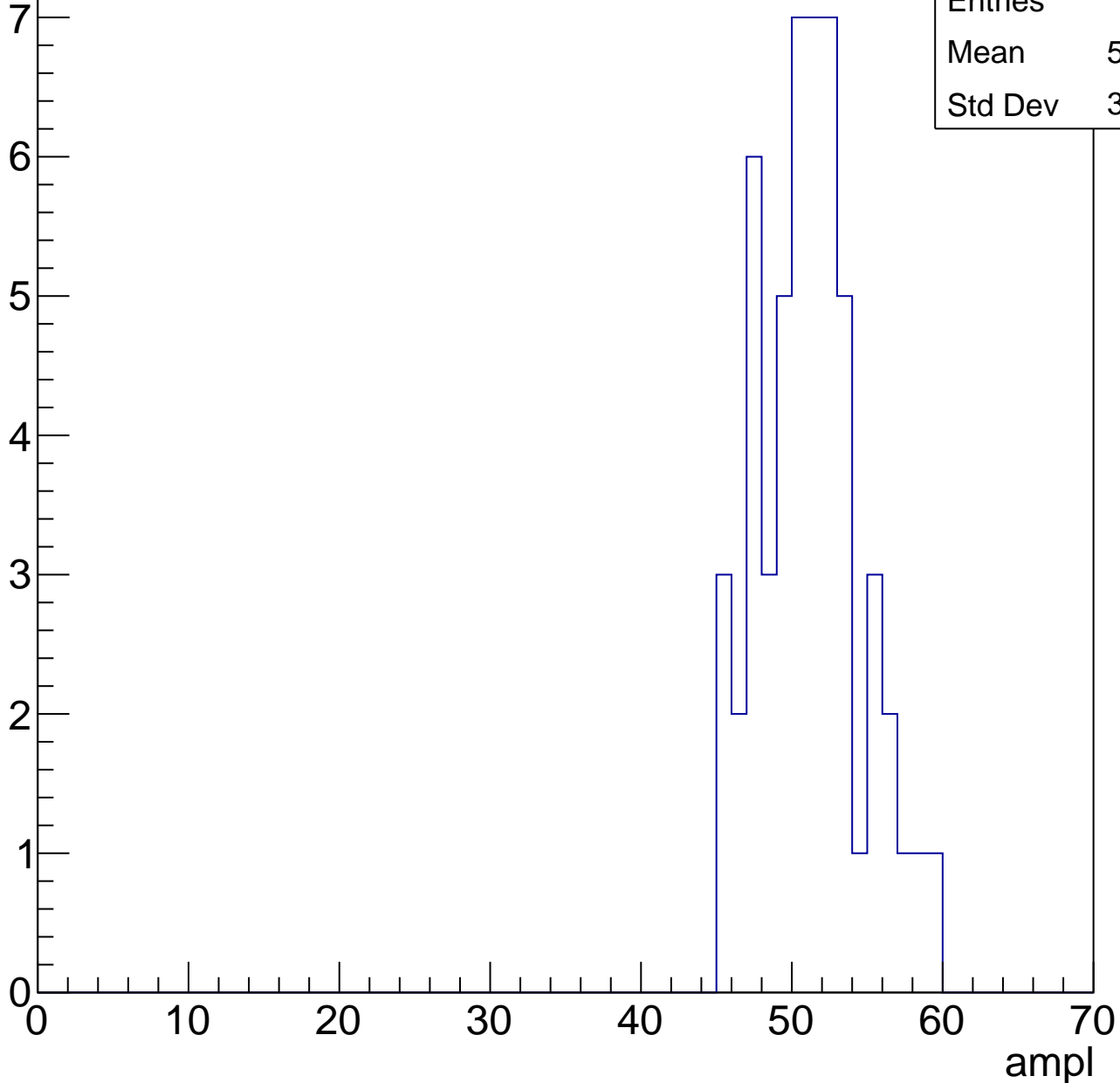


B0L102S, U12-ch108, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	50.72
Std Dev	3.297

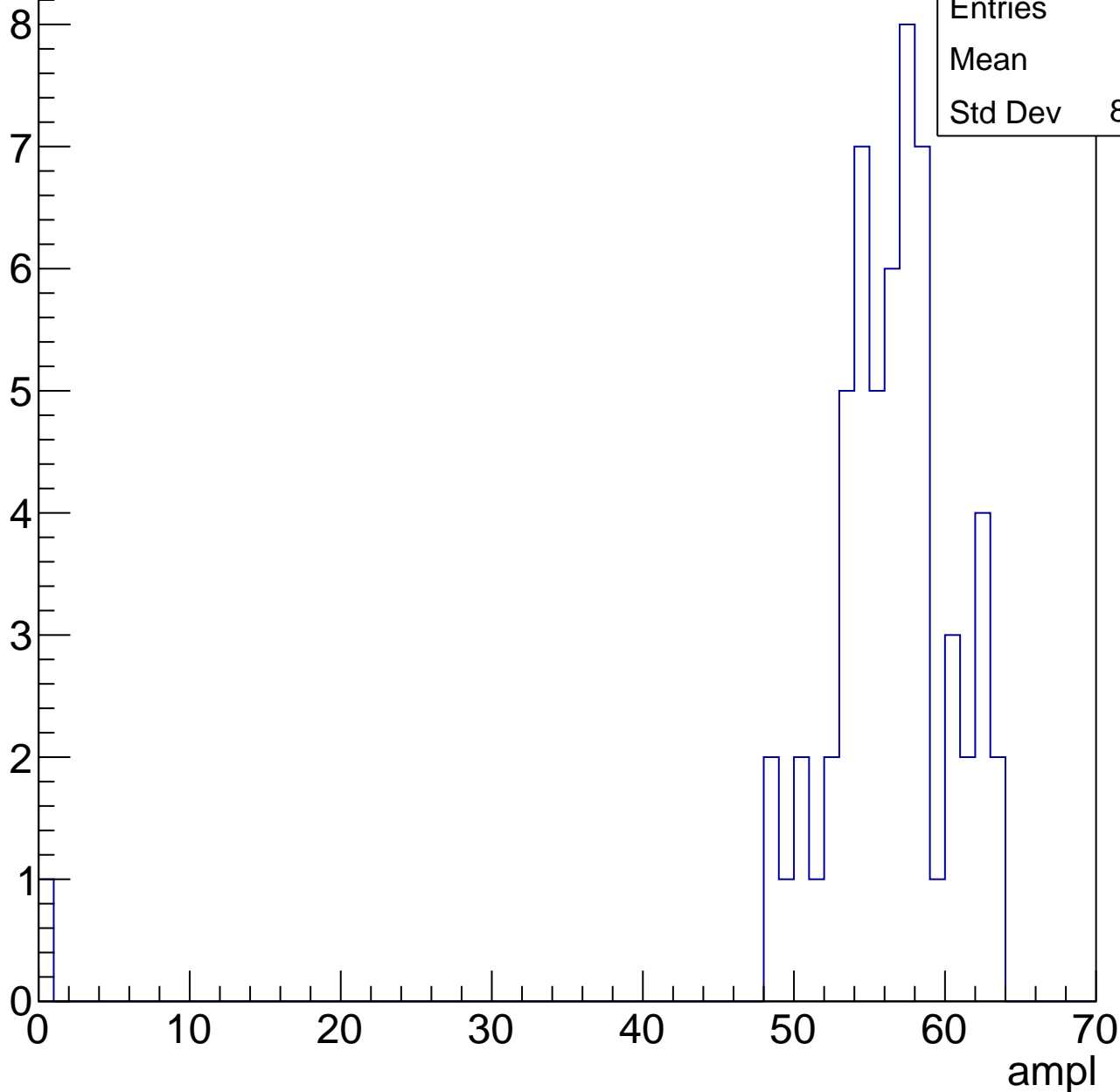


B0L102S, U12-ch108, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	55.1
Std Dev	8.086



B0L102S, U12-ch108, adc5

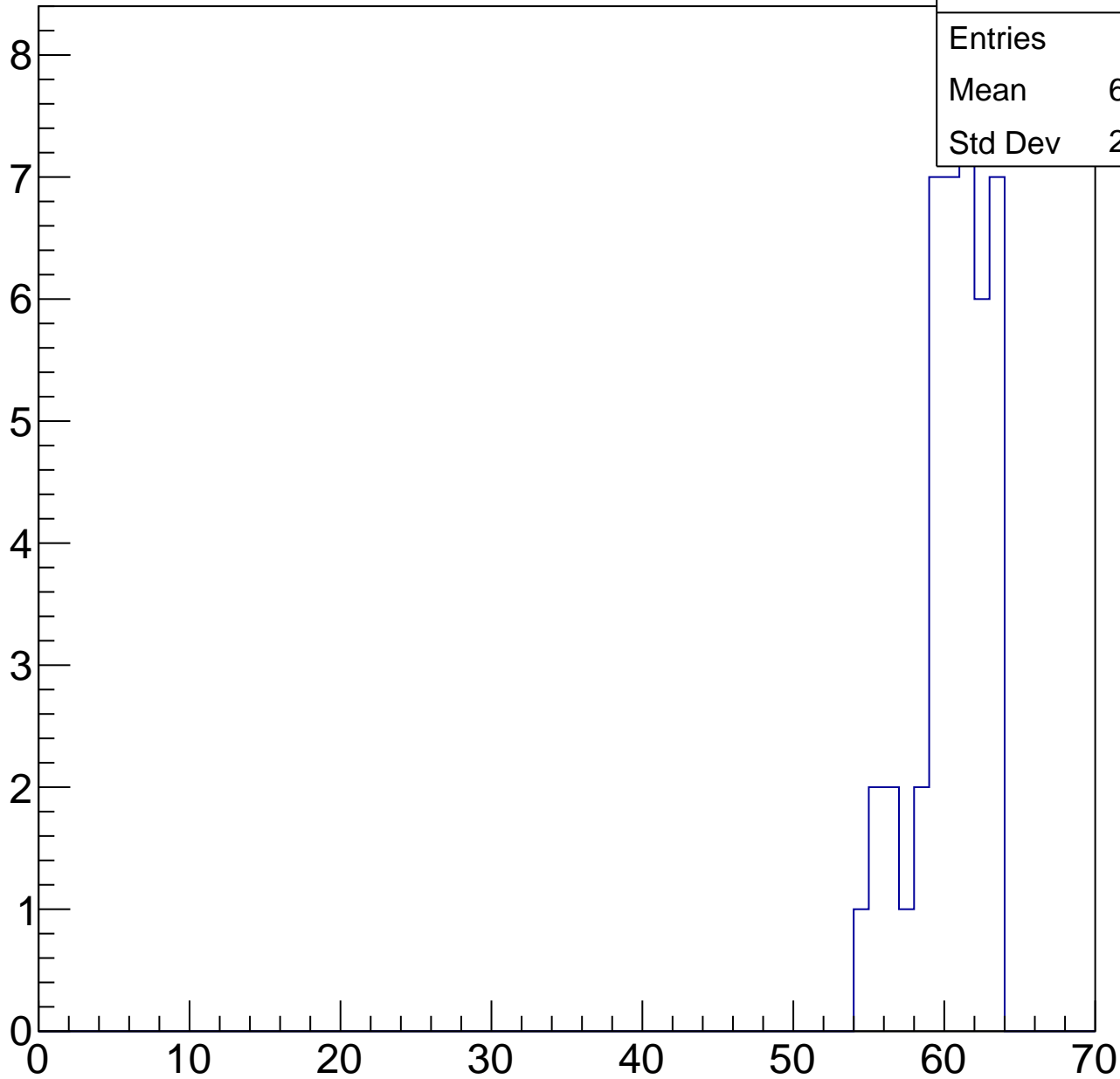
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.07
Std Dev	2.347

ampl



B0L102S, U12-ch108, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	2
Mean	61
Std Dev	2

B0L102S, U12-ch108, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch109, adc0

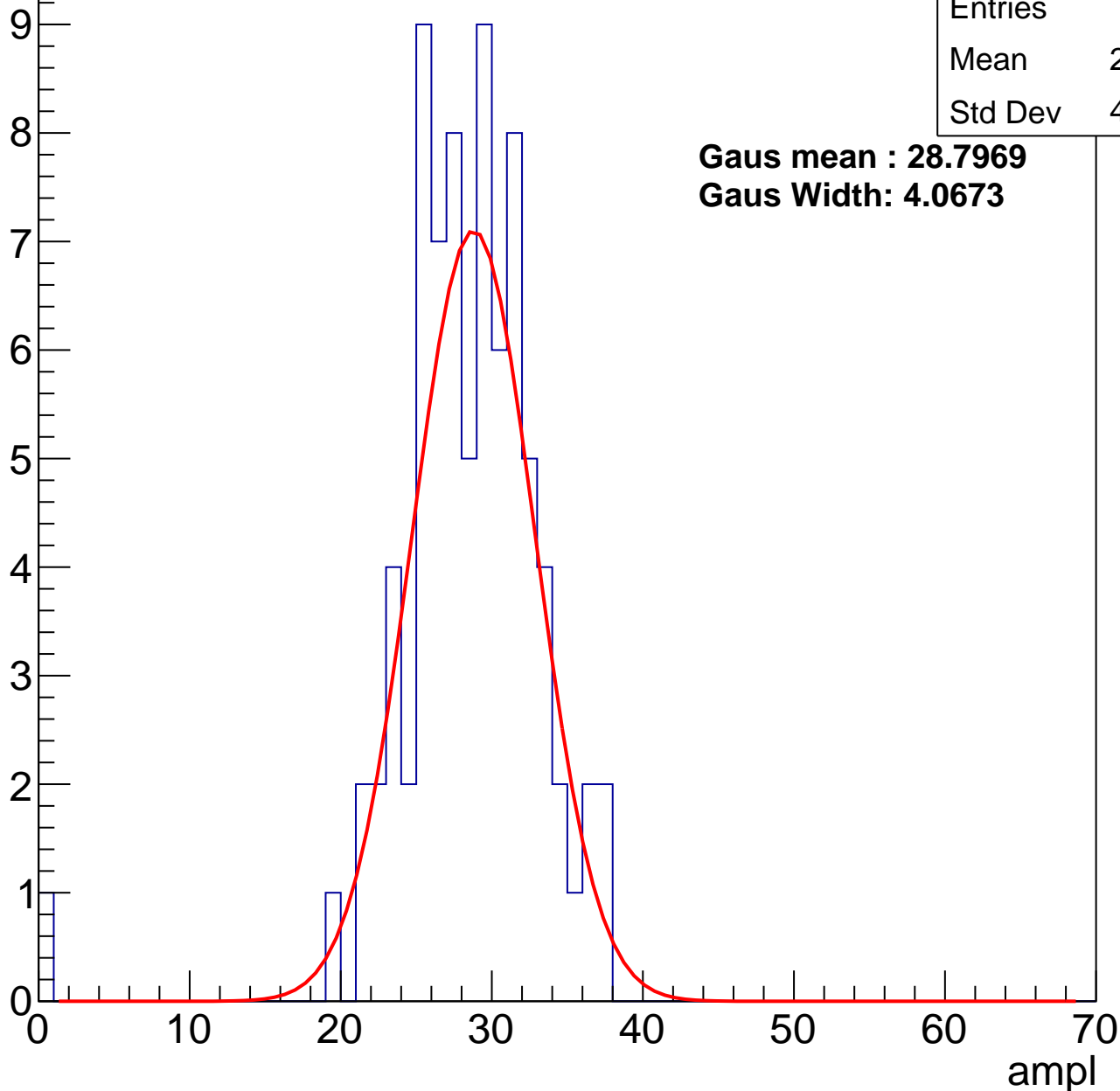
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	27.98
Std Dev	4.975

Gaus mean : 28.7969

Gaus Width: 4.0673



B0L102S, U12-ch109, adc1

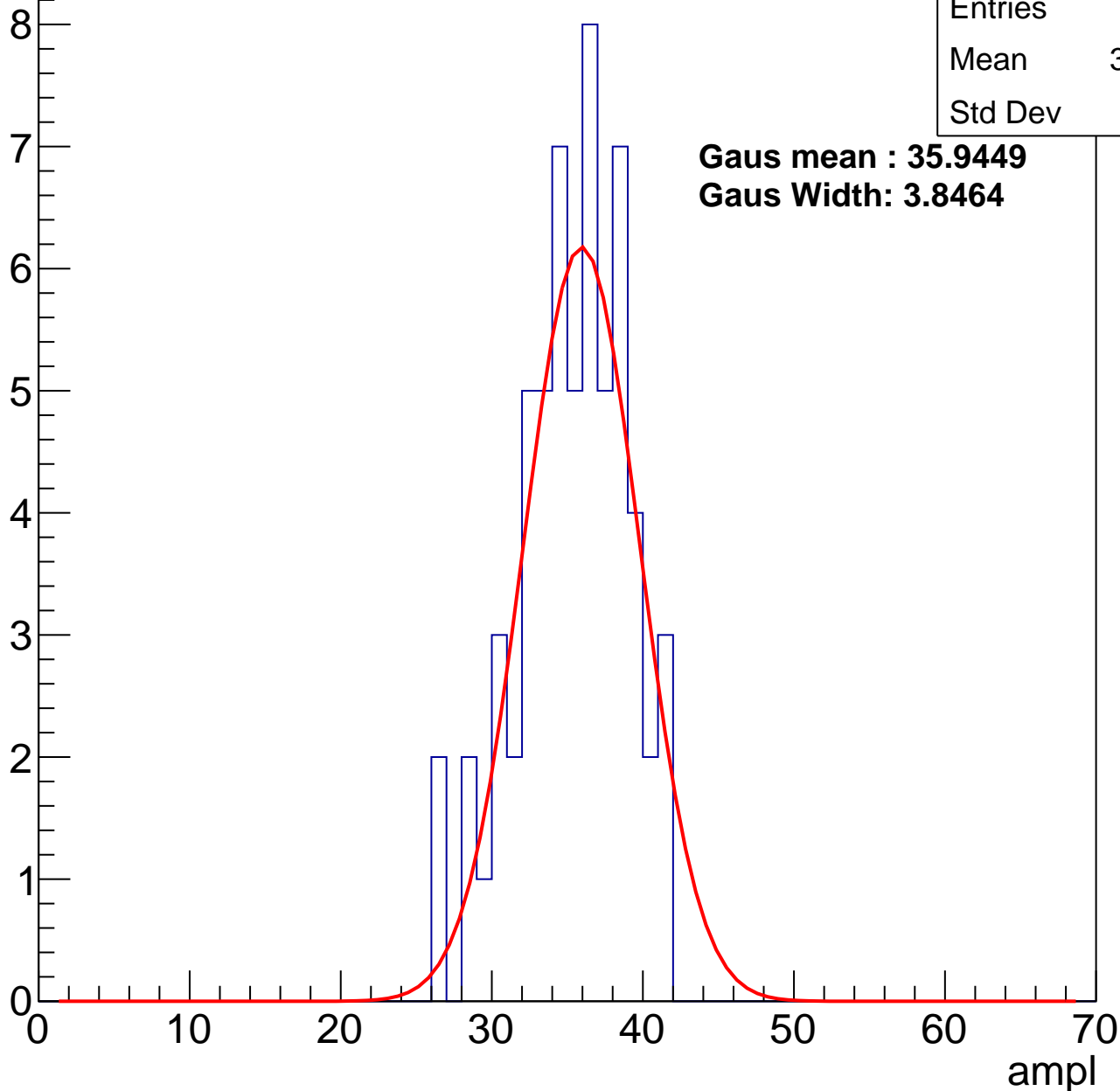
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	34.84
Std Dev	3.59

Gaus mean : 35.9449

Gaus Width: 3.8464



B0L102S, U12-ch109, adc2

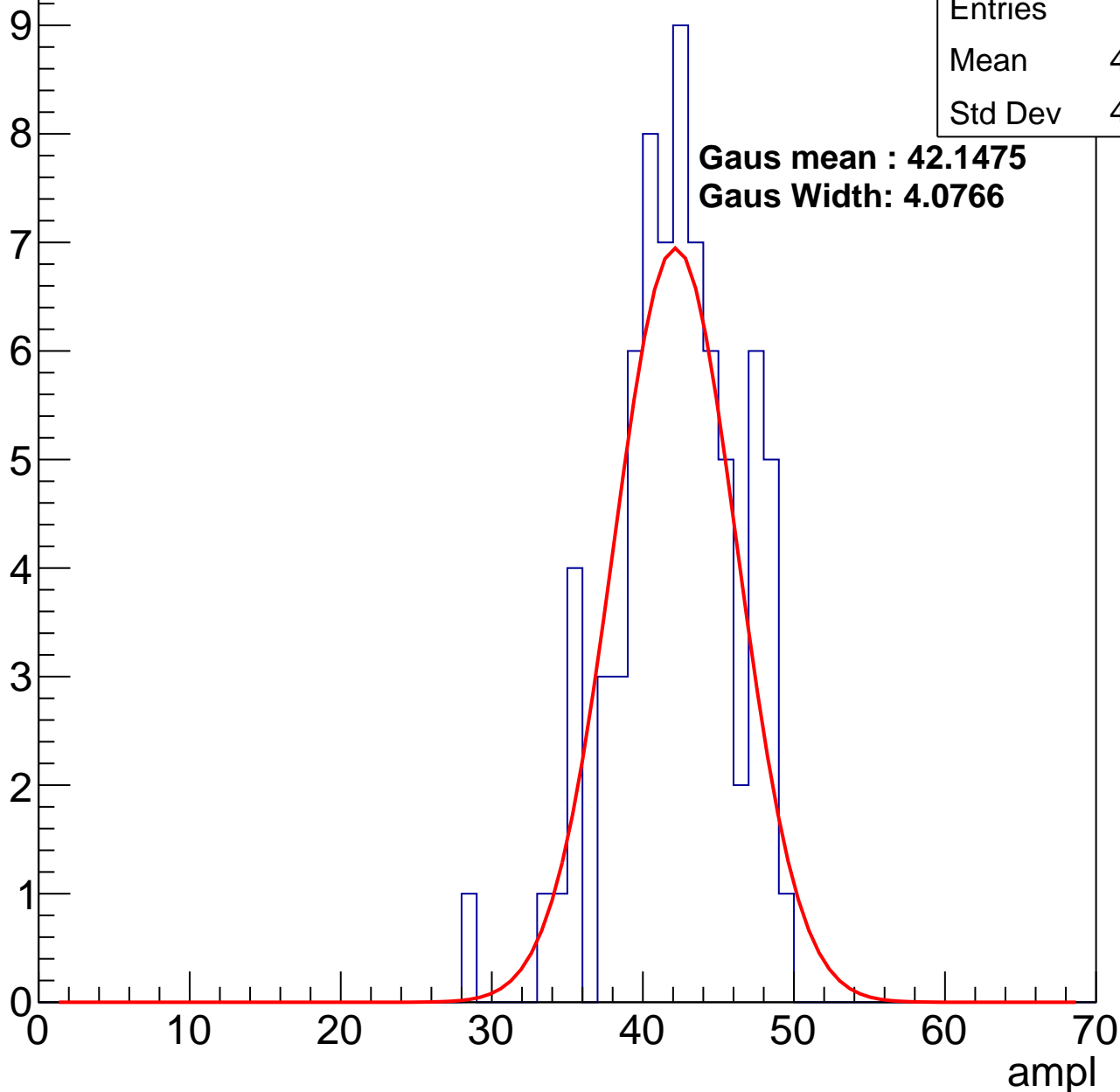
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	41.76
Std Dev	4.059

Gaus mean : 42.1475

Gaus Width: 4.0766

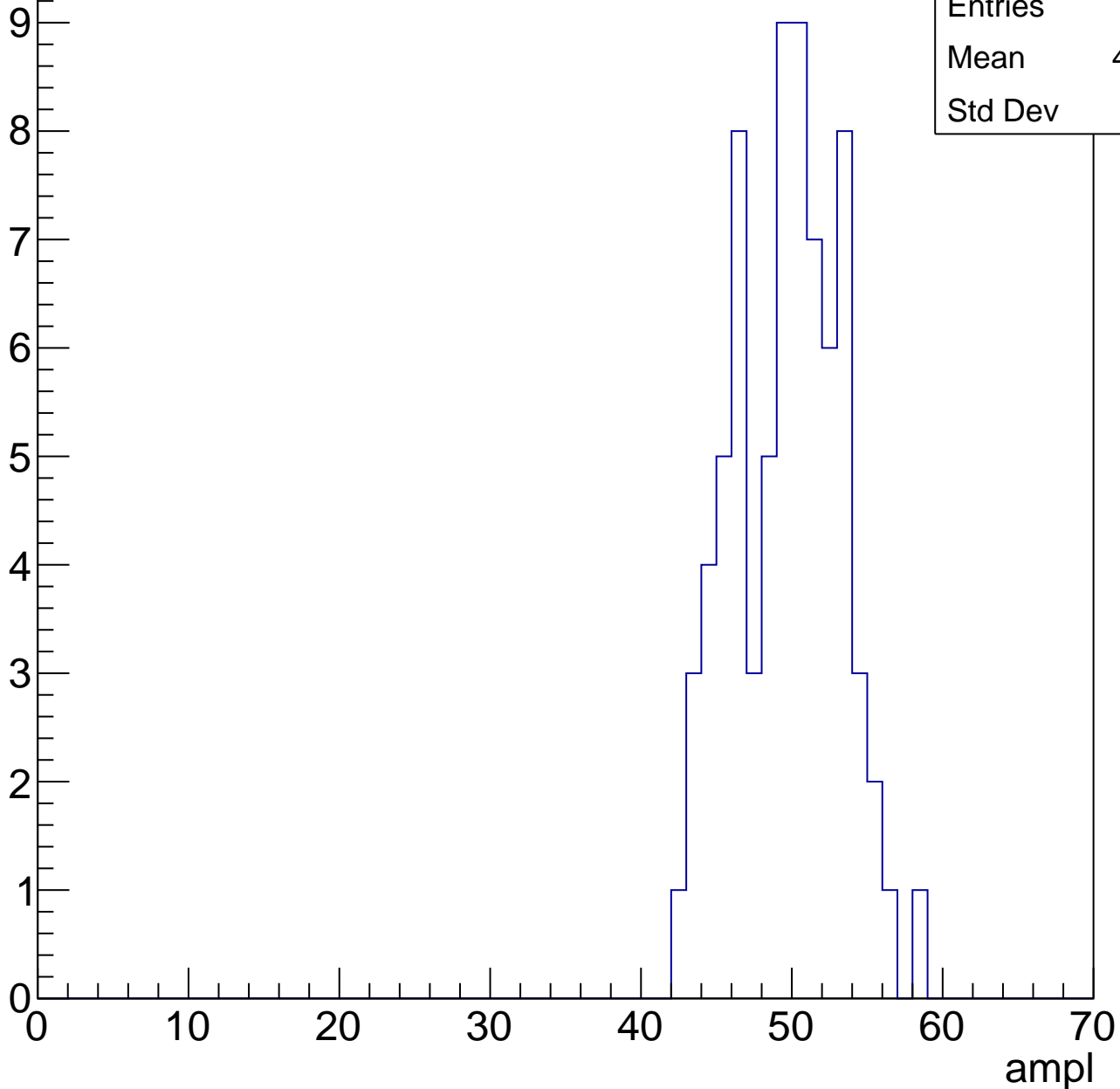


B0L102S, U12-ch109, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	49.21
Std Dev	3.5

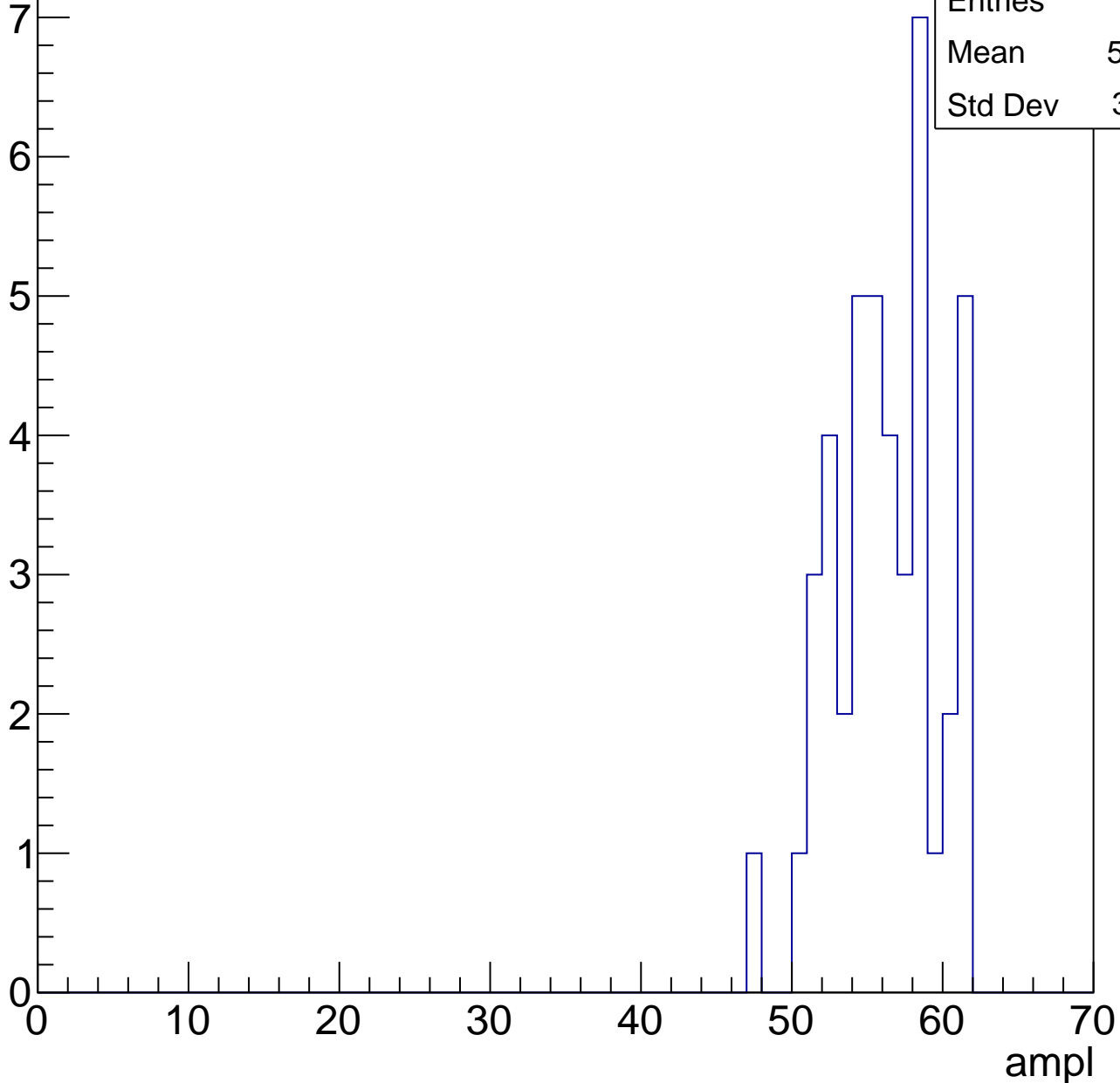


B0L102S, U12-ch109, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	55.67
Std Dev	3.381



B0L102S, U12-ch109, adc5

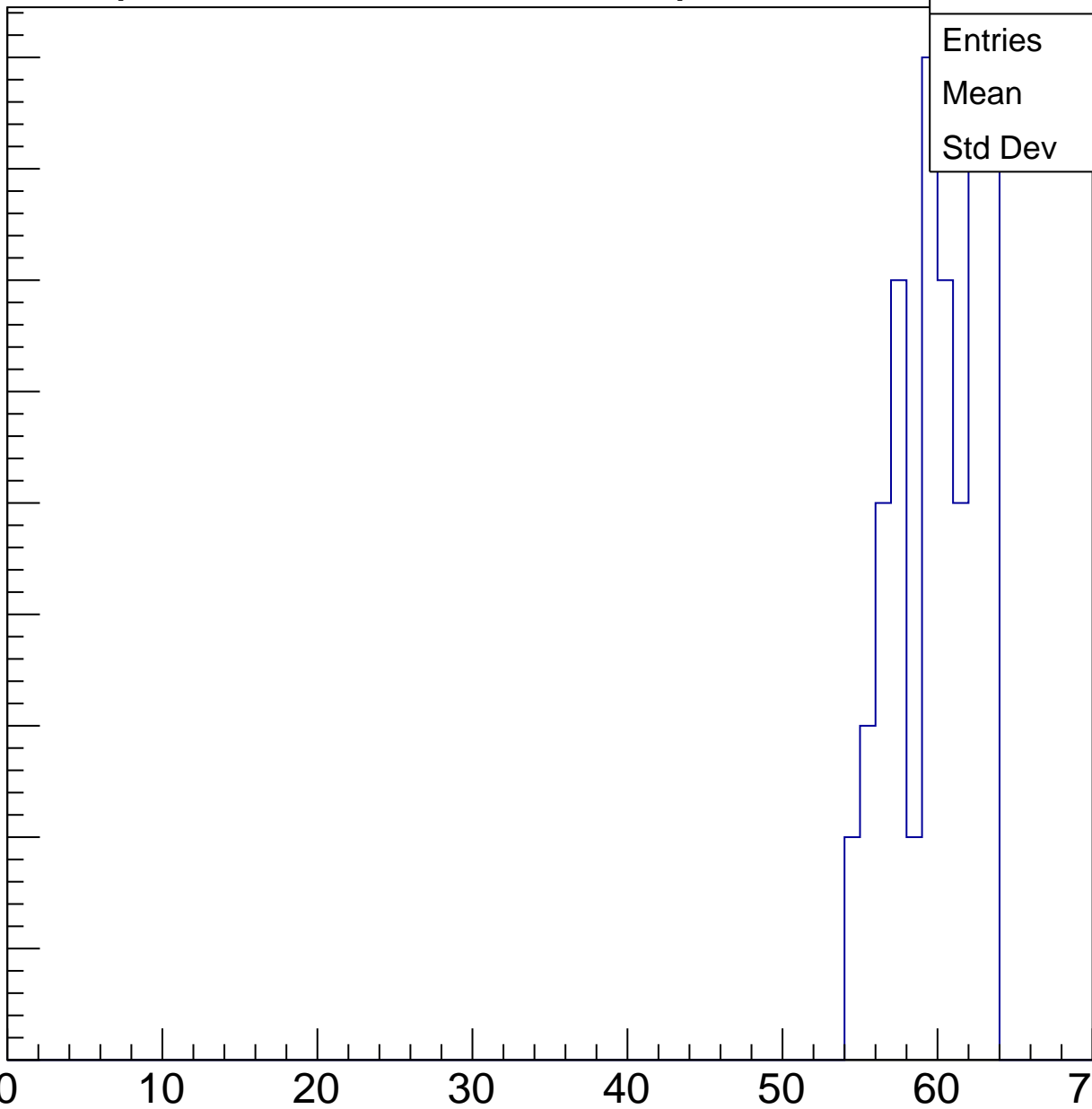
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.36
Std Dev	2.649

ampl



B0L102S, U12-ch109, adc6

calib_packv5_042523_0143.root, FC#12, port B1

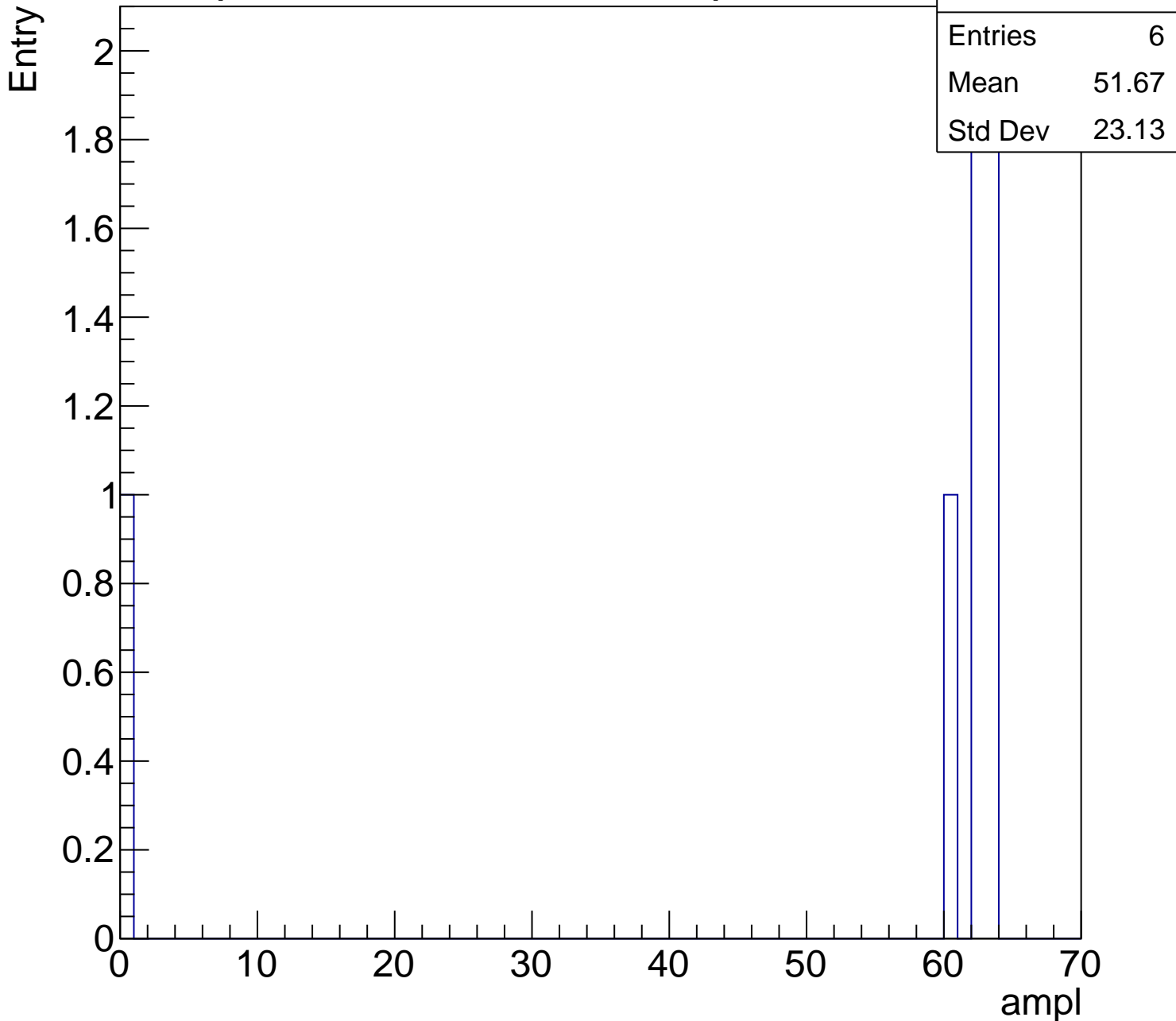
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.13

0 10 20 30 40 50 60 70

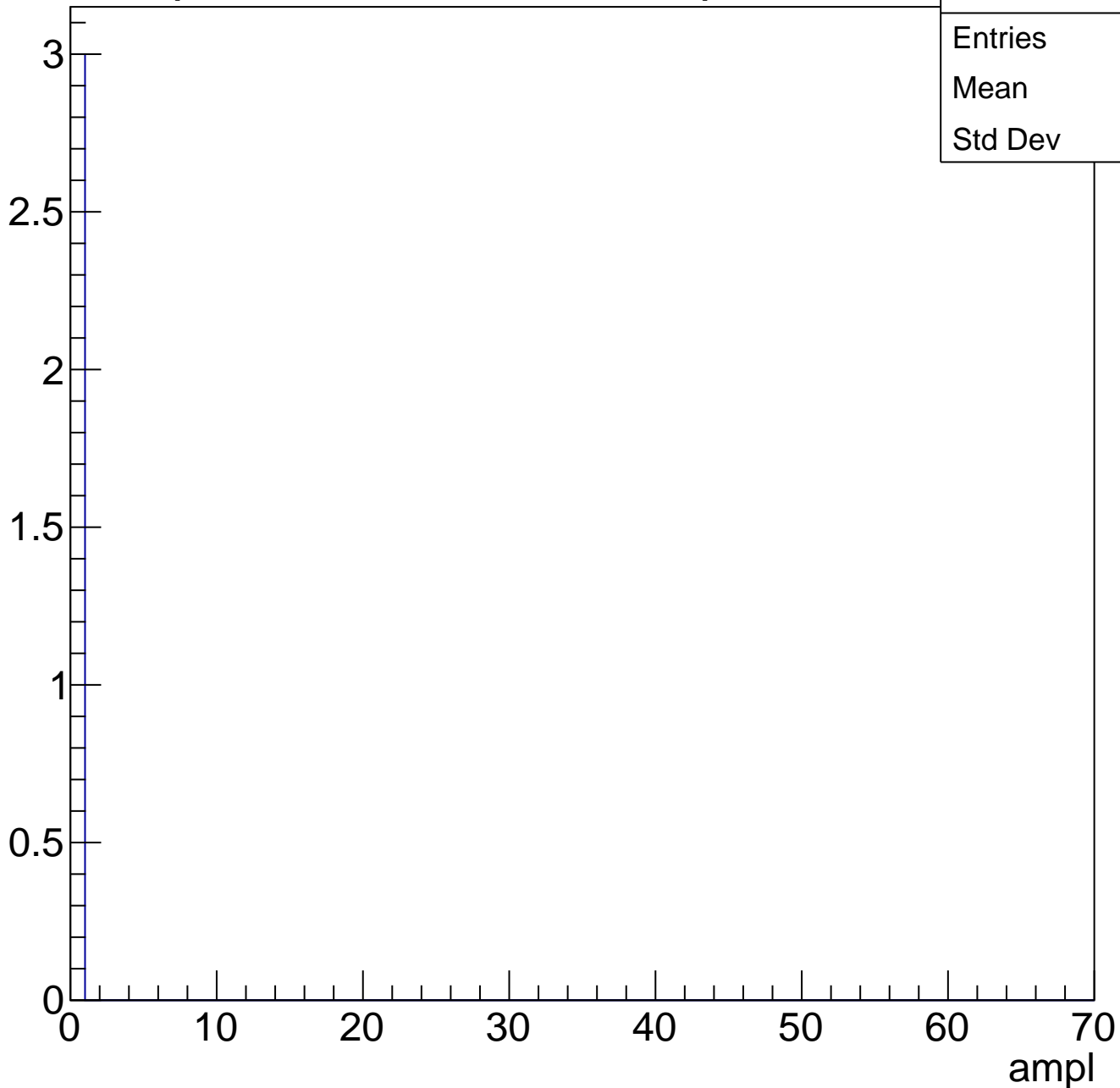
ampl



B0L102S, U12-ch109, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U12-ch110, adc0

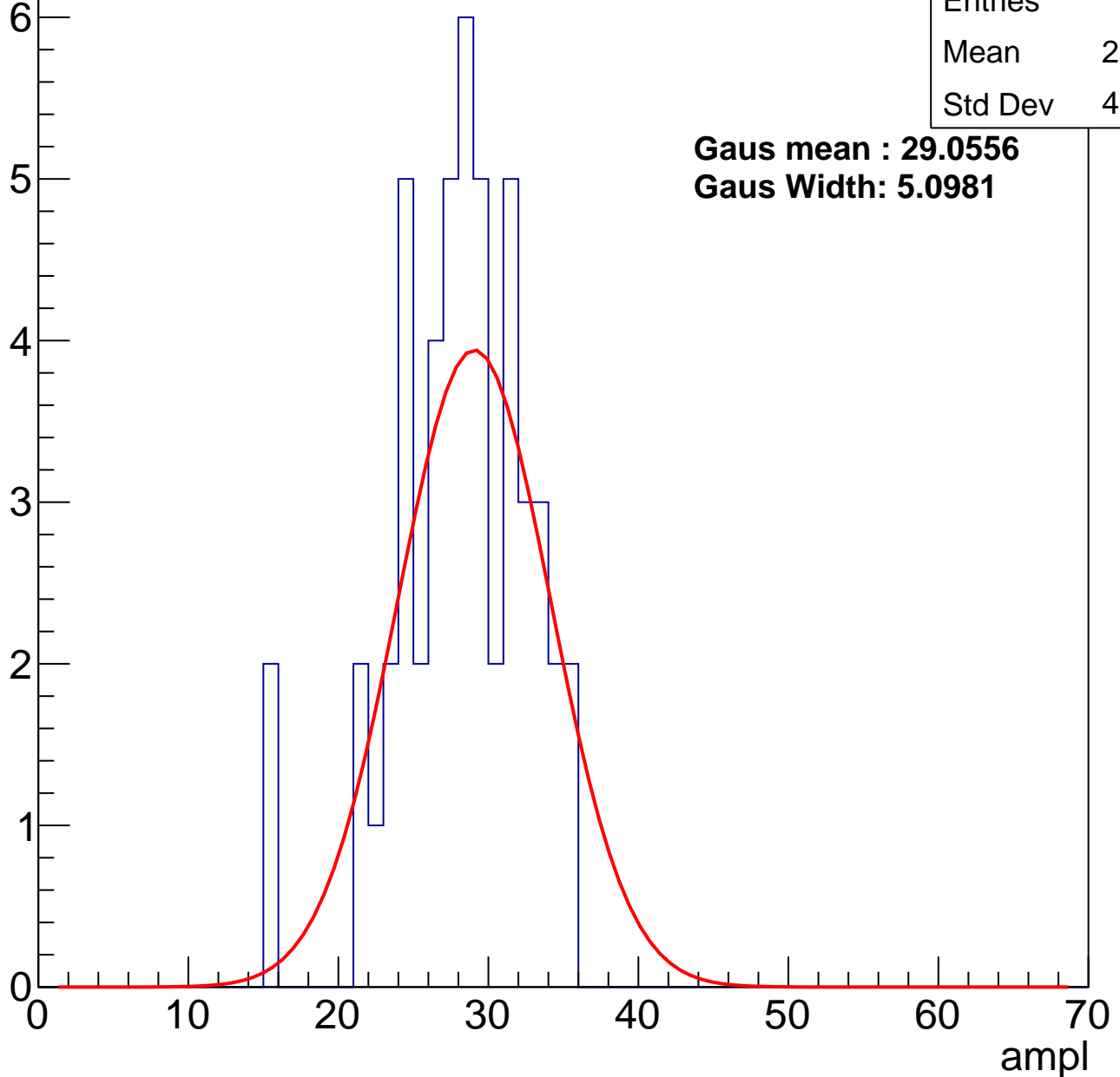
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	27.65
Std Dev	4.396

Gaus mean : 29.0556

Gaus Width: 5.0981



B0L102S, U12-ch110, adc1

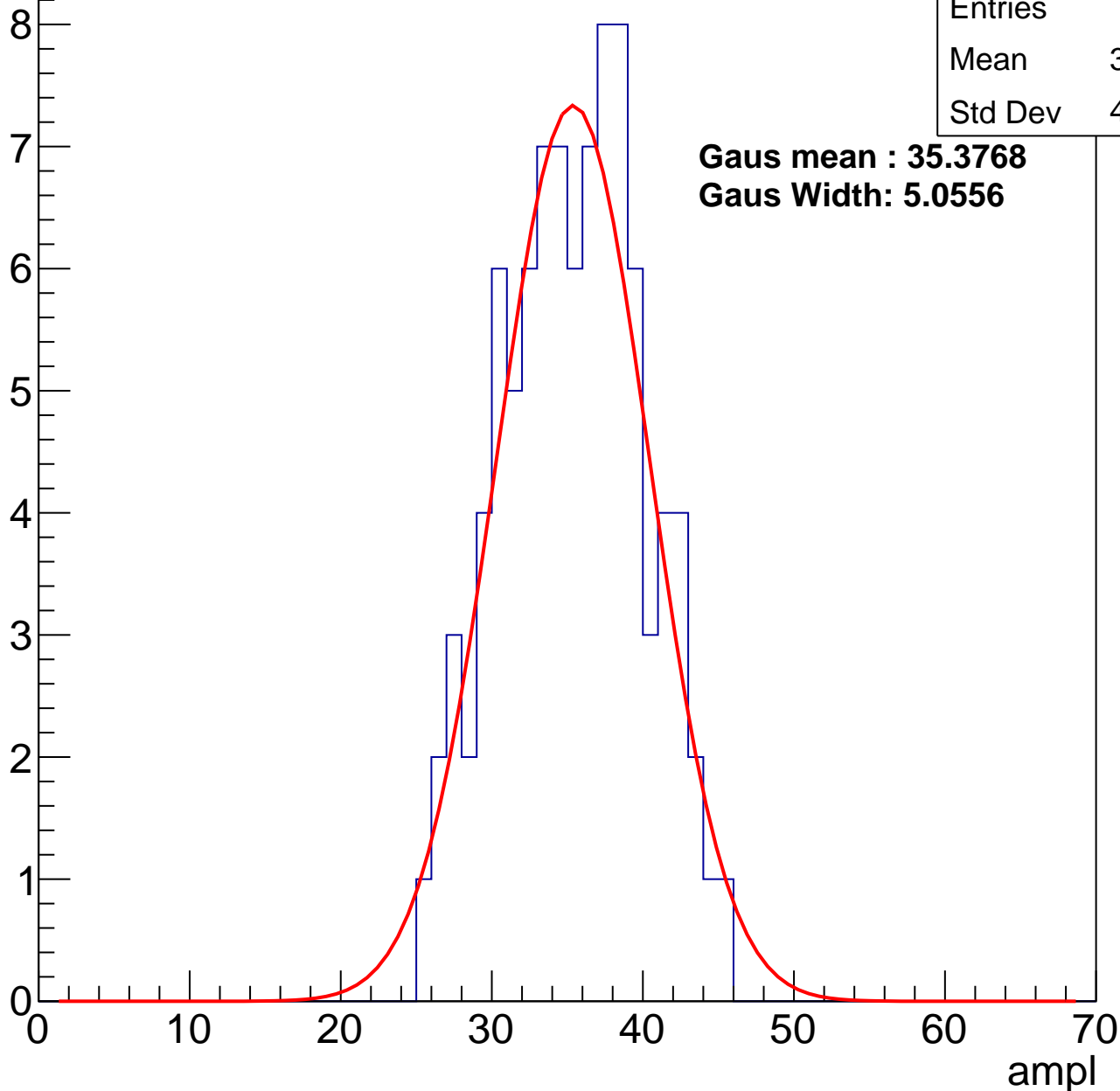
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	93
Mean	34.94
Std Dev	4.574

Gaus mean : 35.3768

Gaus Width: 5.0556



B0L102S, U12-ch110, adc2

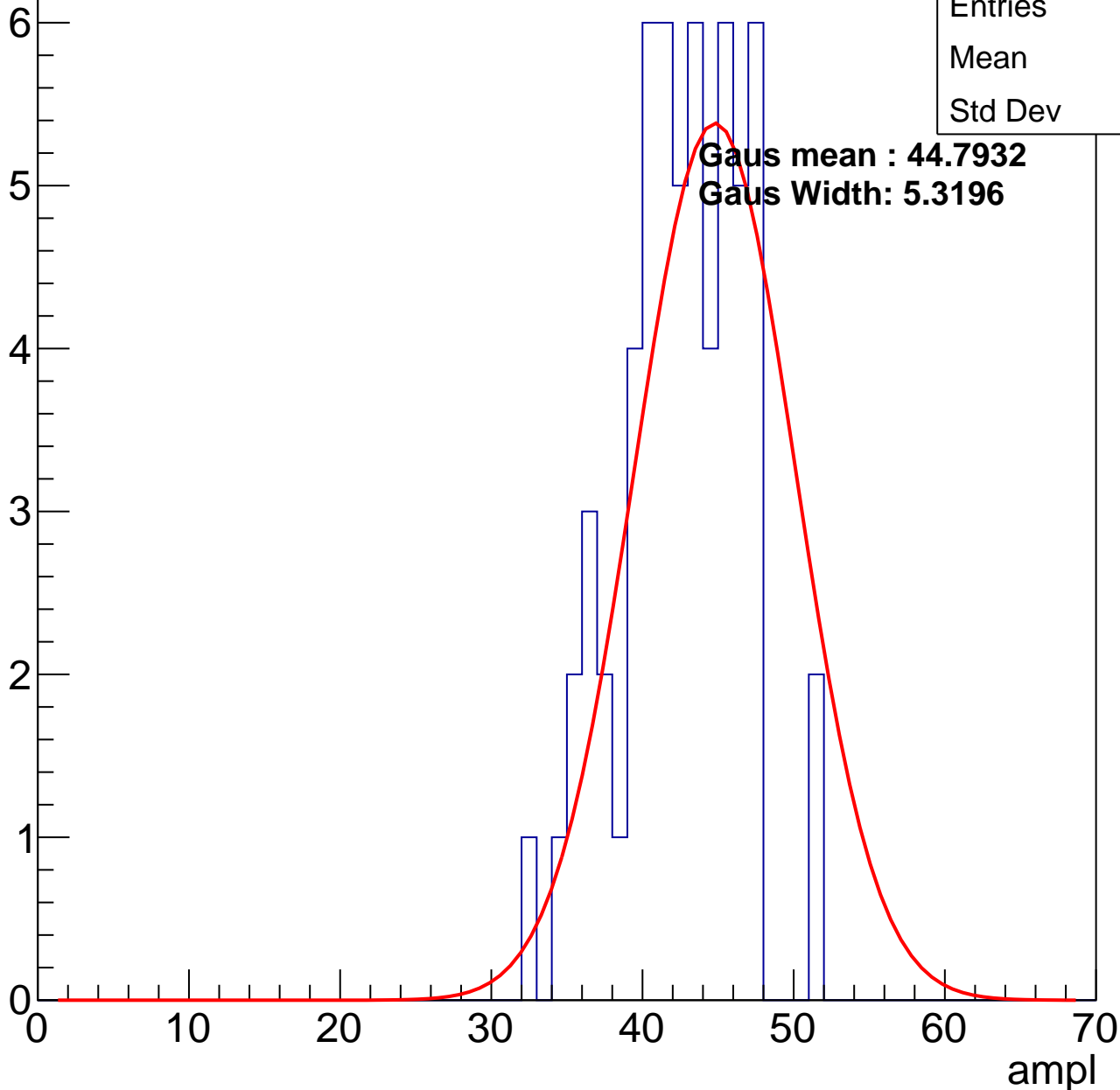
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.1
Std Dev	4.02

Gaus mean : 44.7932

Gaus Width: 5.3196

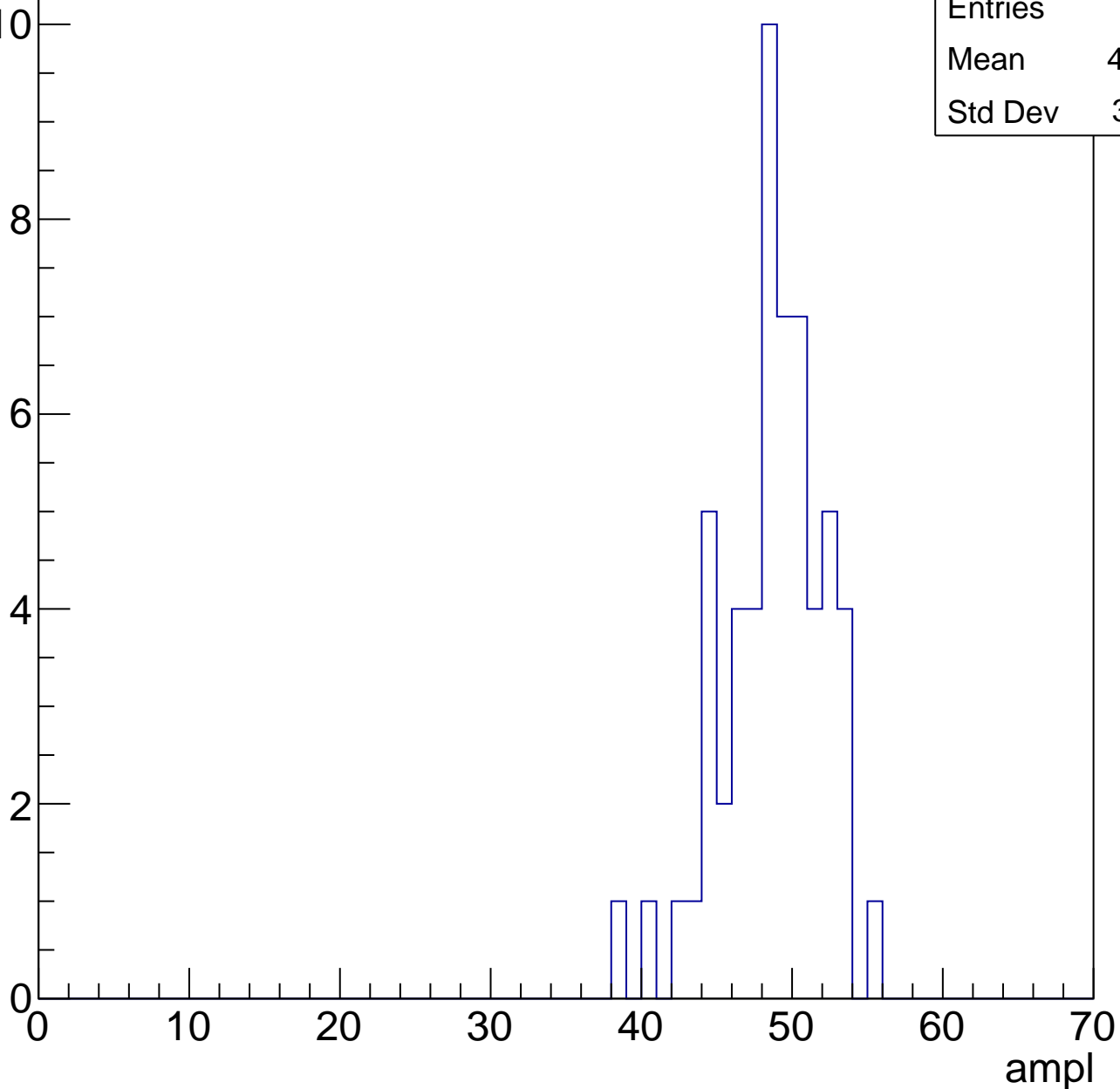


B0L102S, U12-ch110, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.23
Std Dev	3.351

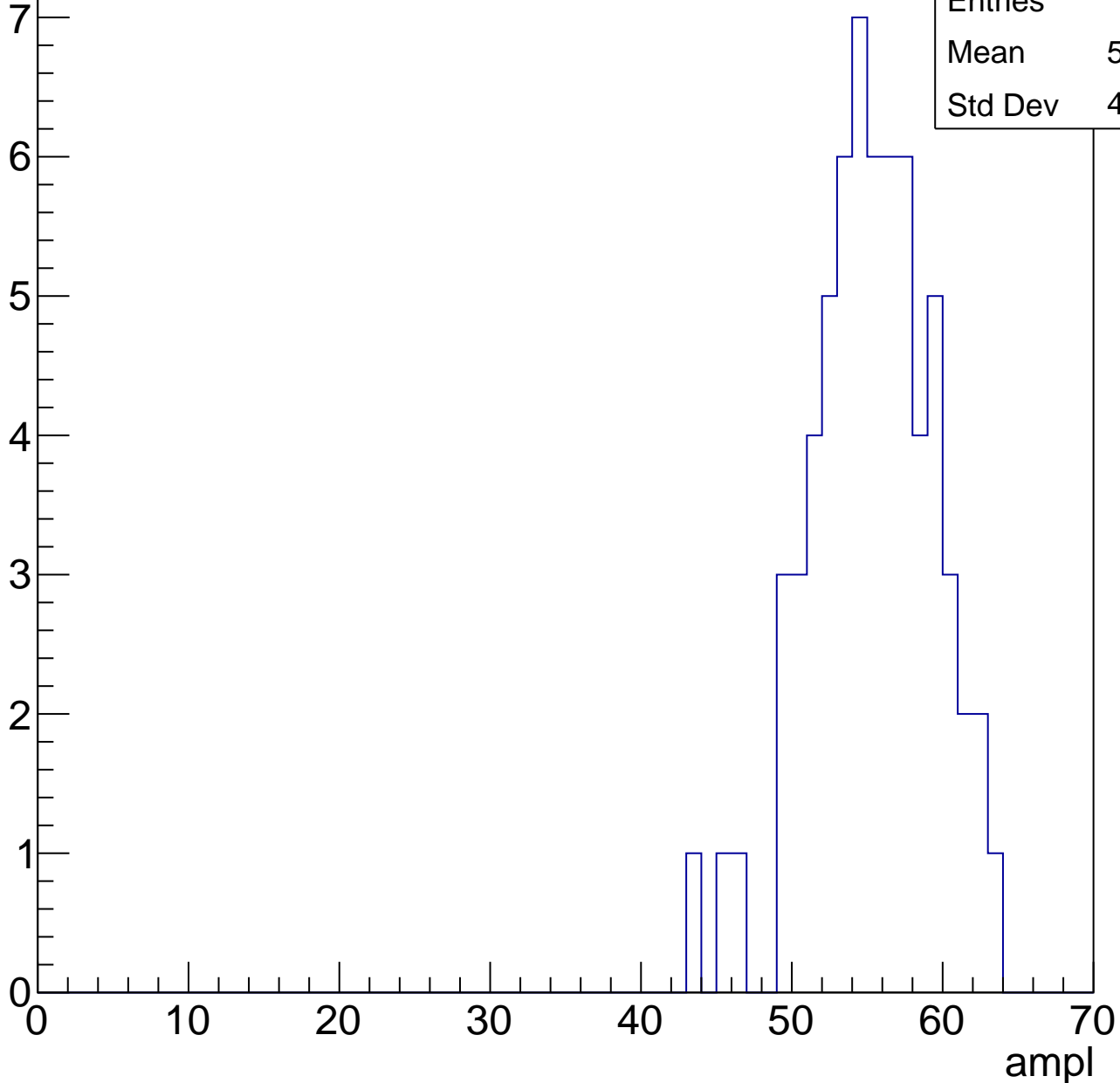


B0L102S, U12-ch110, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	54.77
Std Dev	4.082

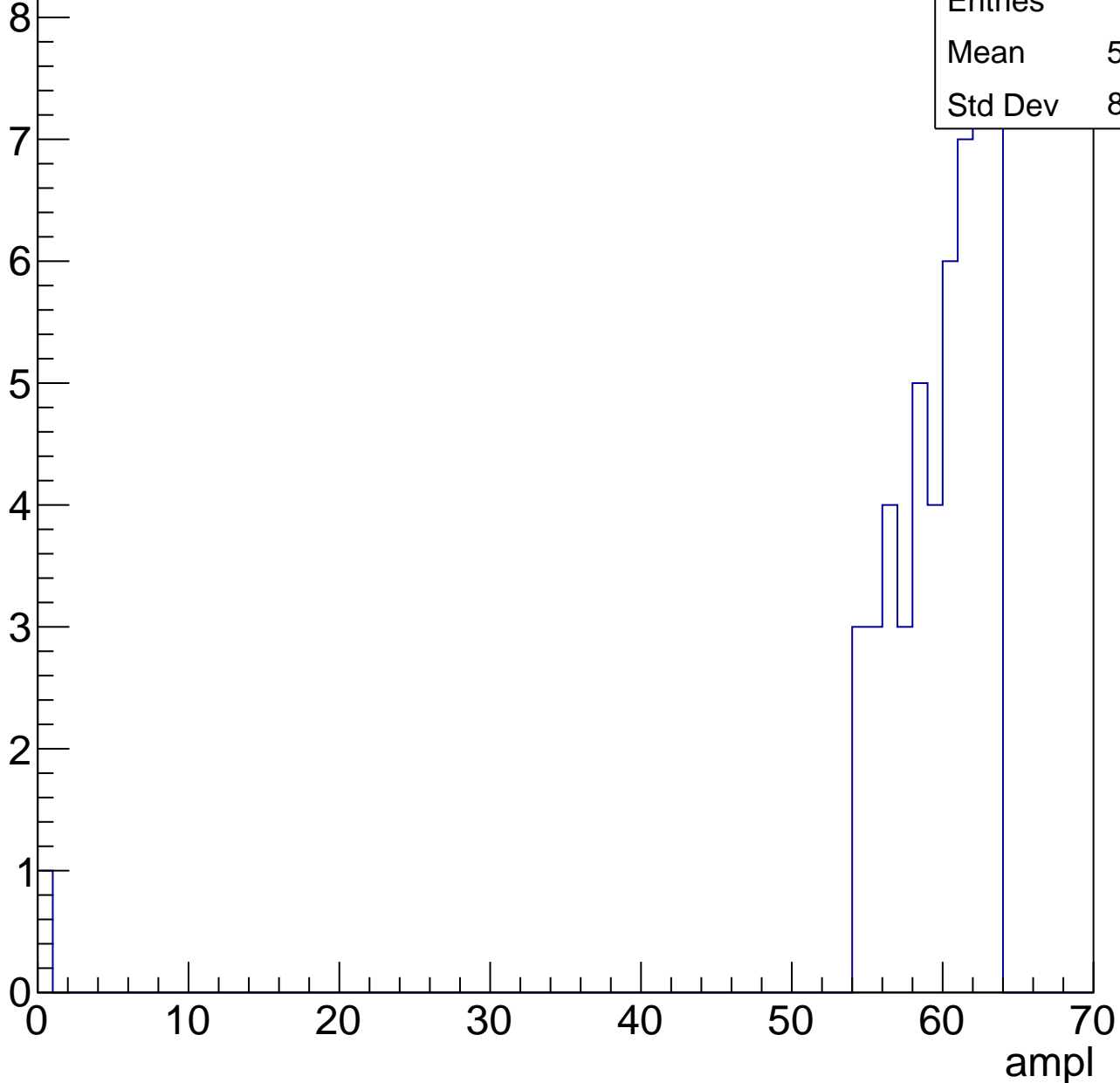


B0L102S, U12-ch110, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	58.37
Std Dev	8.627



B0L102S, U12-ch110, adc6

calib_packv5_042523_0143.root, FC#12, port B1

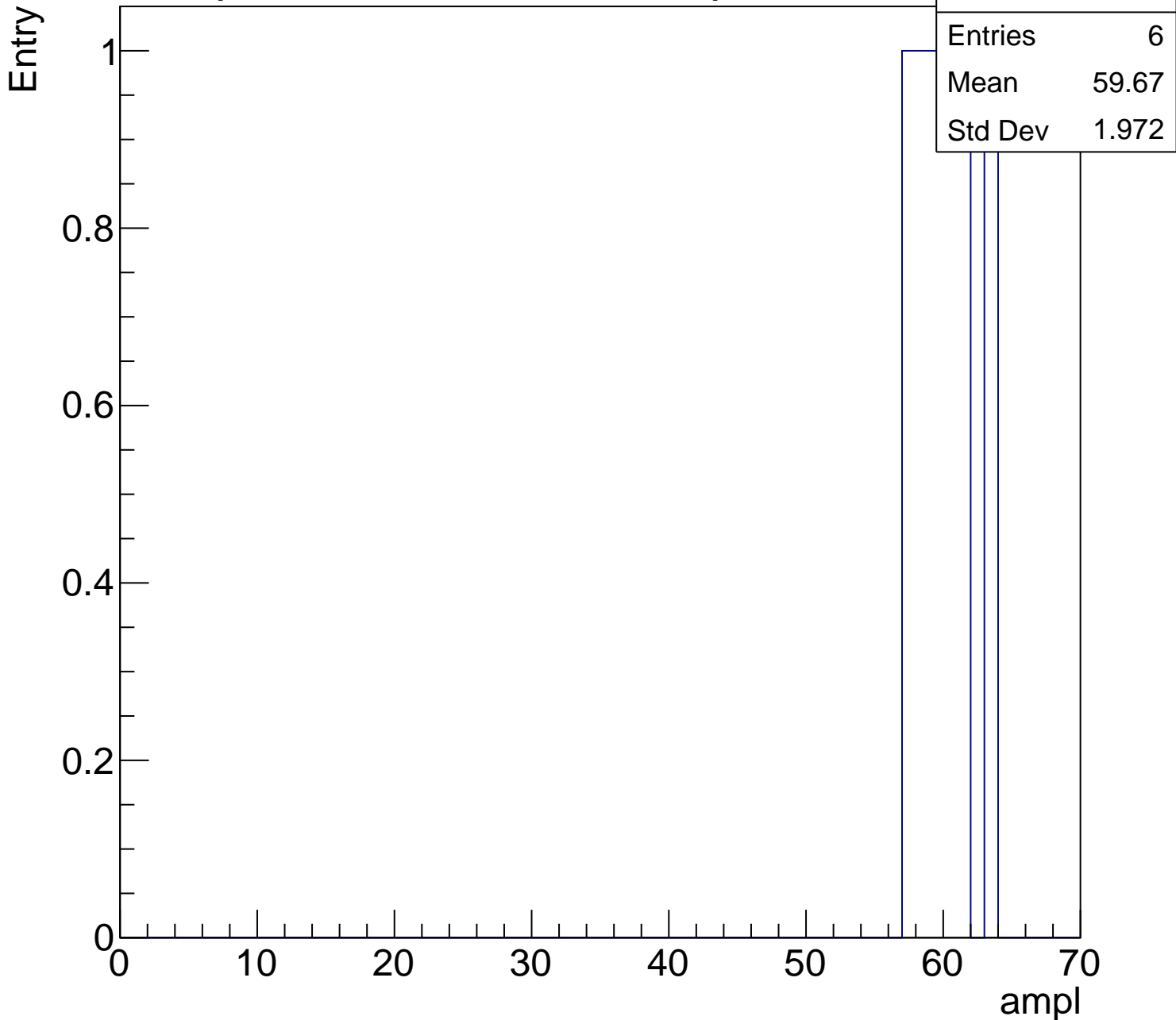
Entry

1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	59.67
Std Dev	1.972

0 10 20 30 40 50 60 70

ampl



B0L102S, U12-ch110, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch111, adc0

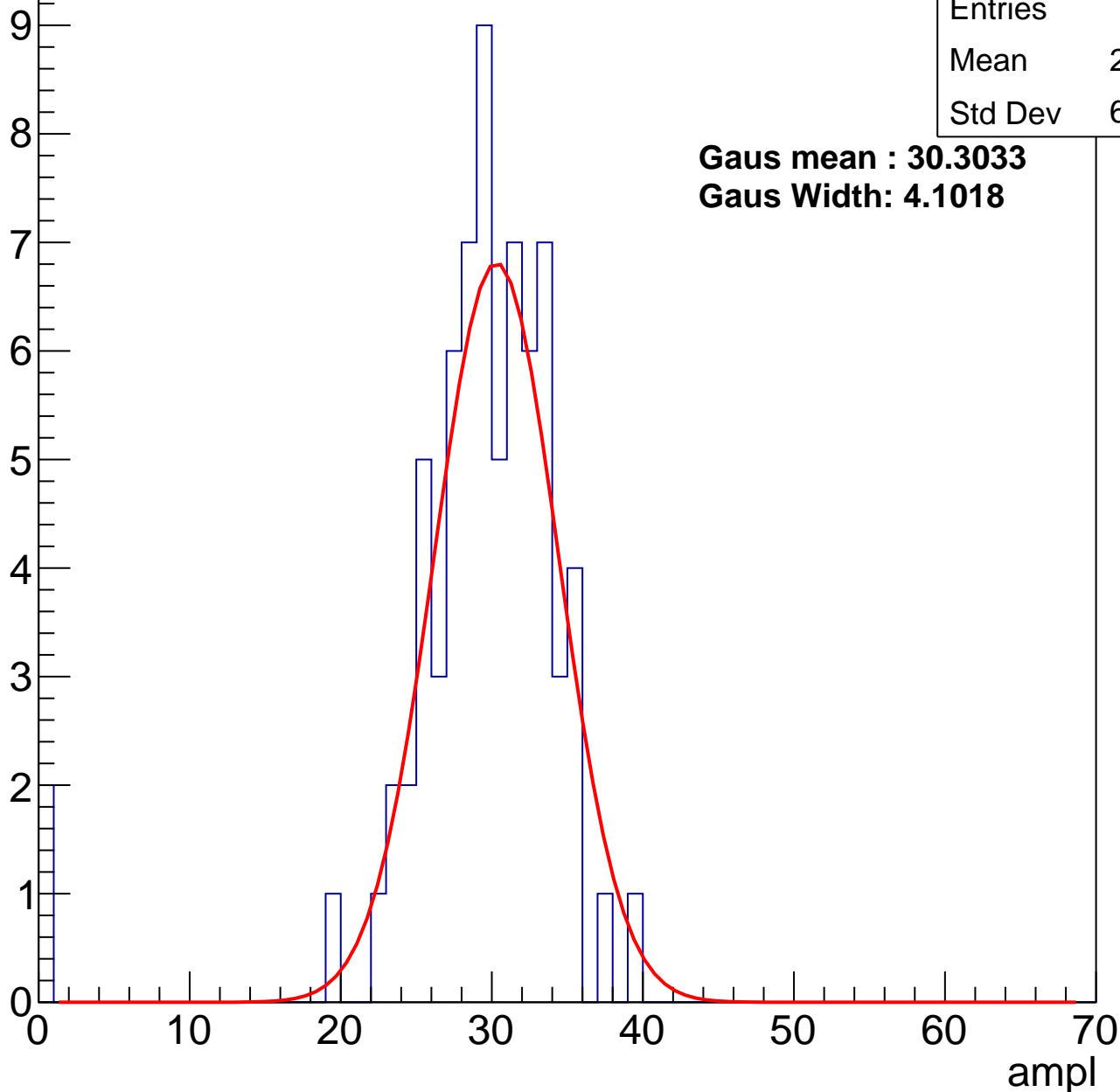
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	28.68
Std Dev	6.085

Gaus mean : 30.3033

Gaus Width: 4.1018



B0L102S, U12-ch111, adc1

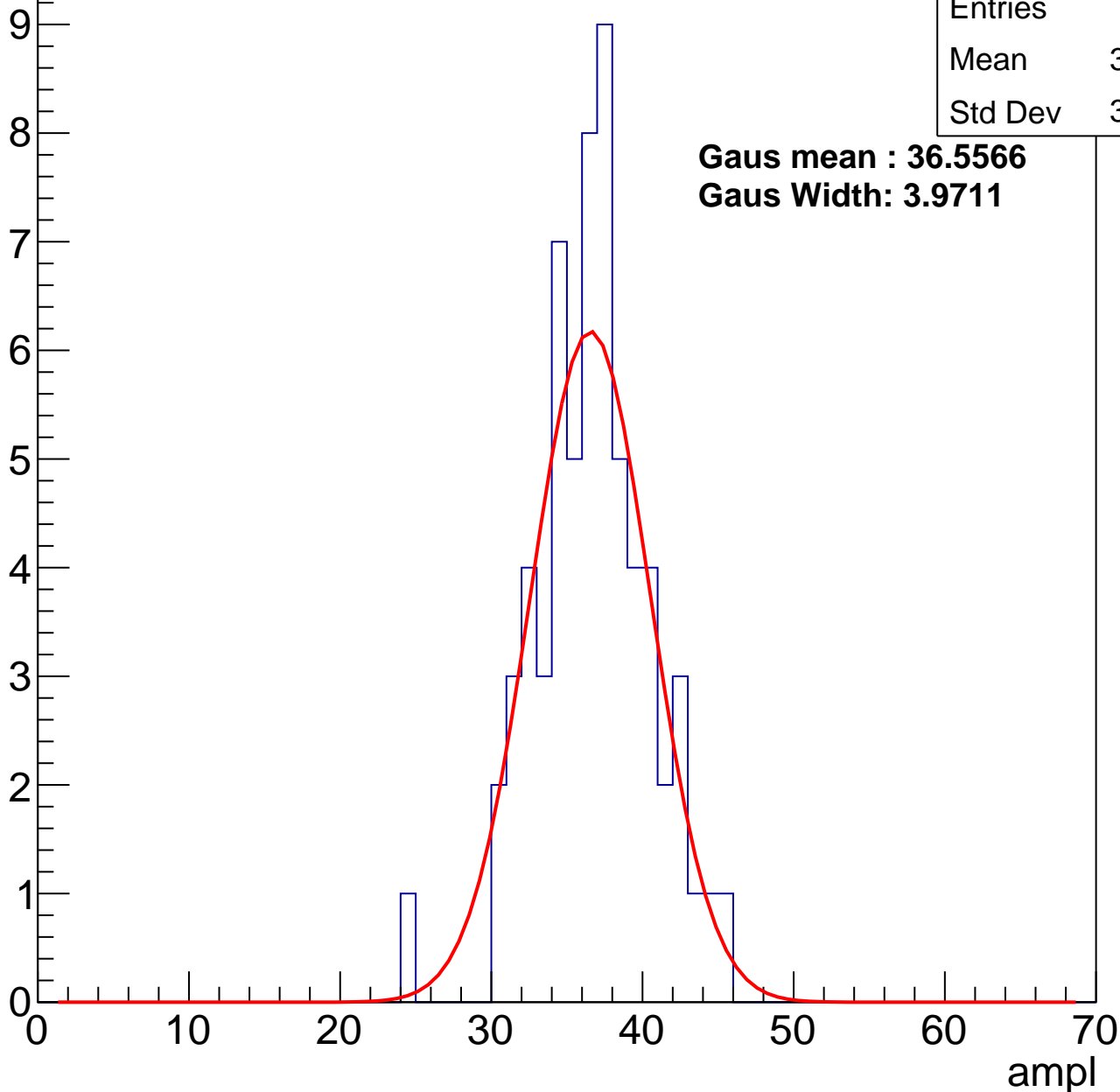
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.25
Std Dev	3.763

Gaus mean : 36.5566

Gaus Width: 3.9711



B0L102S, U12-ch111, adc2

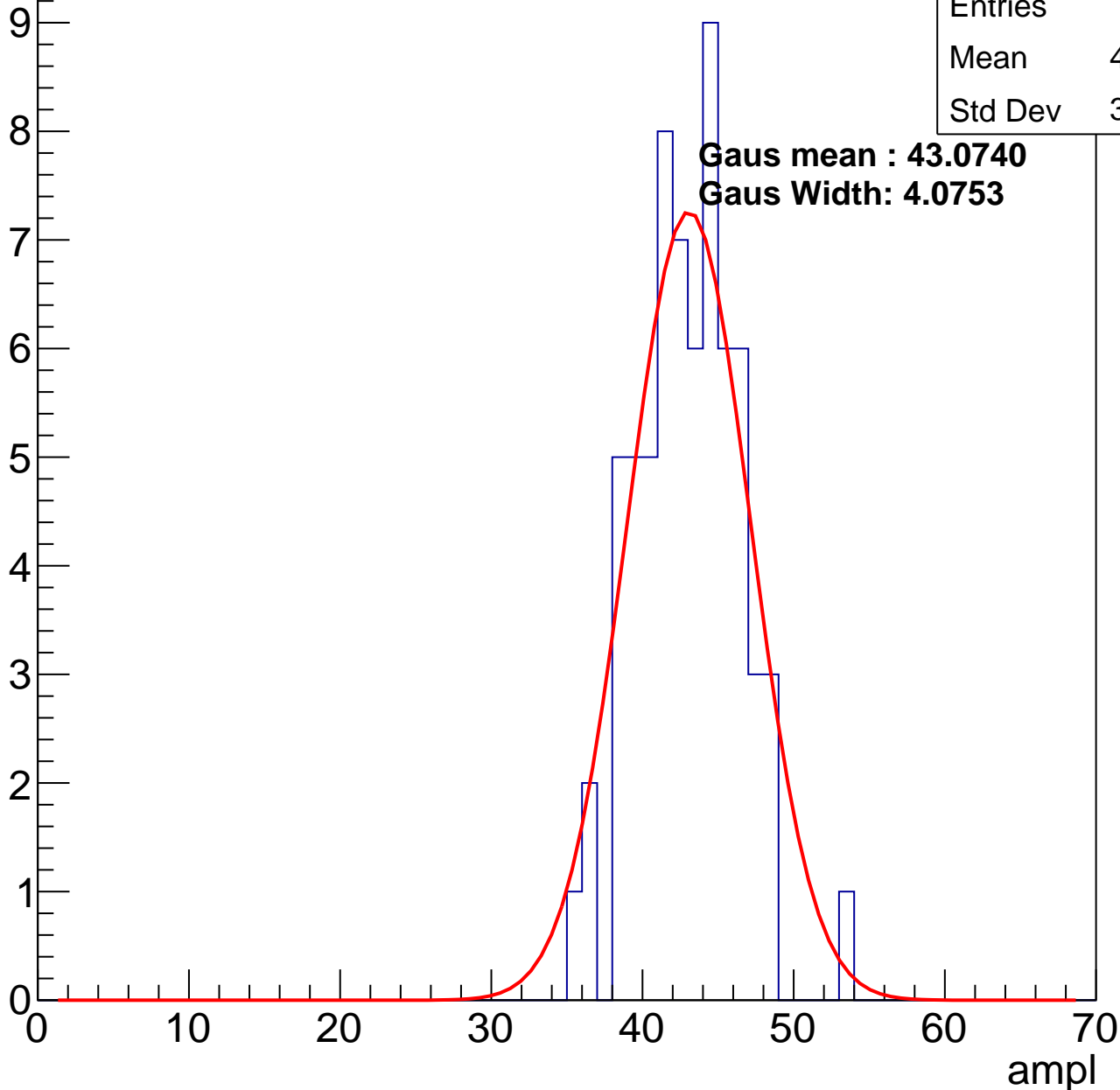
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	42.57
Std Dev	3.342

Gaus mean : 43.0740

Gaus Width: 4.0753

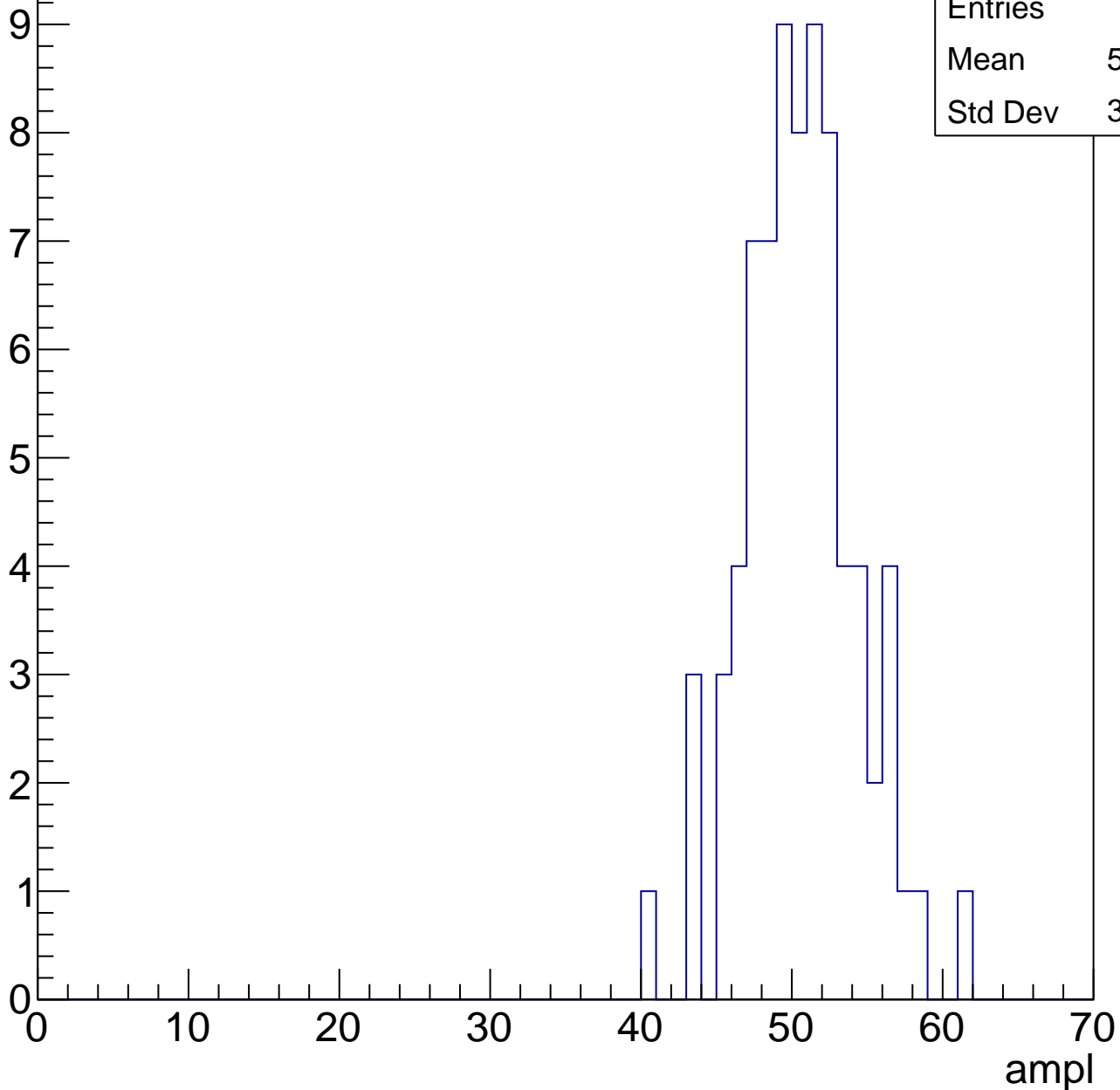


B0L102S, U12-ch111, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

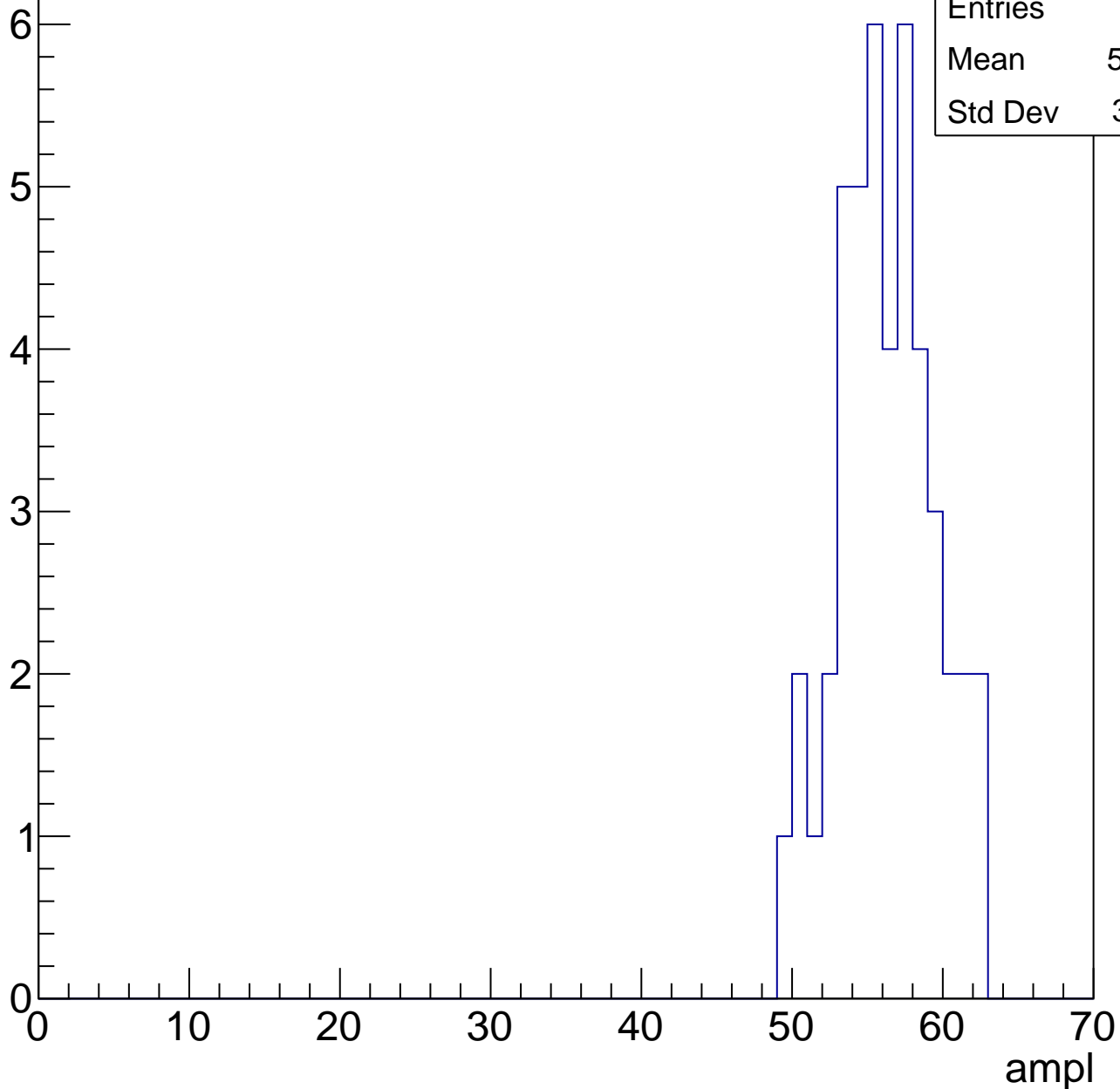
Entries	76
Mean	50.09
Std Dev	3.725



B0L102S, U12-ch111, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



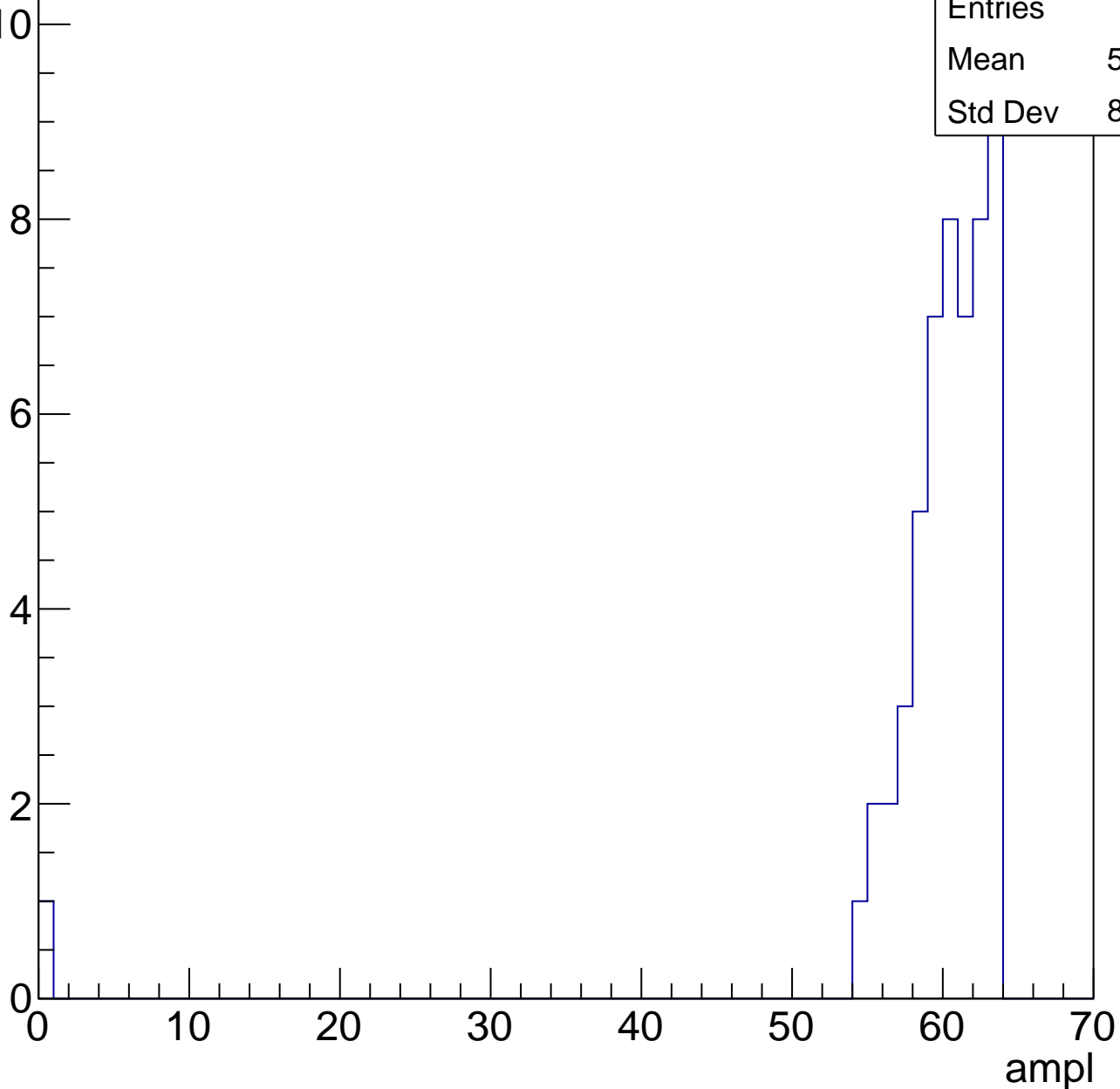
Entries	45
Mean	55.78
Std Dev	3.161

B0L102S, U12-ch111, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	58.94
Std Dev	8.434



B0L102S, U12-ch111, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L102S, U12-ch111, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch112, adc0

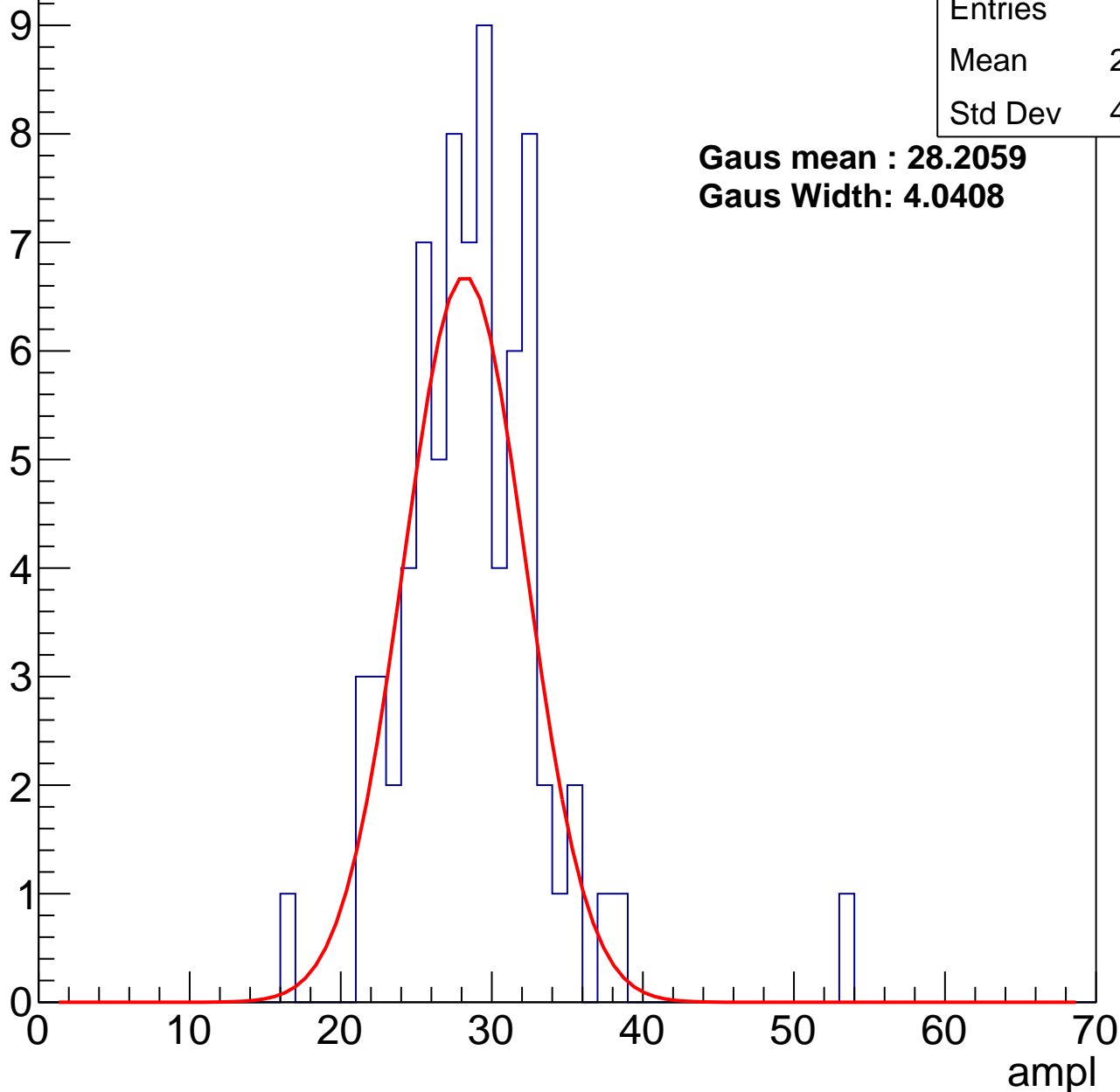
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	28.33
Std Dev	4.889

Gaus mean : 28.2059

Gaus Width: 4.0408



B0L102S, U12-ch112, adc1

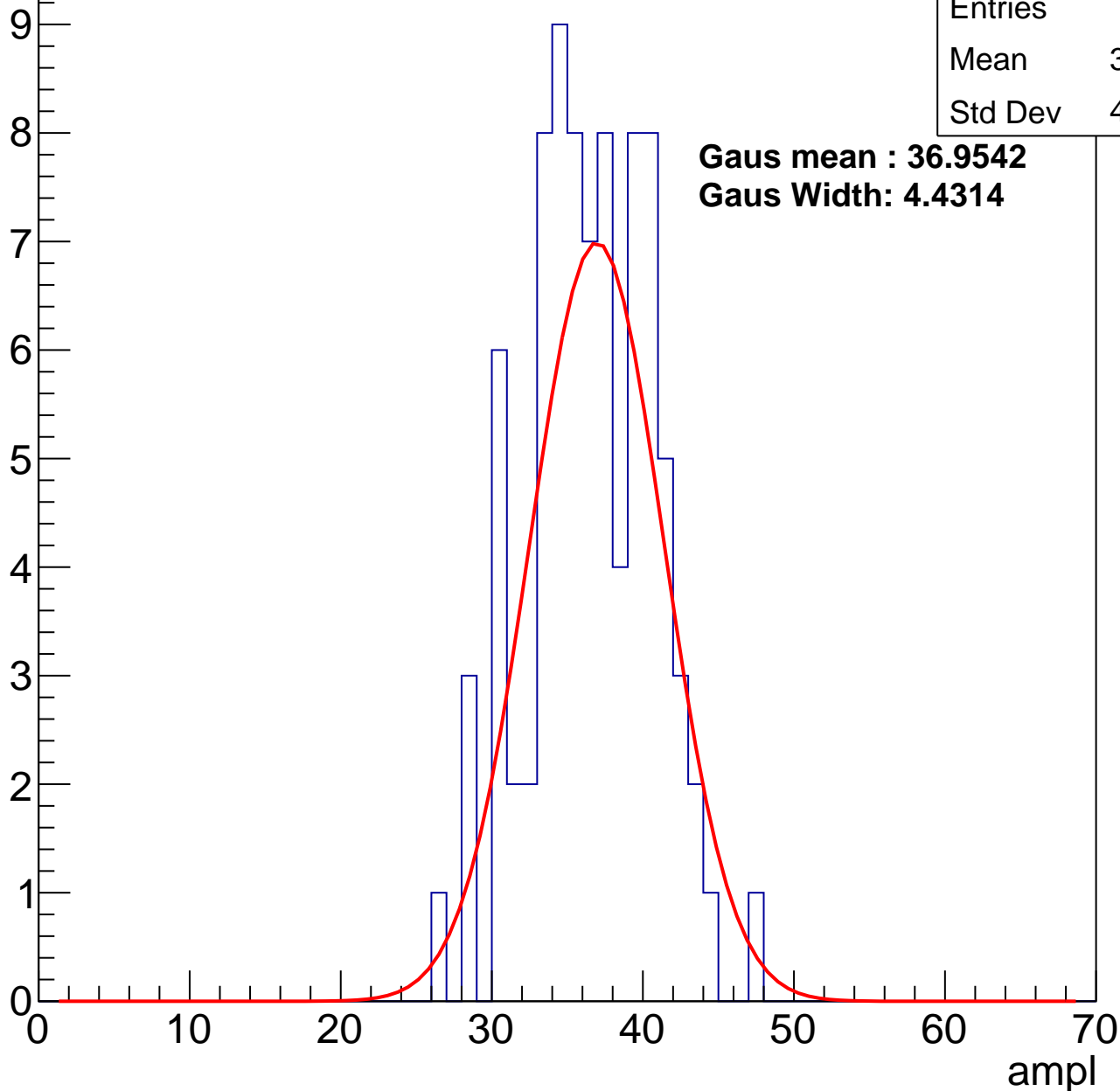
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	36.12
Std Dev	4.099

Gaus mean : 36.9542

Gaus Width: 4.4314



B0L102S, U12-ch112, adc2

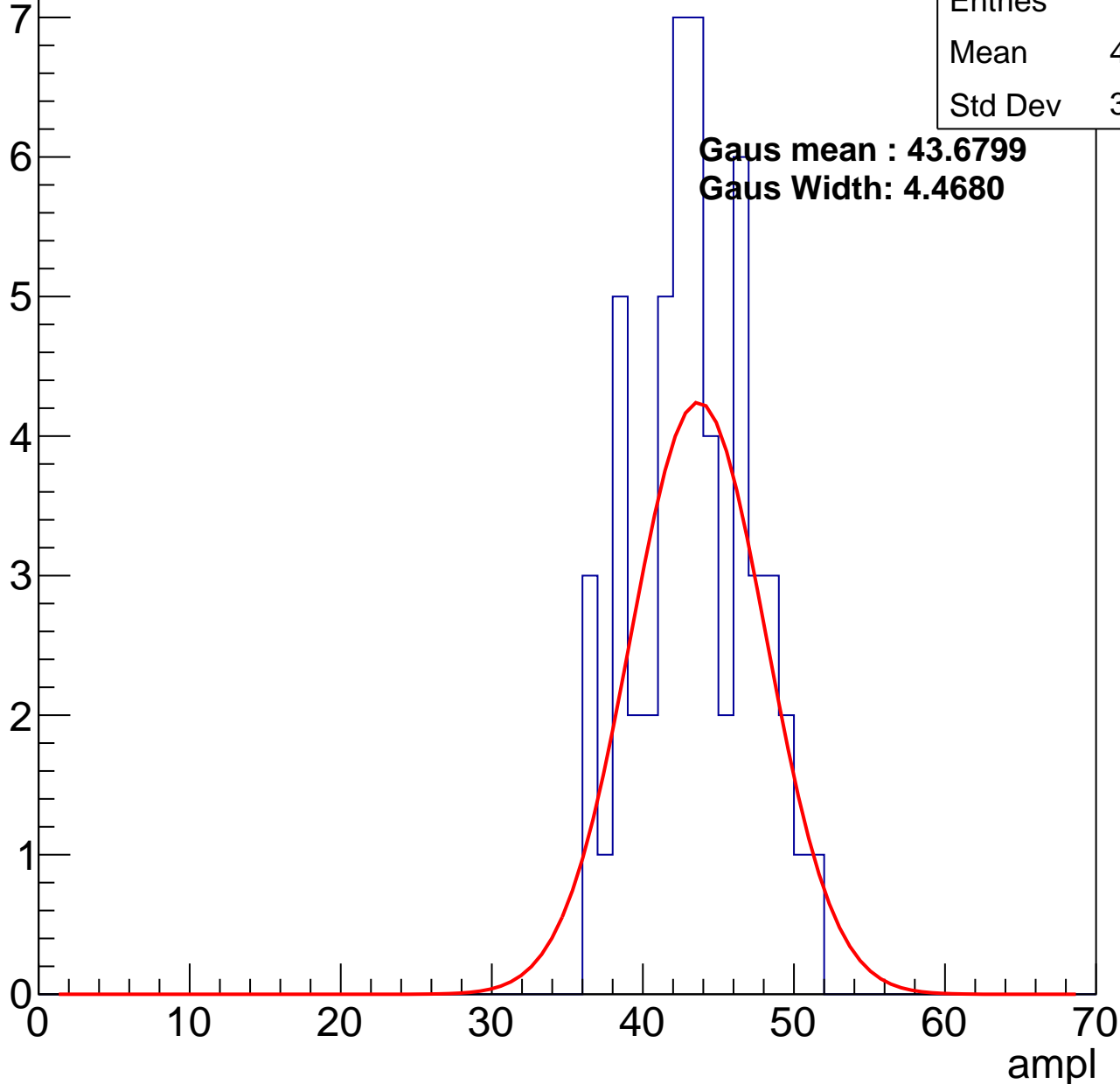
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	42.94
Std Dev	3.749

Gaus mean : 43.6799

Gaus Width: 4.4680

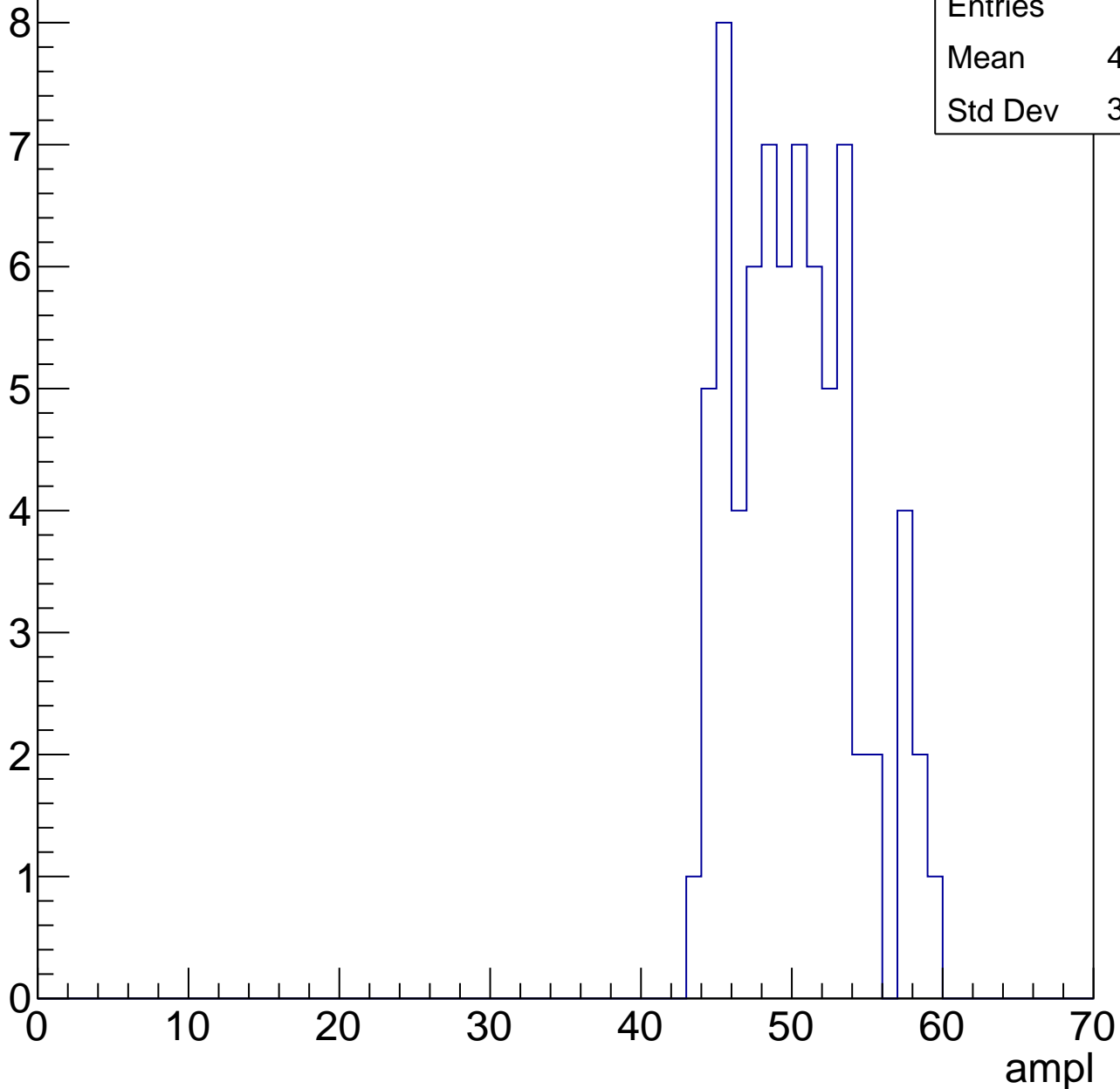


B0L102S, U12-ch112, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

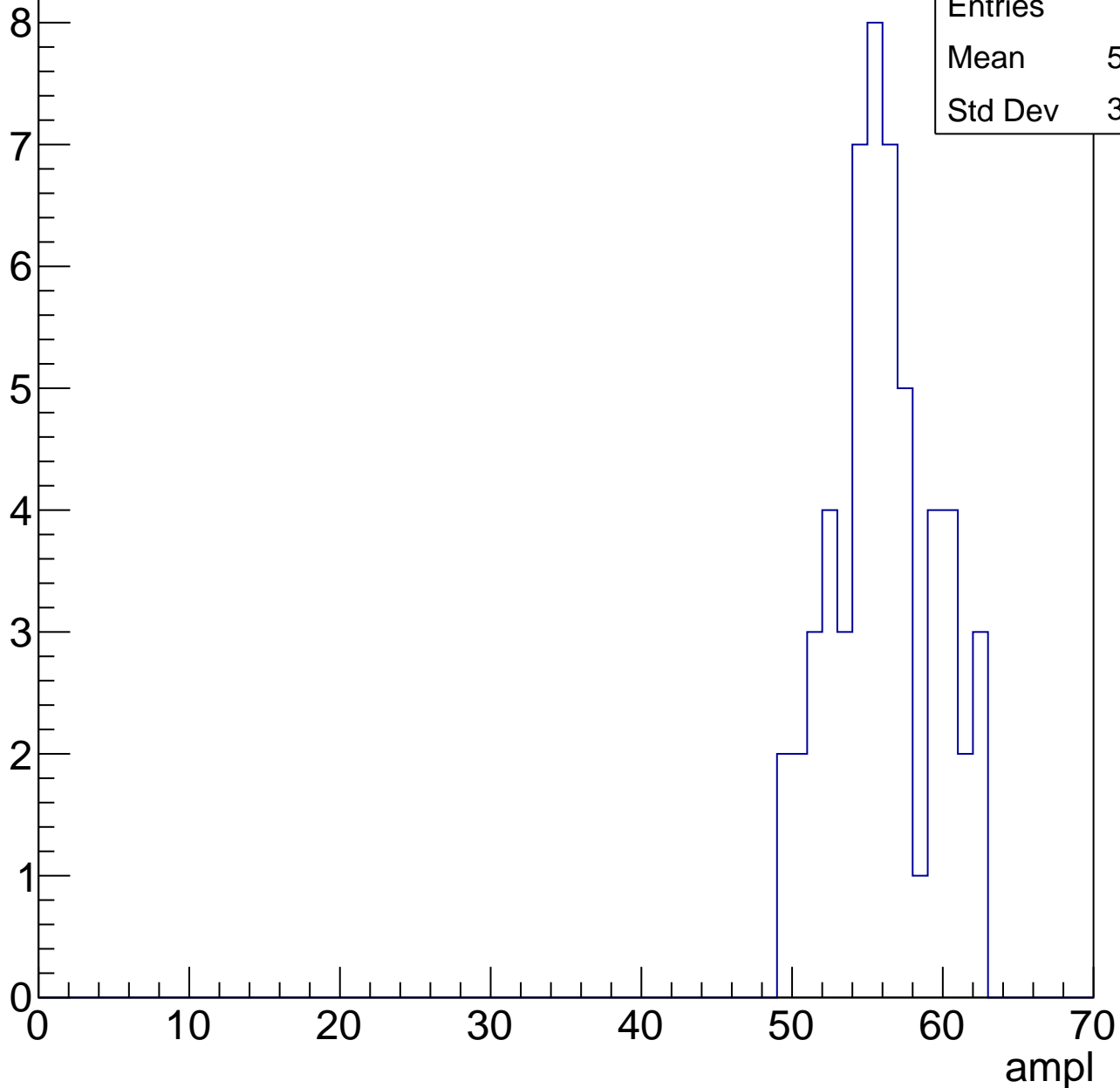
Entries	73
Mean	49.68
Std Dev	3.969



B0L102S, U12-ch112, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

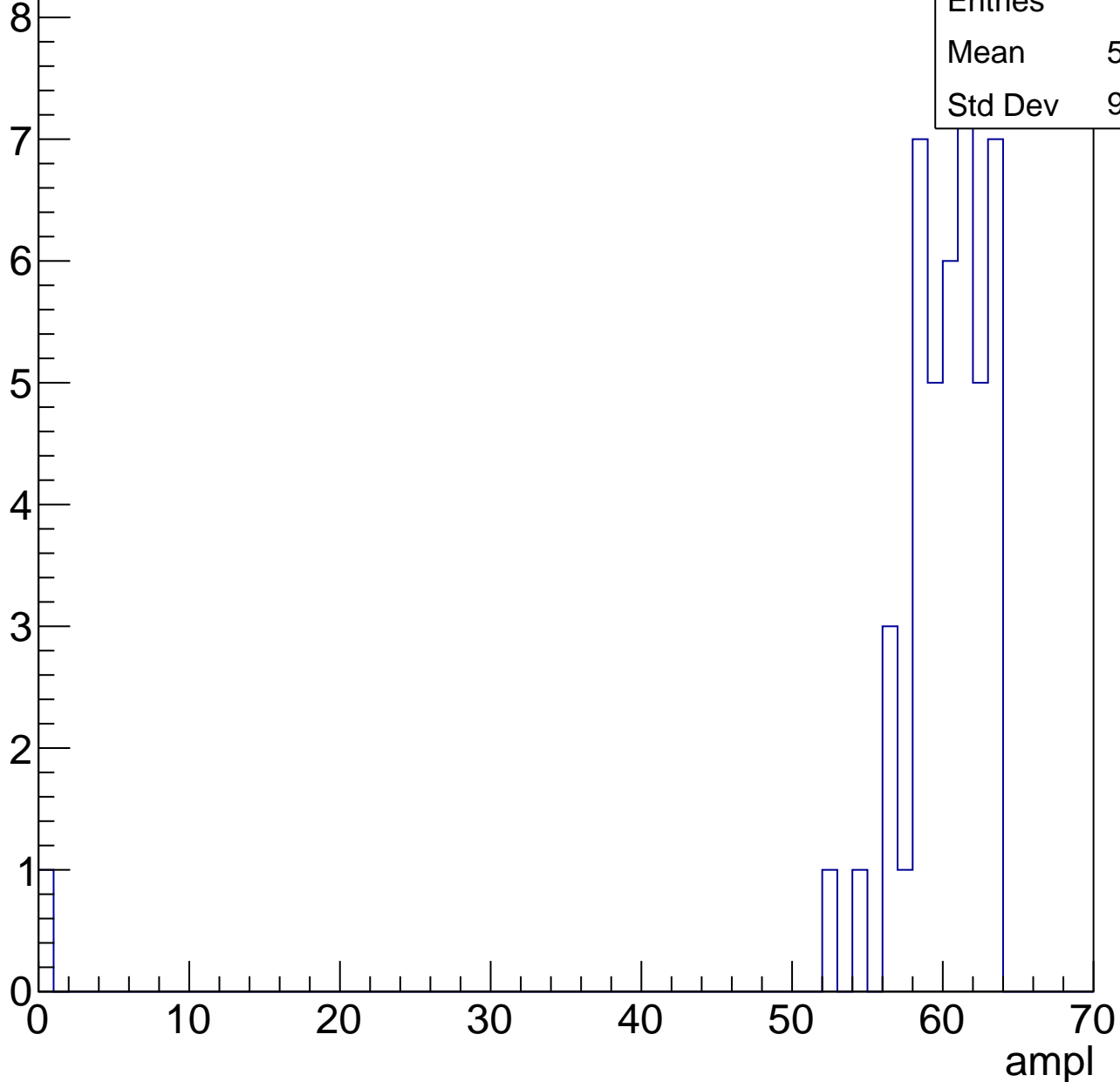


B0L102S, U12-ch112, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	45
Mean	58.47
Std Dev	9.159



B0L102S, U12-ch112, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch112, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch113, adc0

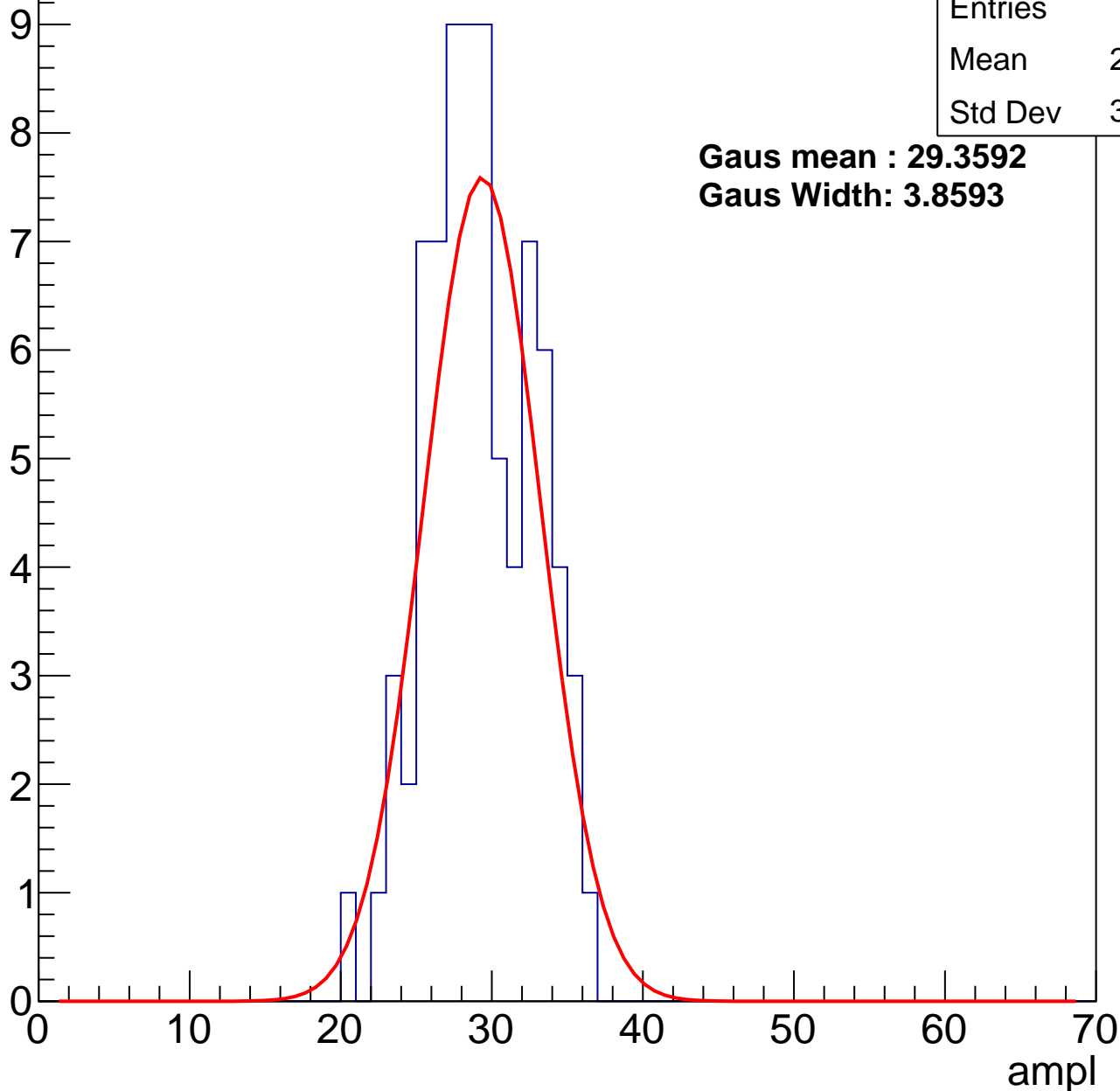
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	28.78
Std Dev	3.474

Gaus mean : 29.3592

Gaus Width: 3.8593



B0L102S, U12-ch113, adc1

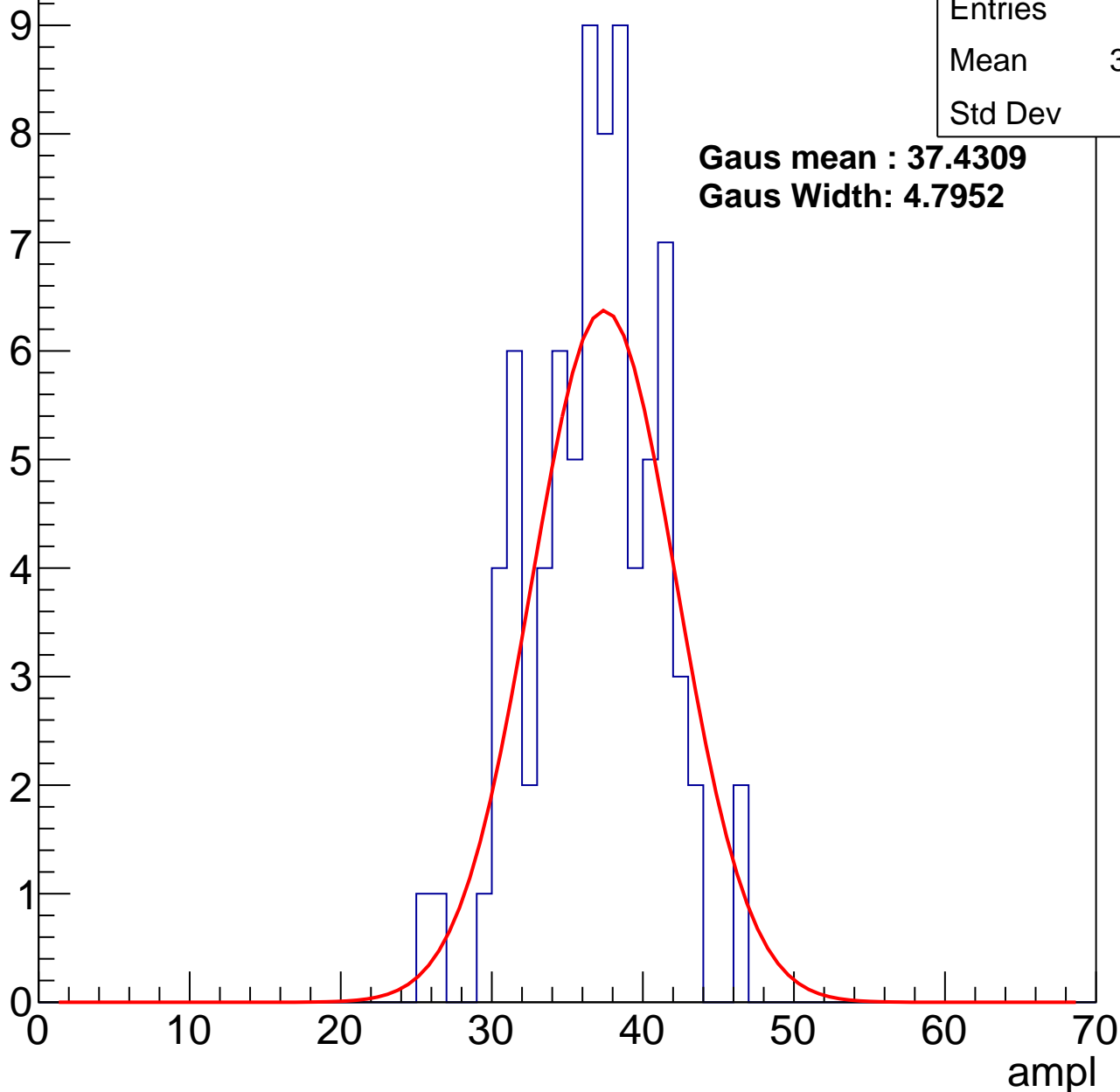
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	36.33
Std Dev	4.2

Gaus mean : 37.4309

Gaus Width: 4.7952



B0L102S, U12-ch113, adc2

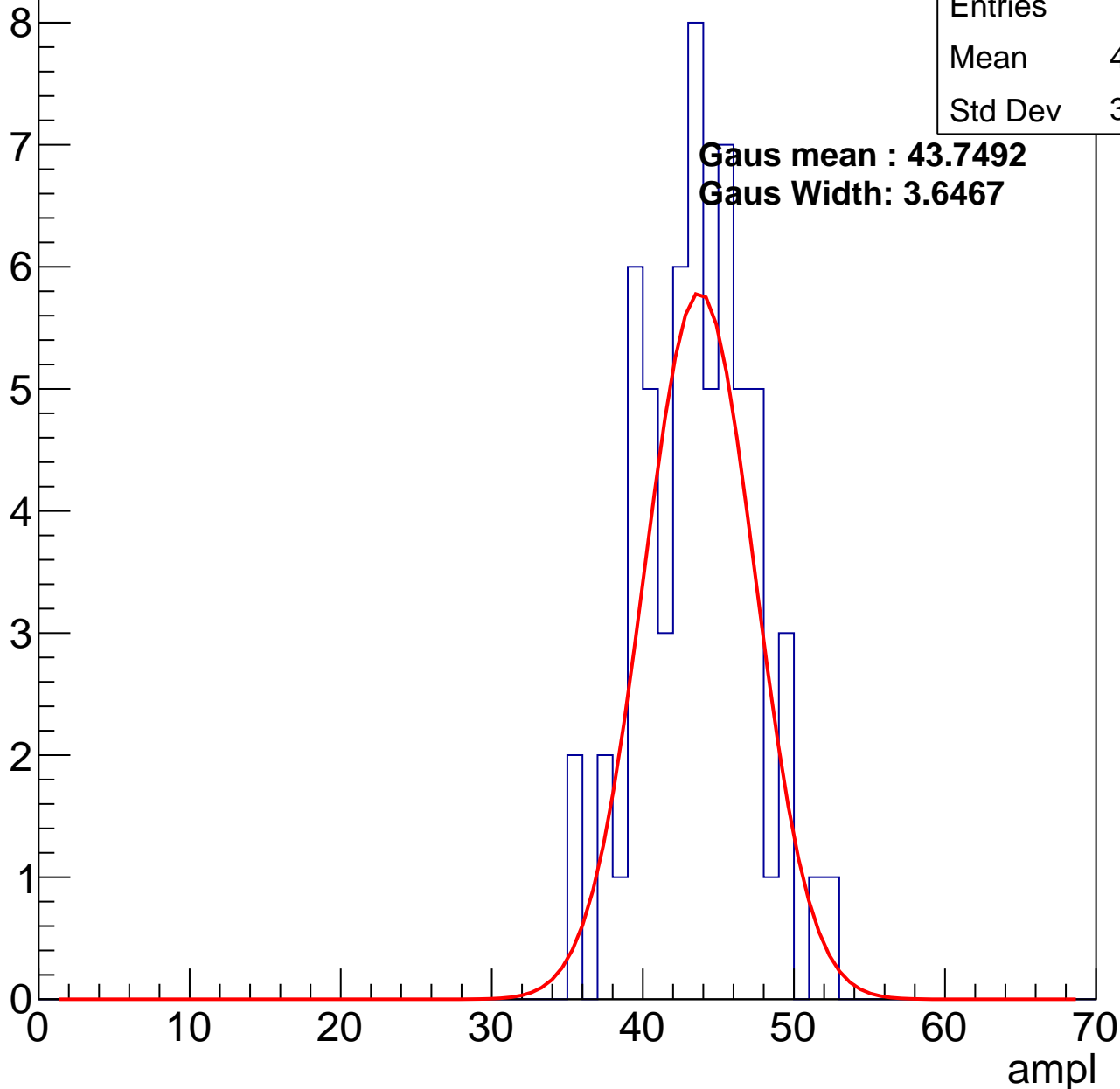
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	43.16
Std Dev	3.663

Gaus mean : 43.7492

Gaus Width: 3.6467

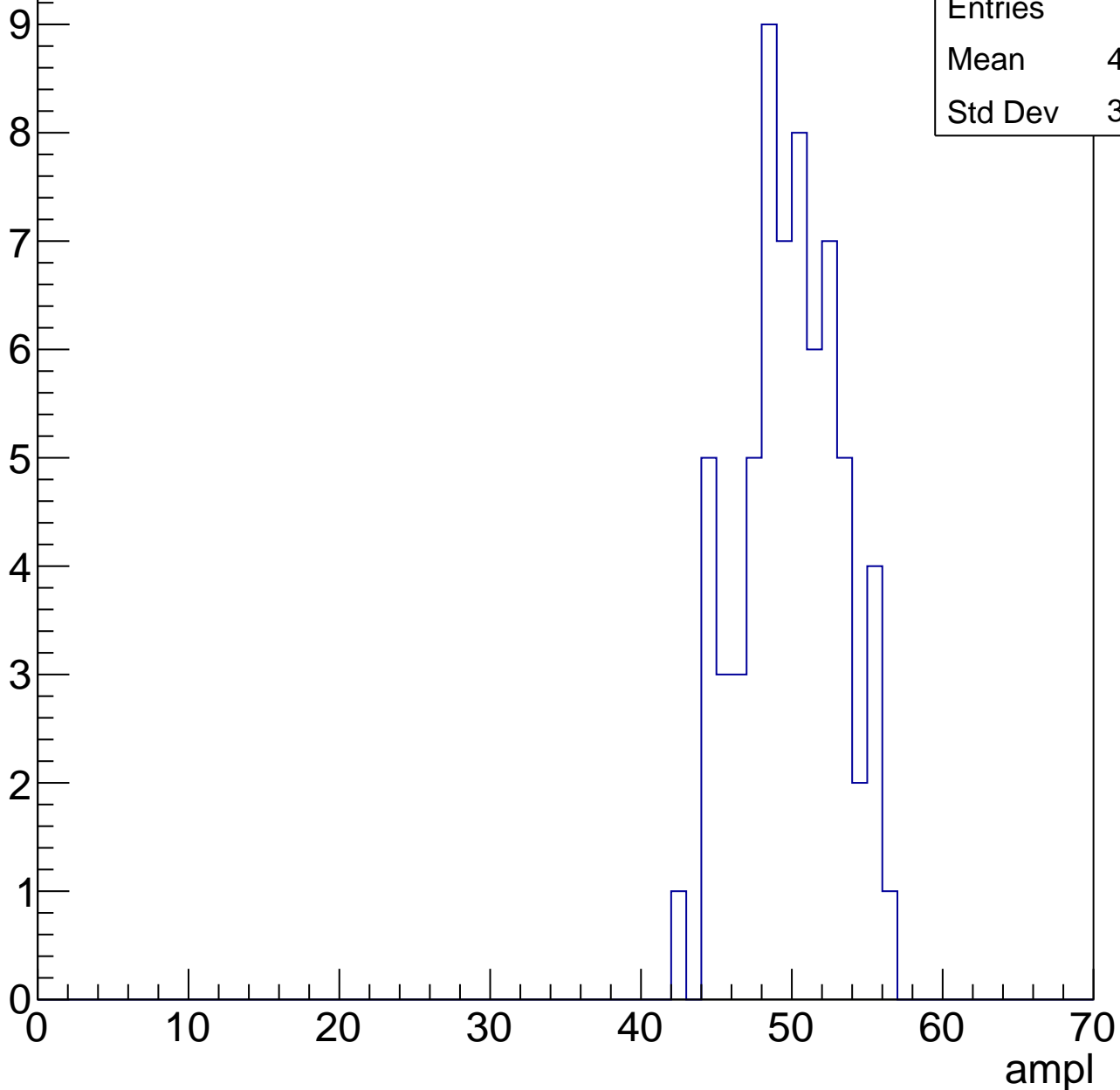


B0L102S, U12-ch113, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	49.45
Std Dev	3.215



B0L102S, U12-ch113, adc4

calib_packv5_042523_0143.root, FC#12, port B1

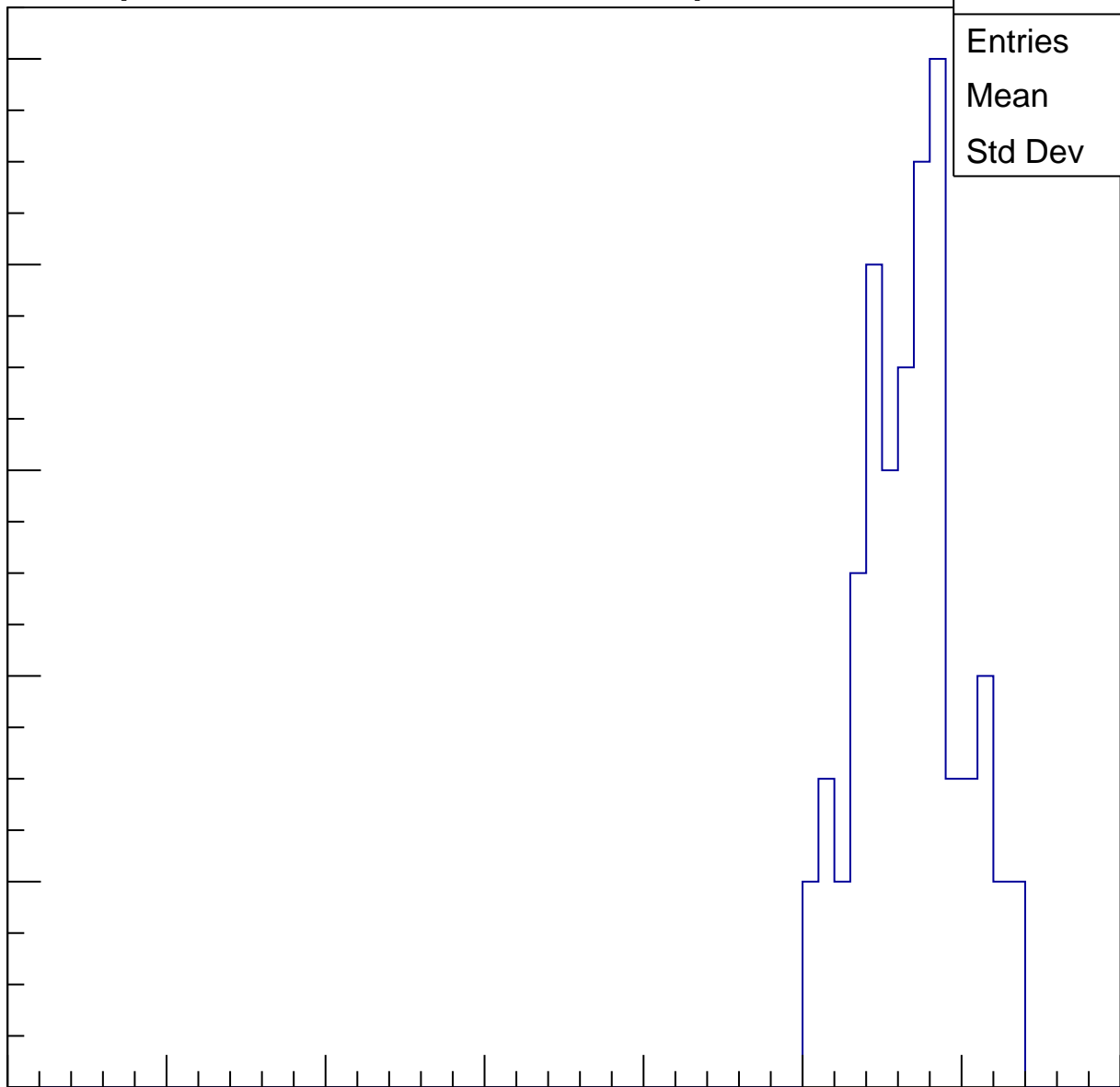
Entries	66
Mean	56.36
Std Dev	3.132

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

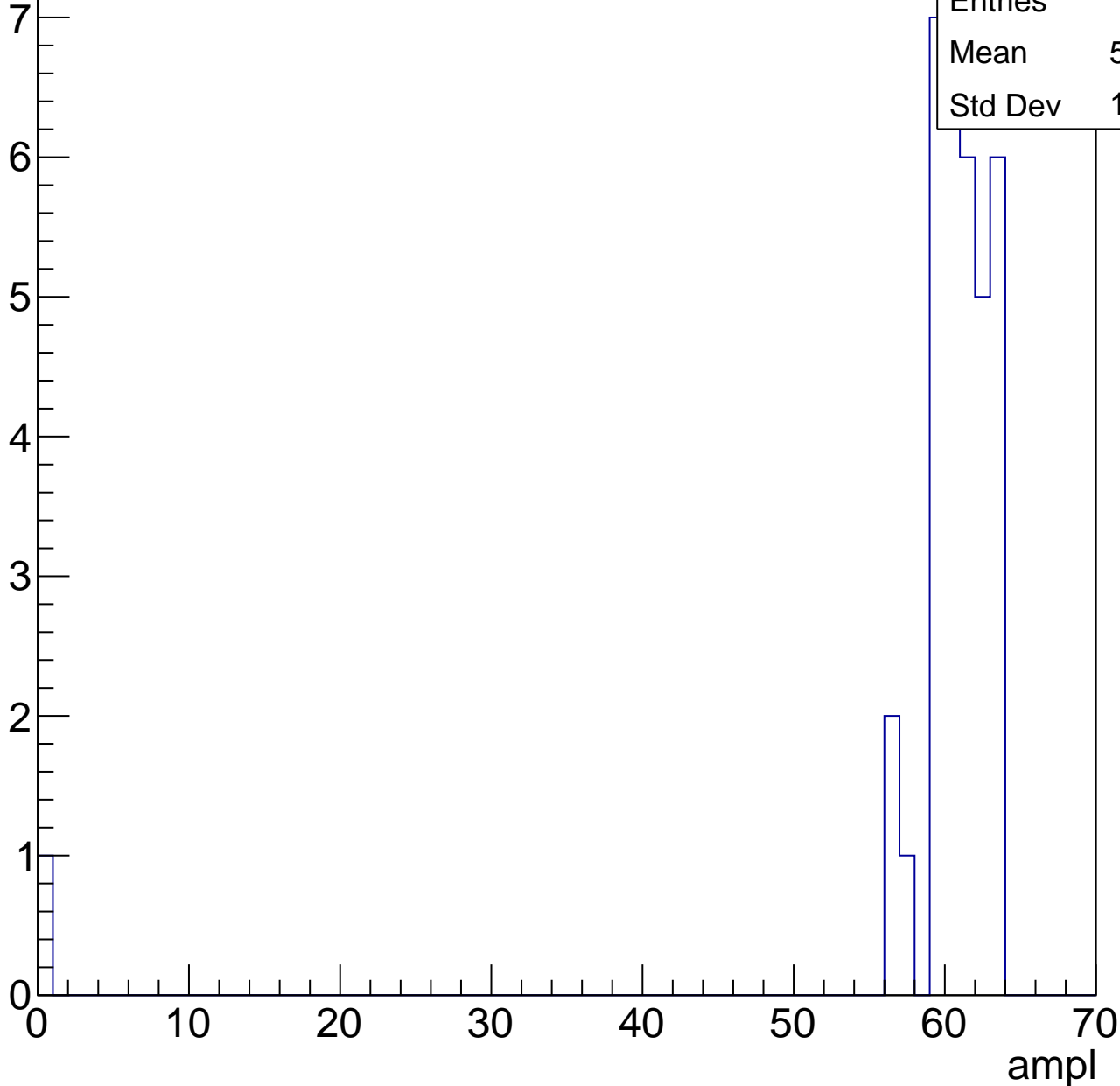


B0L102S, U12-ch113, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	35
Mean	58.74
Std Dev	10.24



B0L102S, U12-ch113, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch113, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch114, adc0

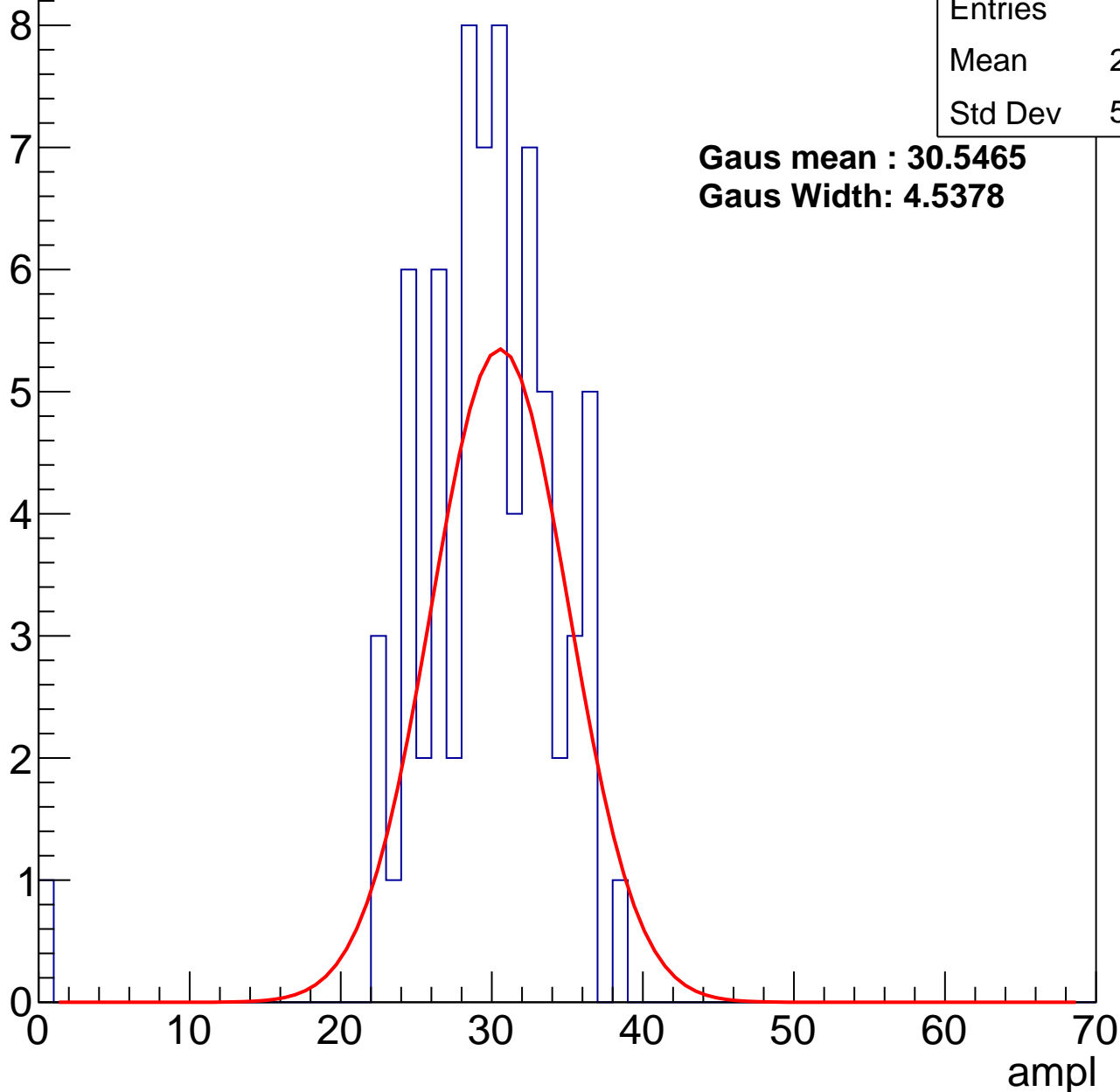
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.07
Std Dev	5.209

Gaus mean : 30.5465

Gaus Width: 4.5378



B0L102S, U12-ch114, adc1

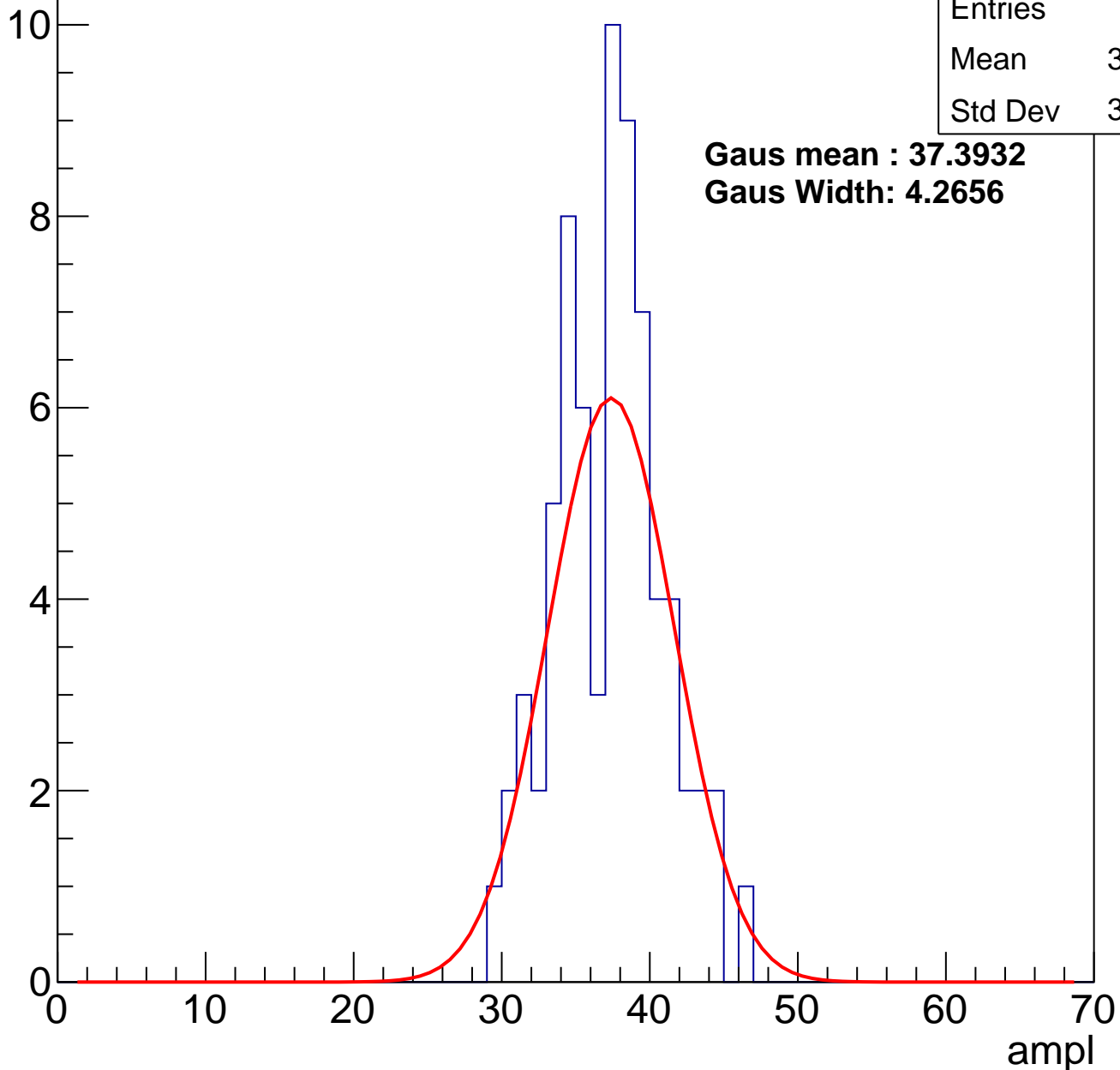
calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	36.82
Std Dev	3.628

Gaus mean : 37.3932

Gaus Width: 4.2656

Entry



B0L102S, U12-ch114, adc2

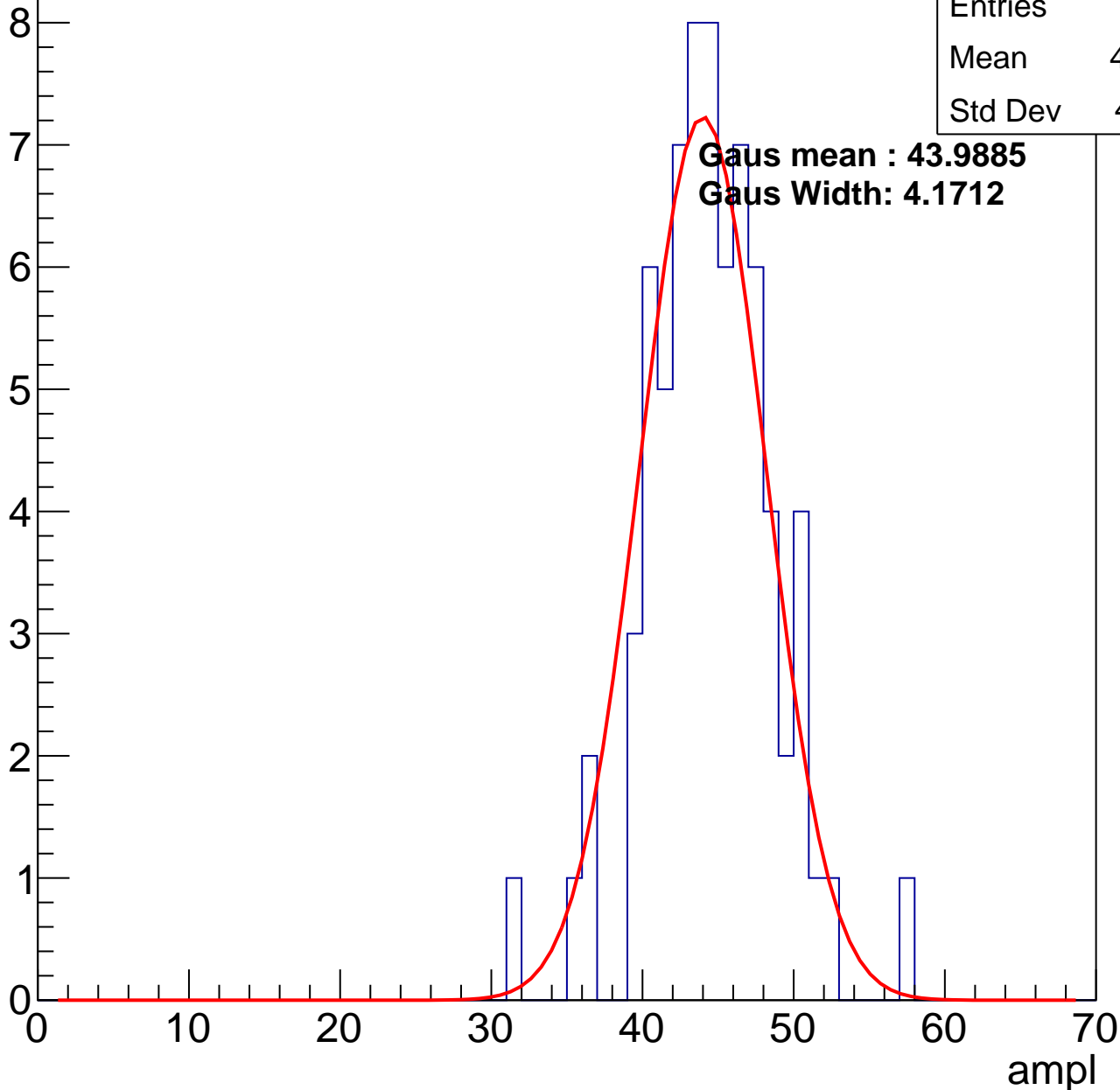
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	44.03
Std Dev	4.151

Gaus mean : 43.9885

Gaus Width: 4.1712

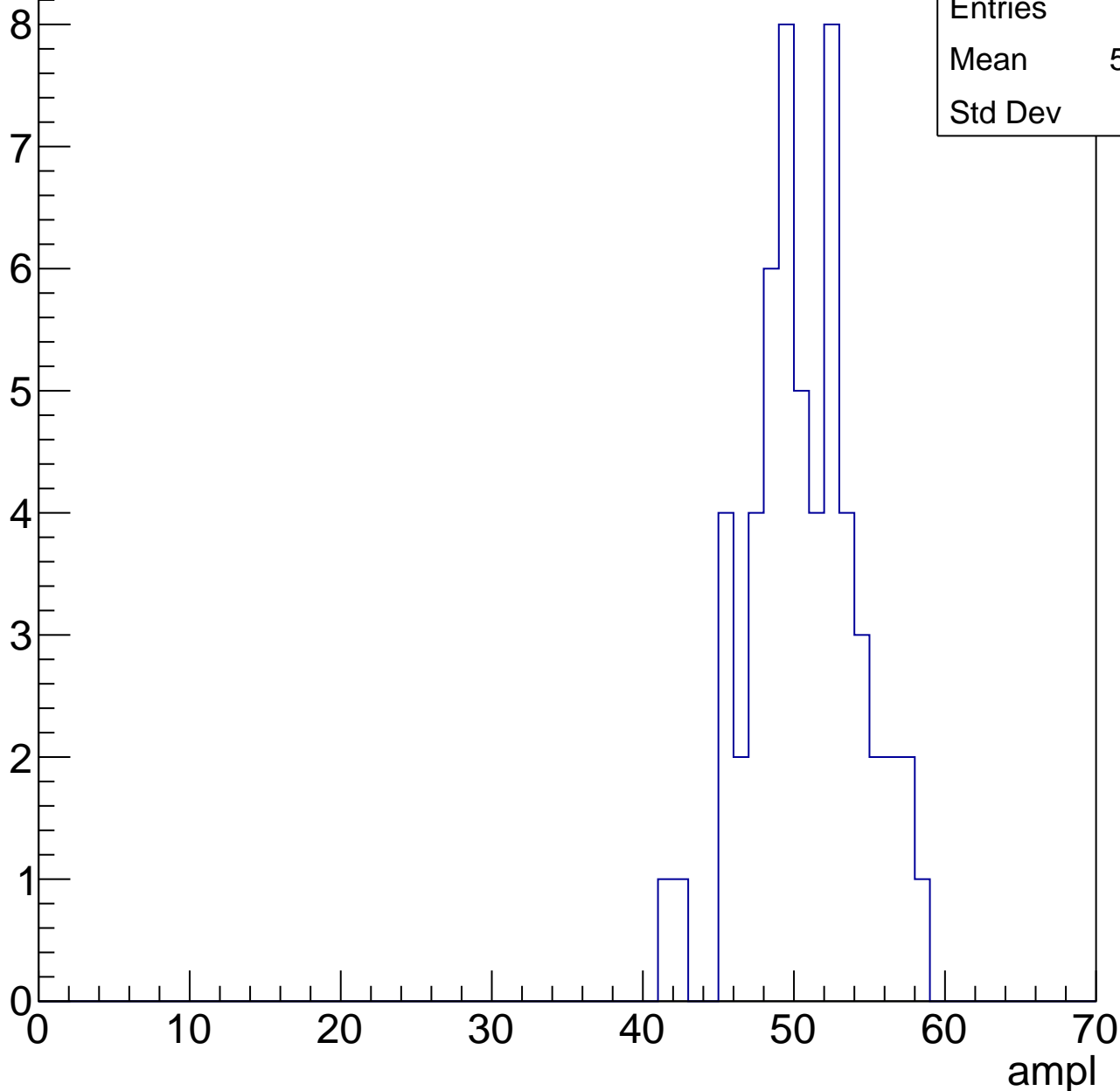


B0L102S, U12-ch114, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	50.19
Std Dev	3.62

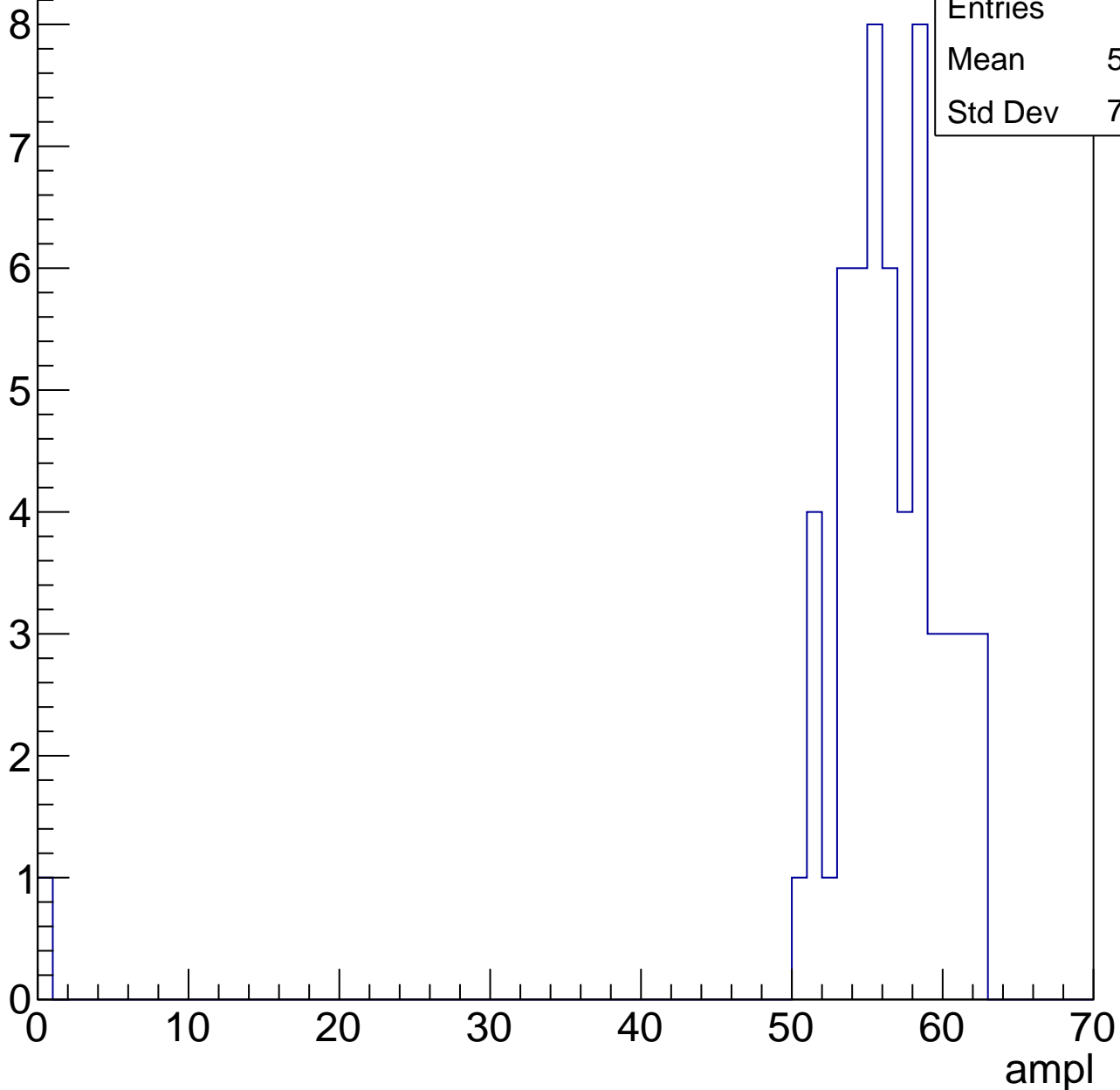


B0L102S, U12-ch114, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55.12
Std Dev	7.974

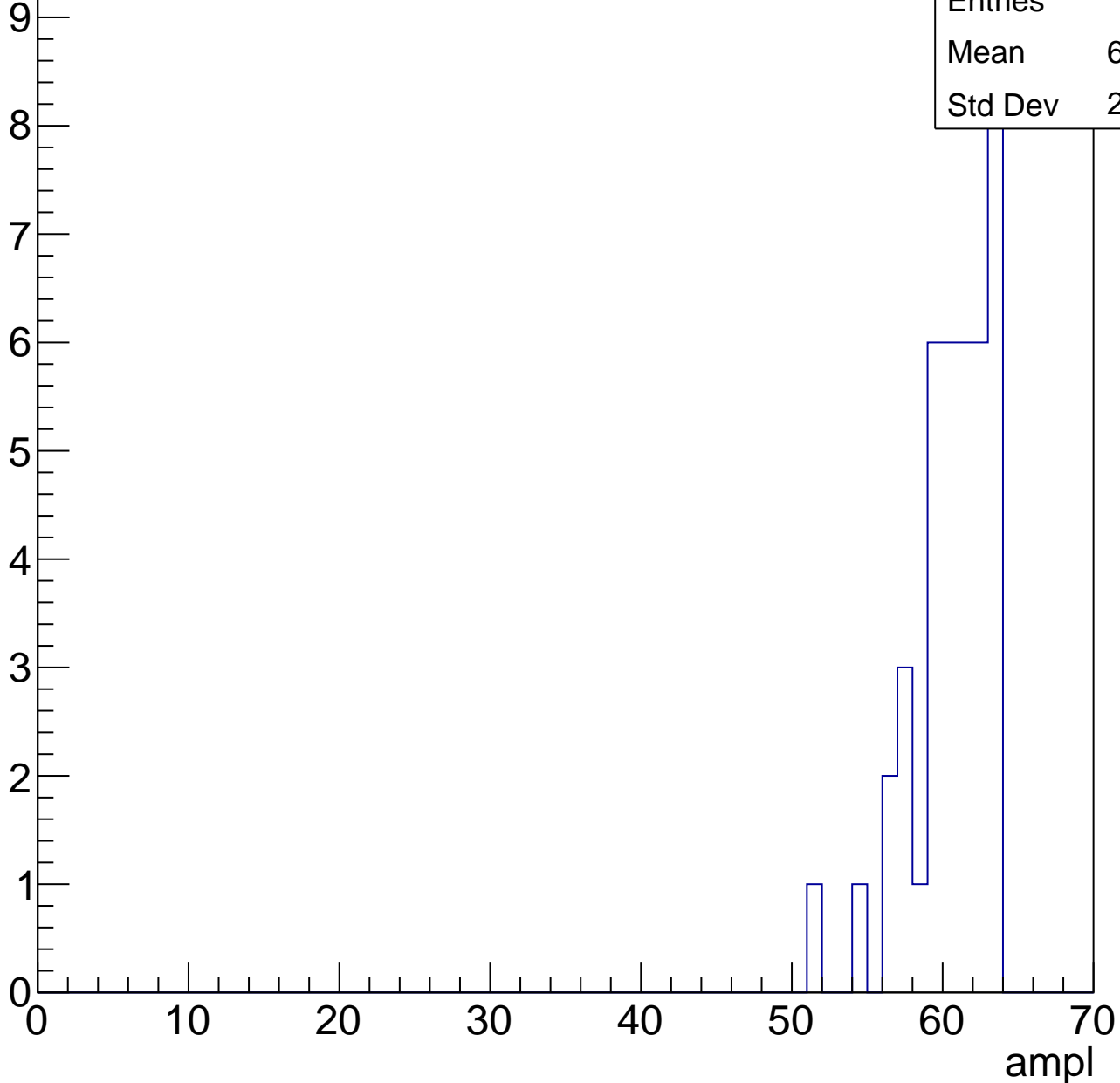


B0L102S, U12-ch114, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	60.12
Std Dev	2.689



B0L102S, U12-ch114, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	1.414

ampl

B0L102S, U12-ch114, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U12-ch115, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	88
Mean	29.89
Std Dev	3.903

Gaus mean : 30.5230

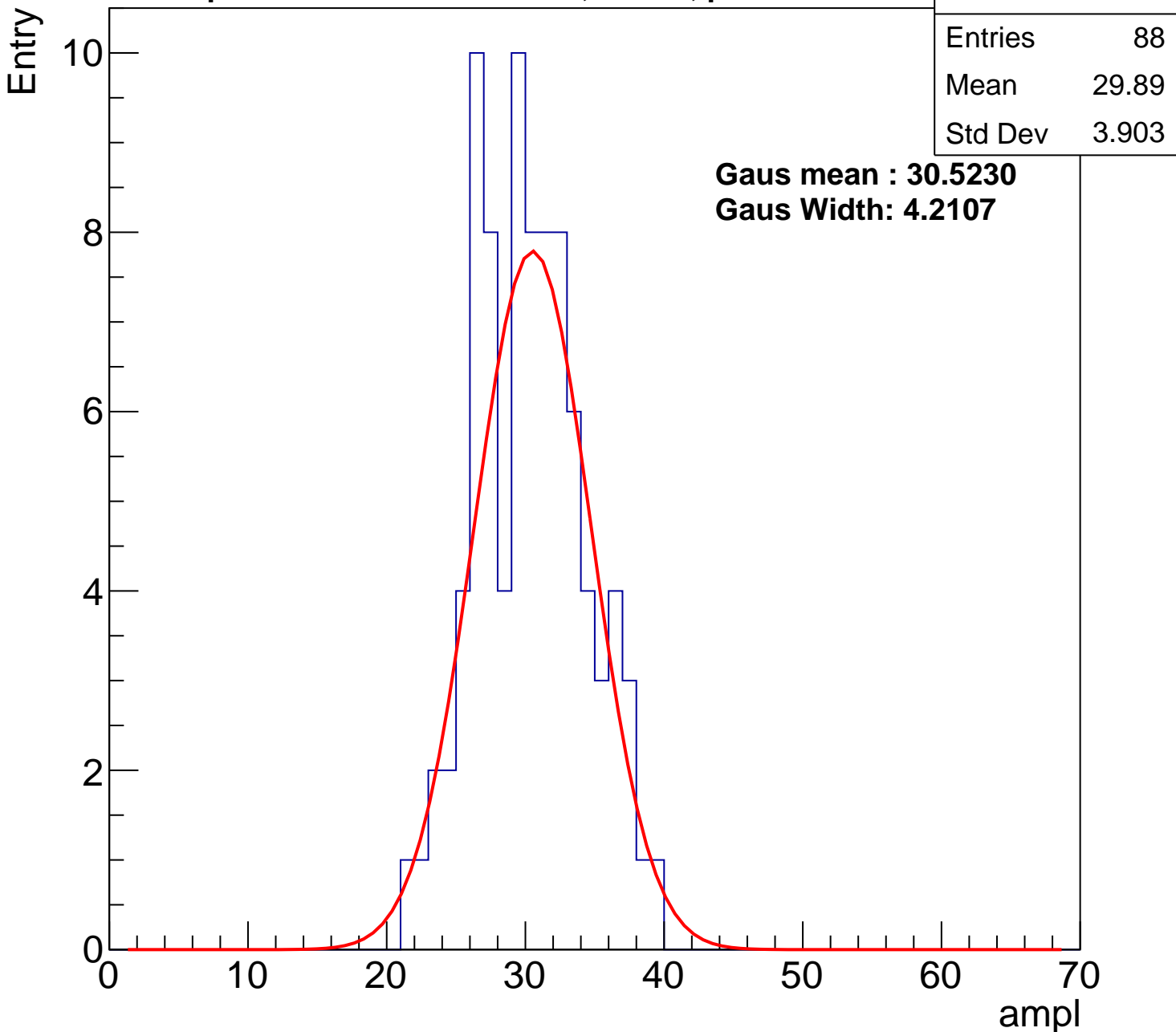
Gaus Width: 4.2107

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch115, adc1

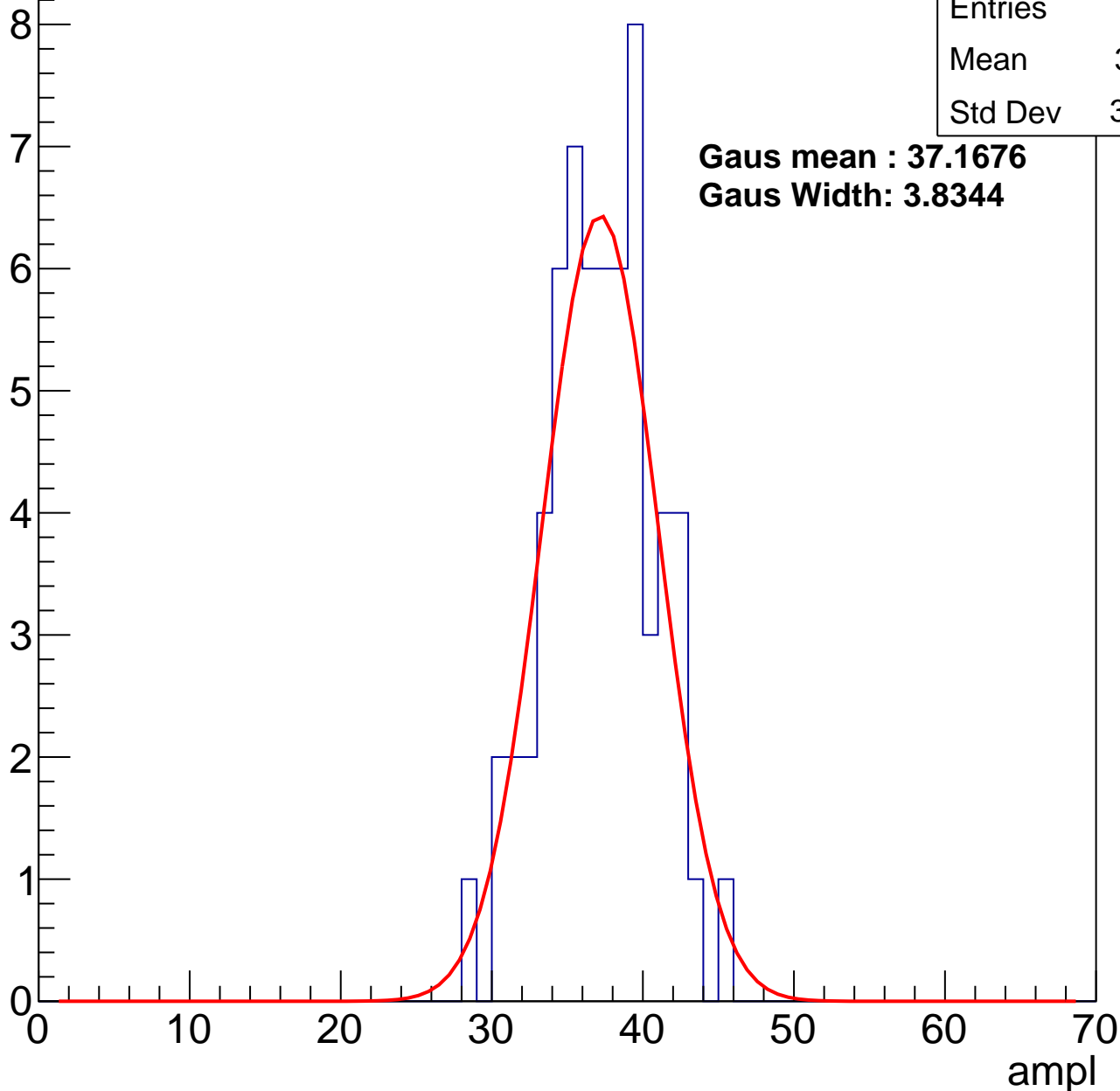
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.71
Std Dev	3.512

Gaus mean : 37.1676

Gaus Width: 3.8344



B0L102S, U12-ch115, adc2

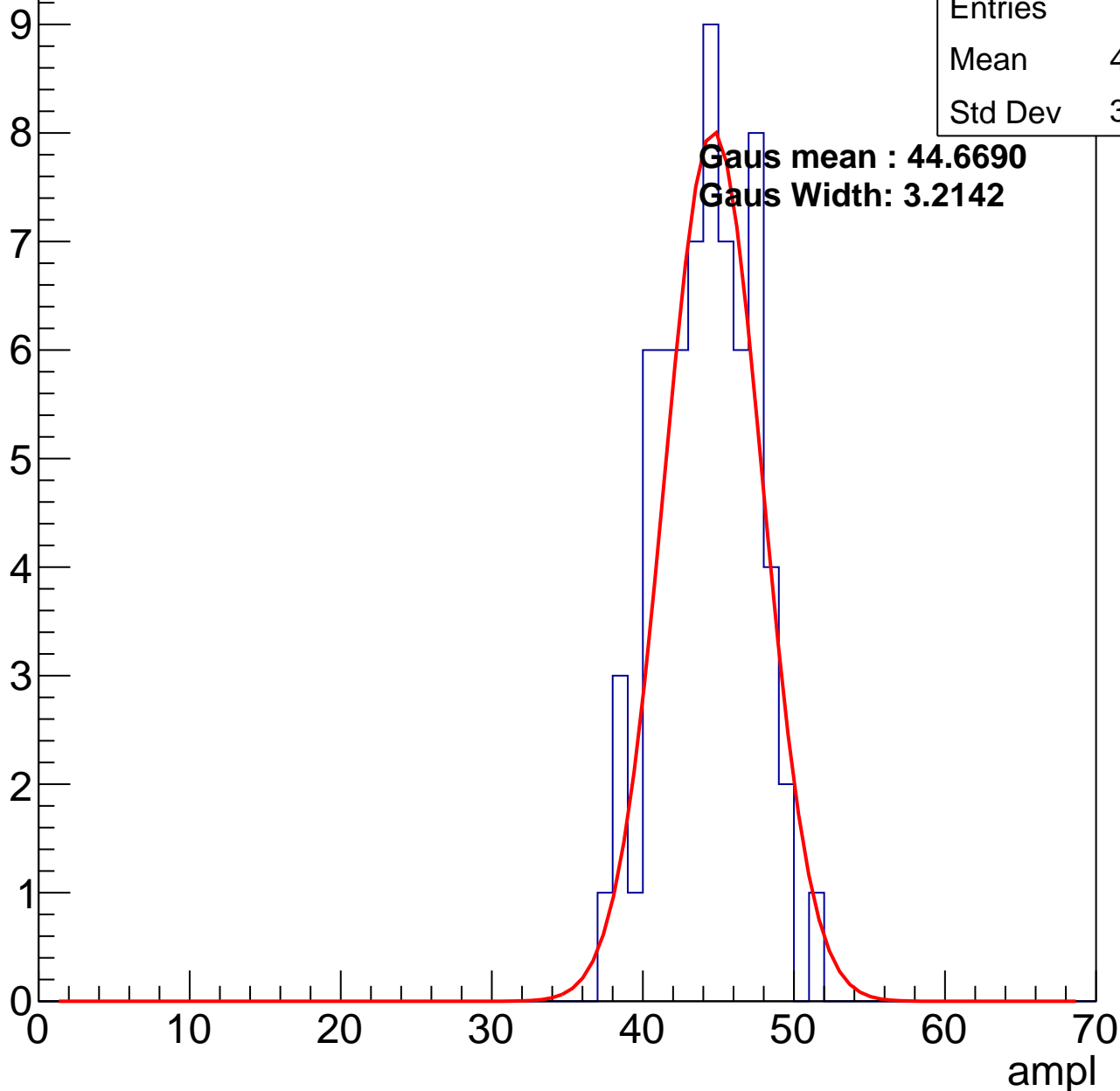
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.78
Std Dev	3.056

Gaus mean : 44.6690

Gaus Width: 3.2142

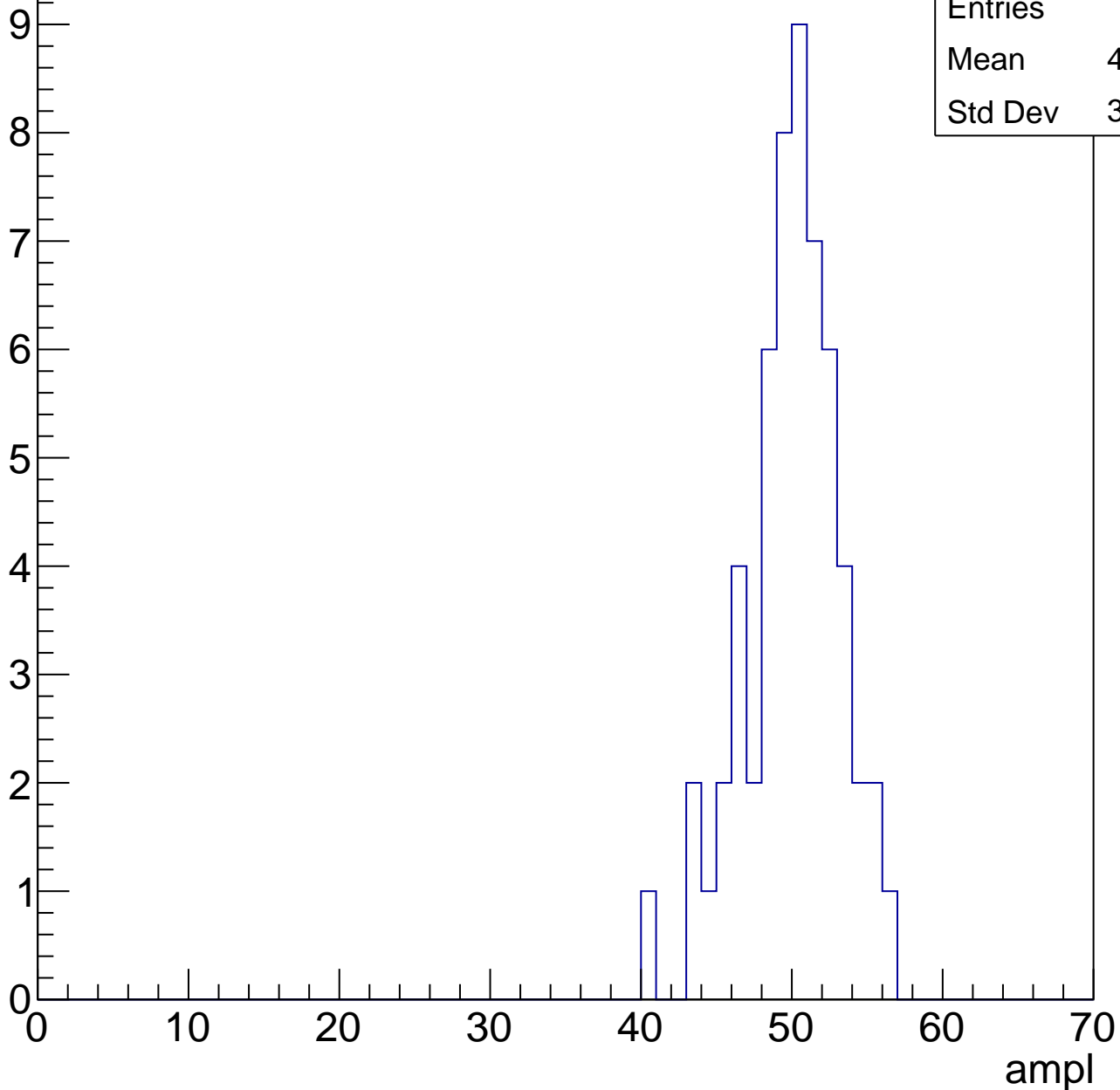


B0L102S, U12-ch115, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	49.53
Std Dev	3.163

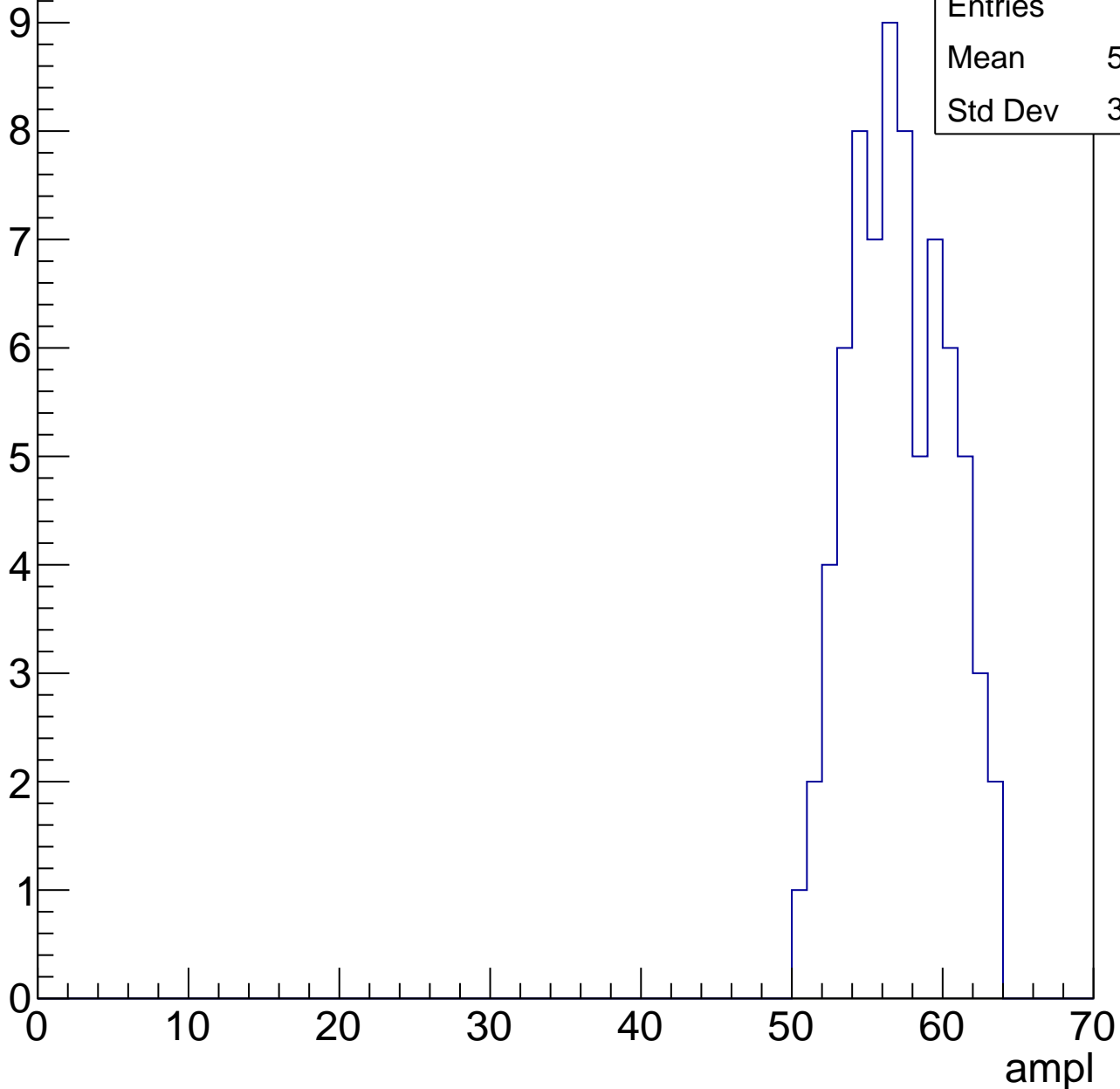


B0L102S, U12-ch115, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	56.64
Std Dev	3.164

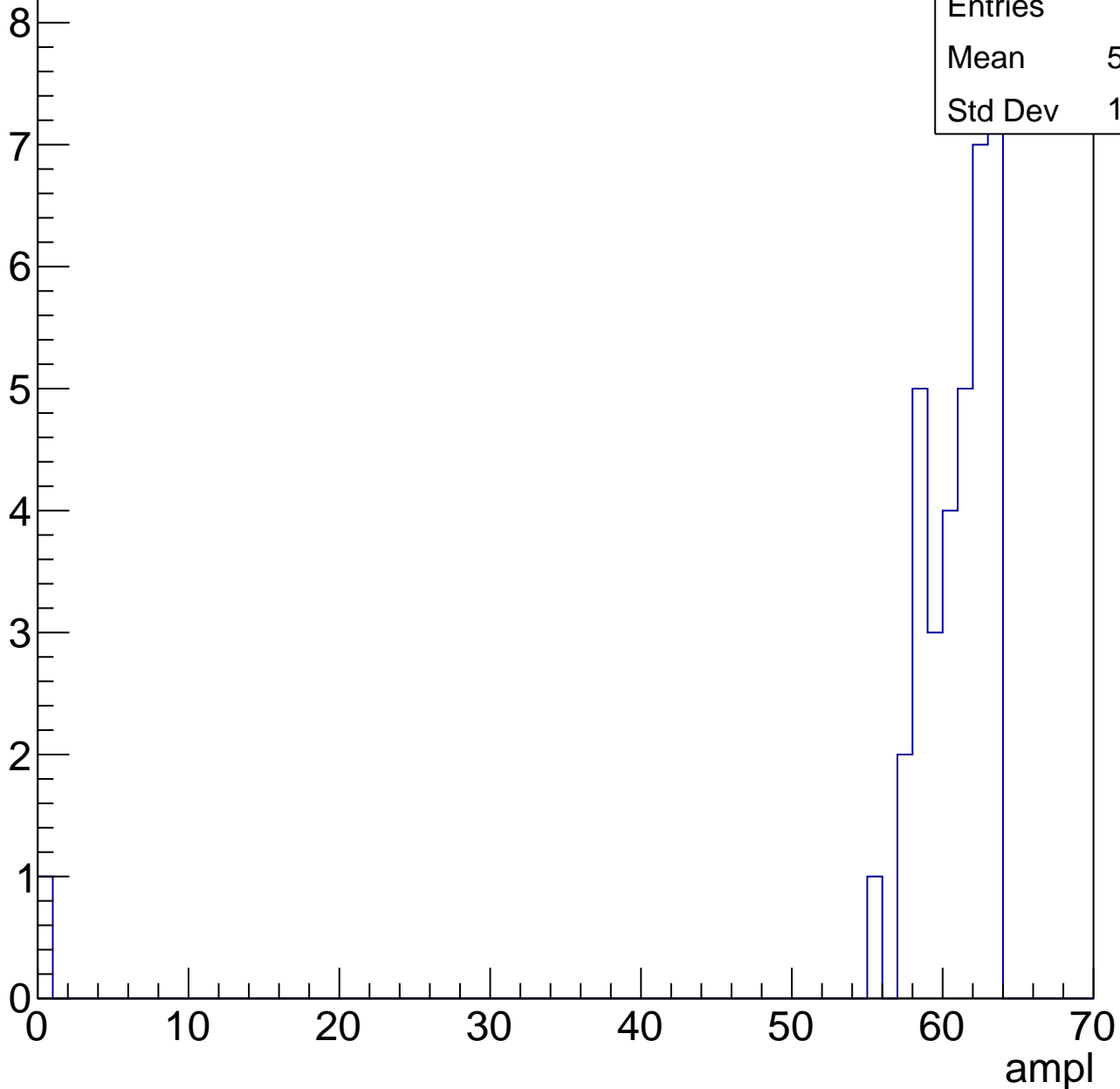


B0L102S, U12-ch115, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	36
Mean	58.86
Std Dev	10.17



B0L102S, U12-ch115, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

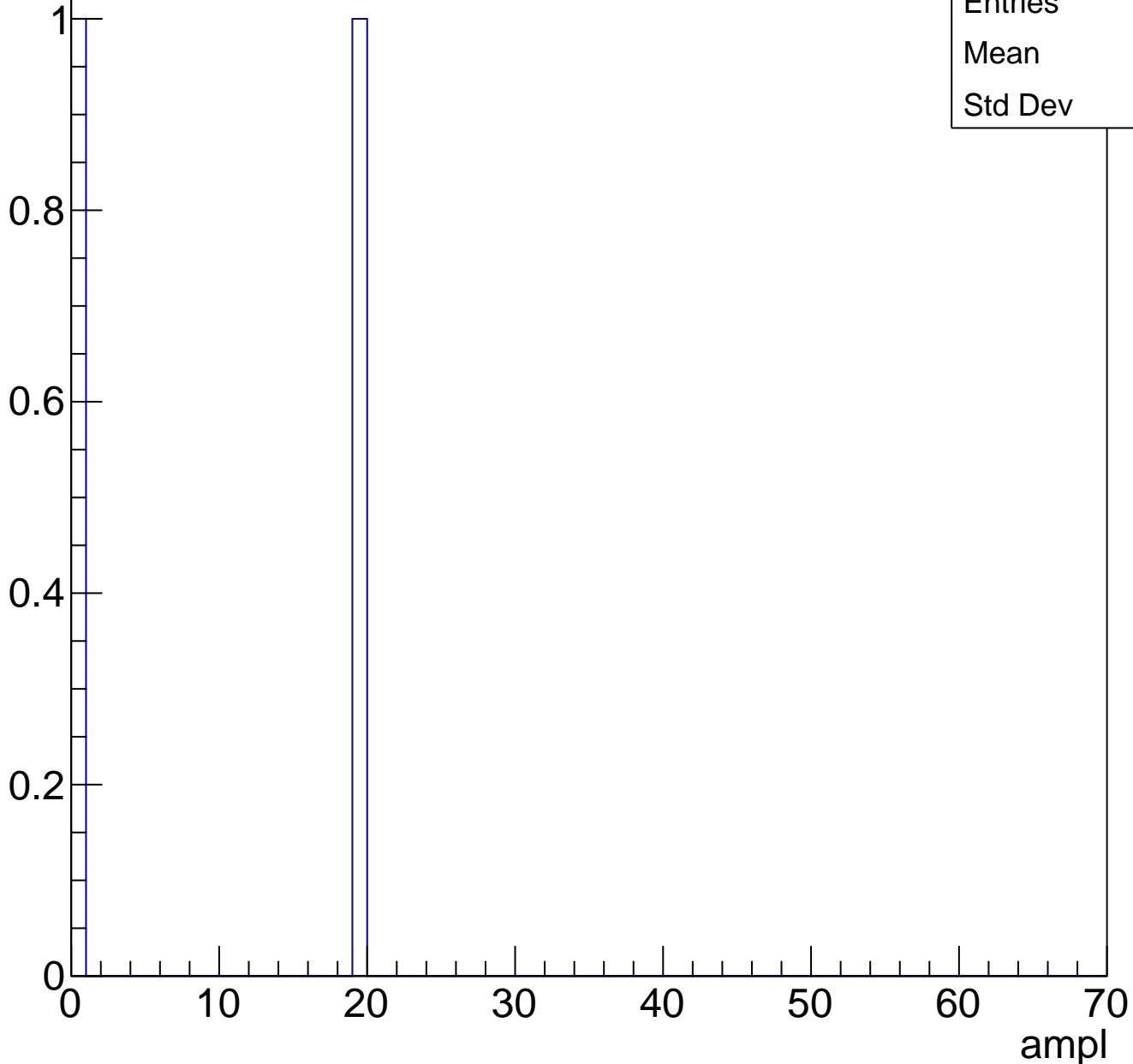


Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch115, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L102S, U12-ch116, adc0

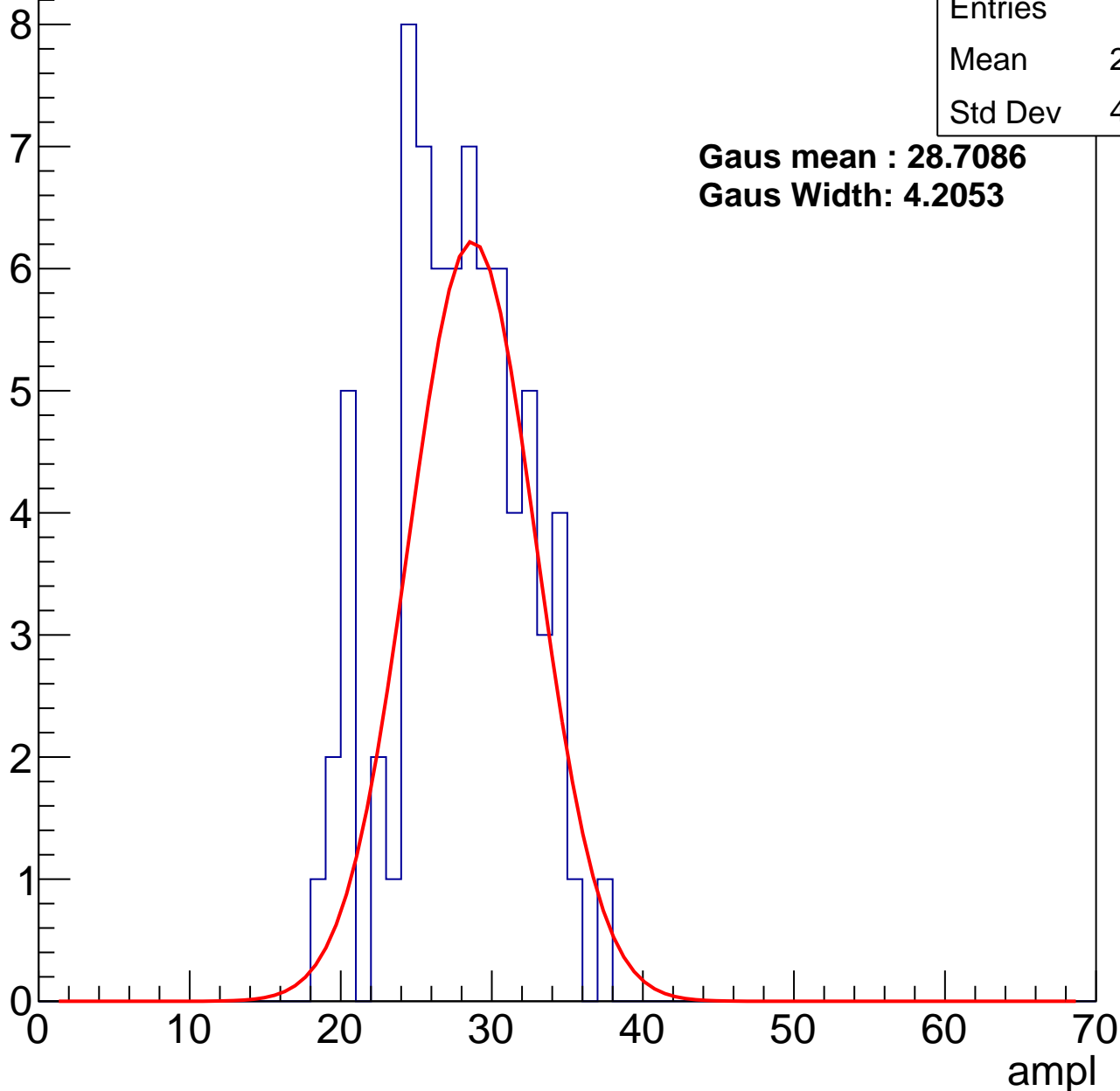
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	27.32
Std Dev	4.259

Gaus mean : 28.7086

Gaus Width: 4.2053



B0L102S, U12-ch116, adc1

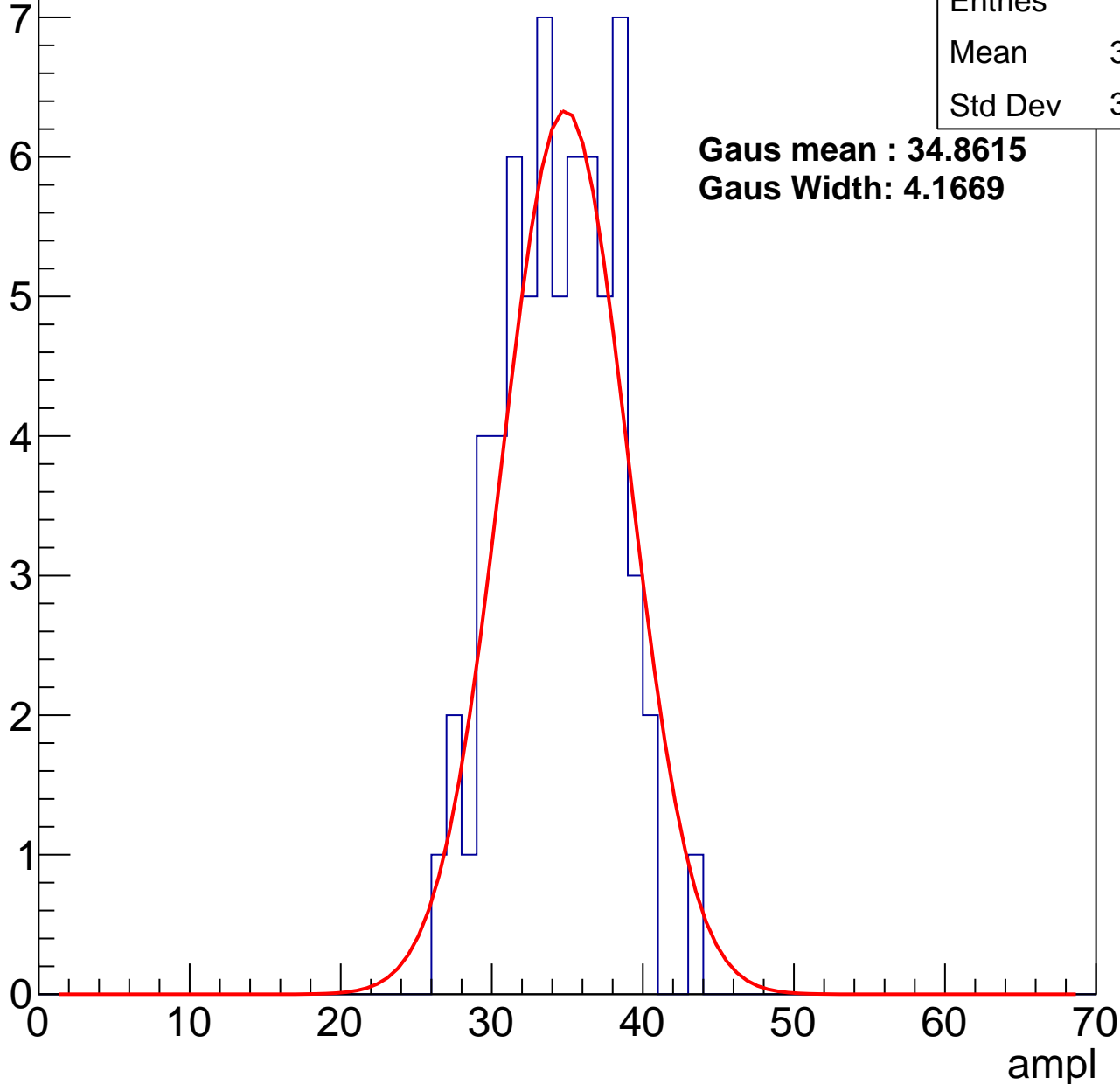
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	33.97
Std Dev	3.633

Gaus mean : 34.8615

Gaus Width: 4.1669



B0L102S, U12-ch116, adc2

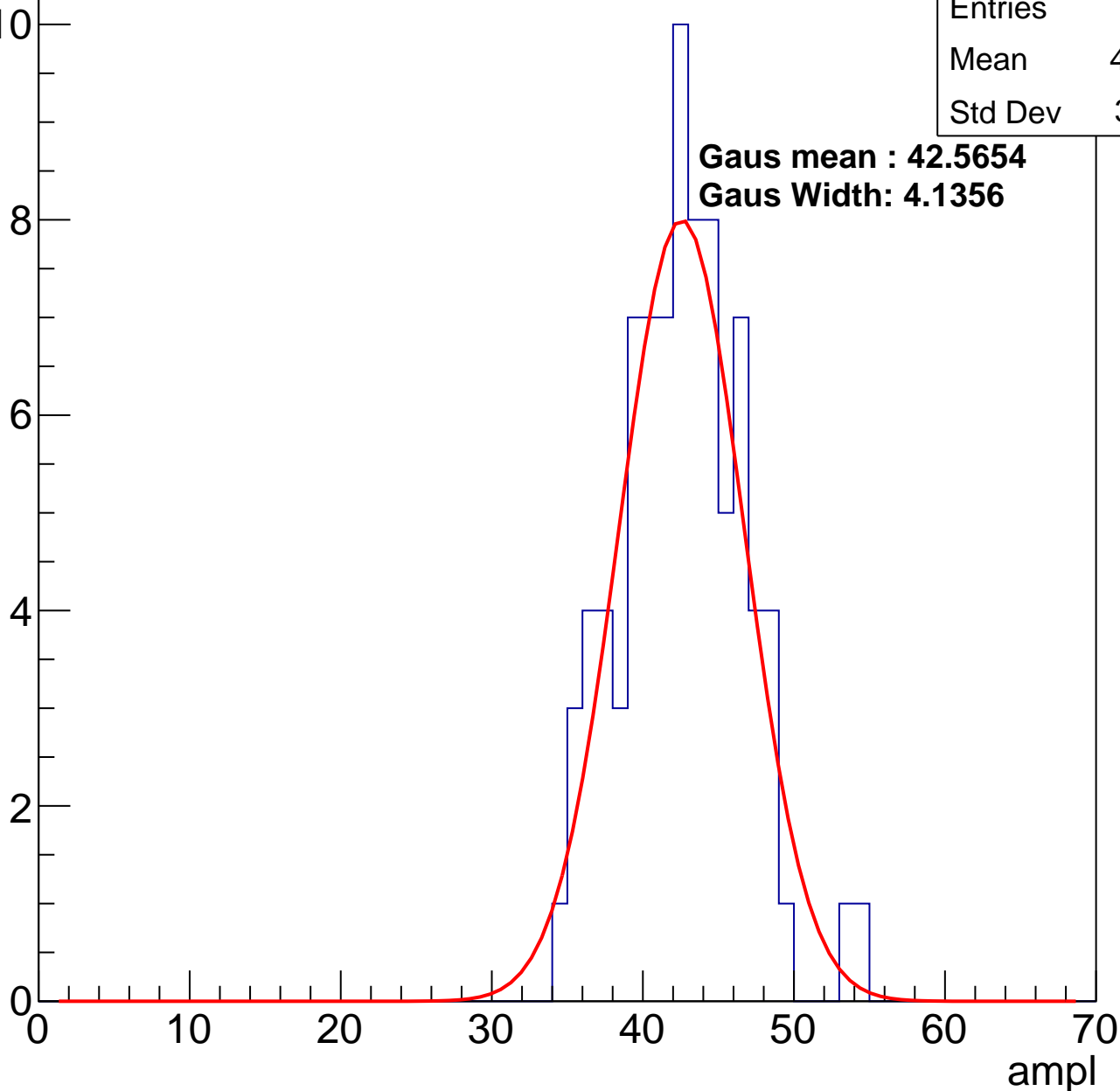
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	42.16
Std Dev	3.991

Gaus mean : 42.5654

Gaus Width: 4.1356

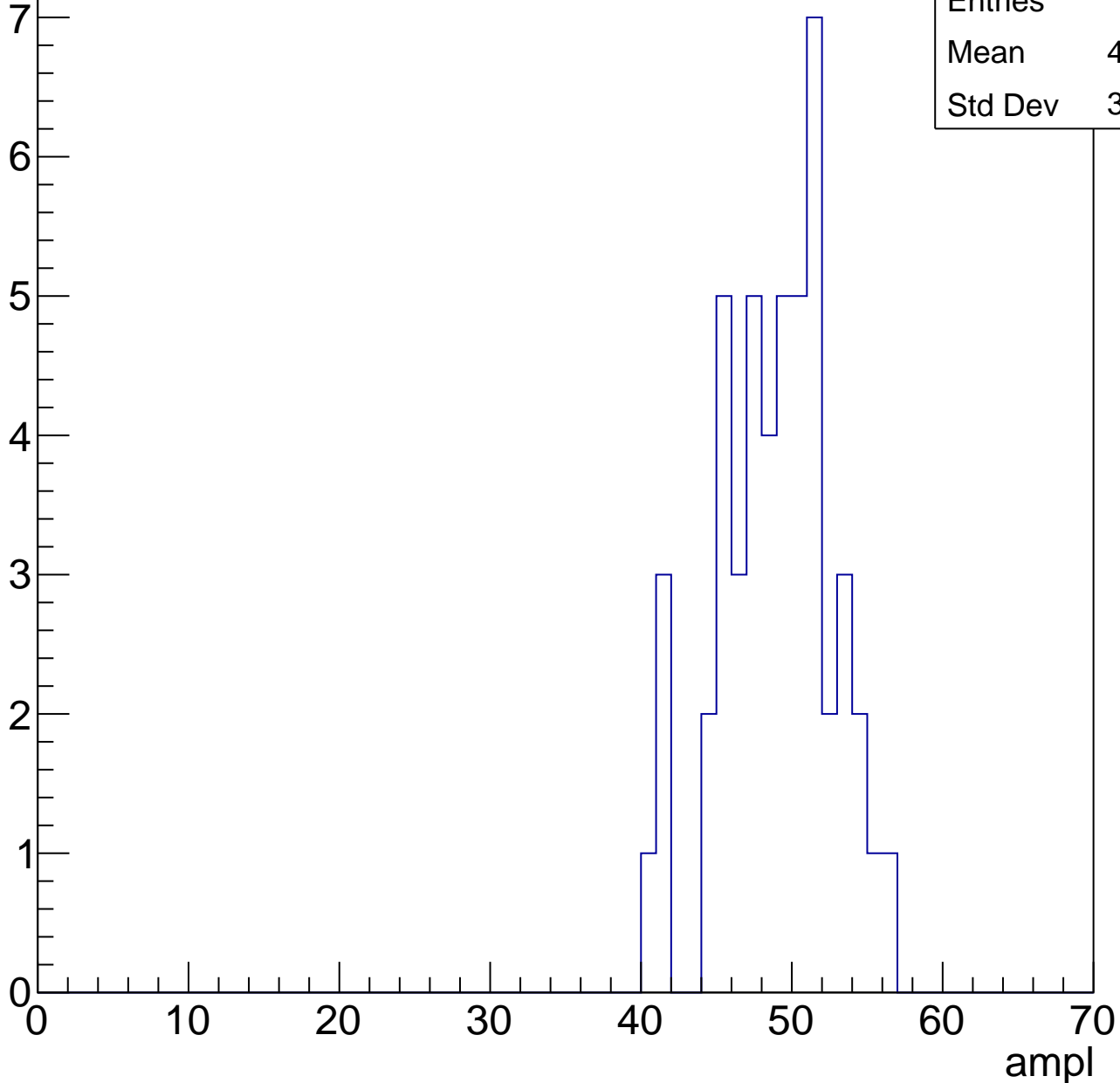


B0L102S, U12-ch116, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	48.47
Std Dev	3.709

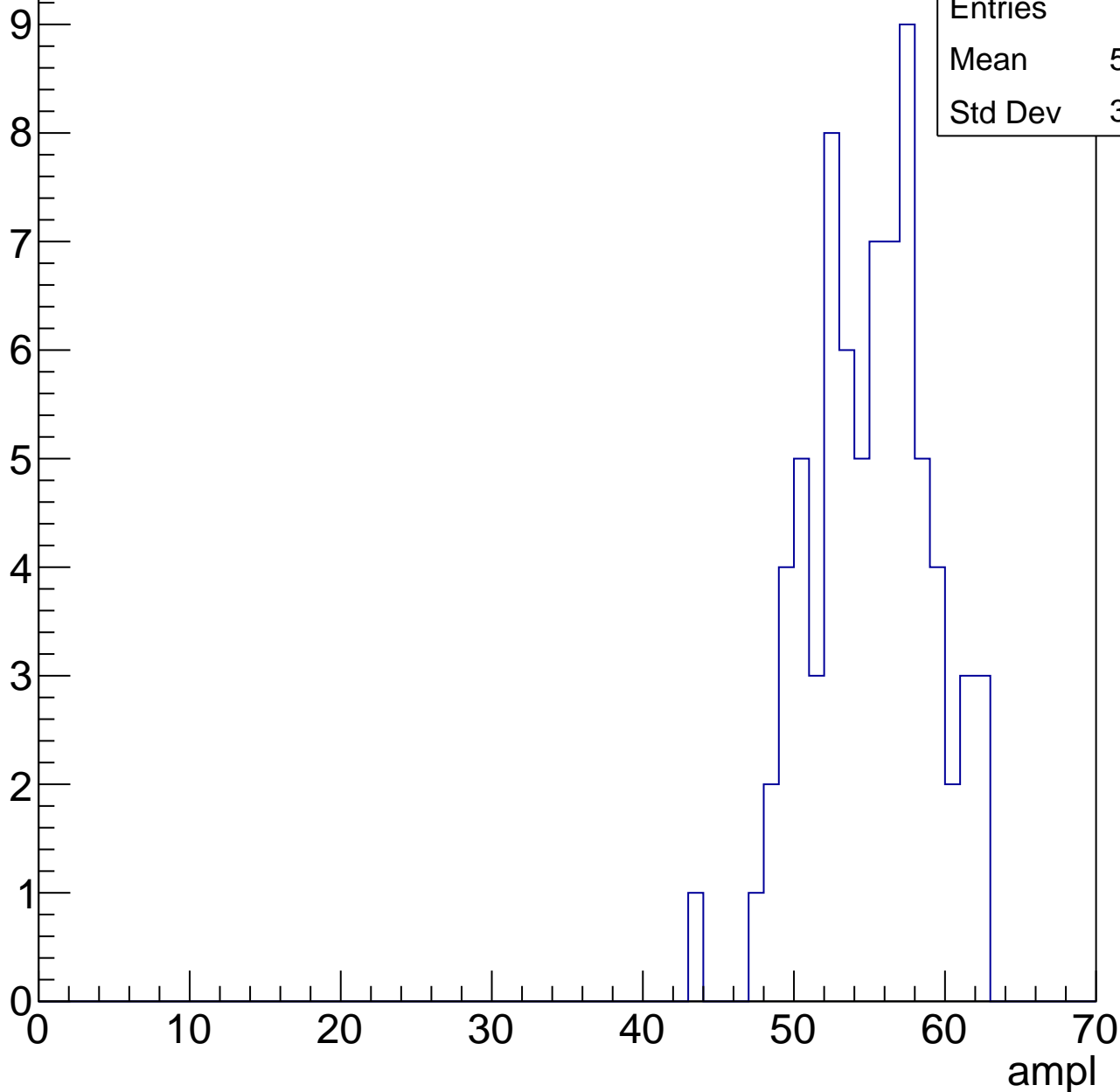


B0L102S, U12-ch116, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	54.59
Std Dev	3.947



B0L102S, U12-ch116, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	49
Mean	58.86
Std Dev	8.841

Entry

10

8

6

4

2

0

0

10

20

30

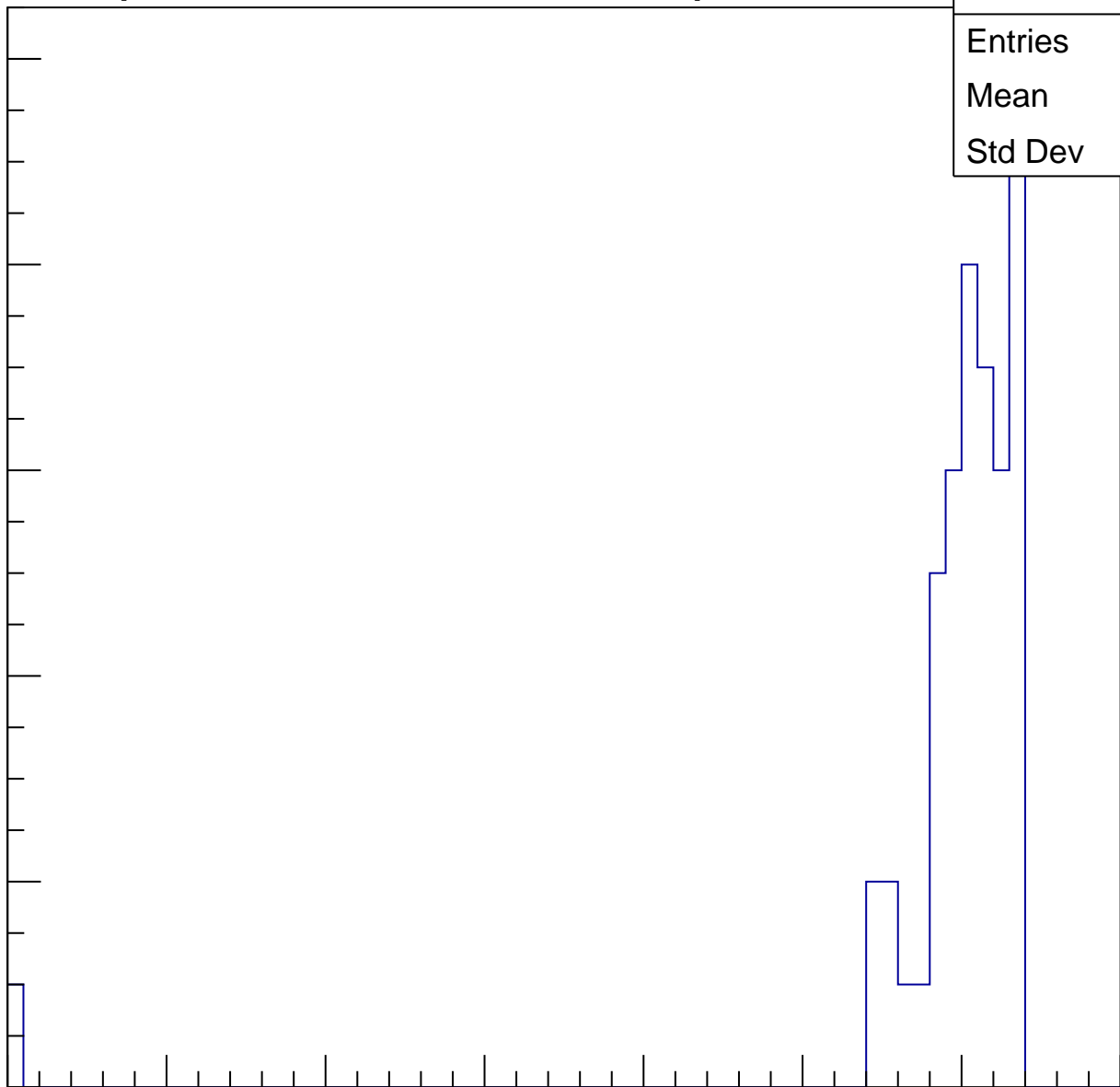
40

50

60

70

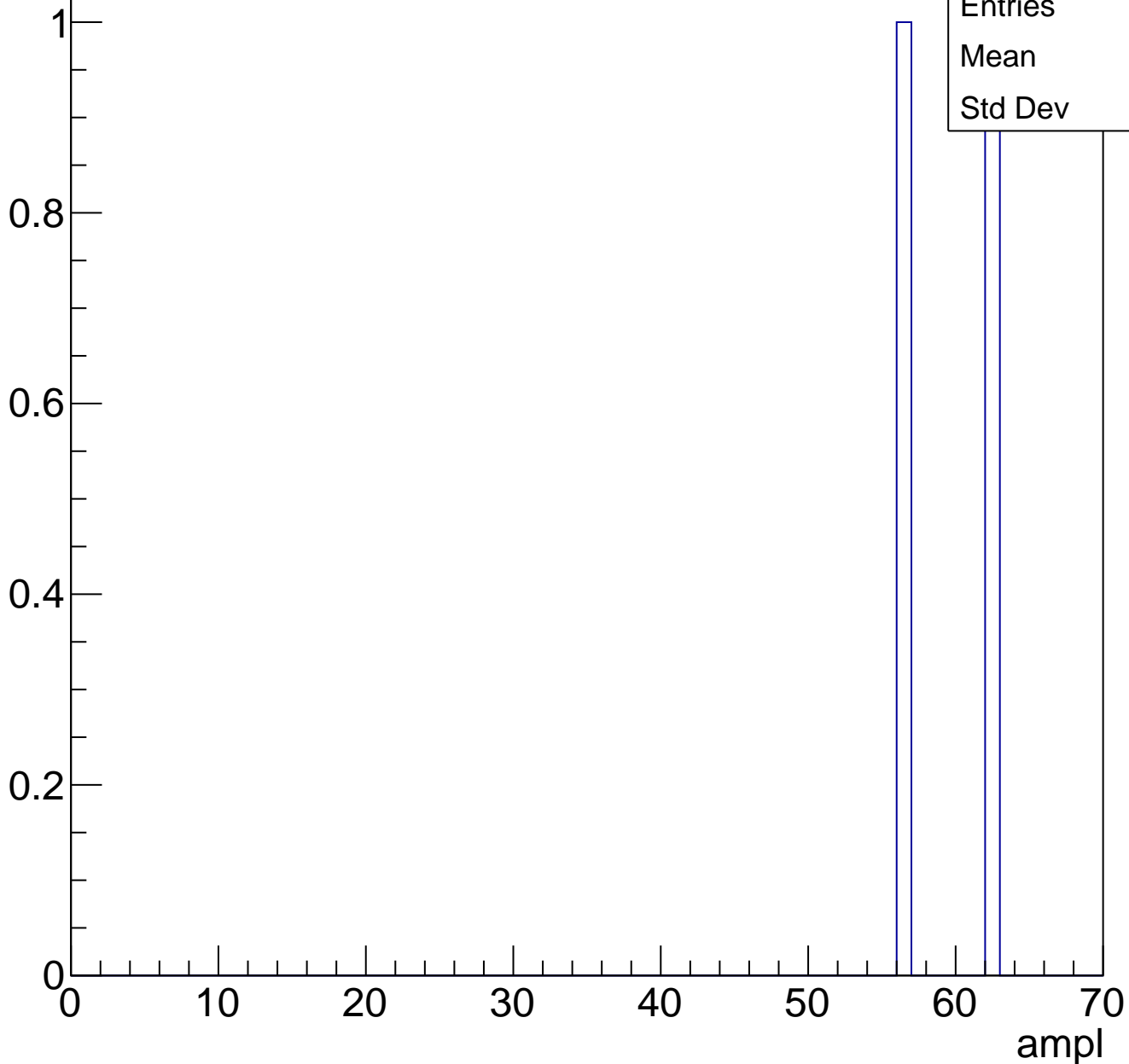
ampl



B0L102S, U12-ch116, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch116, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch117, adc0

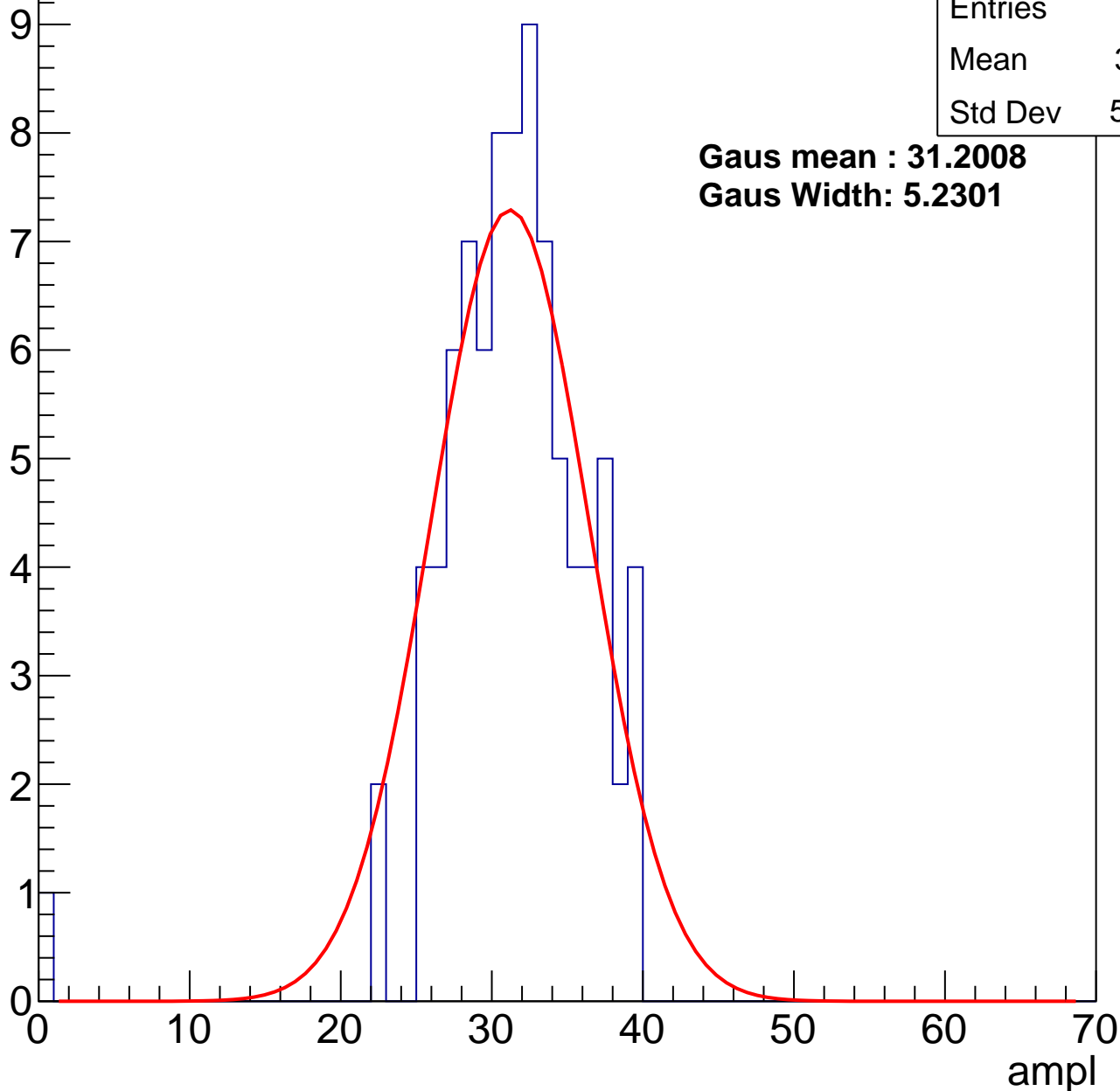
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	30.91
Std Dev	5.213

Gaus mean : 31.2008

Gaus Width: 5.2301



B0L102S, U12-ch117, adc1

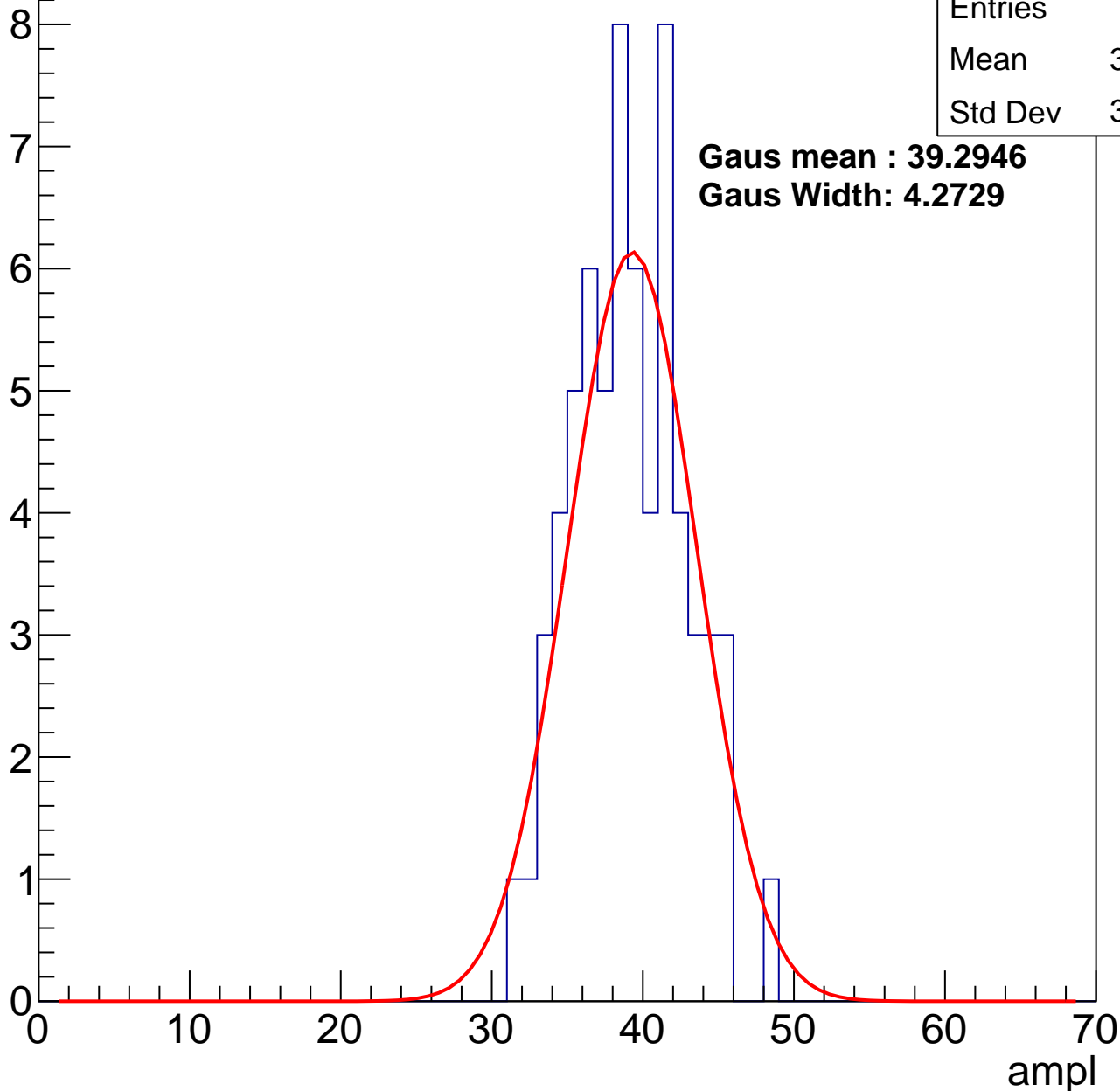
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	38.65
Std Dev	3.635

Gaus mean : 39.2946

Gaus Width: 4.2729



B0L102S, U12-ch117, adc2

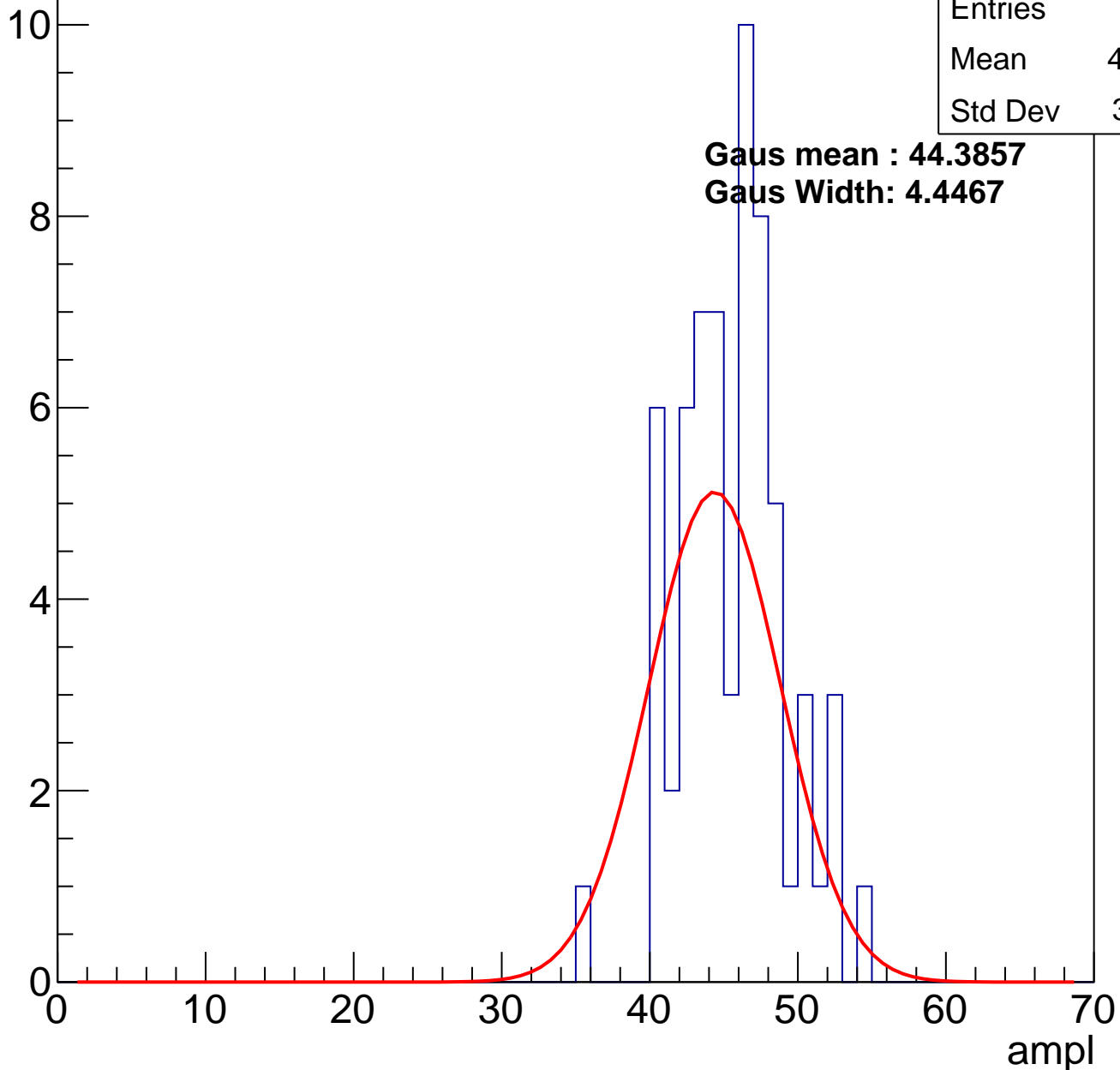
calib_packv5_042523_0143.root, FC#12, port B1

Entries	64
Mean	45.14
Std Dev	3.561

Gaus mean : 44.3857

Gaus Width: 4.4467

Entry

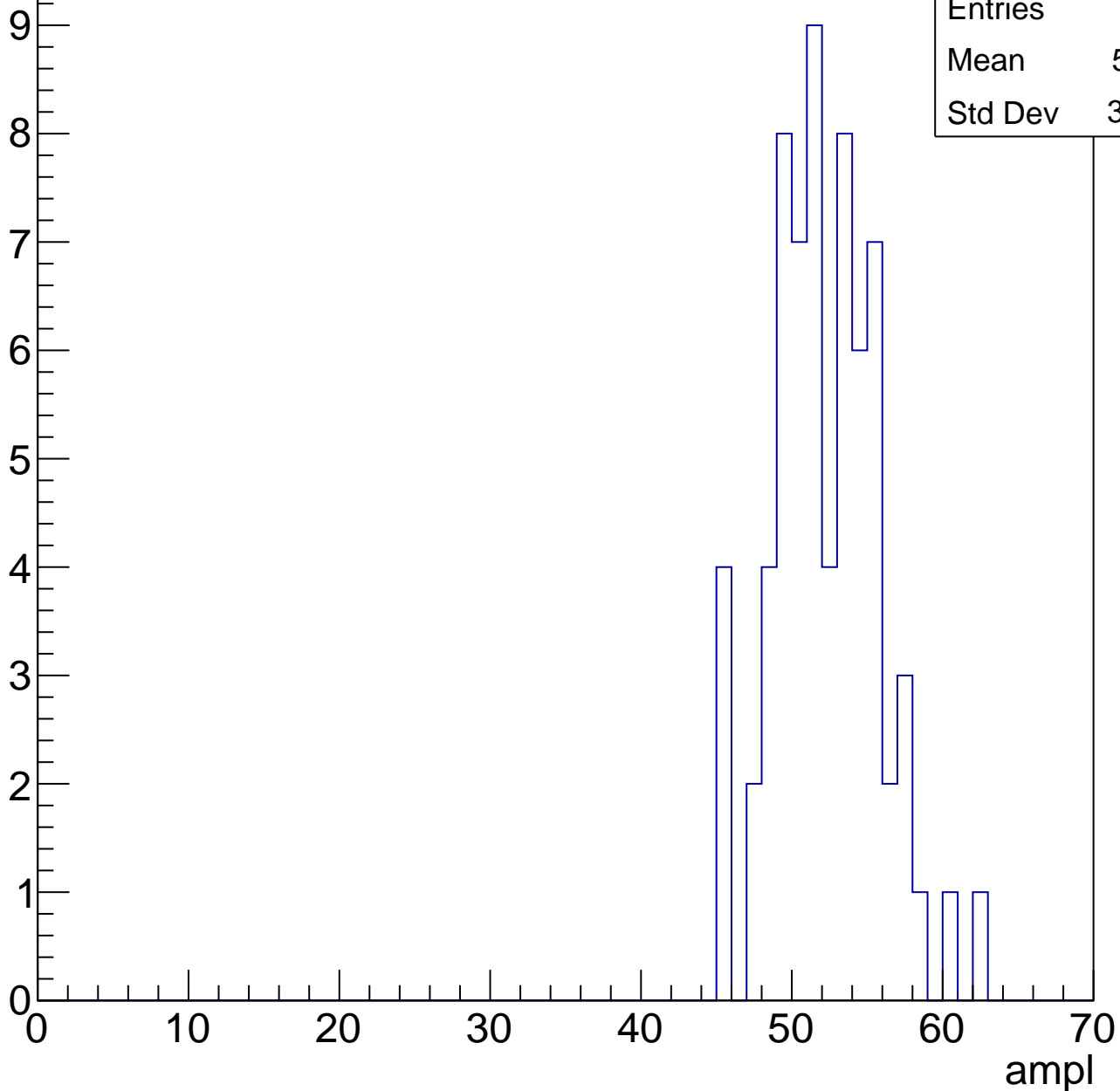


B0L102S, U12-ch117, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	51.81
Std Dev	3.495

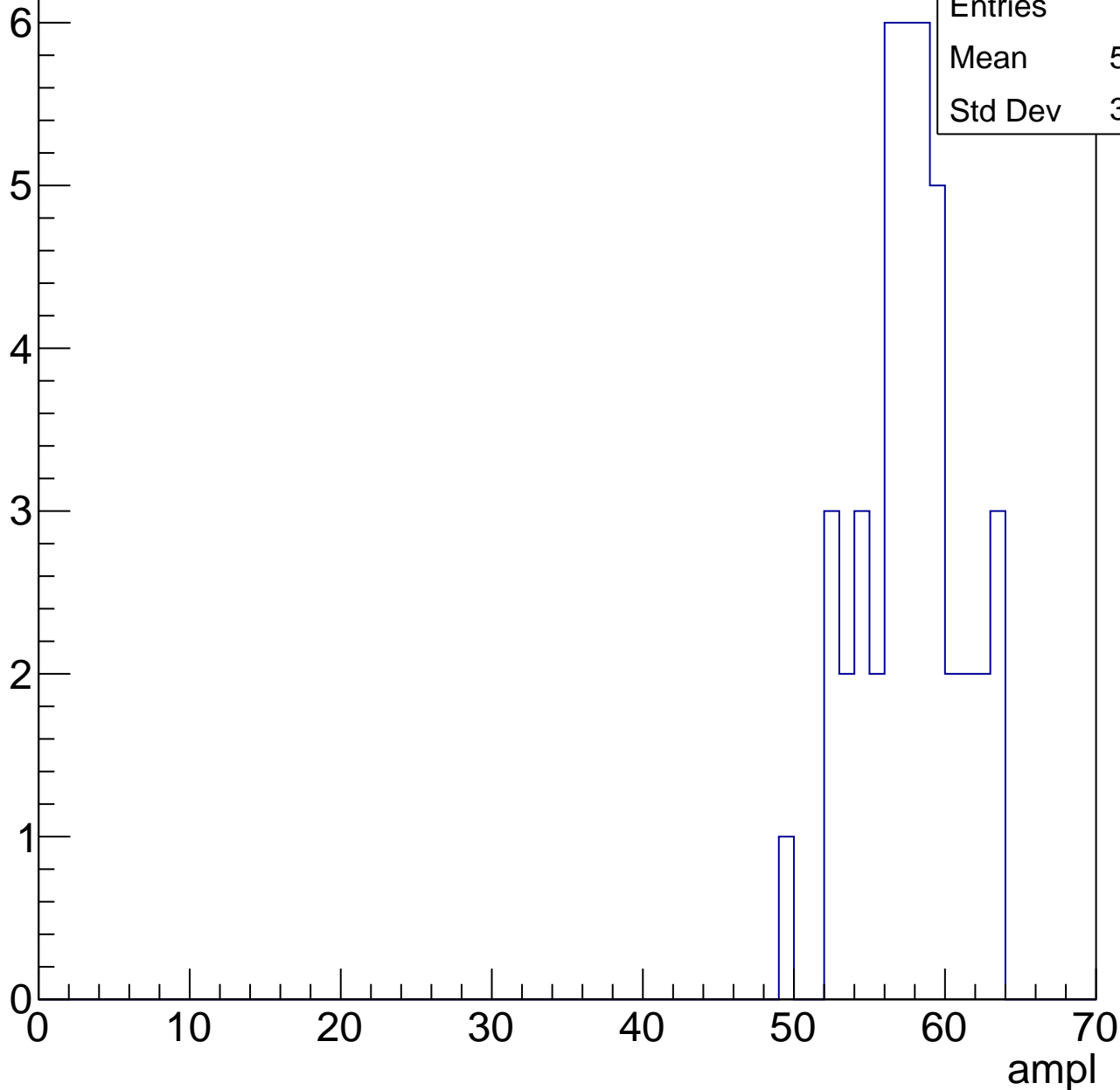


B0L102S, U12-ch117, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	57.19
Std Dev	3.215



B0L102S, U12-ch117, adc5

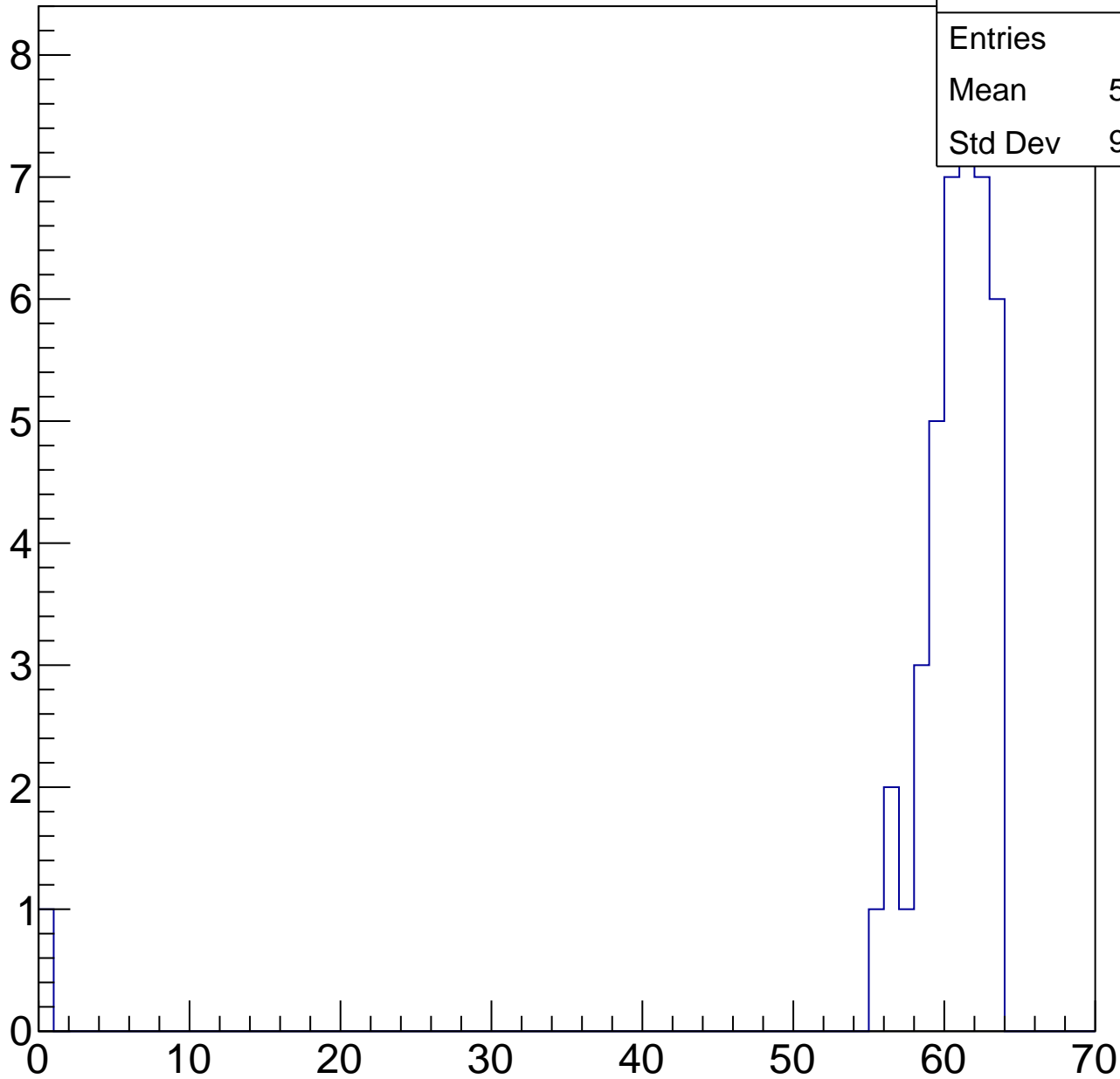
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.85
Std Dev	9.524

ampl



B0L102S, U12-ch117, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch117, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch118, adc0

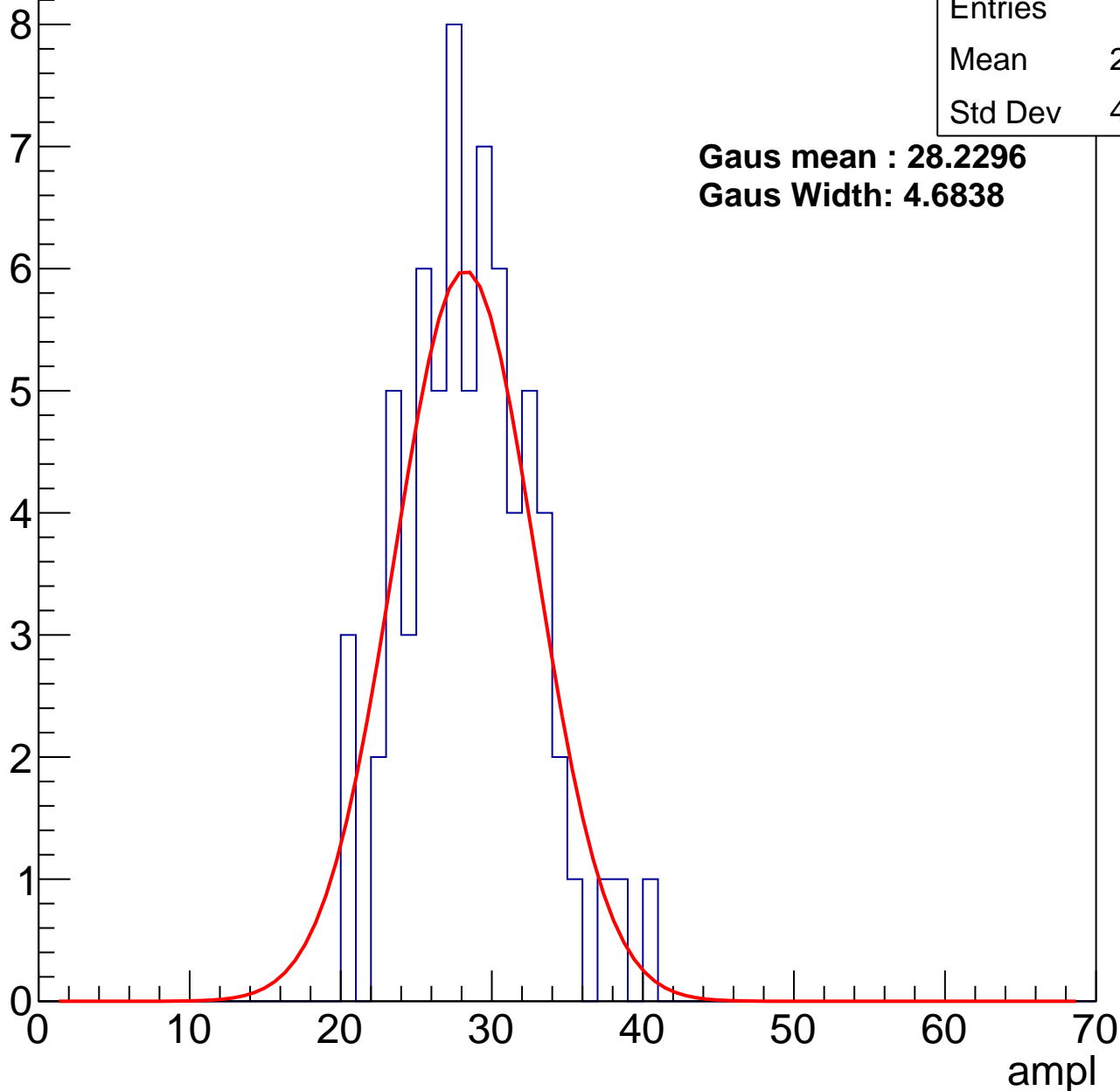
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.17
Std Dev	4.187

Gaus mean : 28.2296

Gaus Width: 4.6838



B0L102S, U12-ch118, adc1

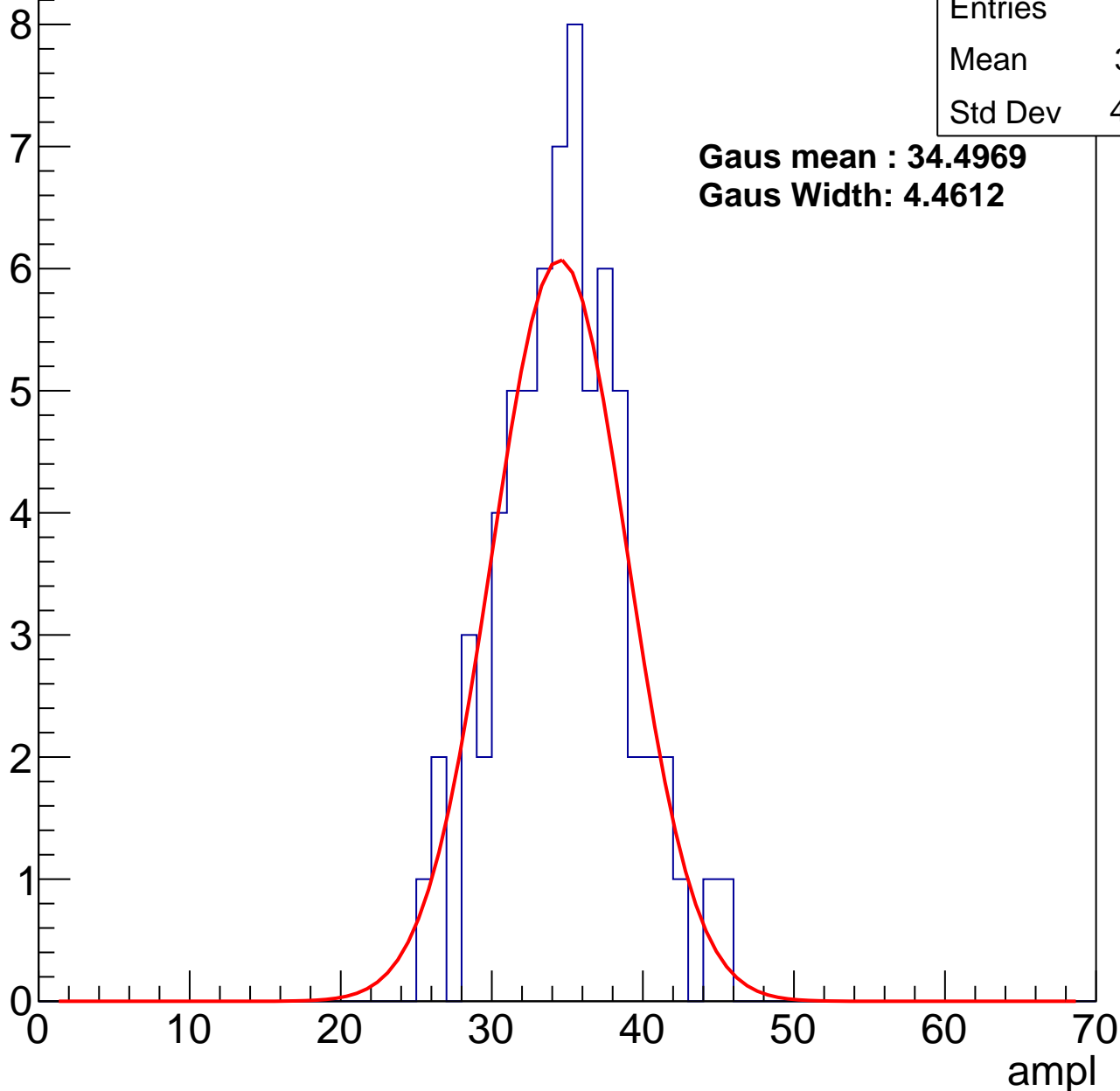
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	34.31
Std Dev	4.135

Gaus mean : 34.4969

Gaus Width: 4.4612



B0L102S, U12-ch118, adc2

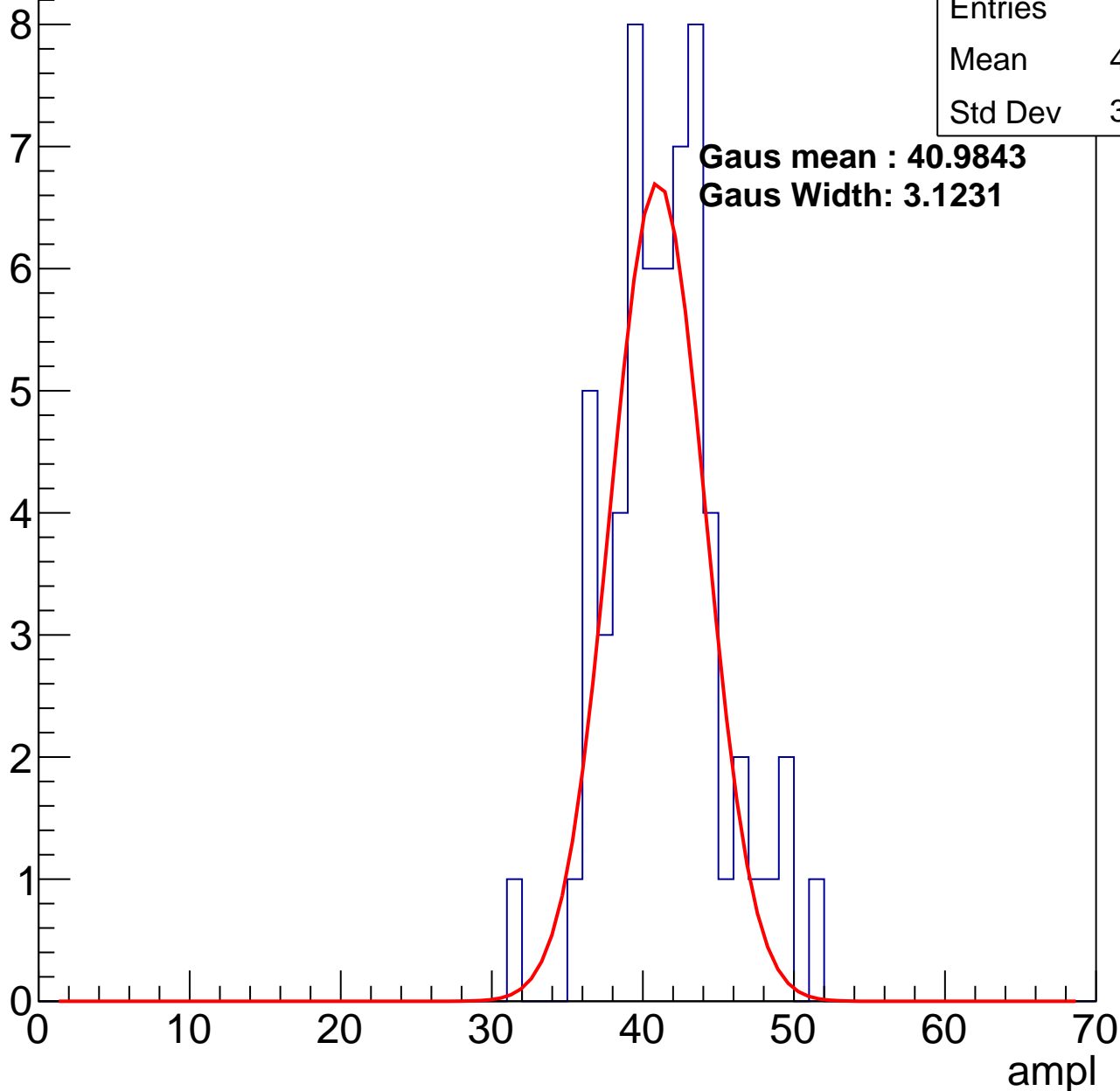
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	41.02
Std Dev	3.704

Gaus mean : 40.9843

Gaus Width: 3.1231

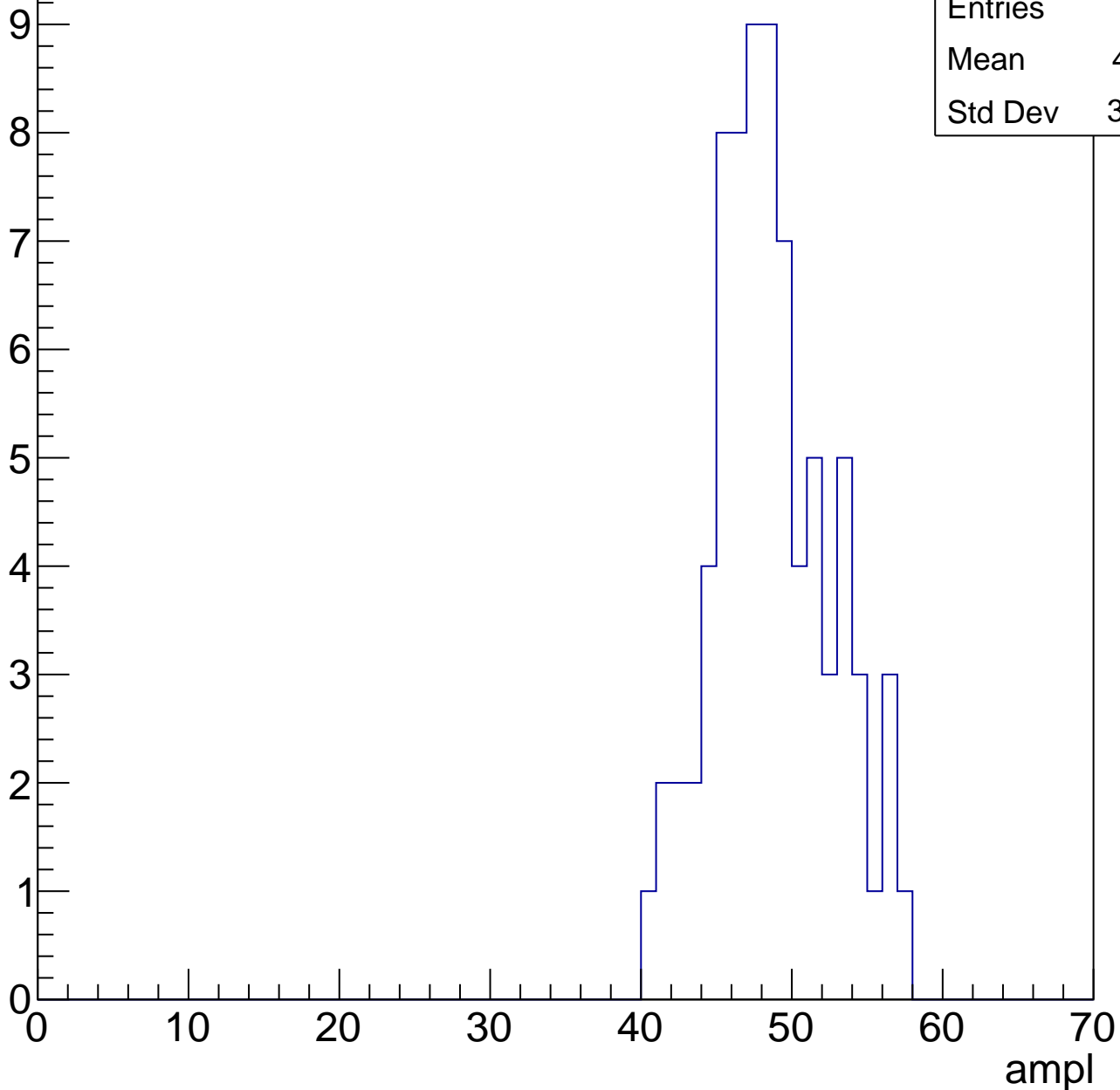


B0L102S, U12-ch118, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	48.21
Std Dev	3.829

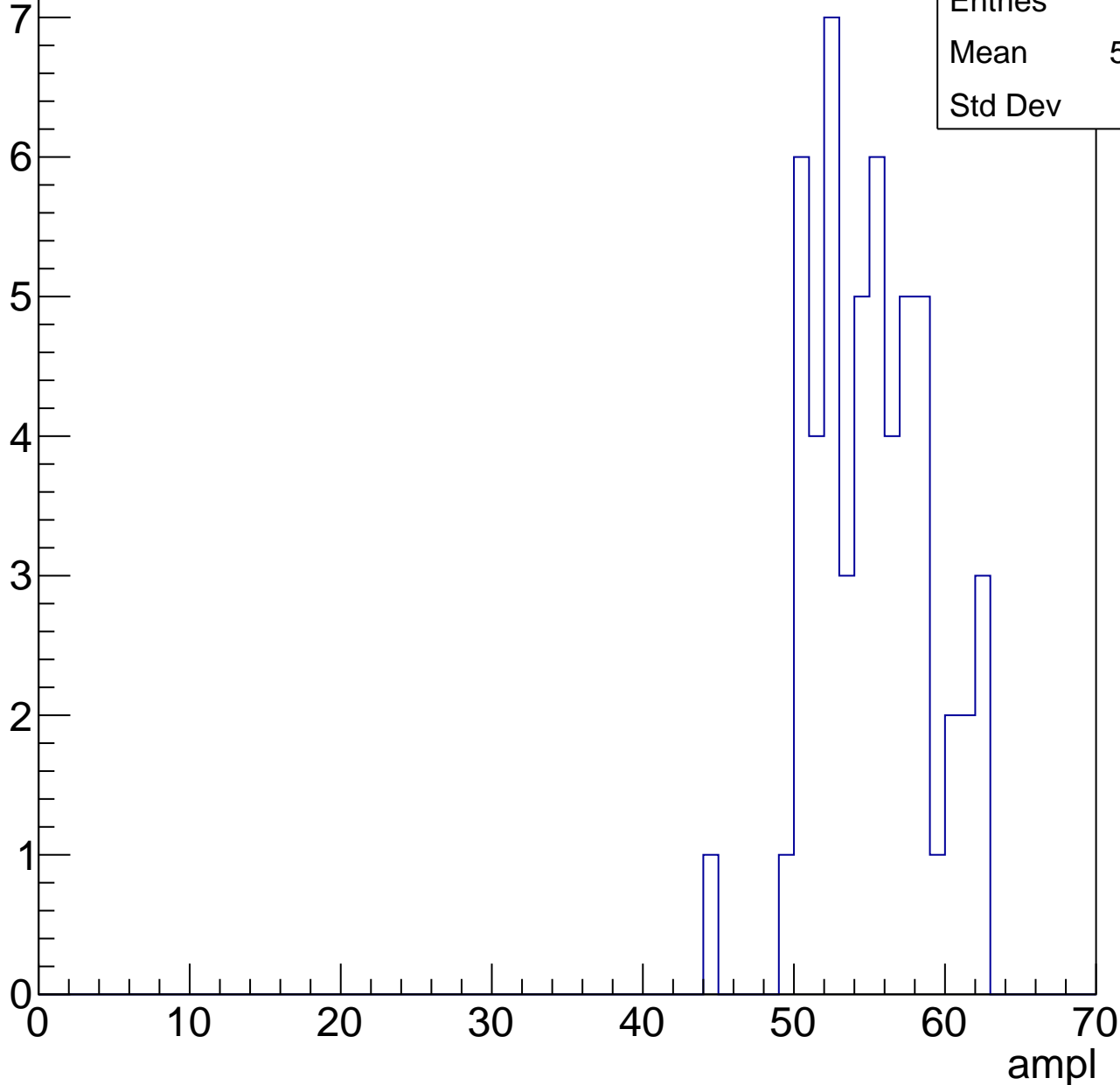


B0L102S, U12-ch118, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

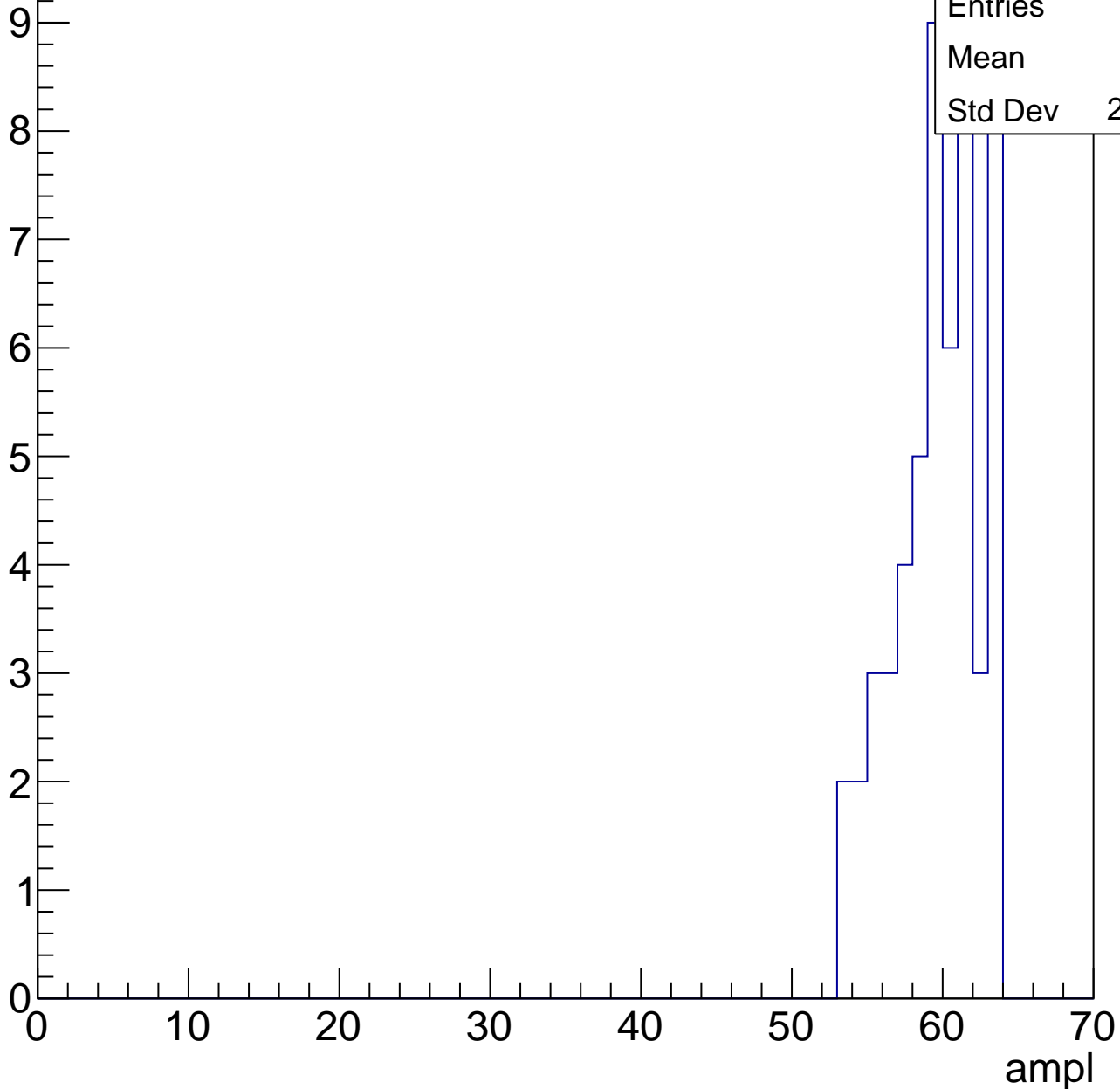
Entries	55
Mean	54.65
Std Dev	3.81



B0L102S, U12-ch118, adc5

calib_packv5_042523_0143.root, FC#12, port B1

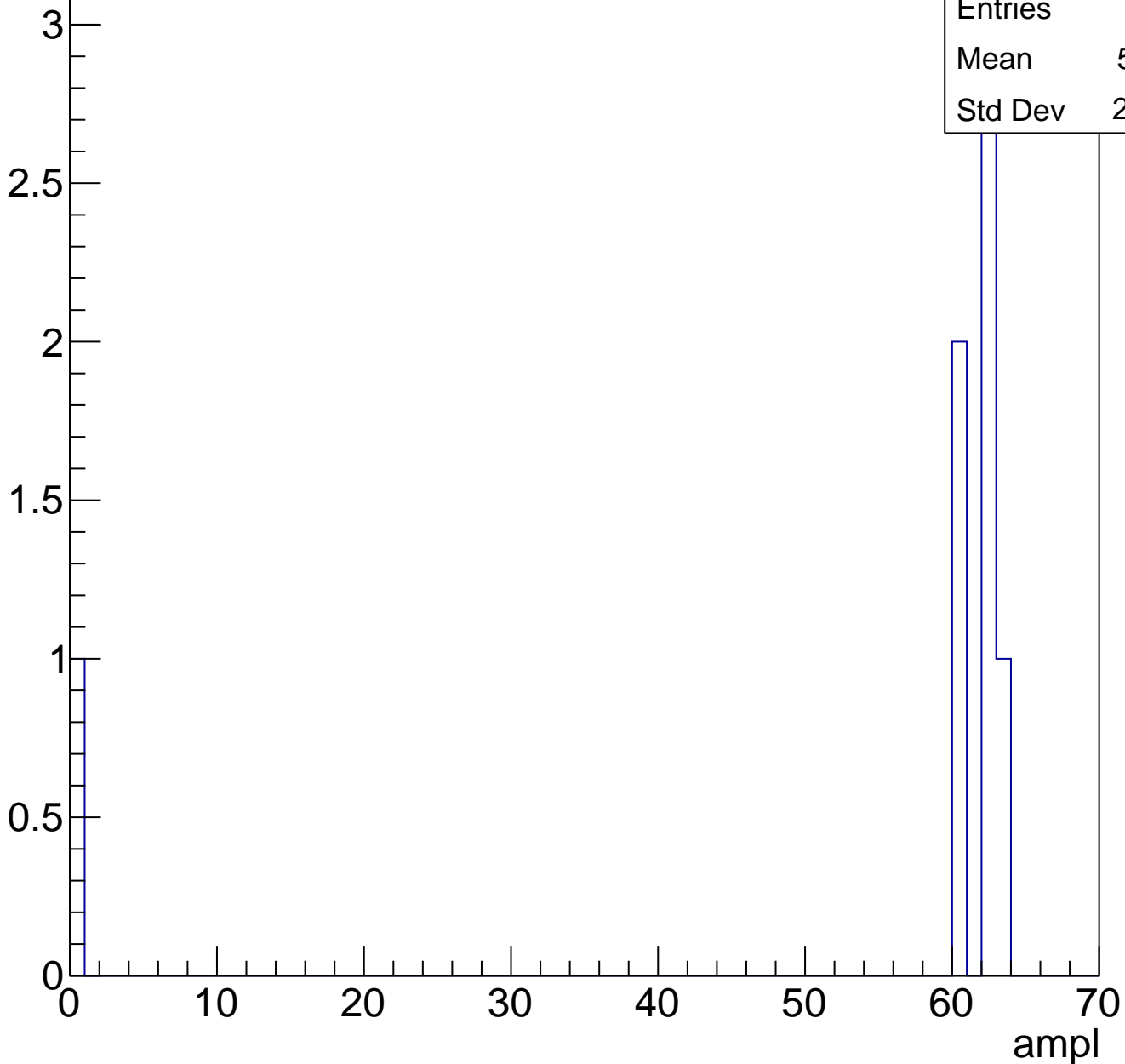
Entry



B0L102S, U12-ch118, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	7
Mean	52.71
Std Dev	21.55

B0L102S, U12-ch118, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch119, adc0

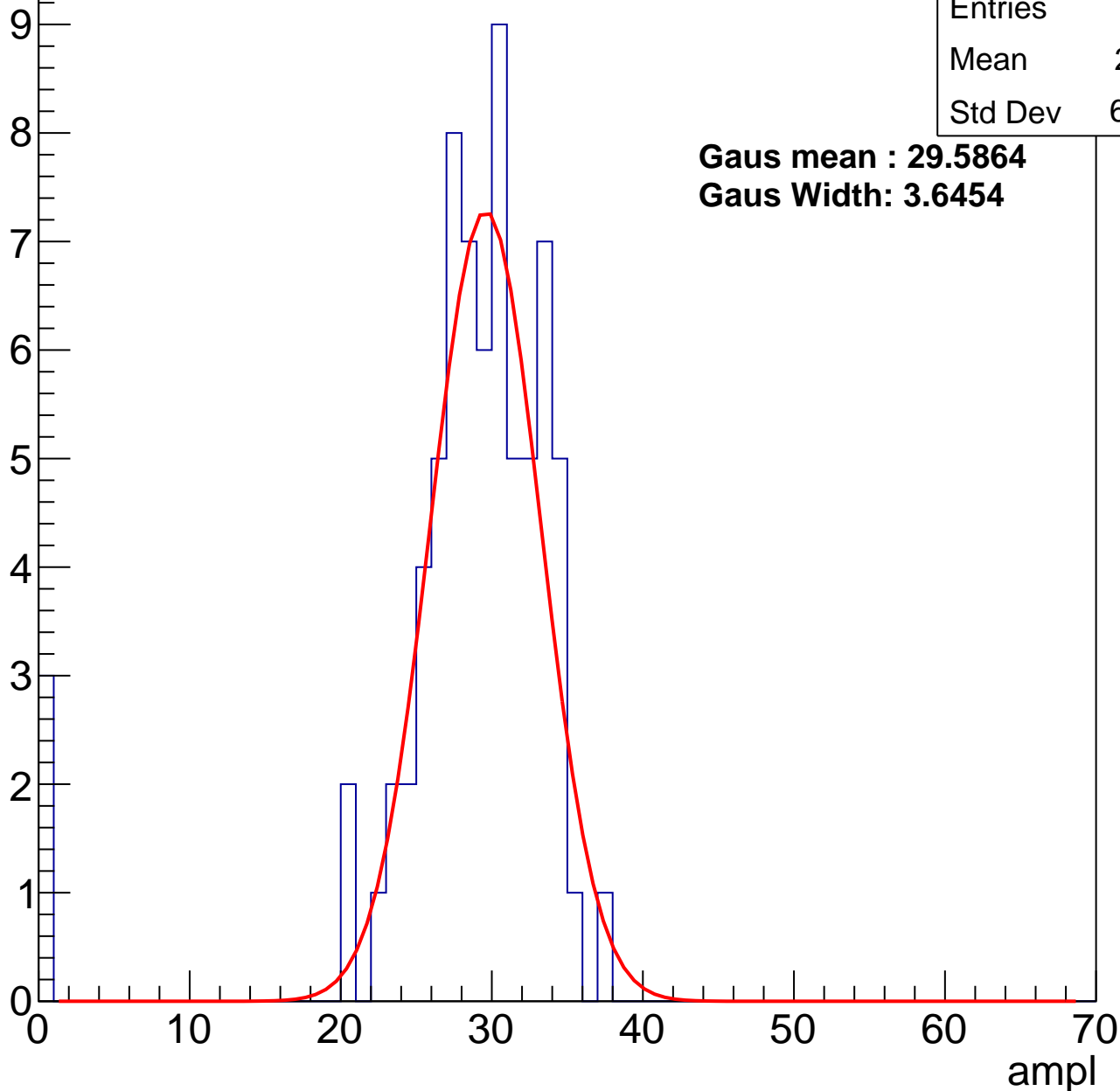
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	27.81
Std Dev	6.735

Gaus mean : 29.5864

Gaus Width: 3.6454



B0L102S, U12-ch119, adc1

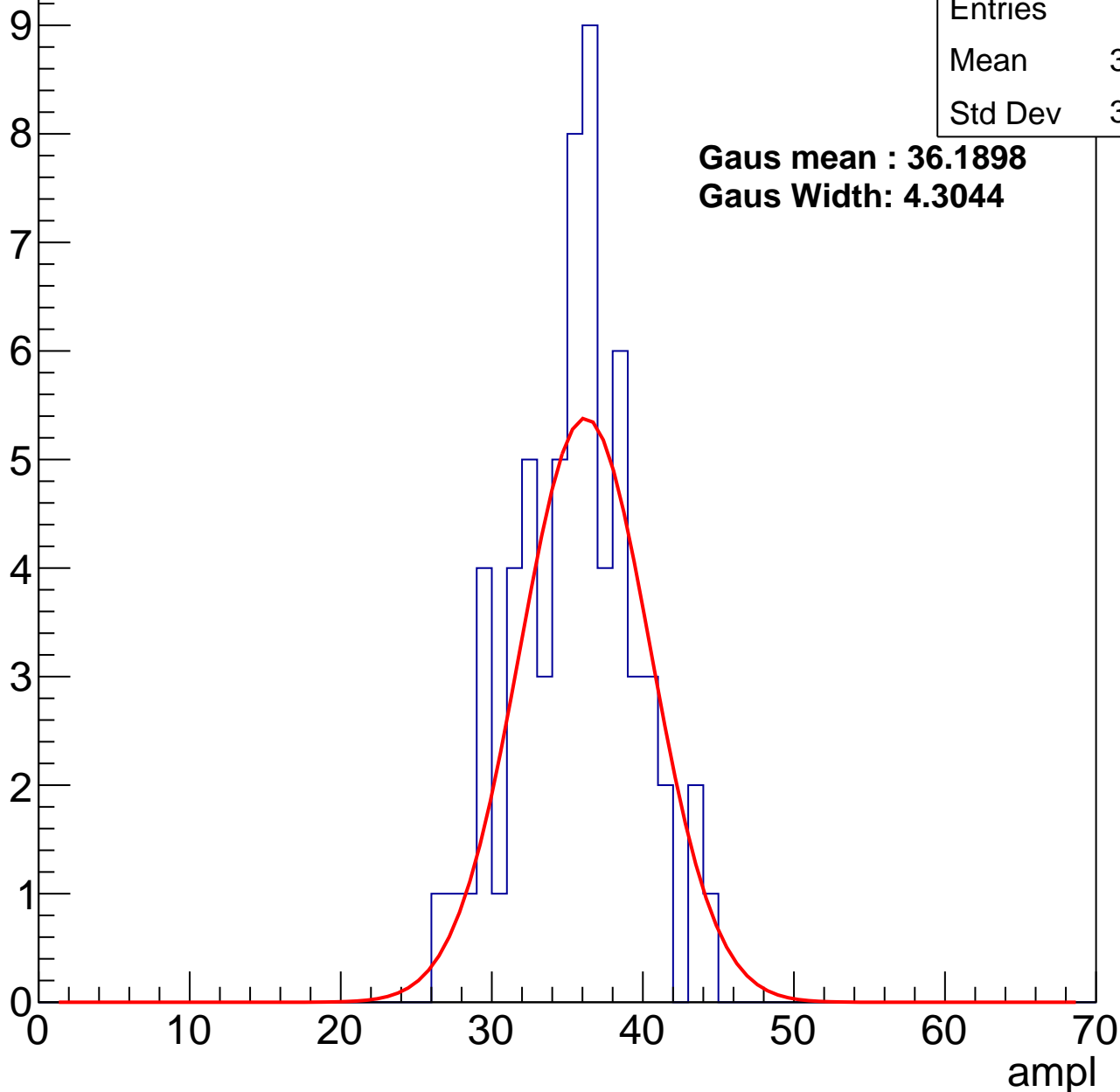
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	35.06
Std Dev	3.927

Gaus mean : 36.1898

Gaus Width: 4.3044



B0L102S, U12-ch119, adc2

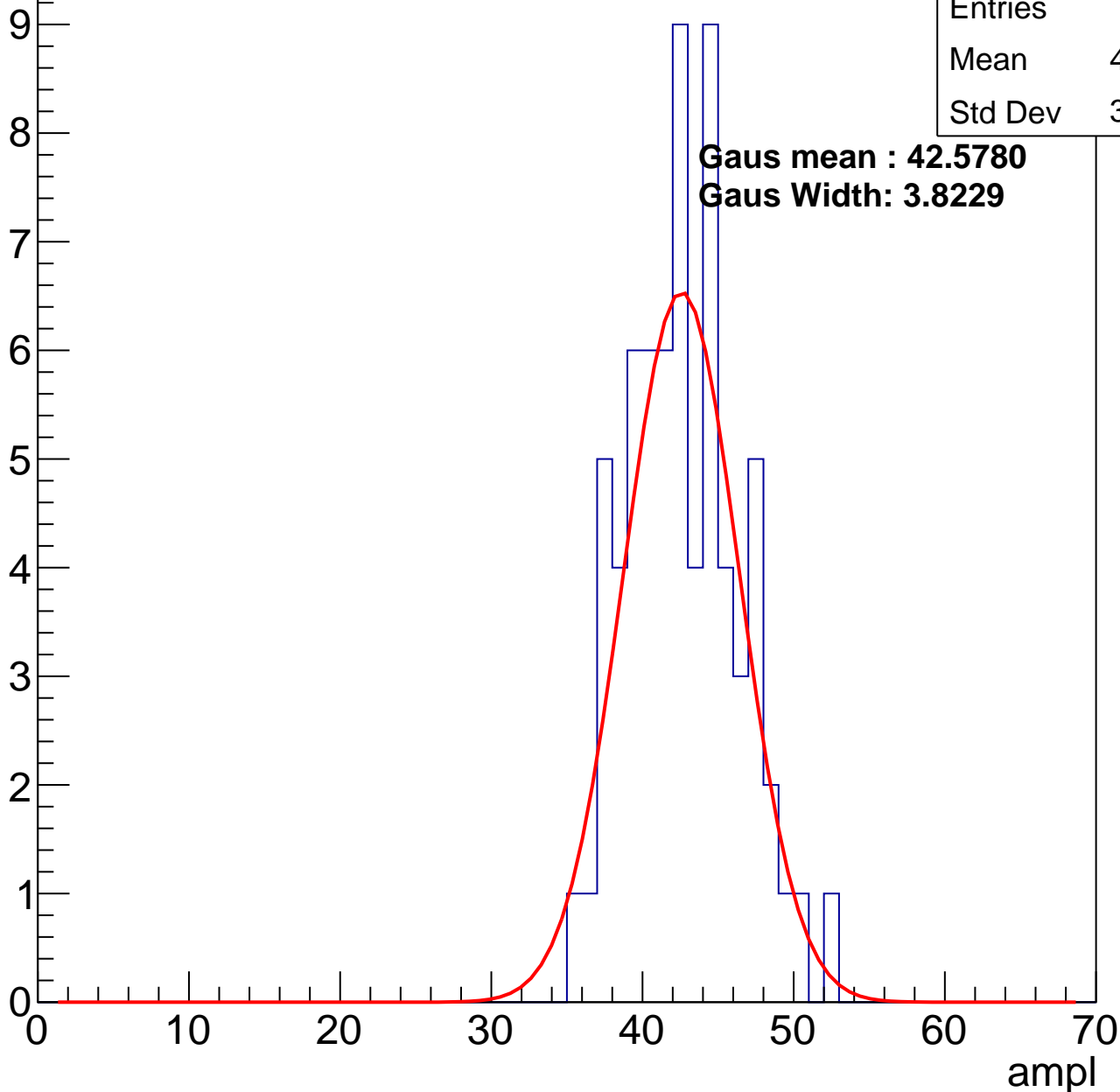
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.26
Std Dev	3.612

Gaus mean : 42.5780

Gaus Width: 3.8229

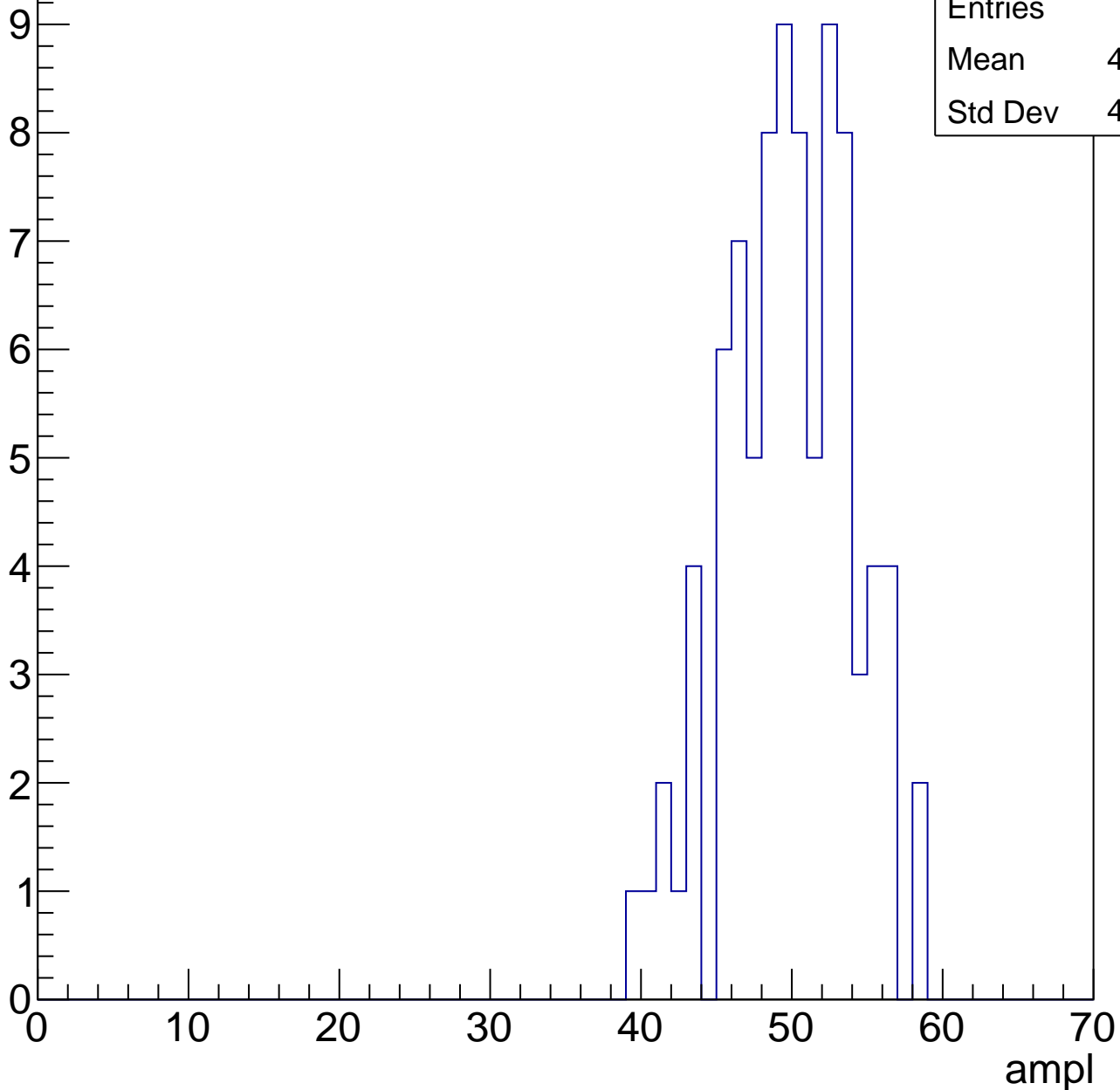


B0L102S, U12-ch119, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	49.38
Std Dev	4.139

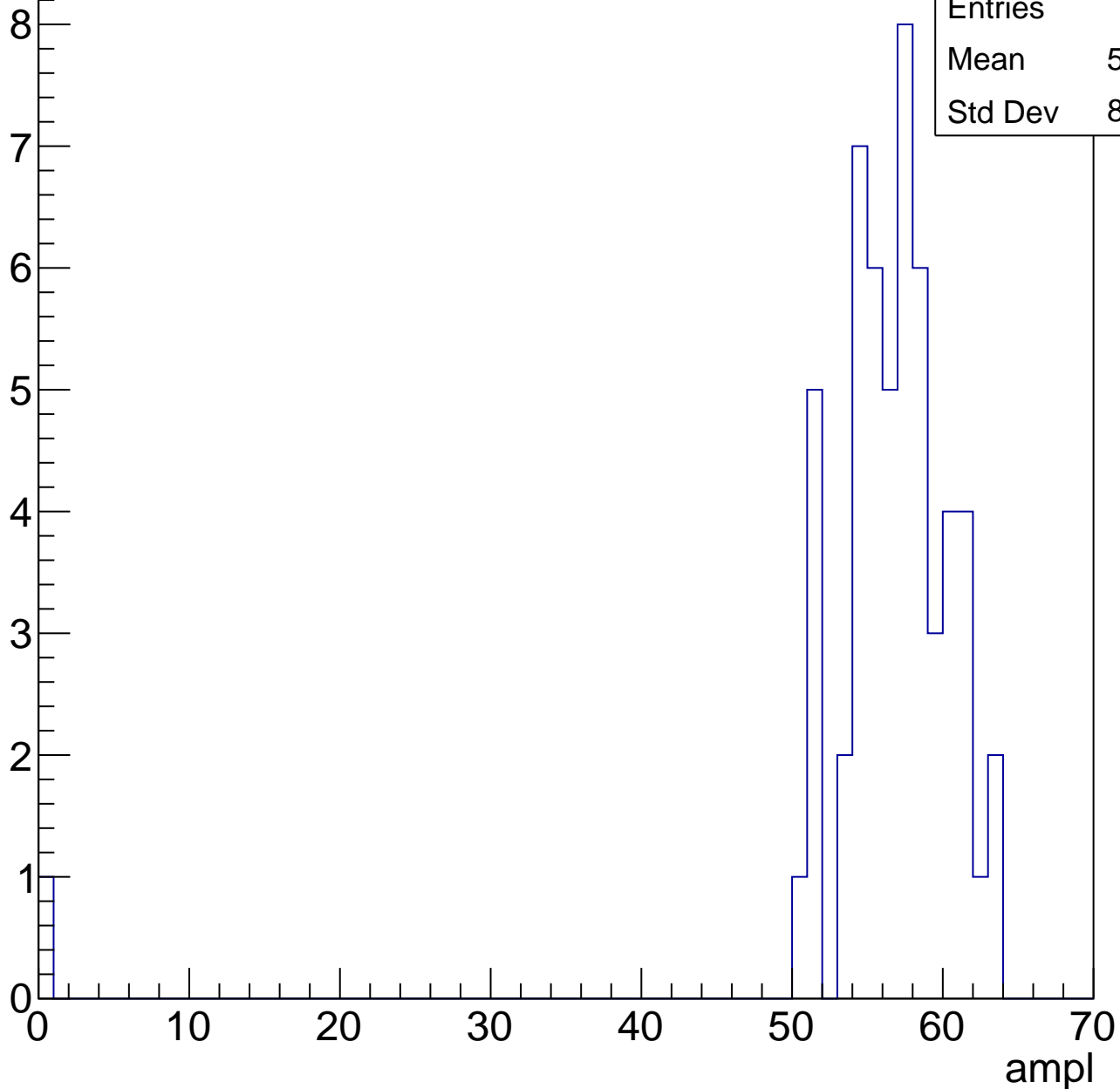


B0L102S, U12-ch119, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	55.49
Std Dev	8.193

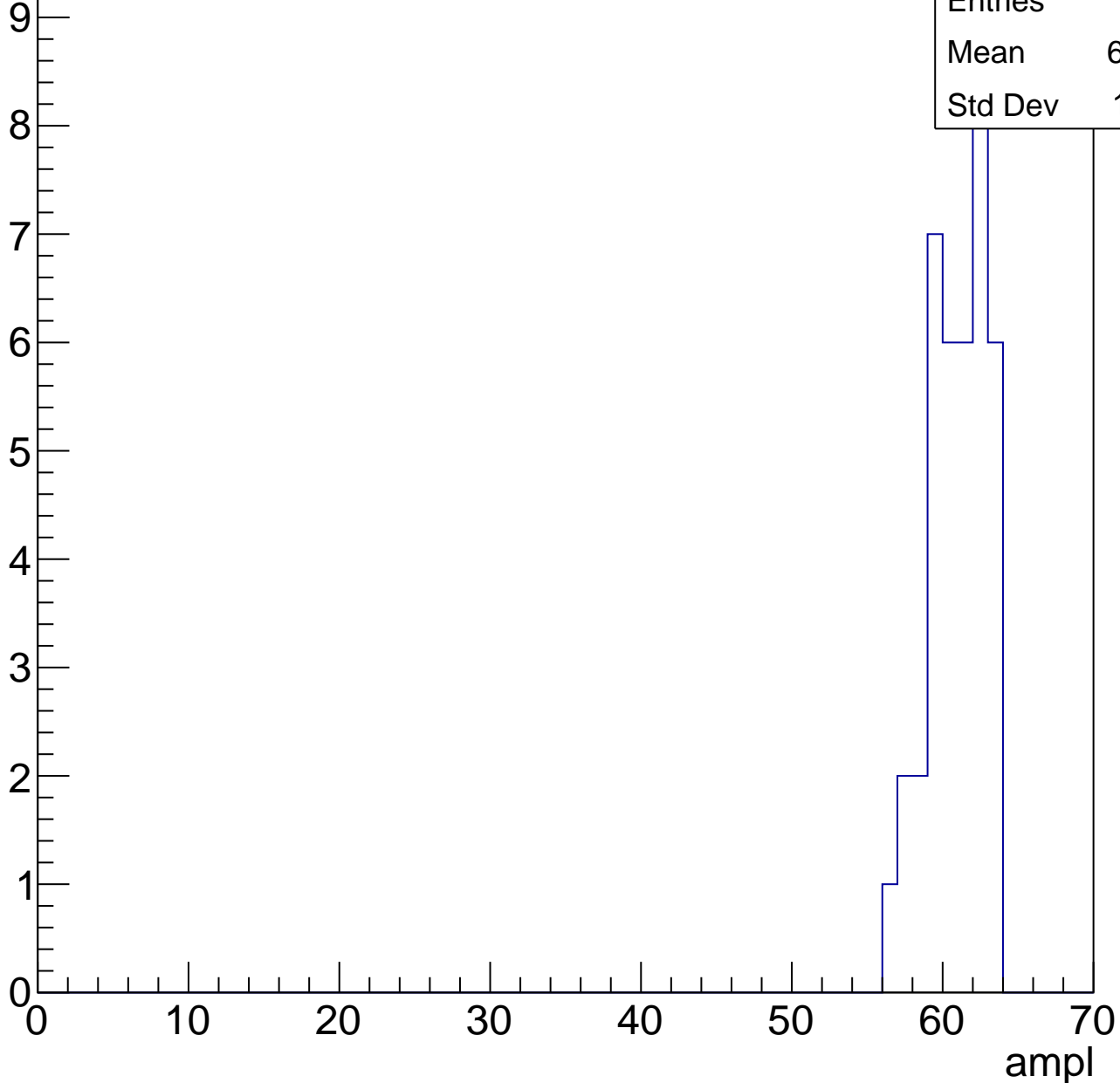


B0L102S, U12-ch119, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	60.54
Std Dev	1.851



B0L102S, U12-ch119, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L102S, U12-ch119, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch120, adc0

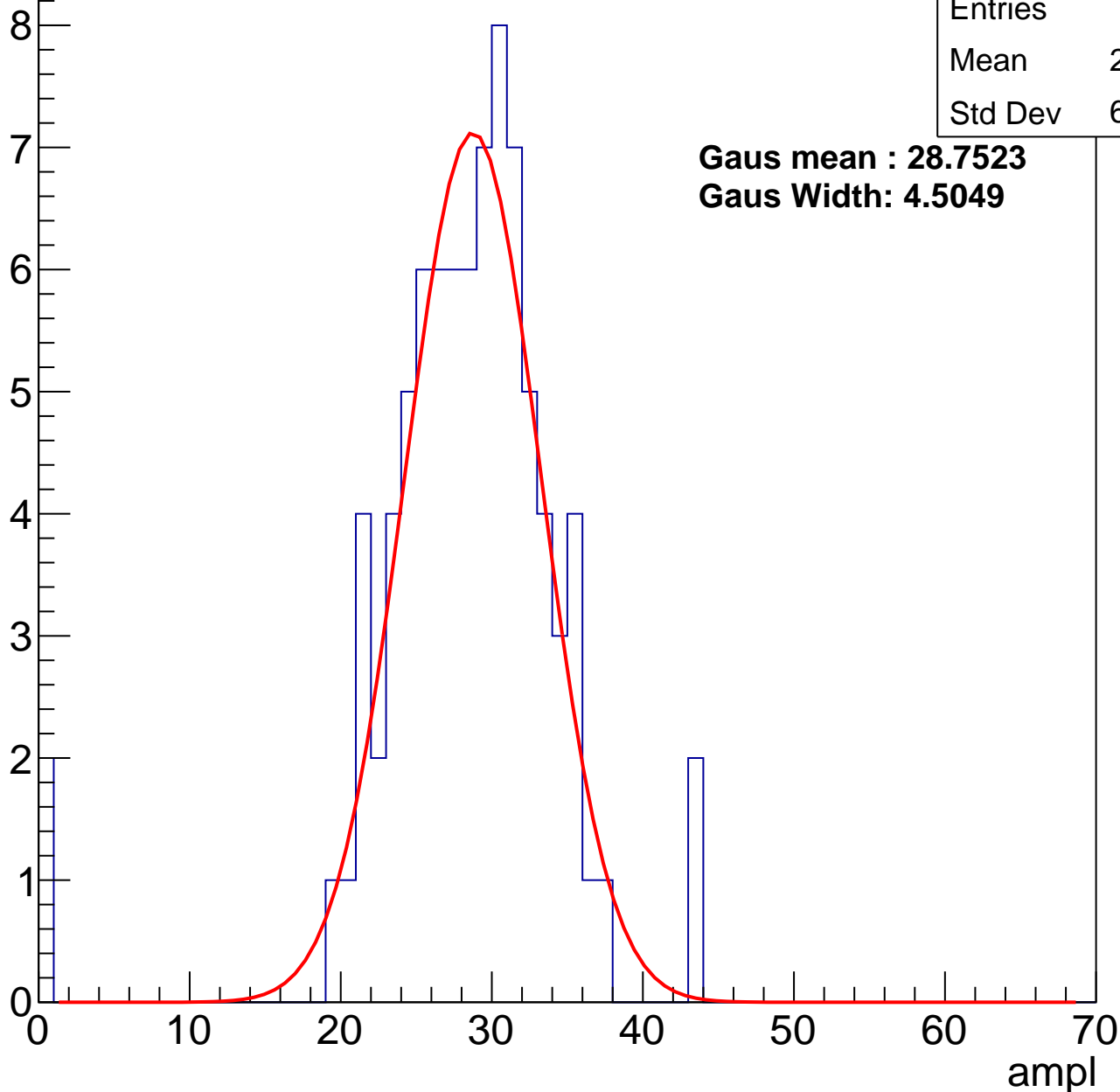
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	27.86
Std Dev	6.347

Gaus mean : 28.7523

Gaus Width: 4.5049



B0L102S, U12-ch120, adc1

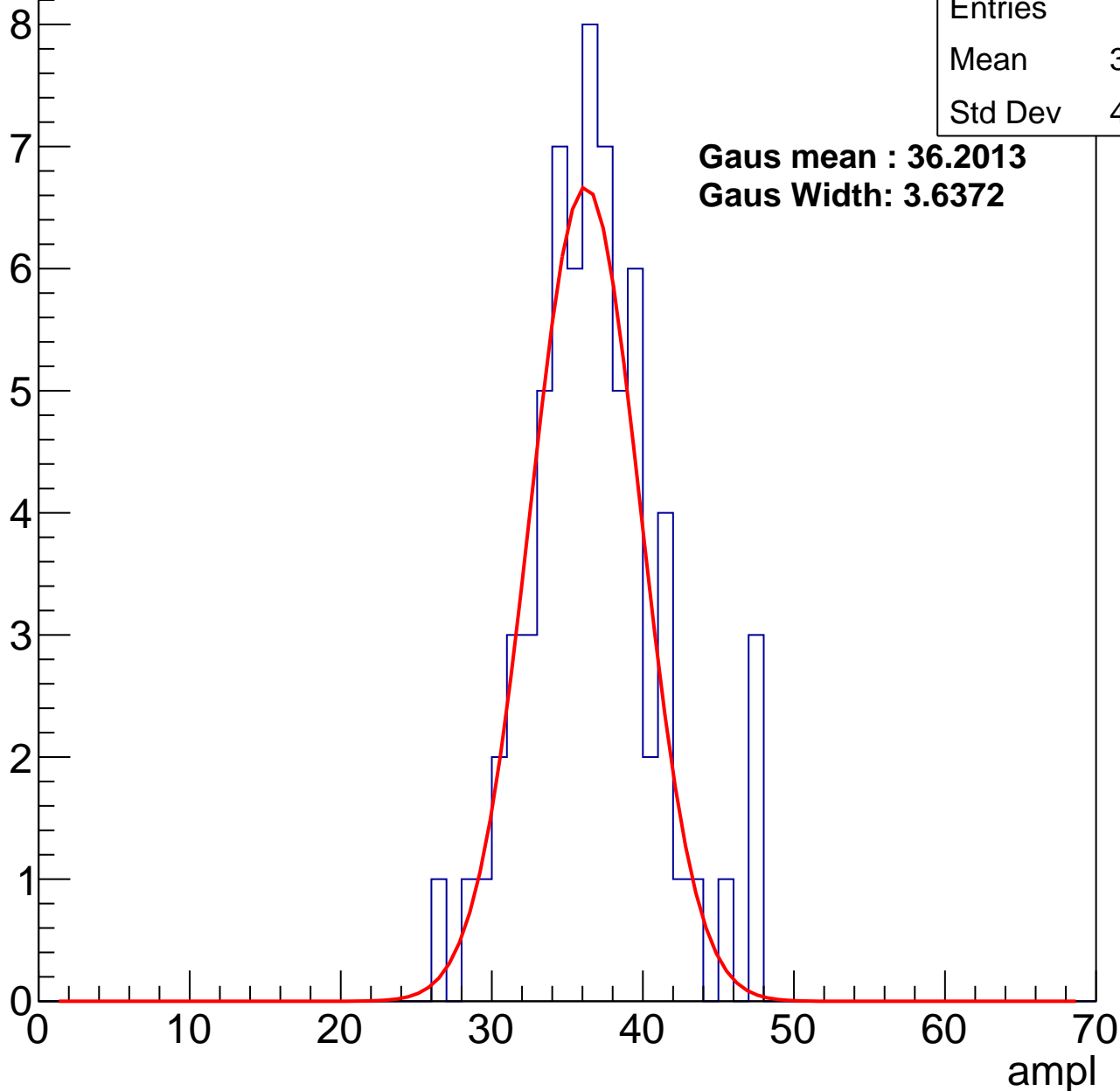
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.28
Std Dev	4.274

Gaus mean : 36.2013

Gaus Width: 3.6372



B0L102S, U12-ch120, adc2

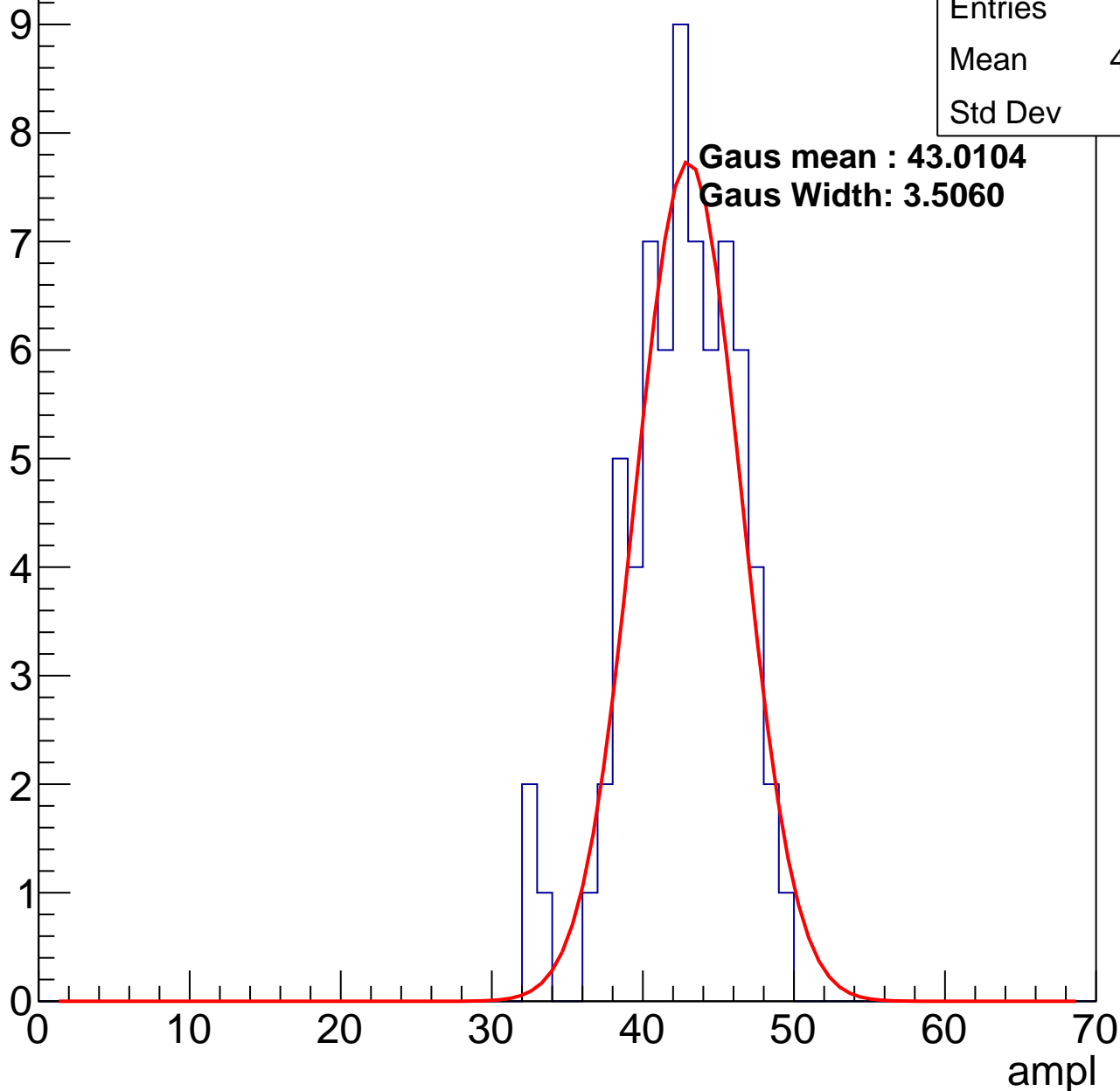
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	42.09
Std Dev	3.64

Gaus mean : 43.0104

Gaus Width: 3.5060

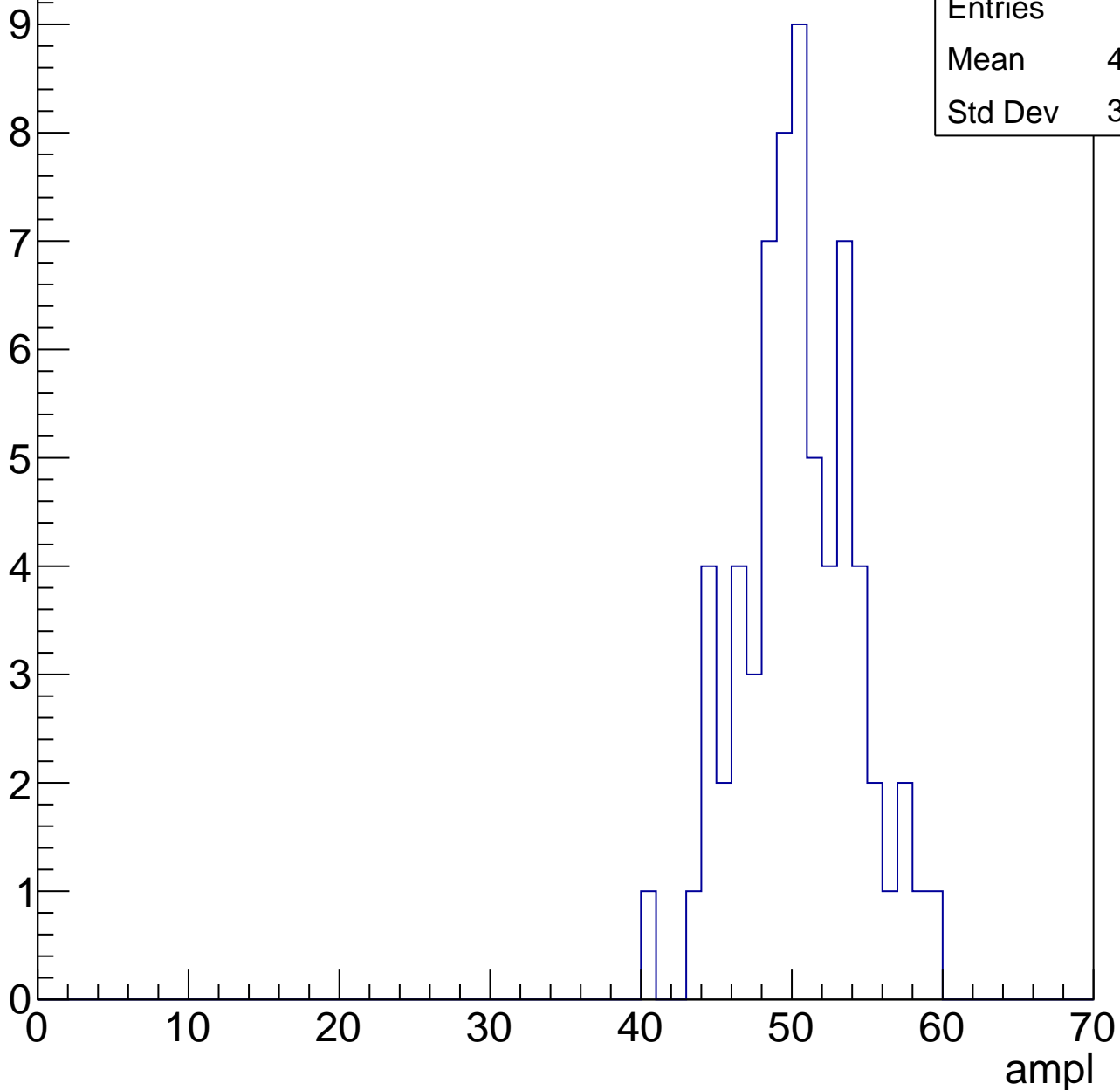


B0L102S, U12-ch120, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	49.98
Std Dev	3.788

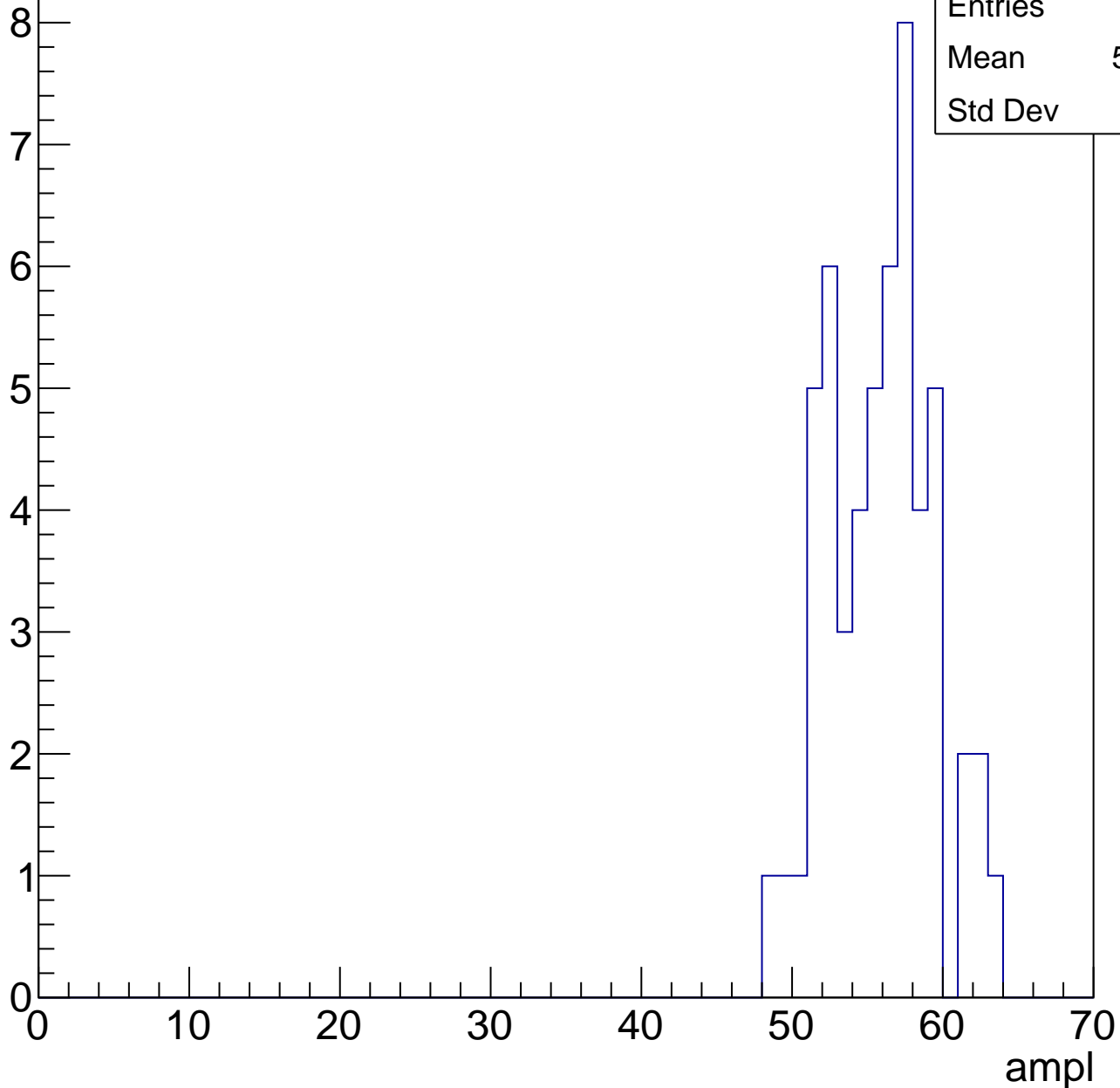


B0L102S, U12-ch120, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	55.41
Std Dev	3.44

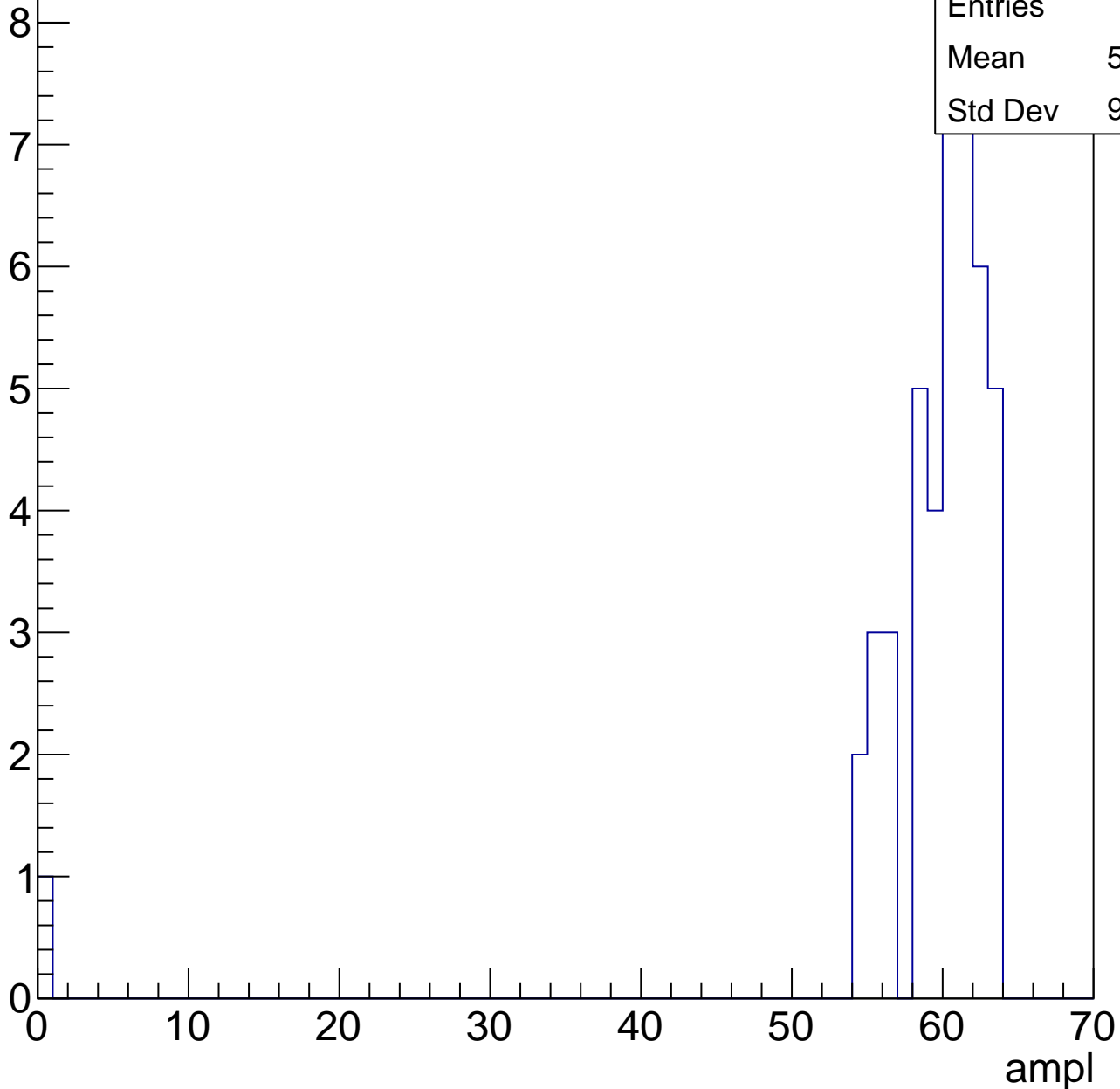


B0L102S, U12-ch120, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

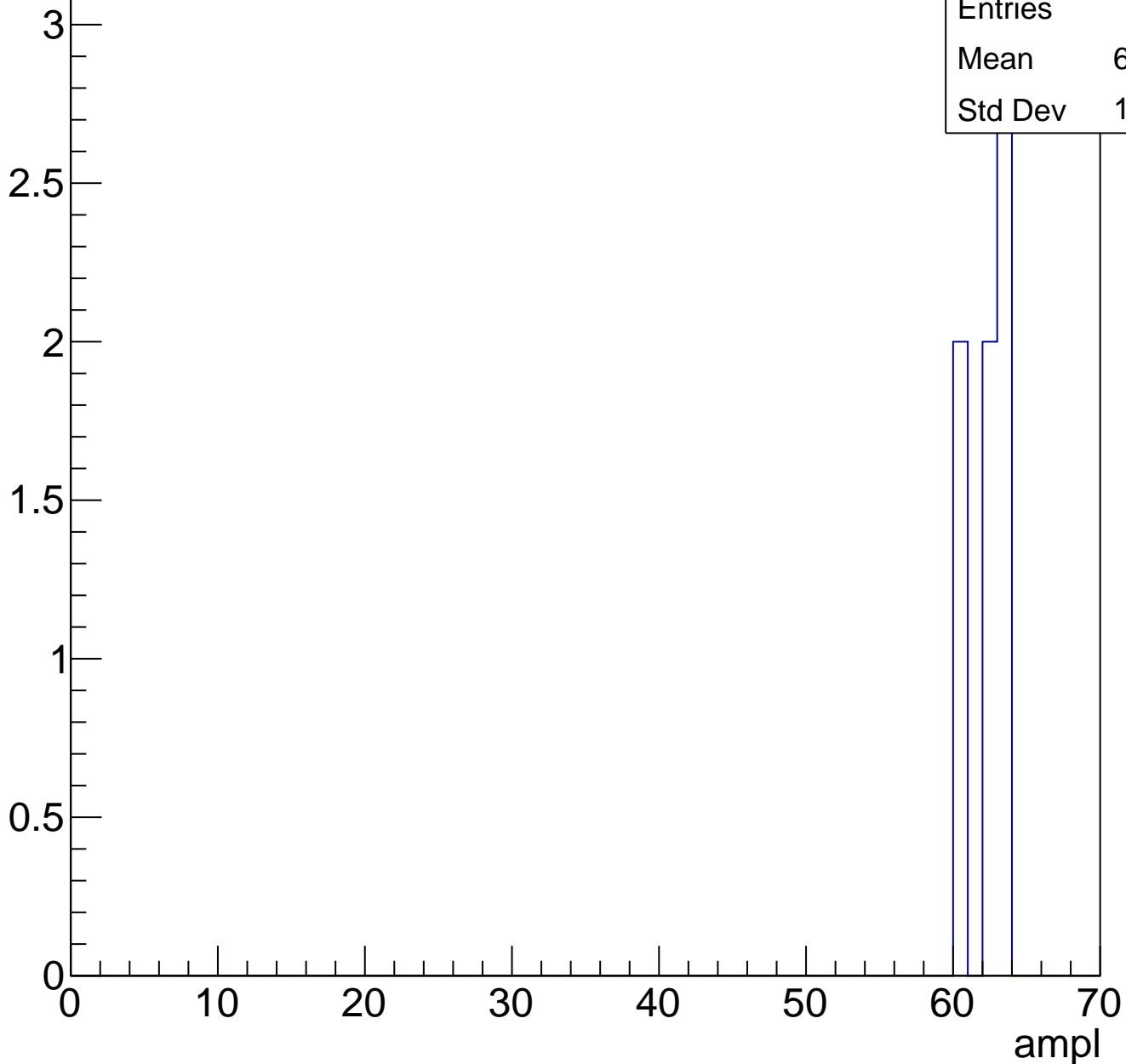
Entries	45
Mean	58.27
Std Dev	9.142



B0L102S, U12-ch120, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch120, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch121, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	99
Mean	28.46
Std Dev	5.657

Gaus mean : 29.0360

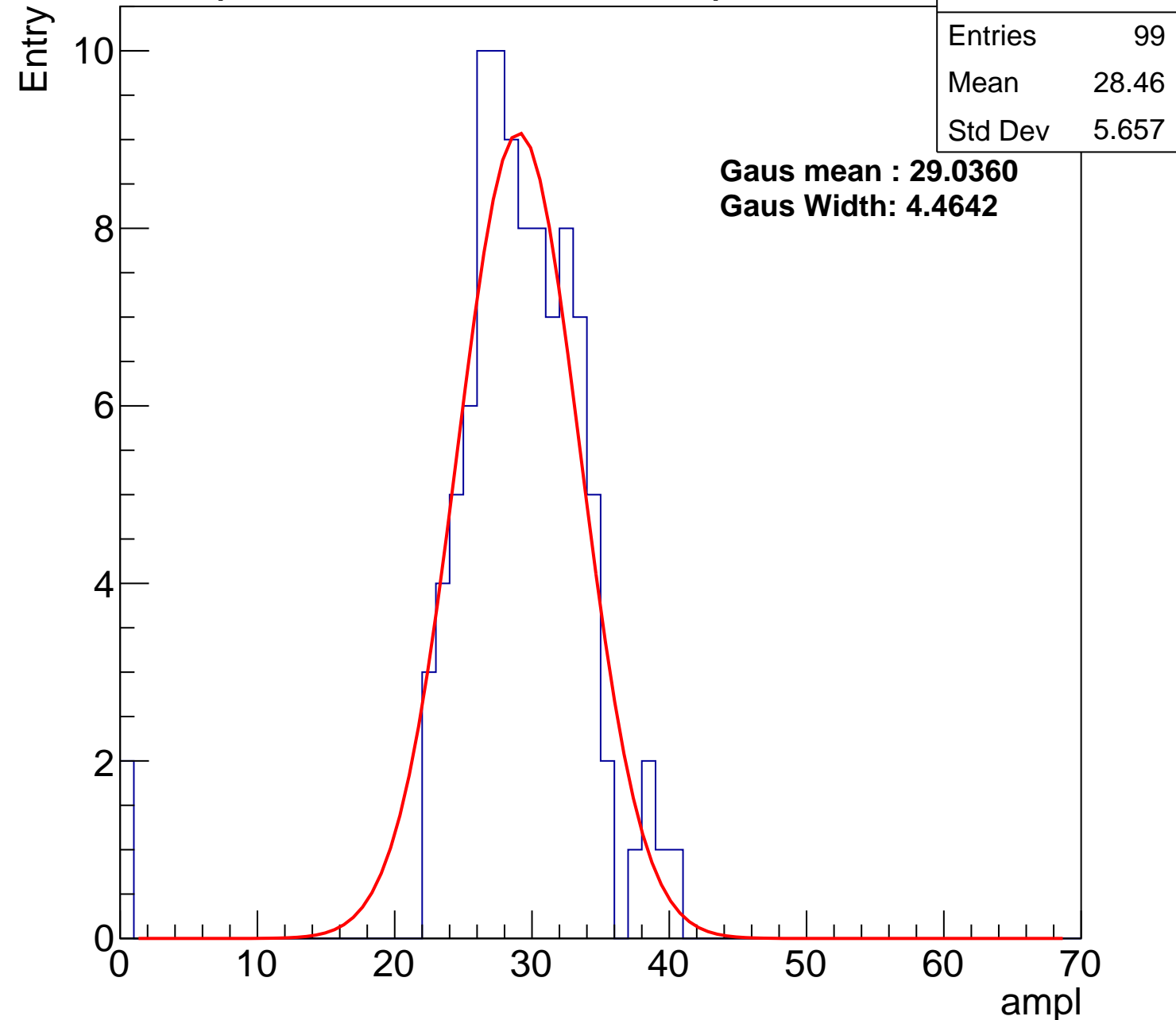
Gaus Width: 4.4642

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch121, adc1

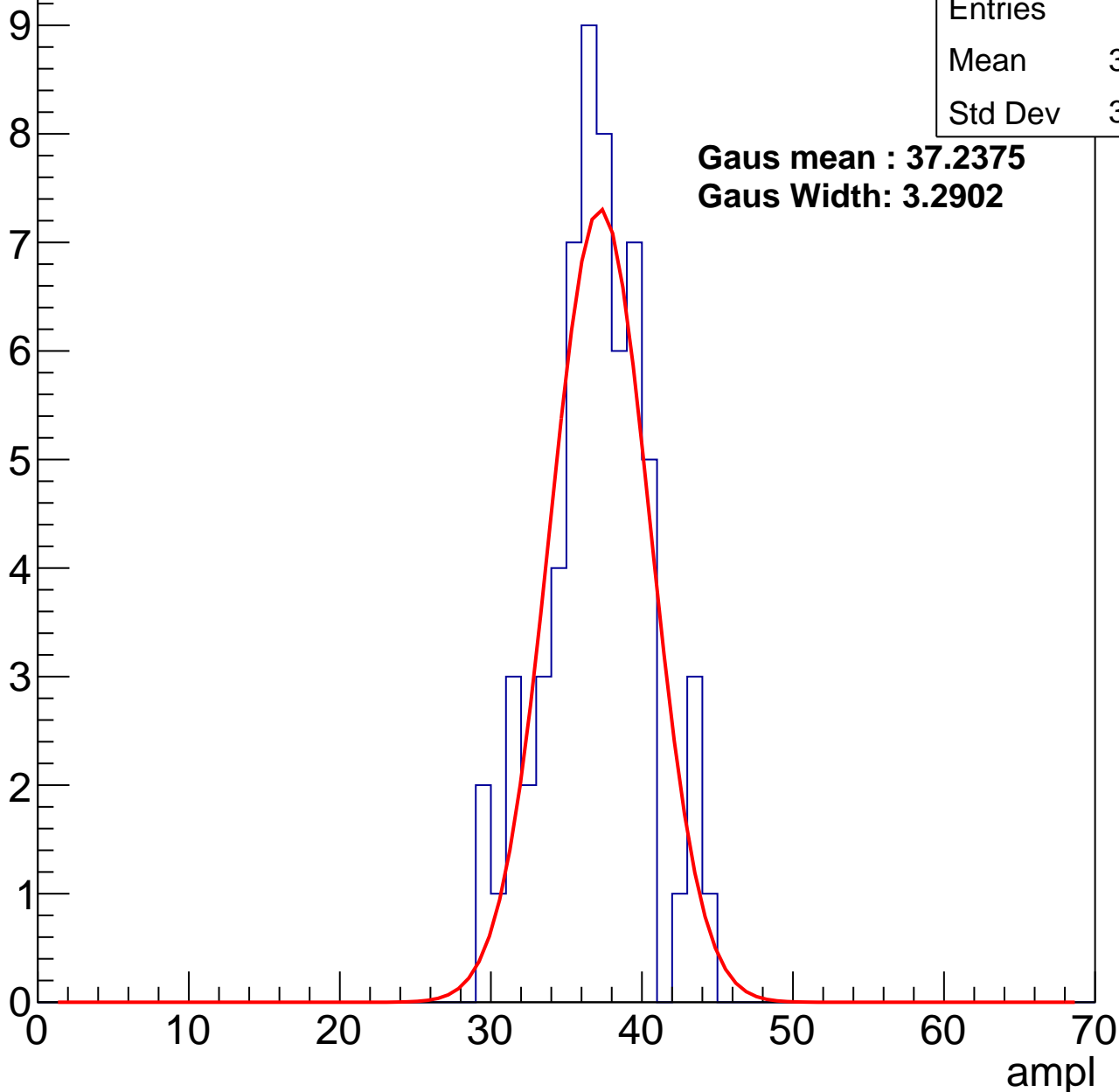
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	36.47
Std Dev	3.359

Gaus mean : 37.2375

Gaus Width: 3.2902



B0L102S, U12-ch121, adc2

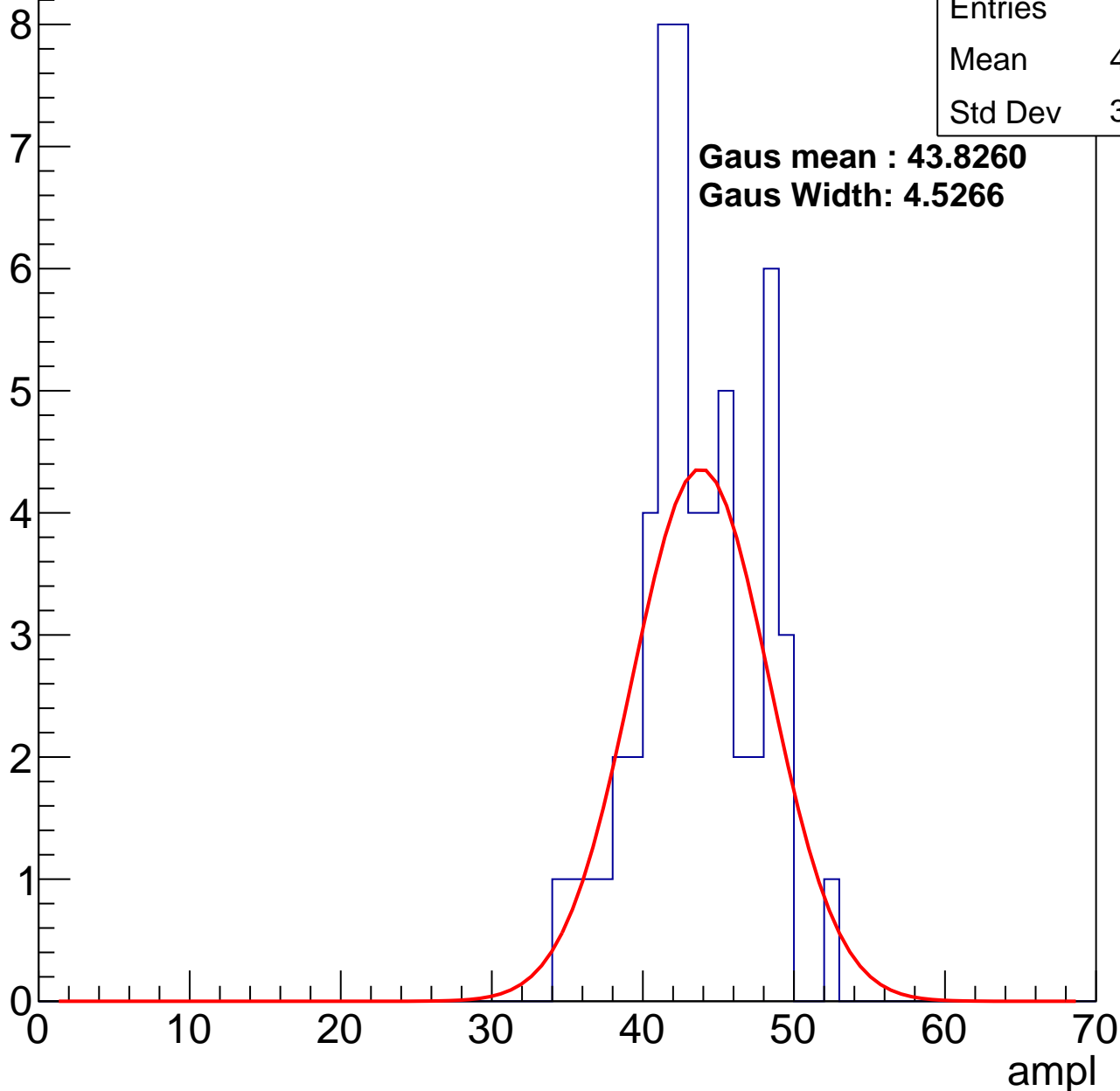
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	43.02
Std Dev	3.816

Gaus mean : 43.8260

Gaus Width: 4.5266

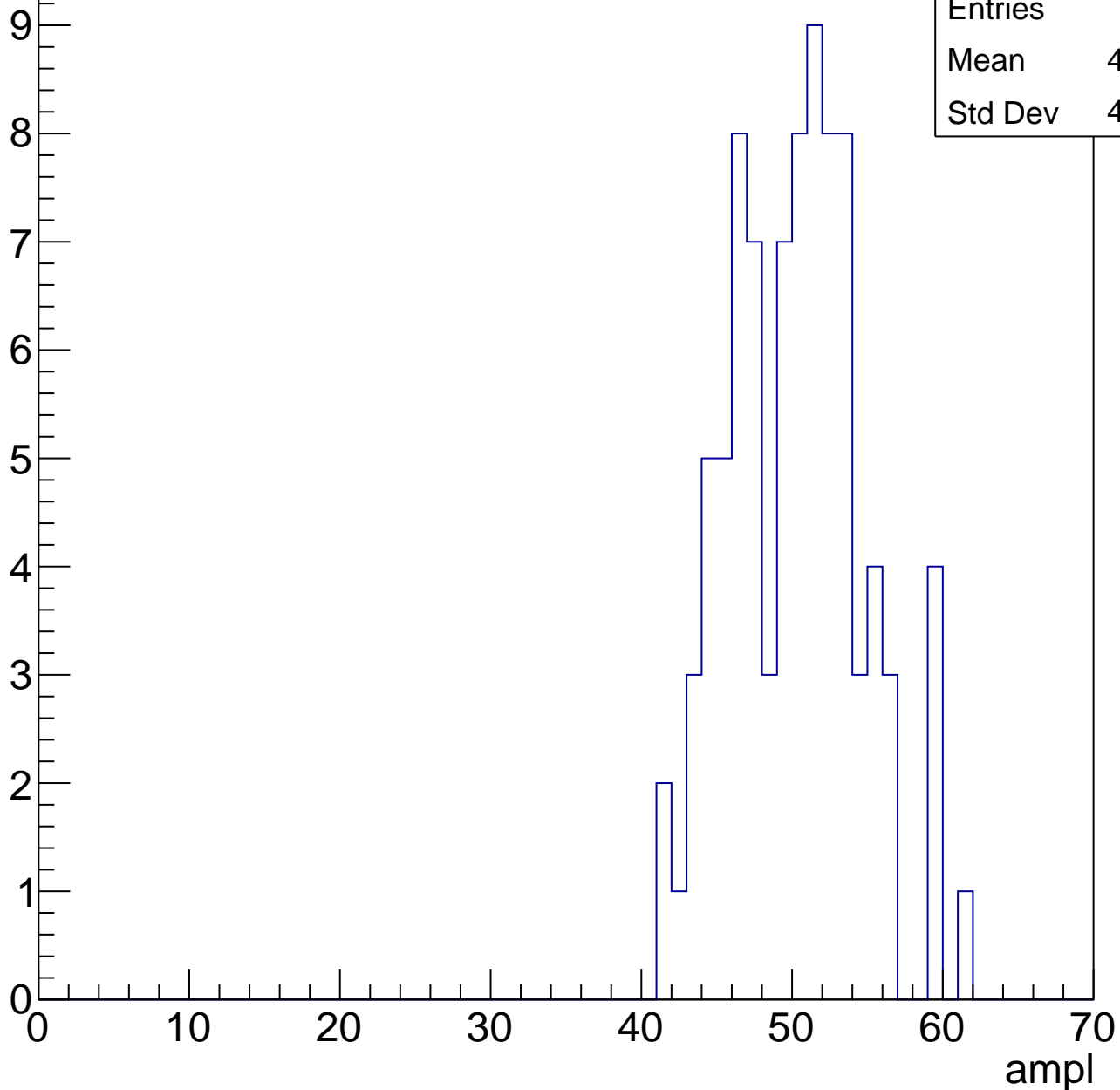


B0L102S, U12-ch121, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	49.75
Std Dev	4.366

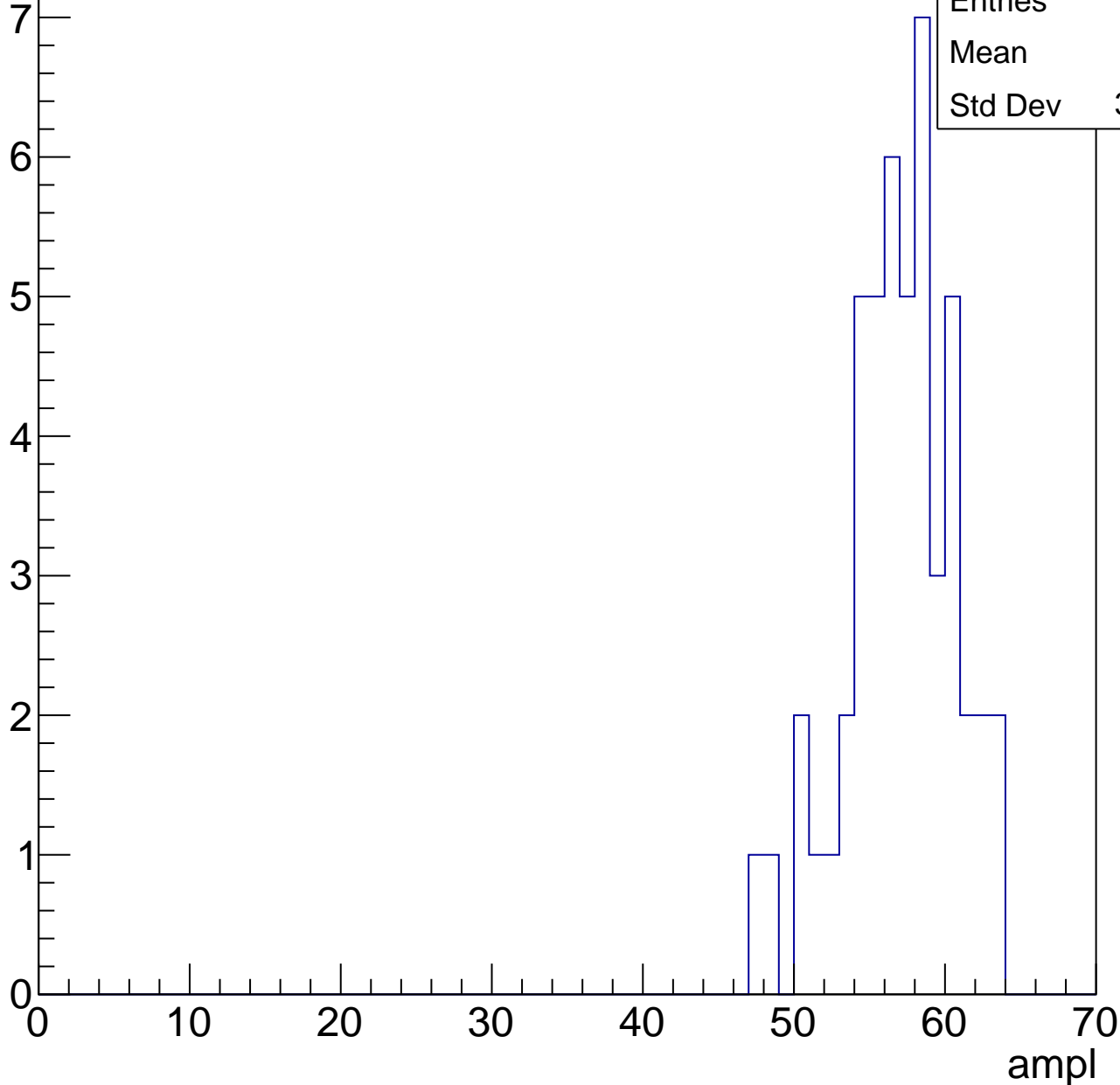


B0L102S, U12-ch121, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	56.5
Std Dev	3.601

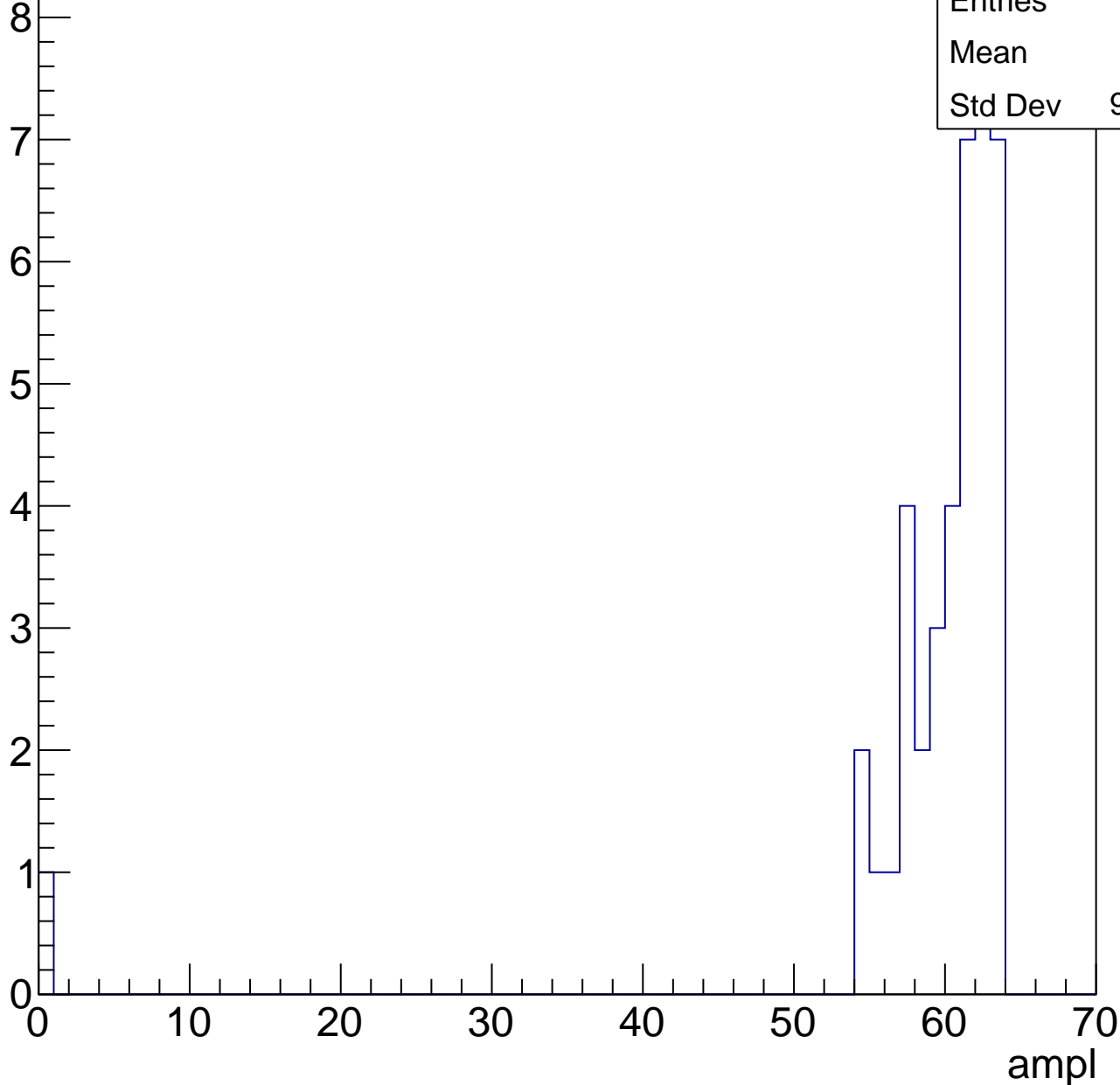


B0L102S, U12-ch121, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	40
Mean	58.6
Std Dev	9.726



B0L102S, U12-ch121, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	59.5
Std Dev	2.291

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch121, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch122, adc0

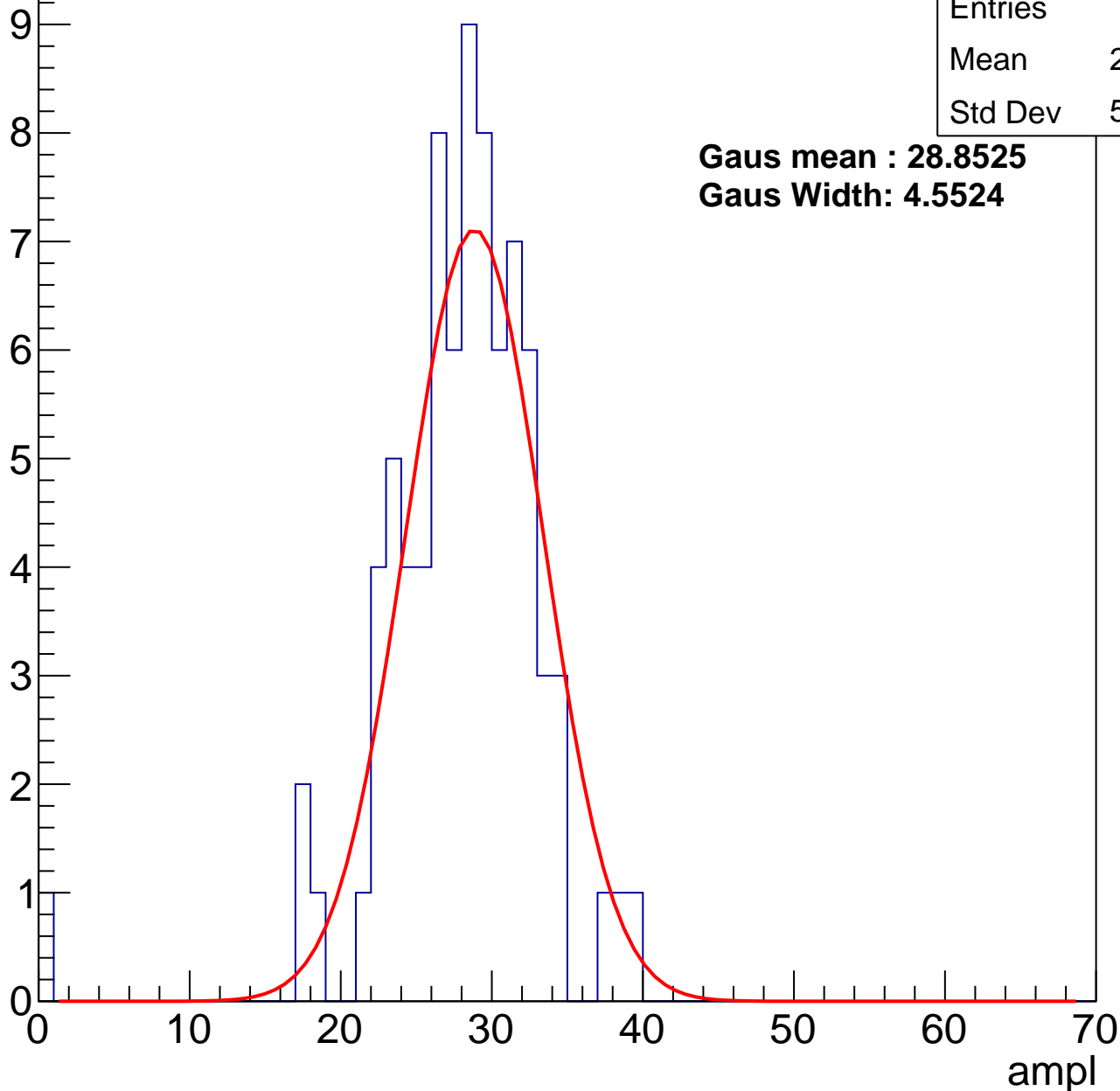
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	27.53
Std Dev	5.259

Gaus mean : 28.8525

Gaus Width: 4.5524



B0L102S, U12-ch122, adc1

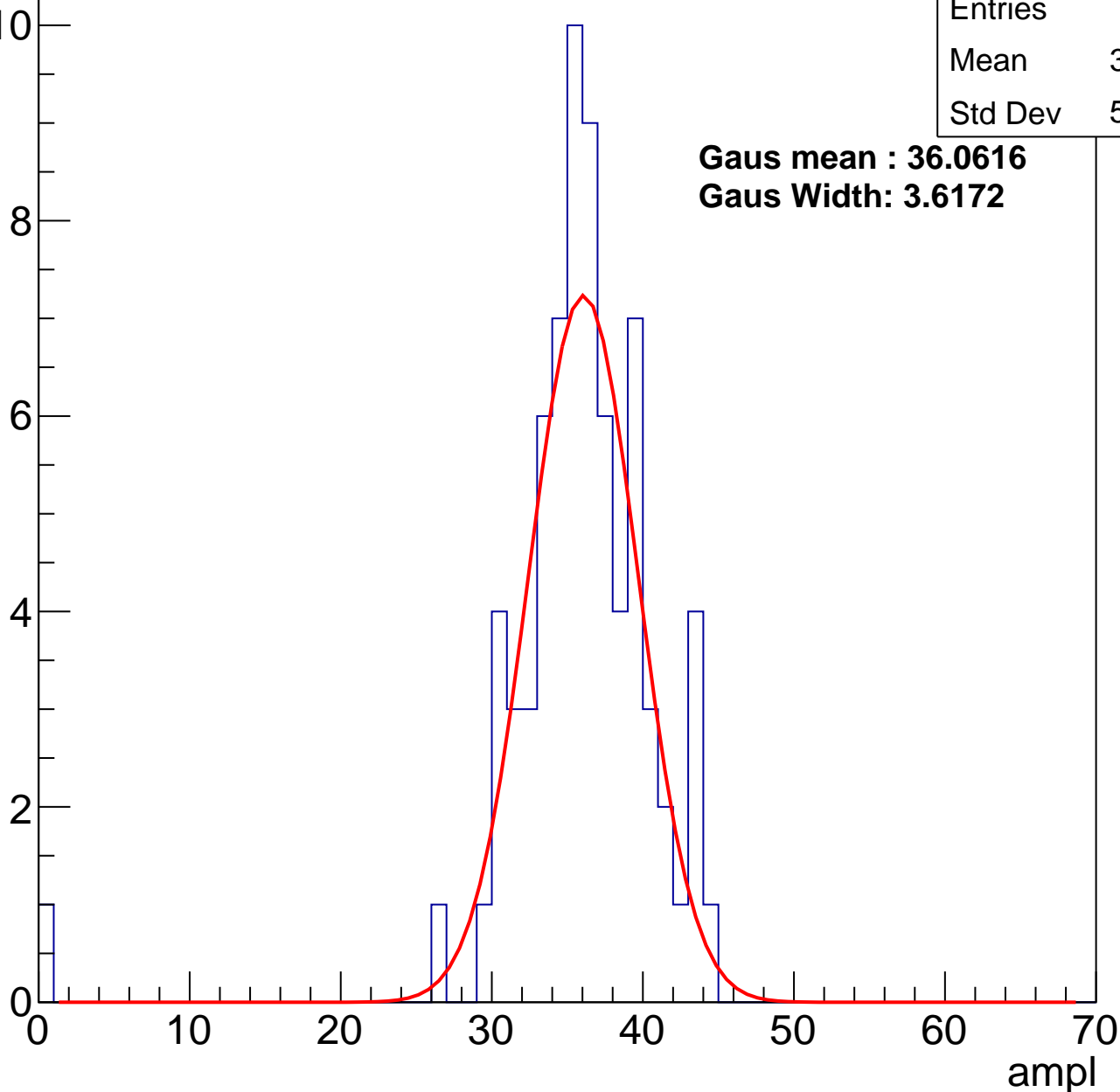
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.36
Std Dev	5.555

Gaus mean : 36.0616

Gaus Width: 3.6172



B0L102S, U12-ch122, adc2

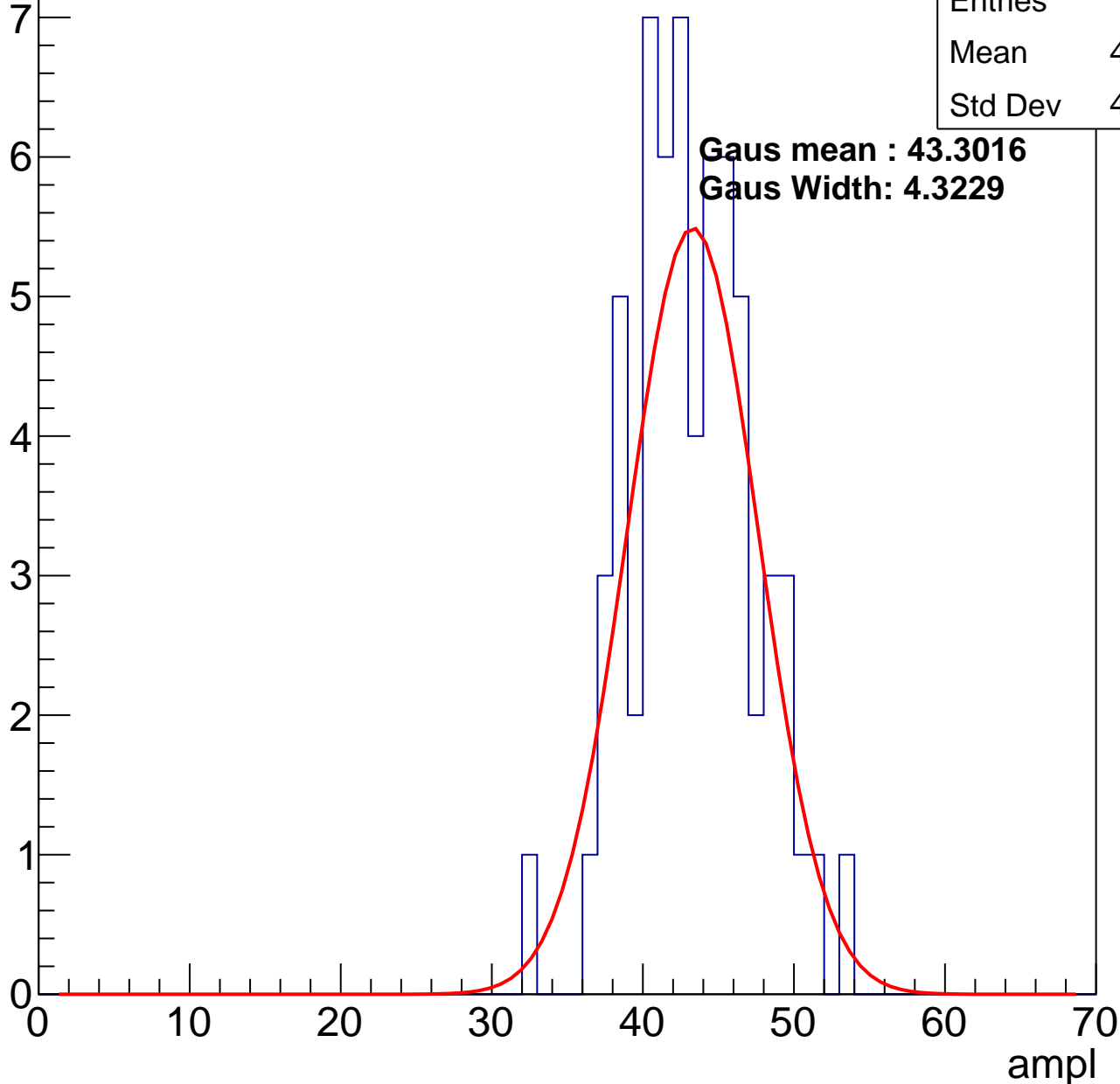
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.84
Std Dev	4.013

Gaus mean : 43.3016

Gaus Width: 4.3229

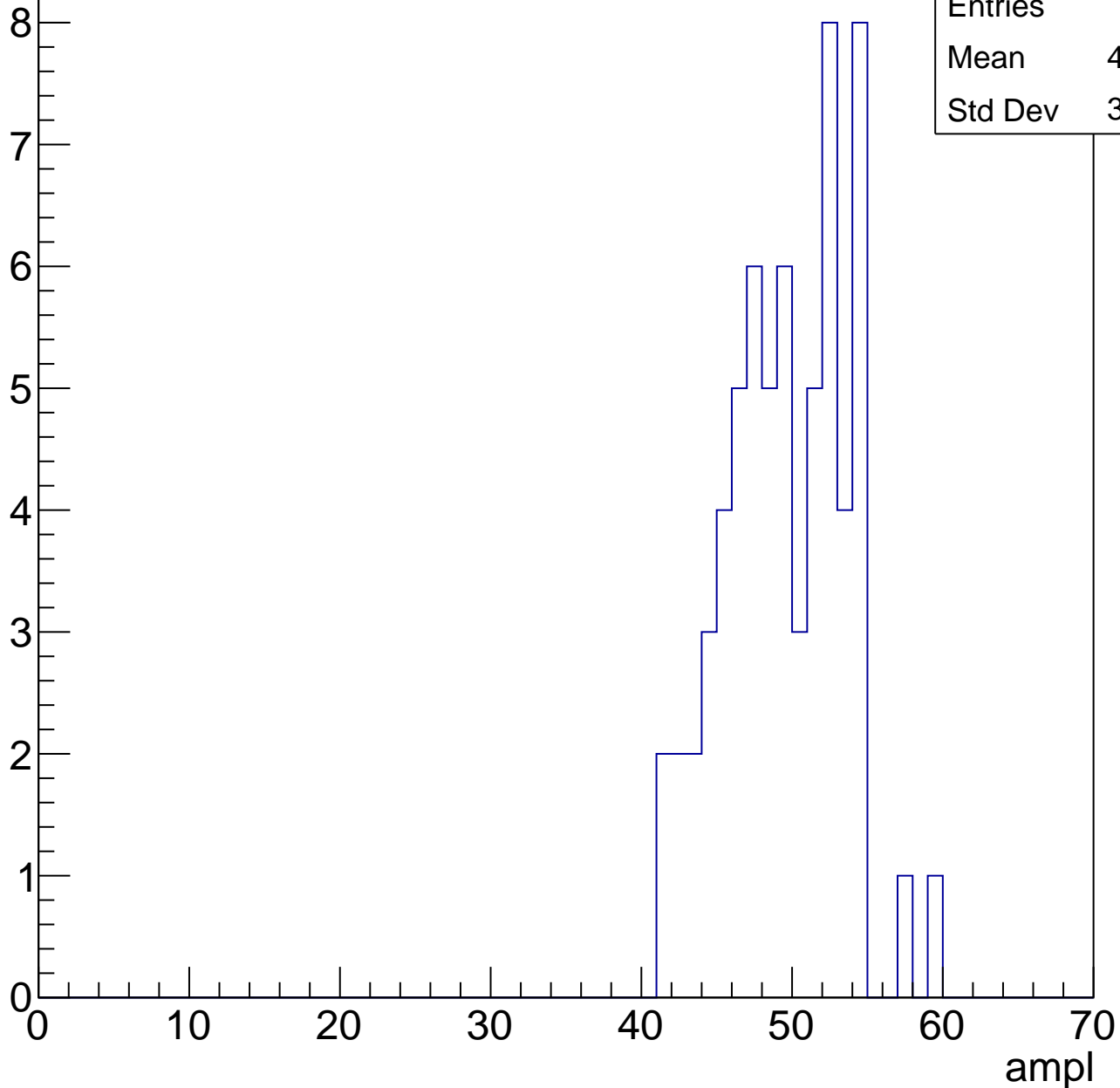


B0L102S, U12-ch122, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.09
Std Dev	3.987

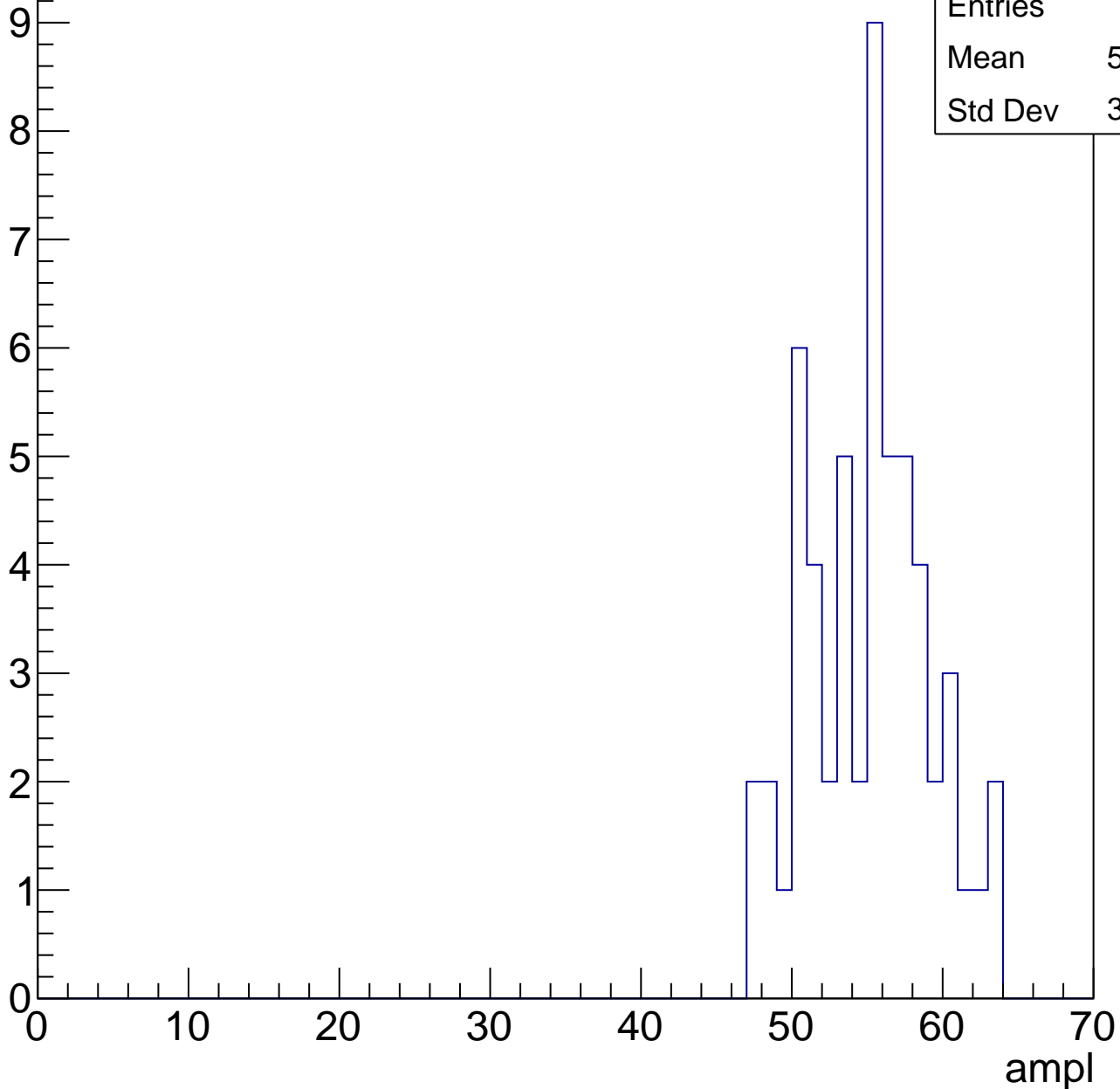


B0L102S, U12-ch122, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

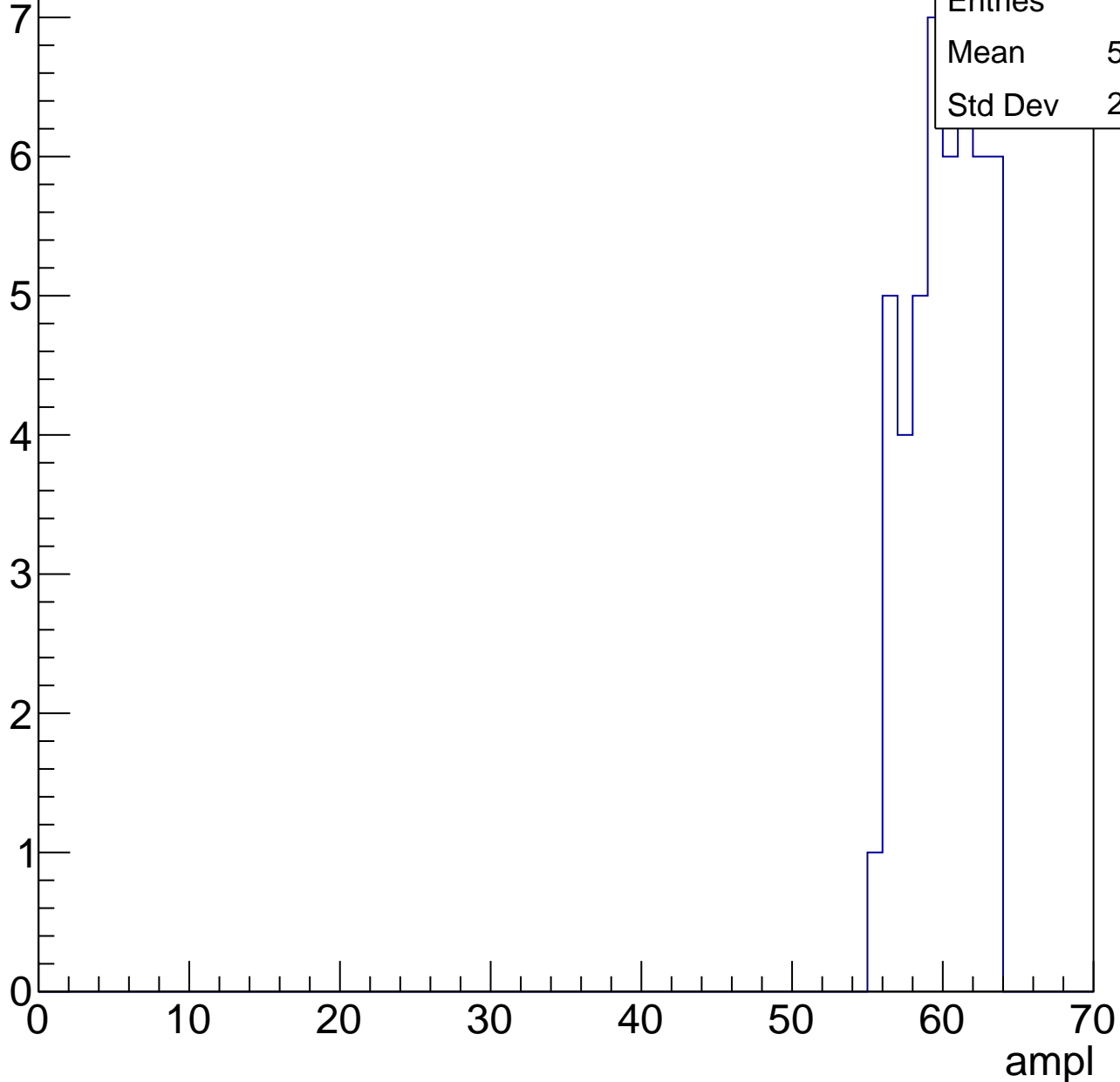
Entries	56
Mean	54.62
Std Dev	3.985



B0L102S, U12-ch122, adc5

calib_packv5_042523_0143.root, FC#12, port B1

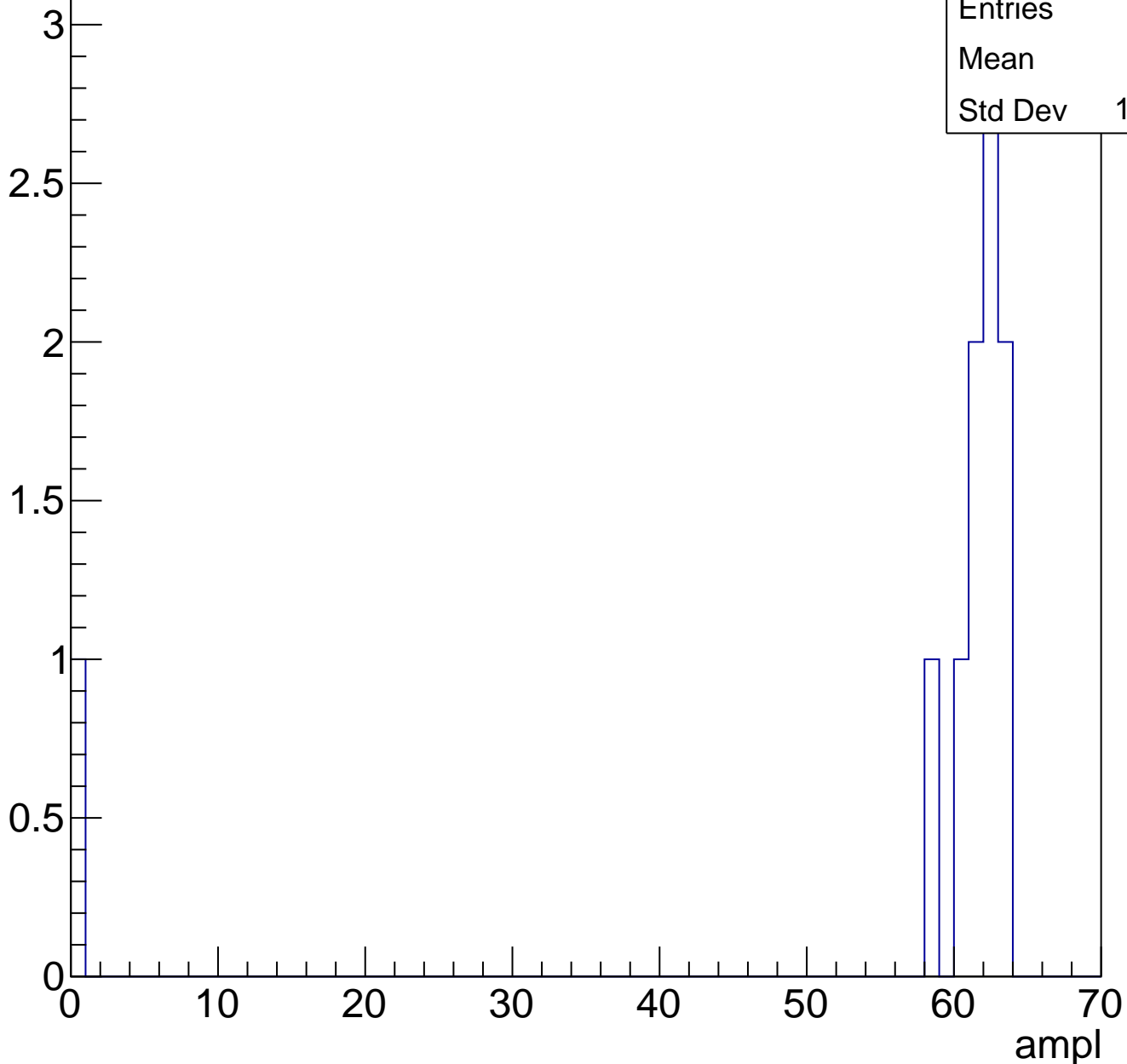
Entry



B0L102S, U12-ch122, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch122, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch123, adc0

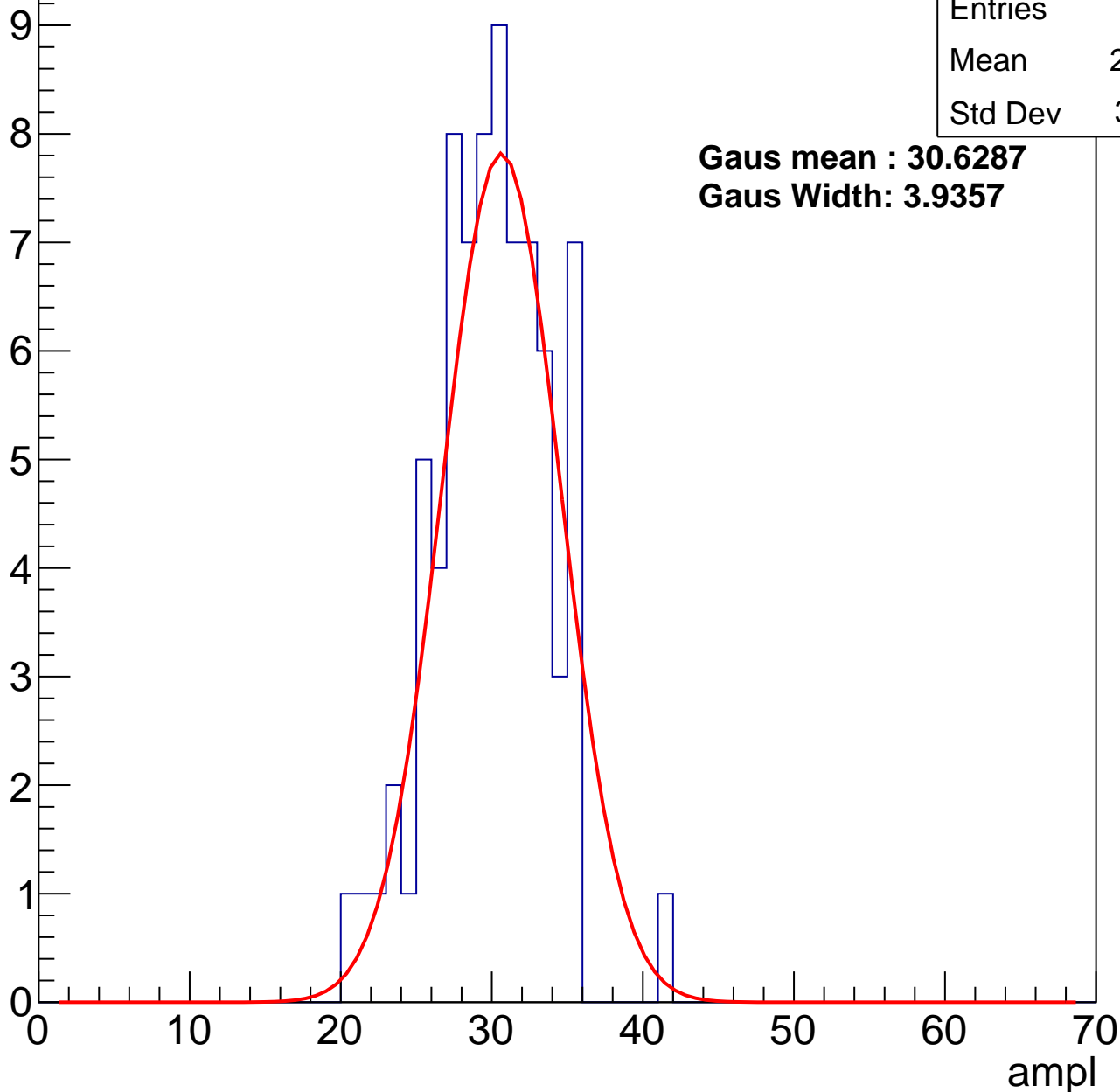
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	29.53
Std Dev	3.741

Gaus mean : 30.6287

Gaus Width: 3.9357



B0L102S, U12-ch123, adc1

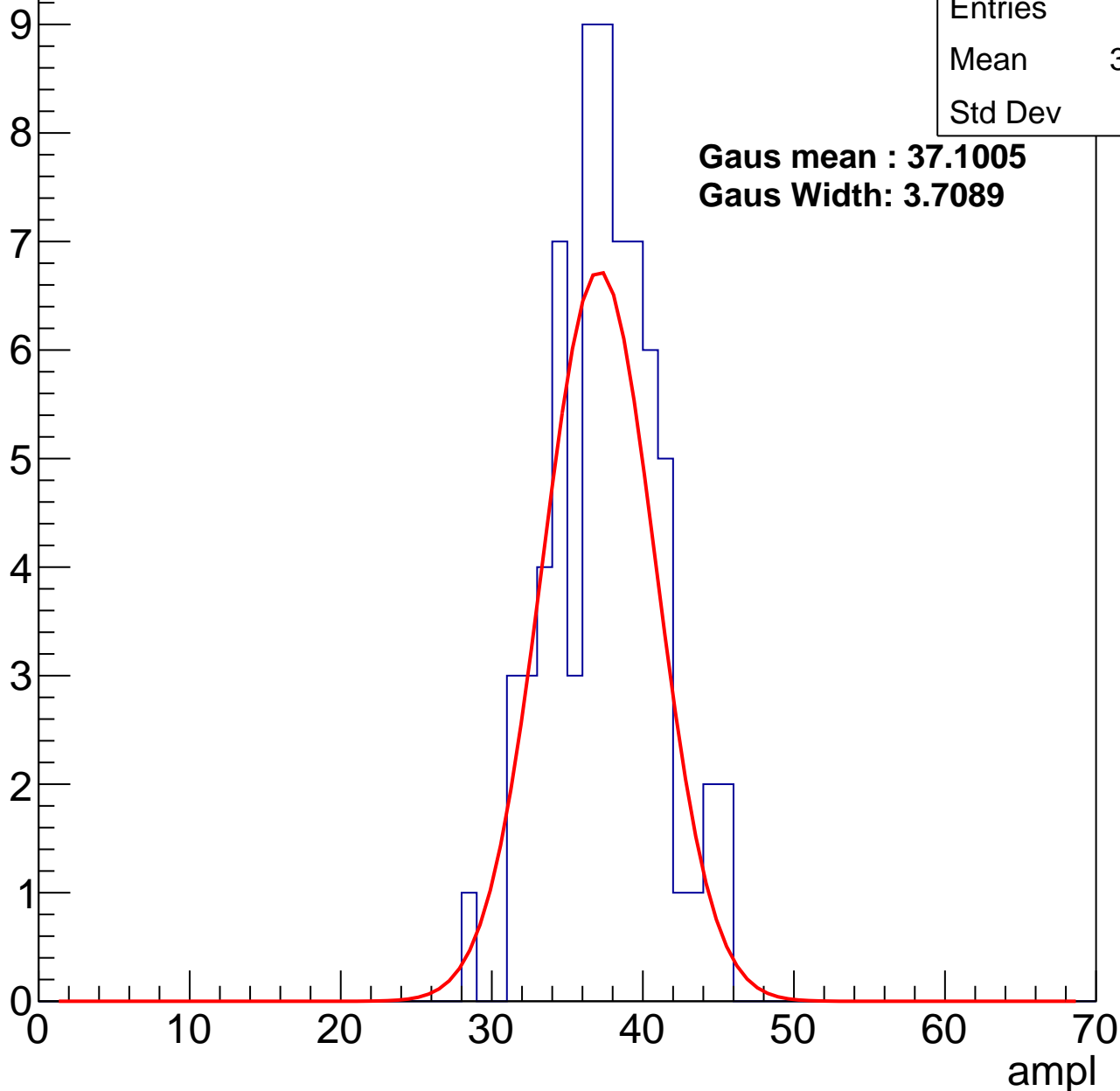
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	37.09
Std Dev	3.52

Gaus mean : 37.1005

Gaus Width: 3.7089



B0L102S, U12-ch123, adc2

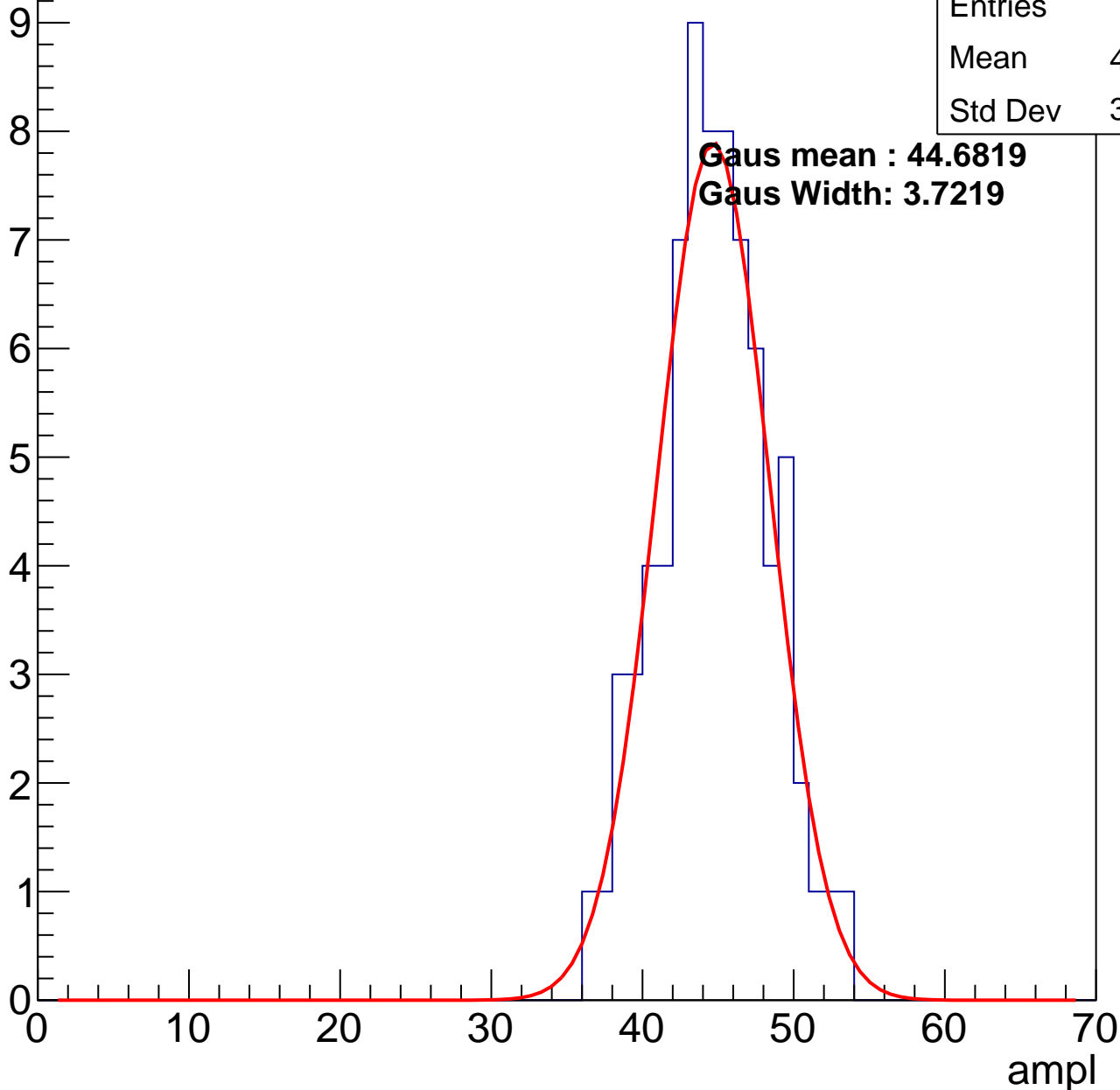
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	44.24
Std Dev	3.603

Gaus mean : 44.6819

Gaus Width: 3.7219

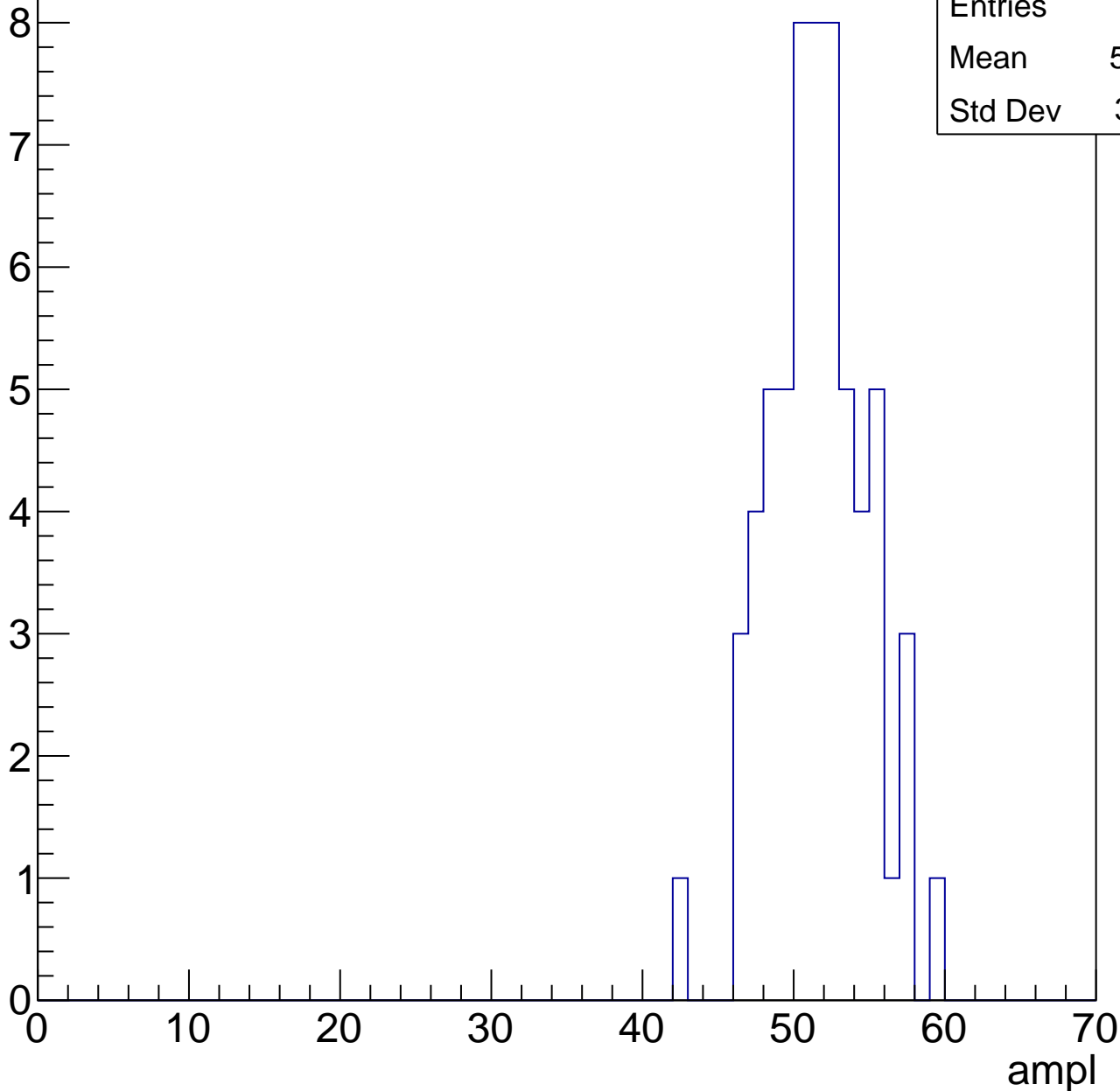


B0L102S, U12-ch123, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	51.13
Std Dev	3.221

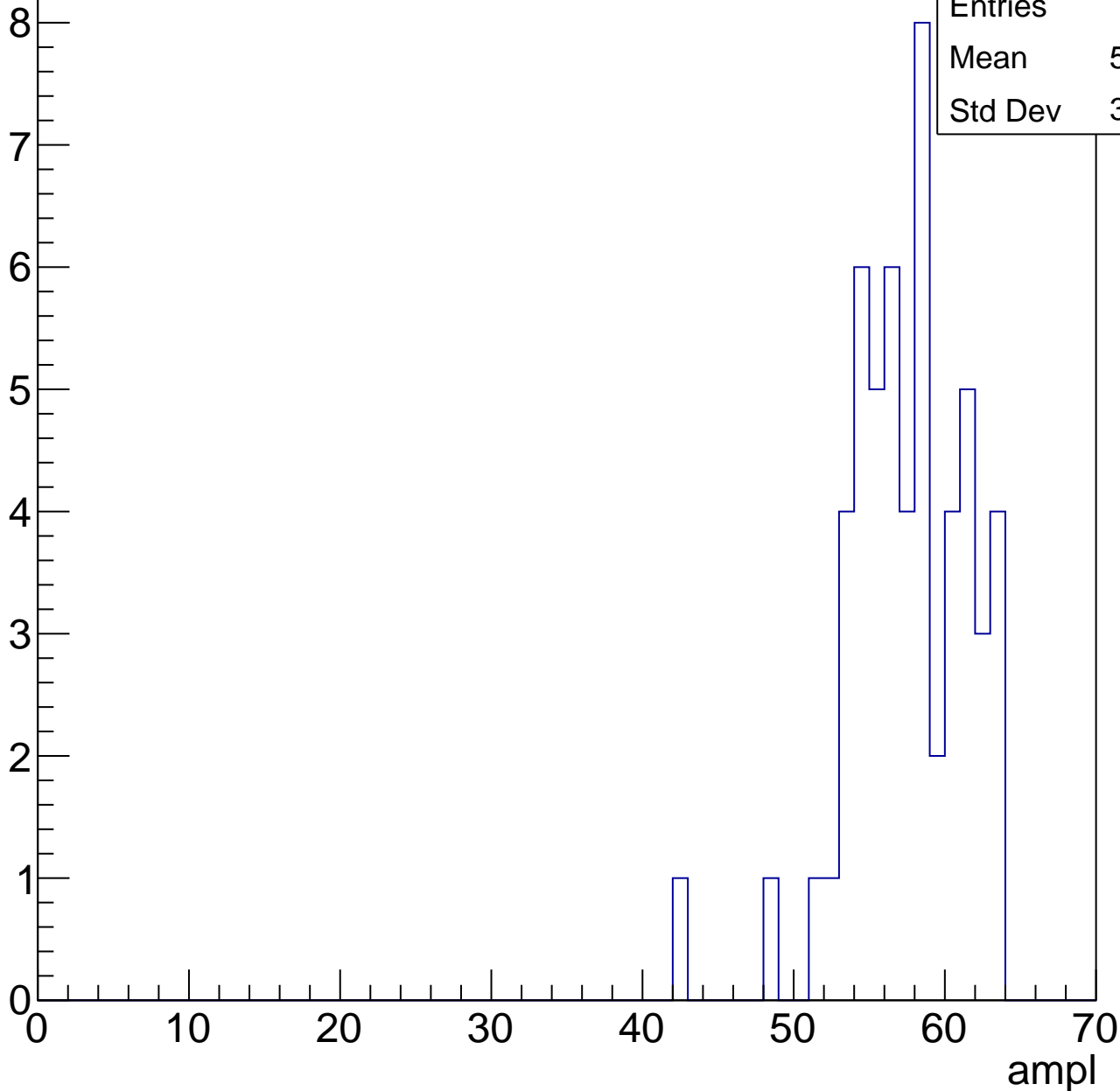


B0L102S, U12-ch123, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

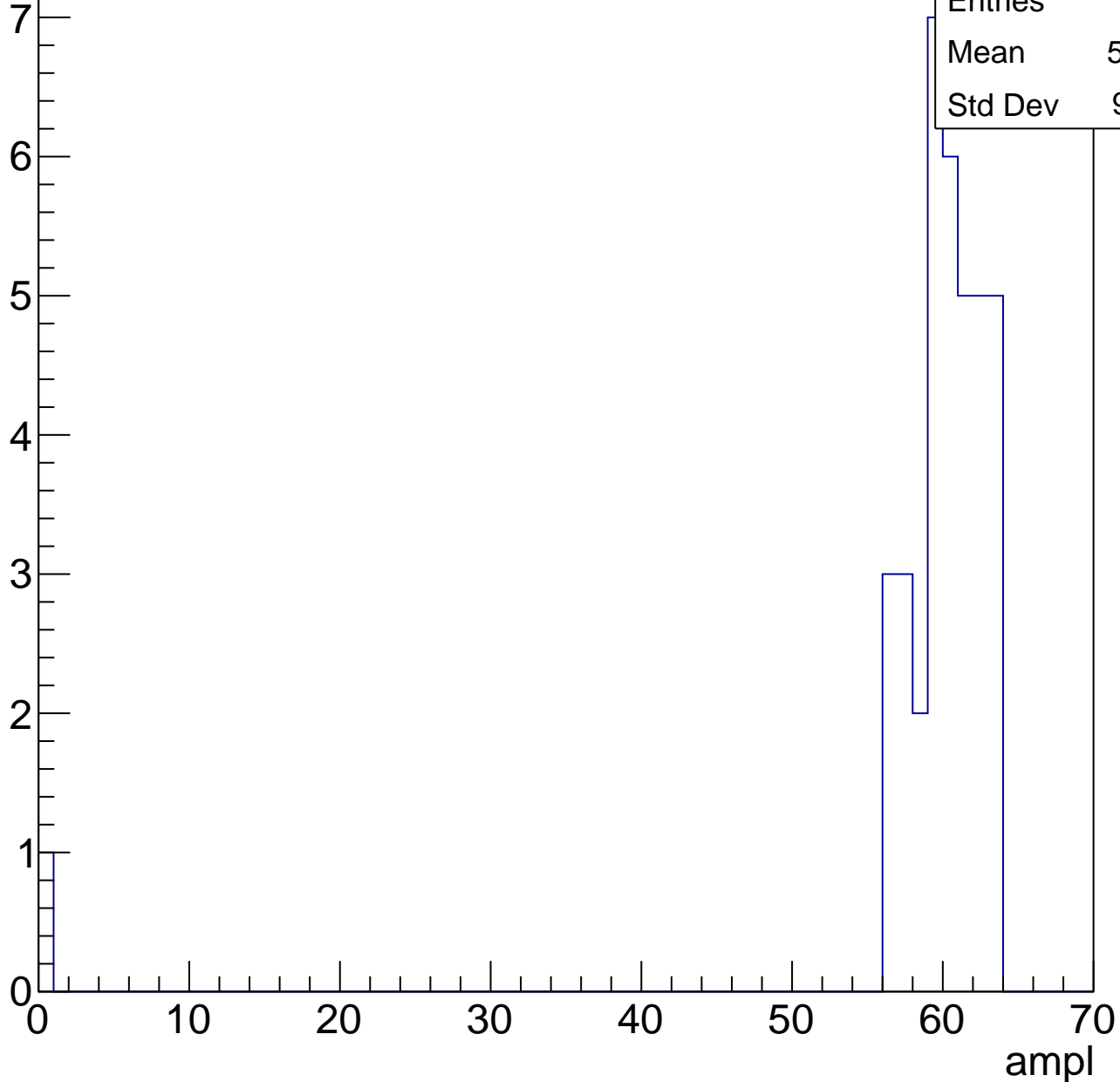
Entries	55
Mean	56.96
Std Dev	3.959



B0L102S, U12-ch123, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch123, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L102S, U12-ch123, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch124, adc0

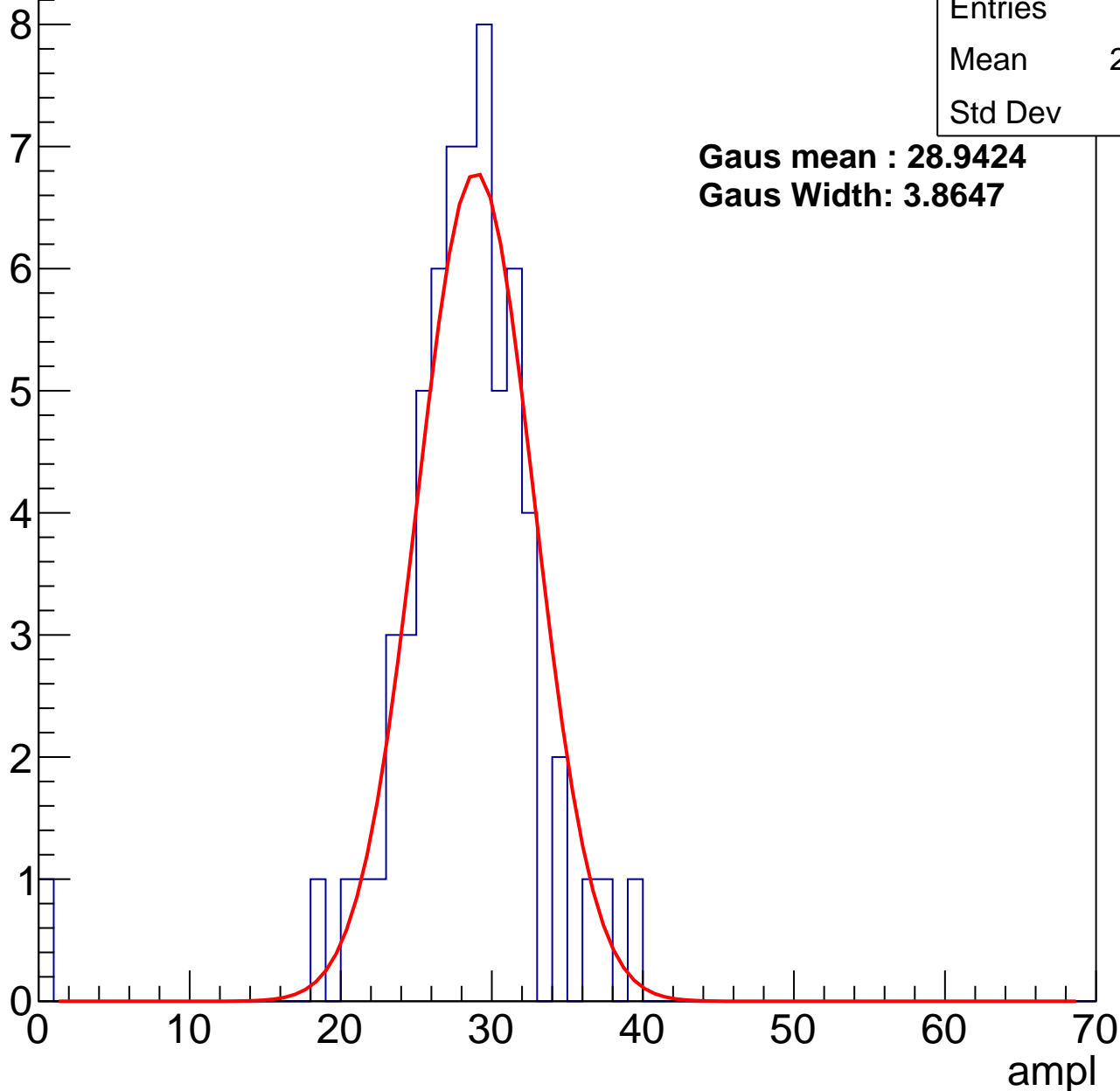
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	27.56
Std Dev	5.16

Gaus mean : 28.9424

Gaus Width: 3.8647



B0L102S, U12-ch124, adc1

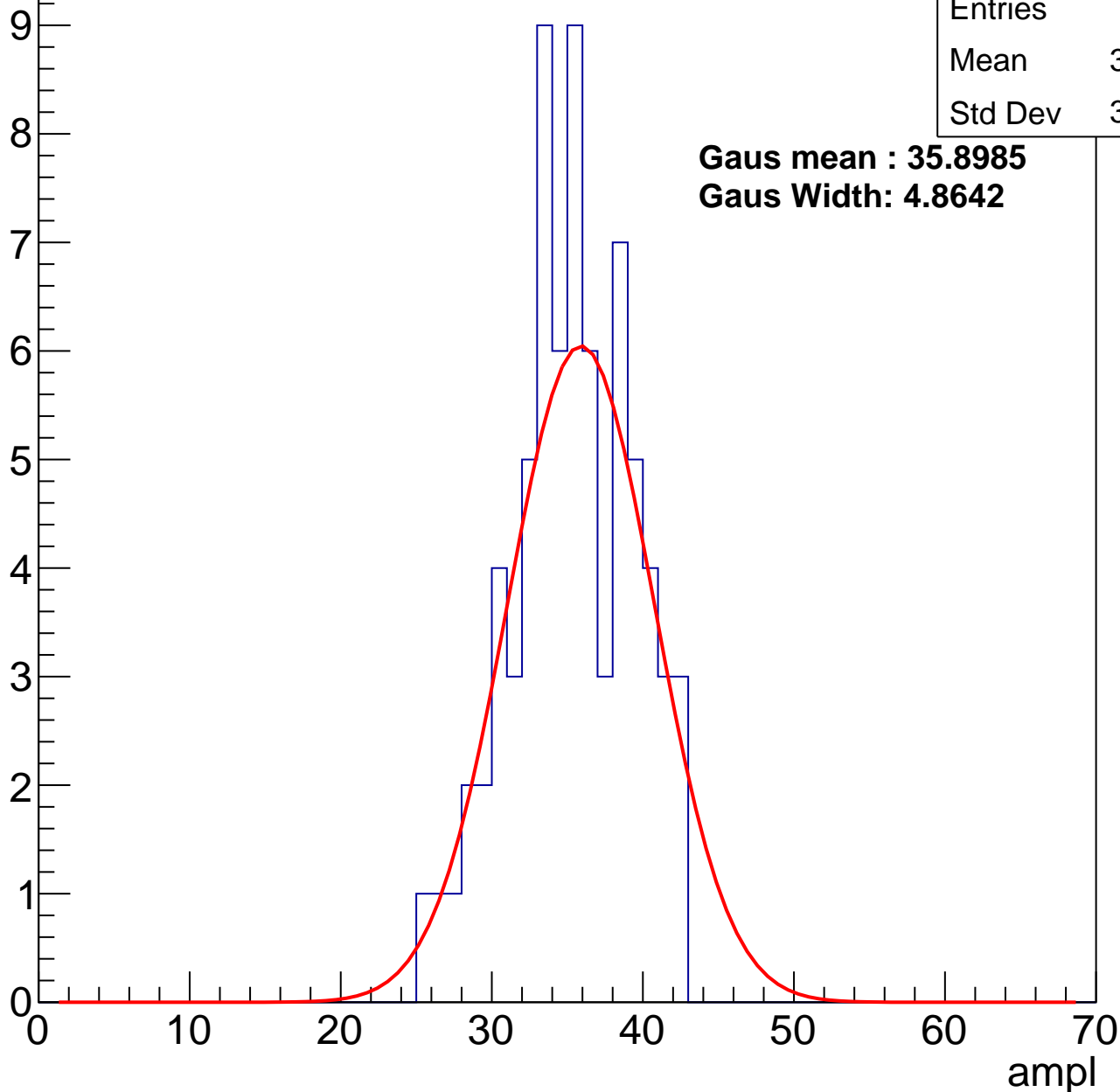
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	34.84
Std Dev	3.966

Gaus mean : 35.8985

Gaus Width: 4.8642



B0L102S, U12-ch124, adc2

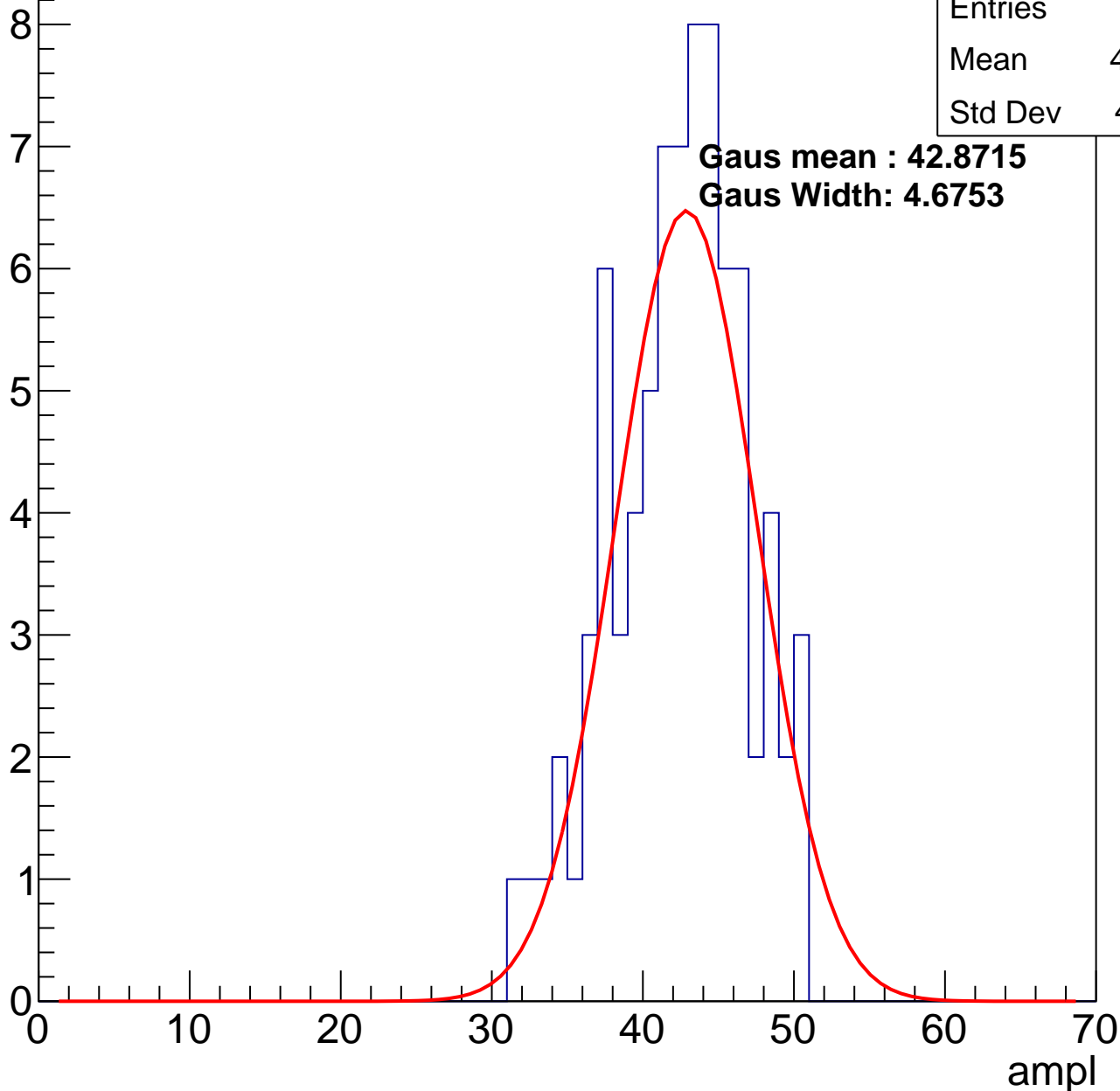
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	41.95
Std Dev	4.361

Gaus mean : 42.8715

Gaus Width: 4.6753

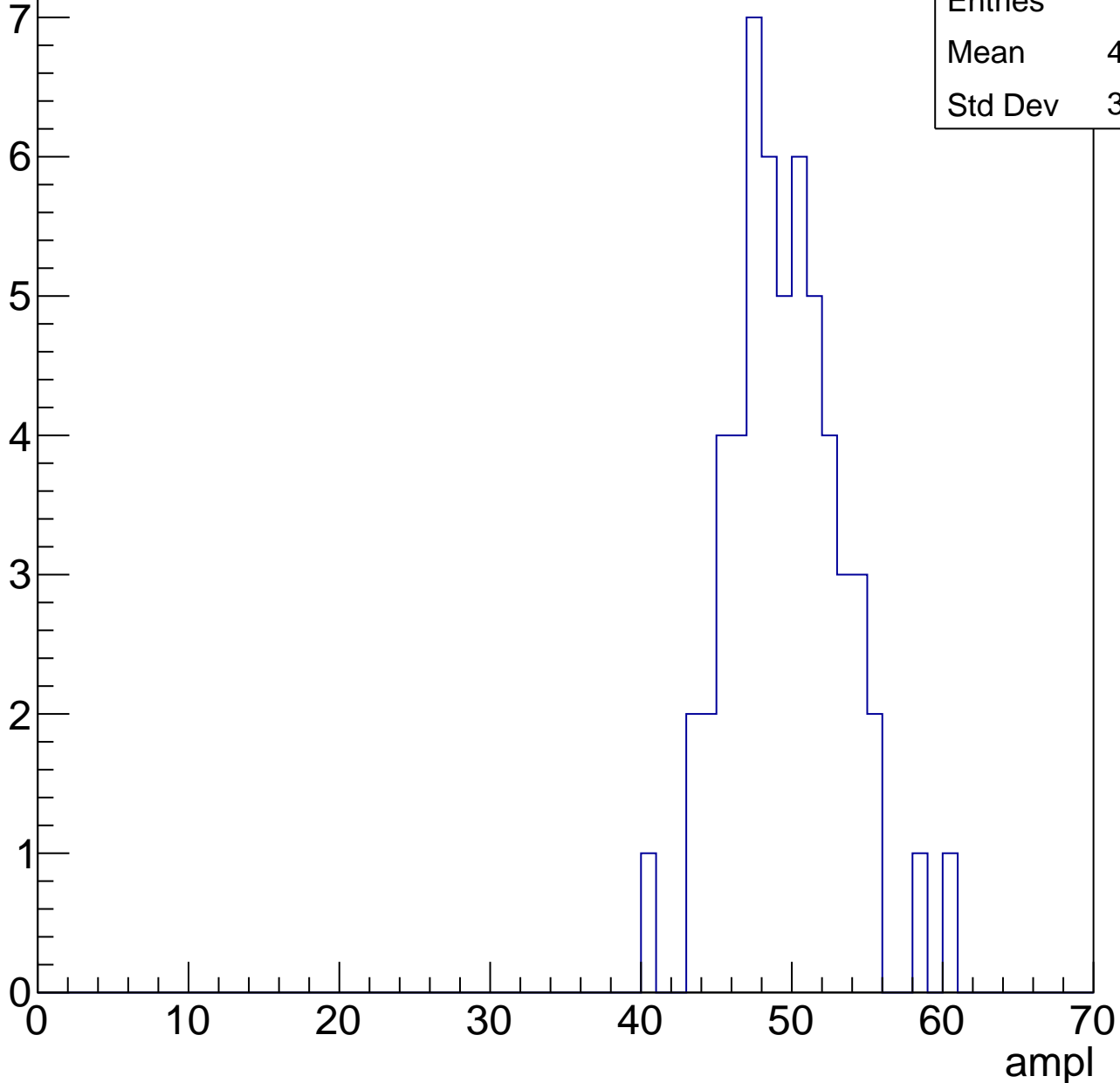


B0L102S, U12-ch124, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	49.14
Std Dev	3.767

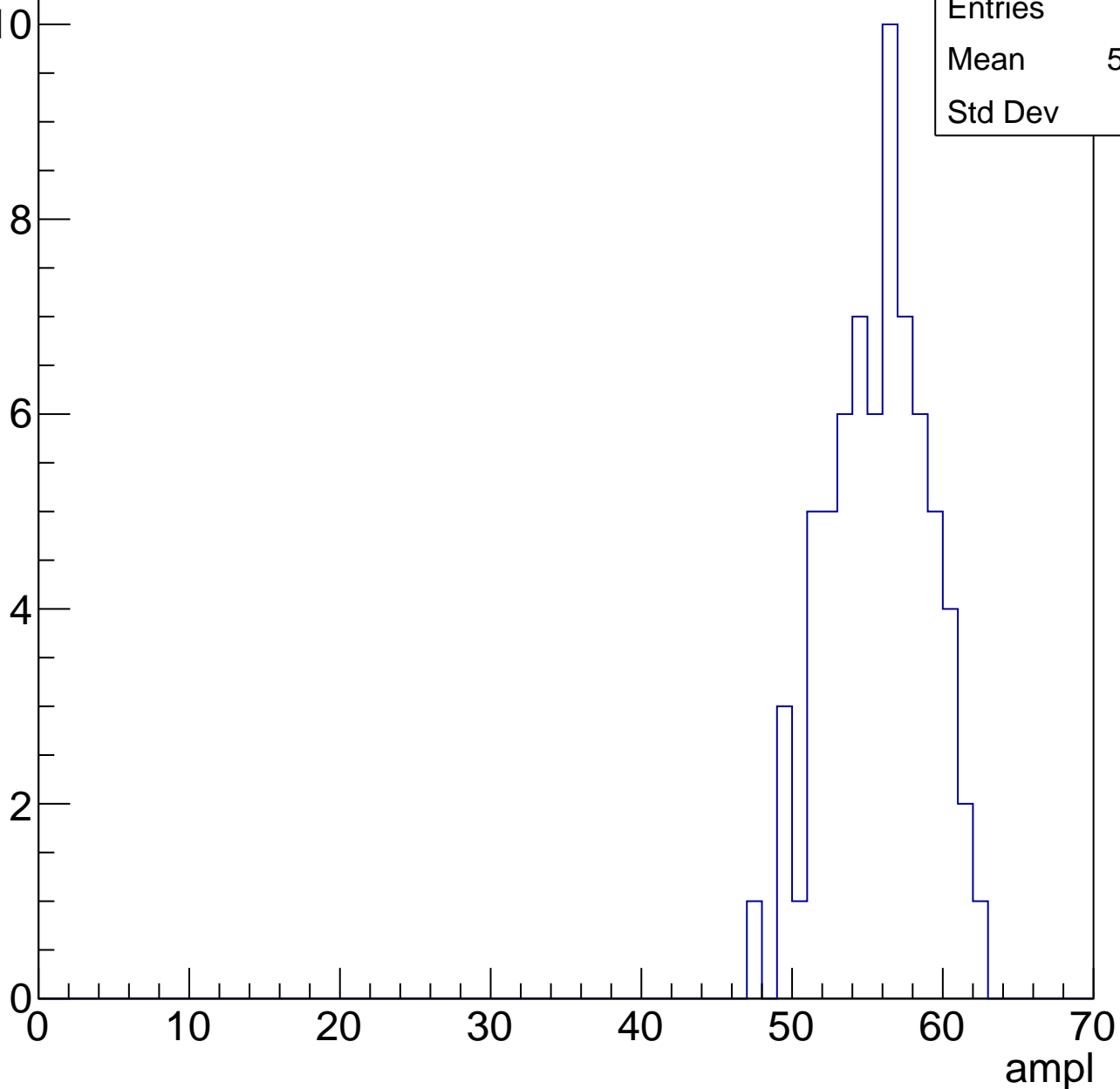


B0L102S, U12-ch124, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	55.23
Std Dev	3.28

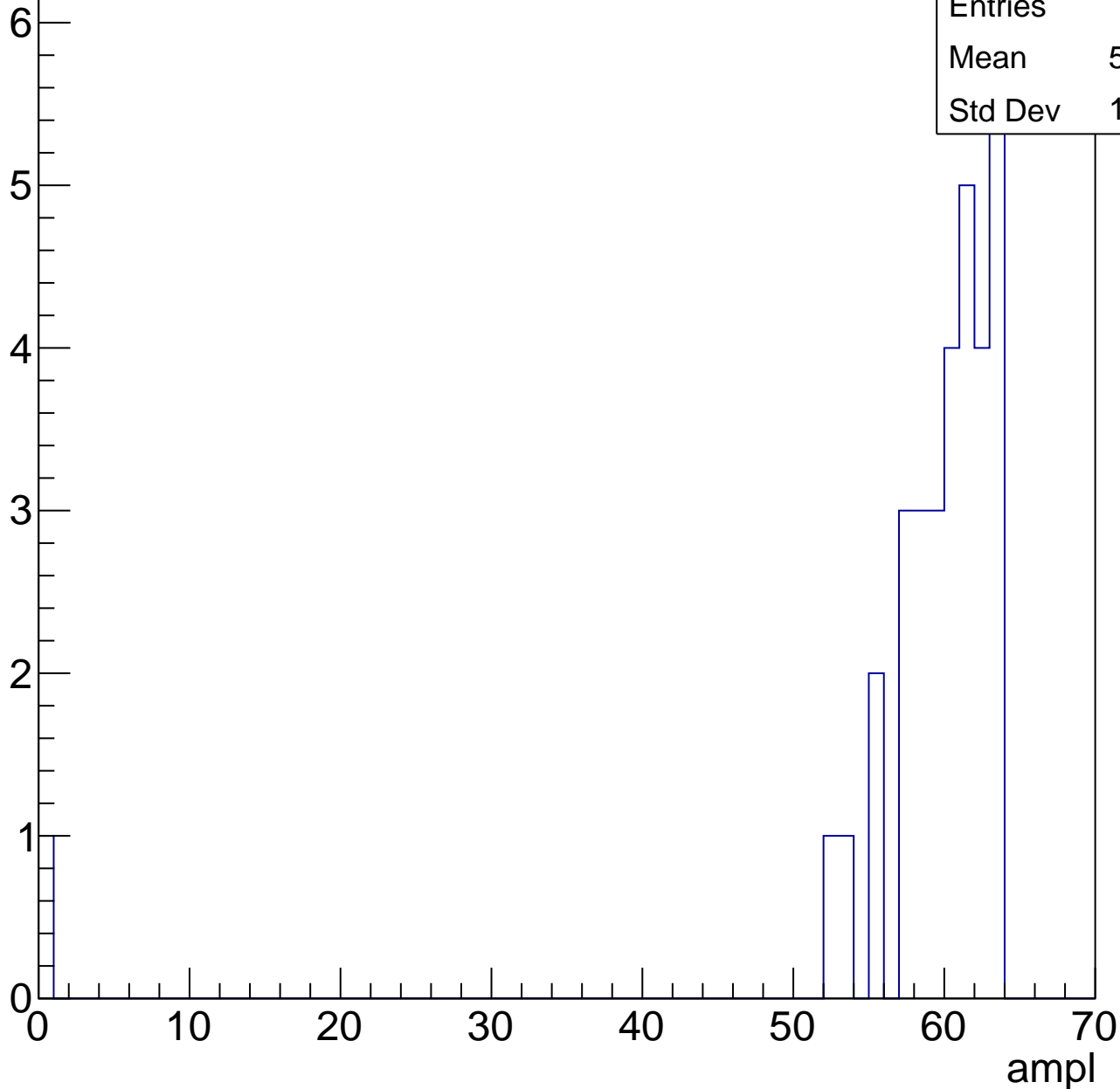


B0L102S, U12-ch124, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	33
Mean	57.82
Std Dev	10.62

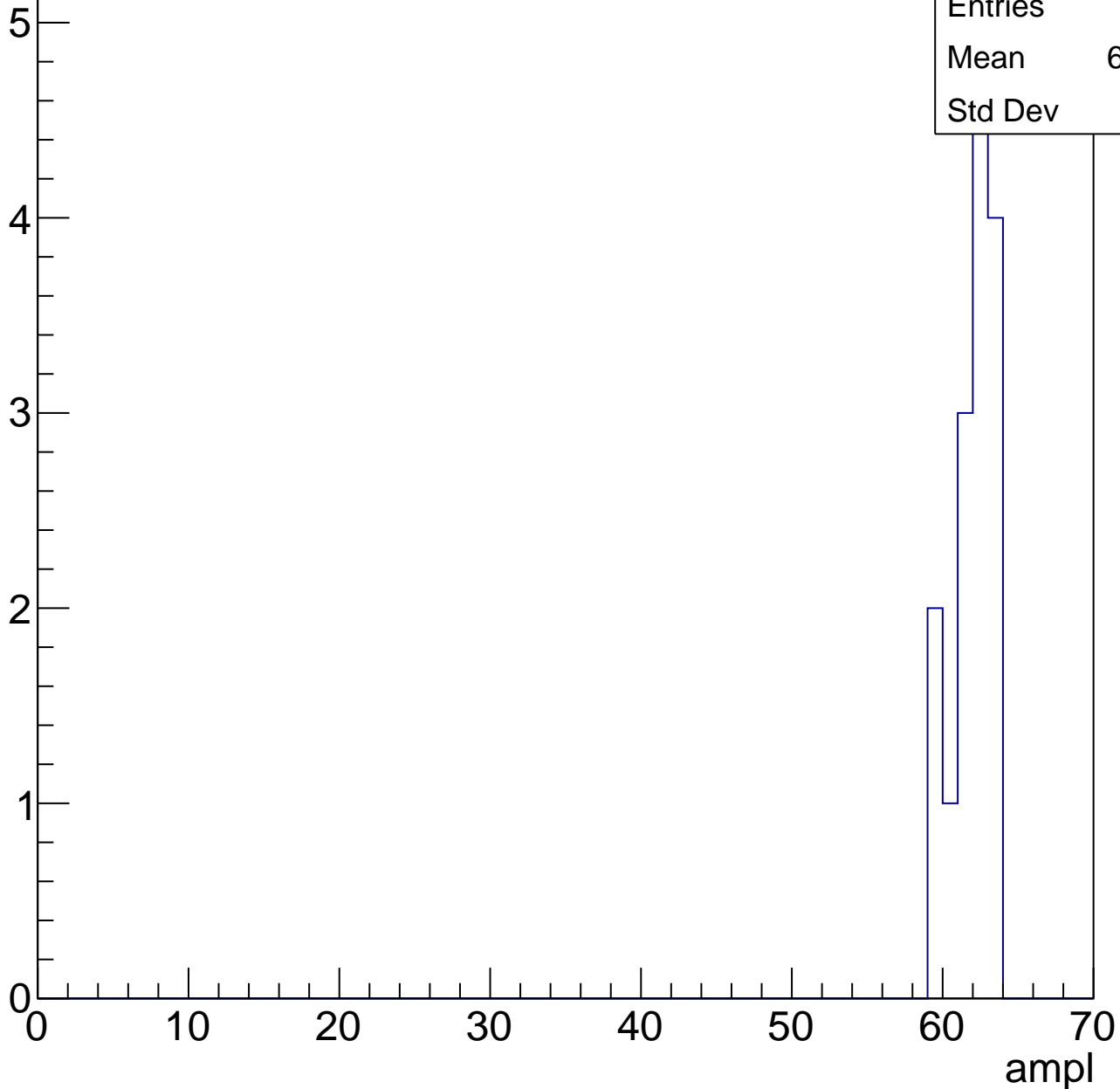


B0L102S, U12-ch124, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.53
Std Dev	1.31



B0L102S, U12-ch124, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U12-ch125, adc0

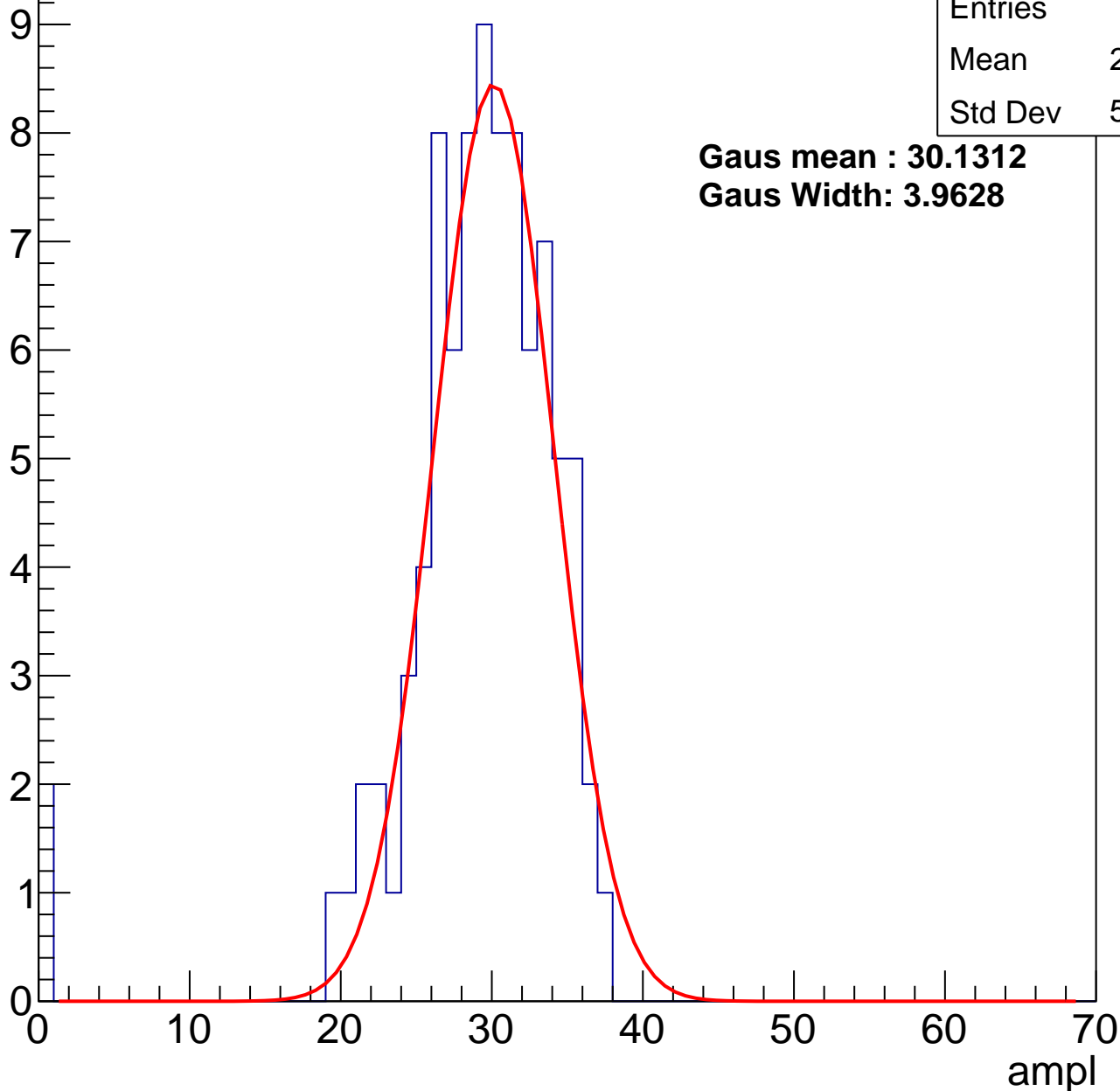
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	28.54
Std Dev	5.827

Gaus mean : 30.1312

Gaus Width: 3.9628



B0L102S, U12-ch125, adc1

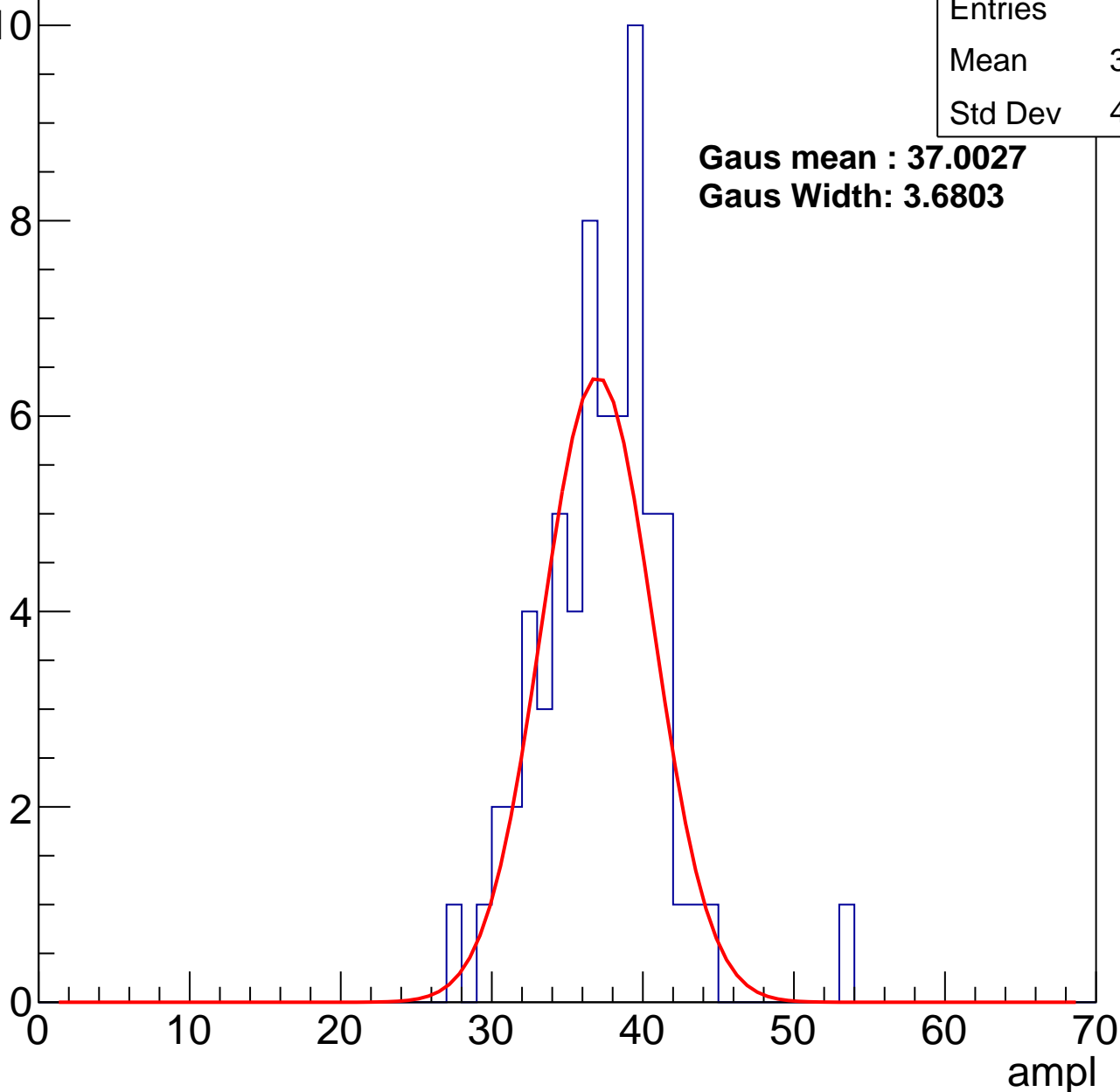
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	36.82
Std Dev	4.056

Gaus mean : 37.0027

Gaus Width: 3.6803



B0L102S, U12-ch125, adc2

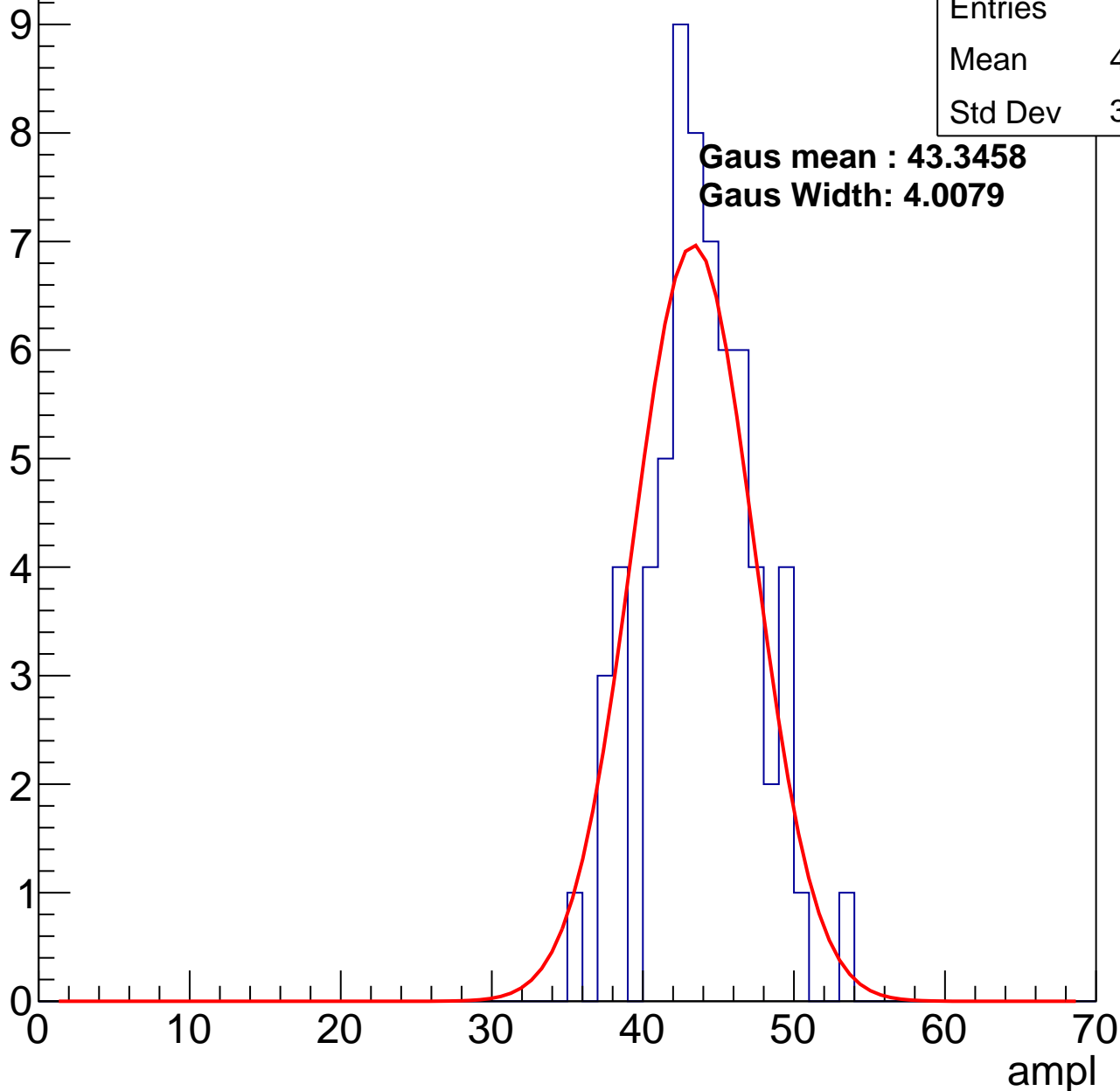
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.42
Std Dev	3.547

Gaus mean : 43.3458

Gaus Width: 4.0079

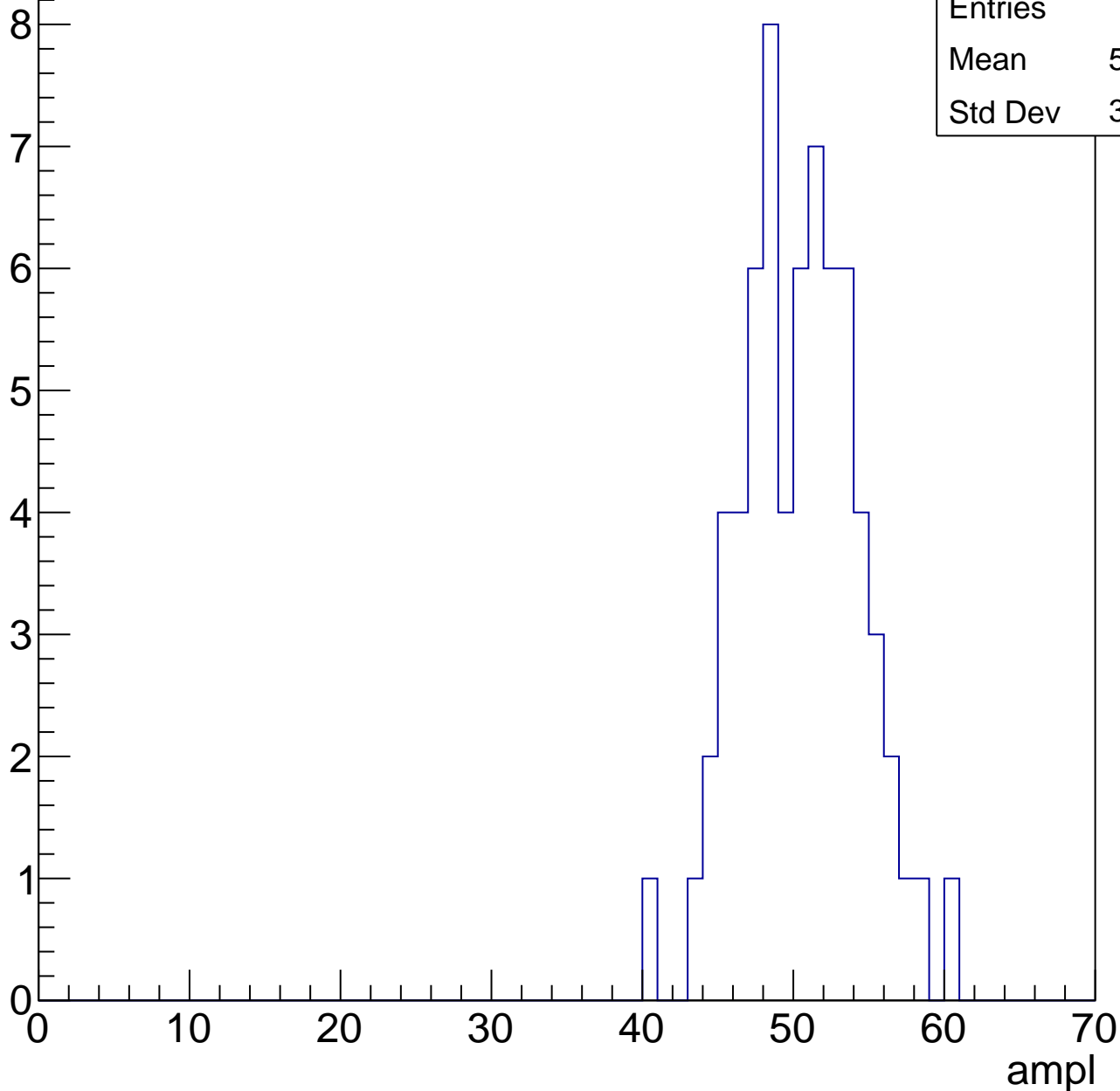


B0L102S, U12-ch125, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	50.03
Std Dev	3.832

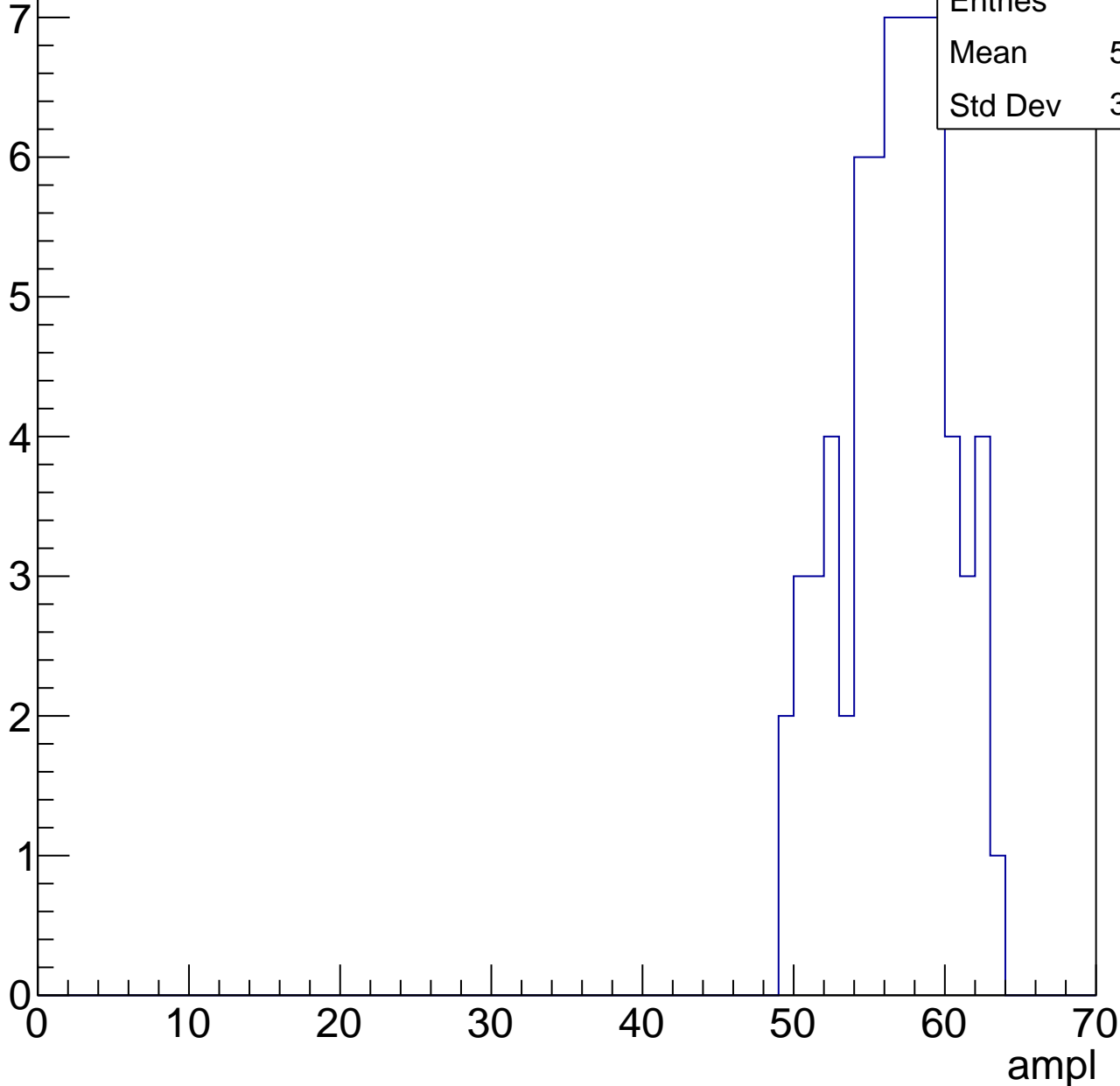


B0L102S, U12-ch125, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	56.26
Std Dev	3.522

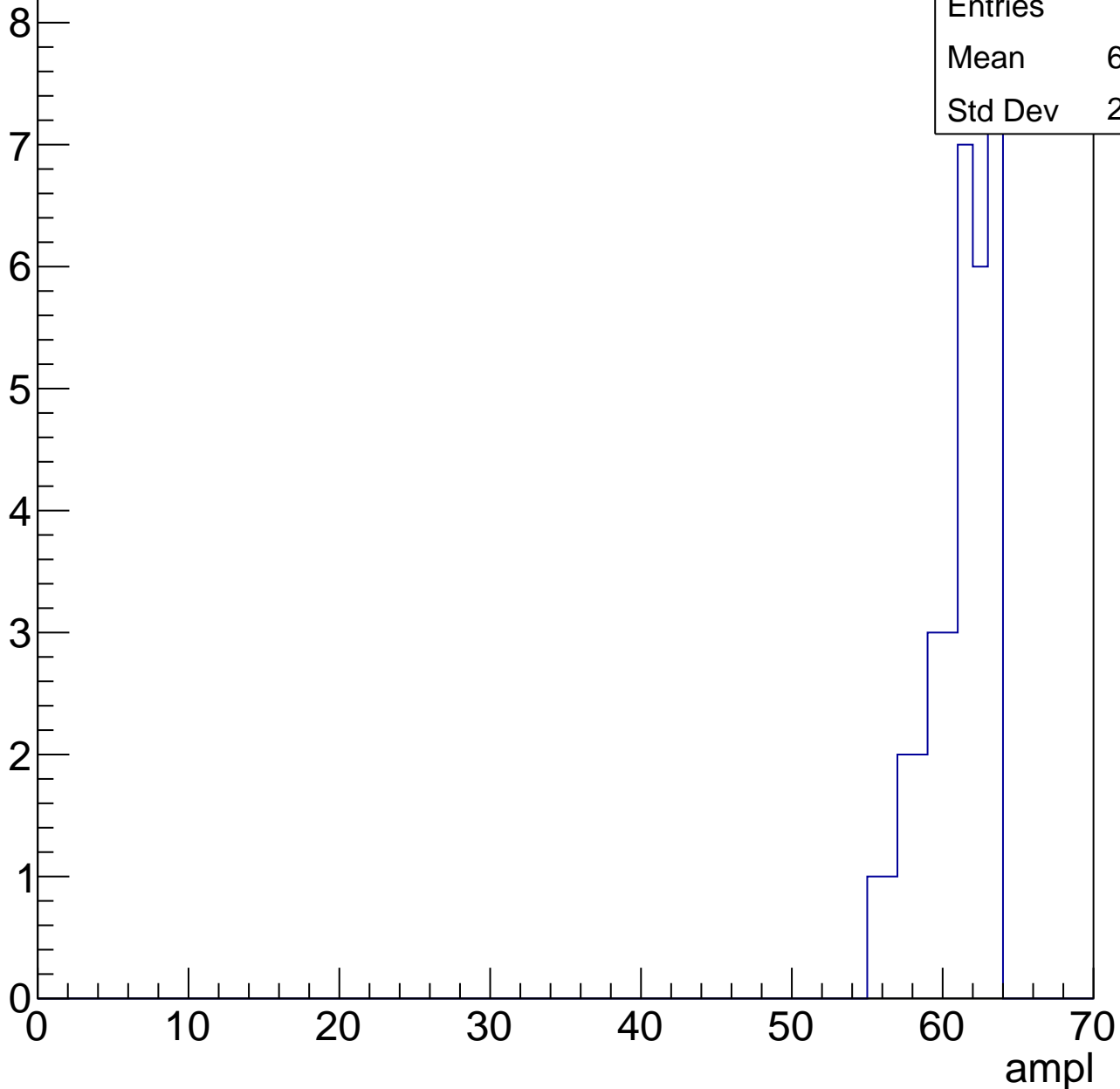


B0L102S, U12-ch125, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	33
Mean	60.64
Std Dev	2.199



B0L102S, U12-ch125, adc6

calib_packv5_042523_0143.root, FC#12, port B1

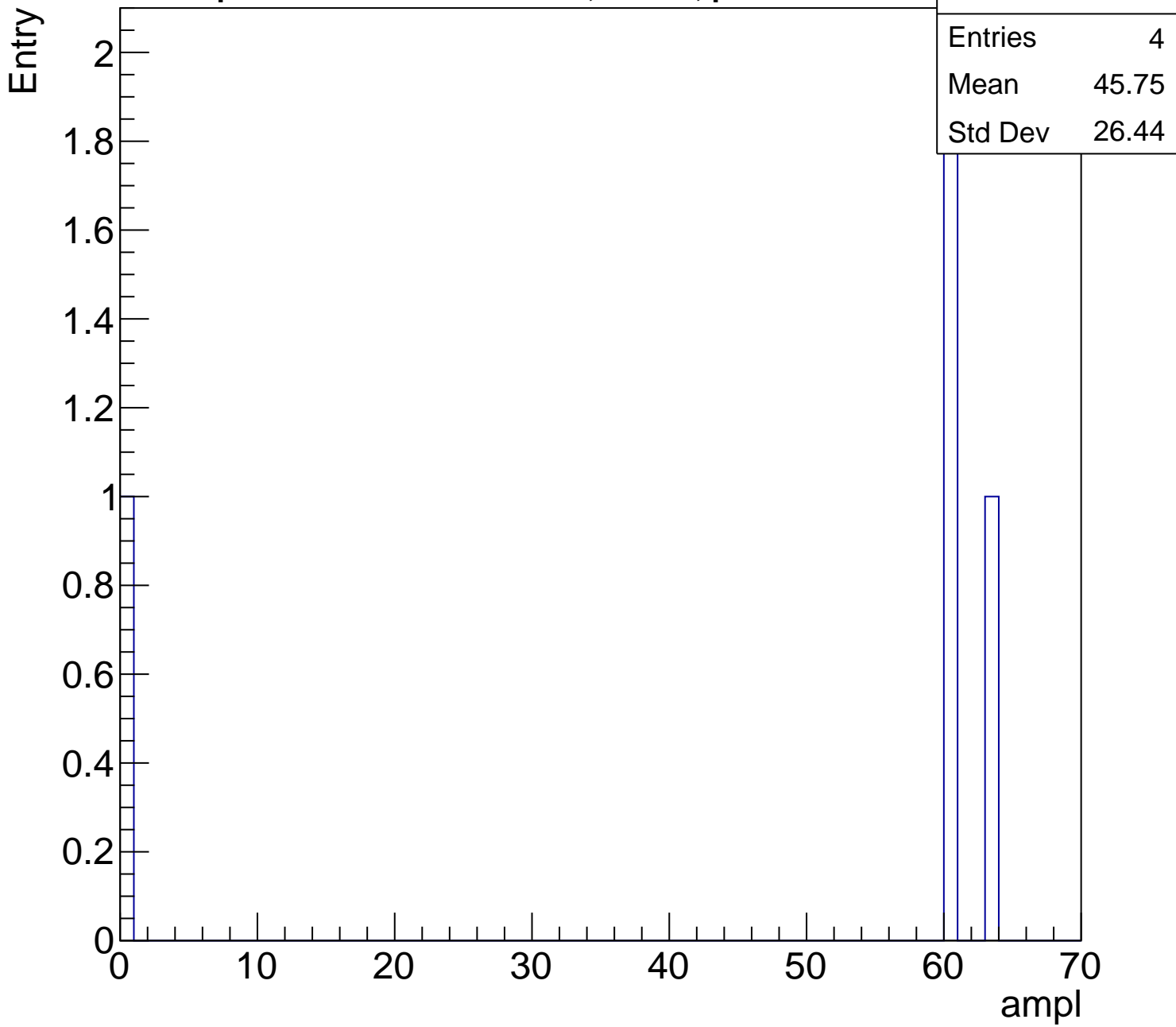
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	45.75
Std Dev	26.44

ampl

0 10 20 30 40 50 60 70



B0L102S, U12-ch125, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch126, adc0

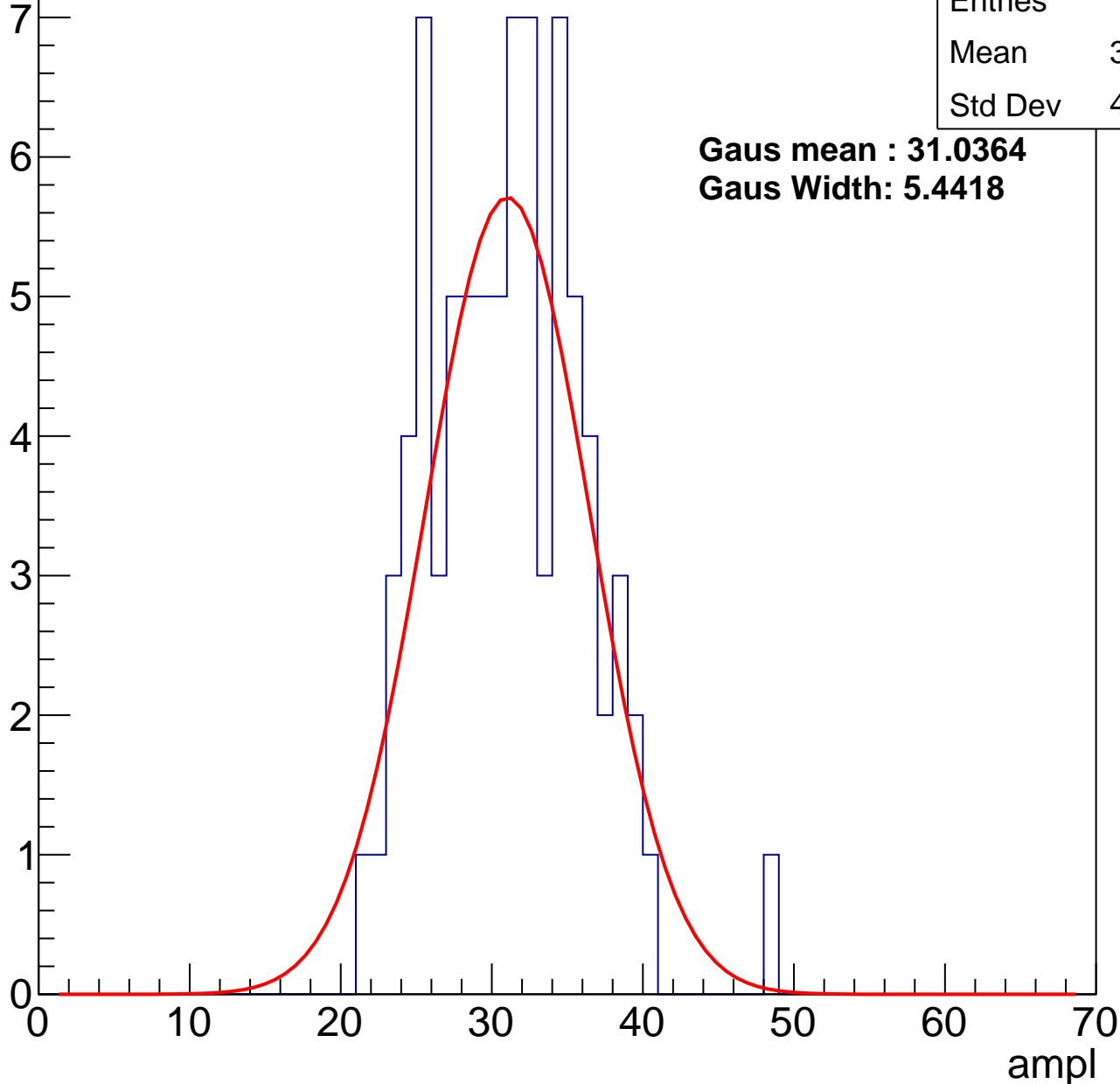
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	30.64
Std Dev	4.987

Gaus mean : 31.0364

Gaus Width: 5.4418



B0L102S, U12-ch126, adc1

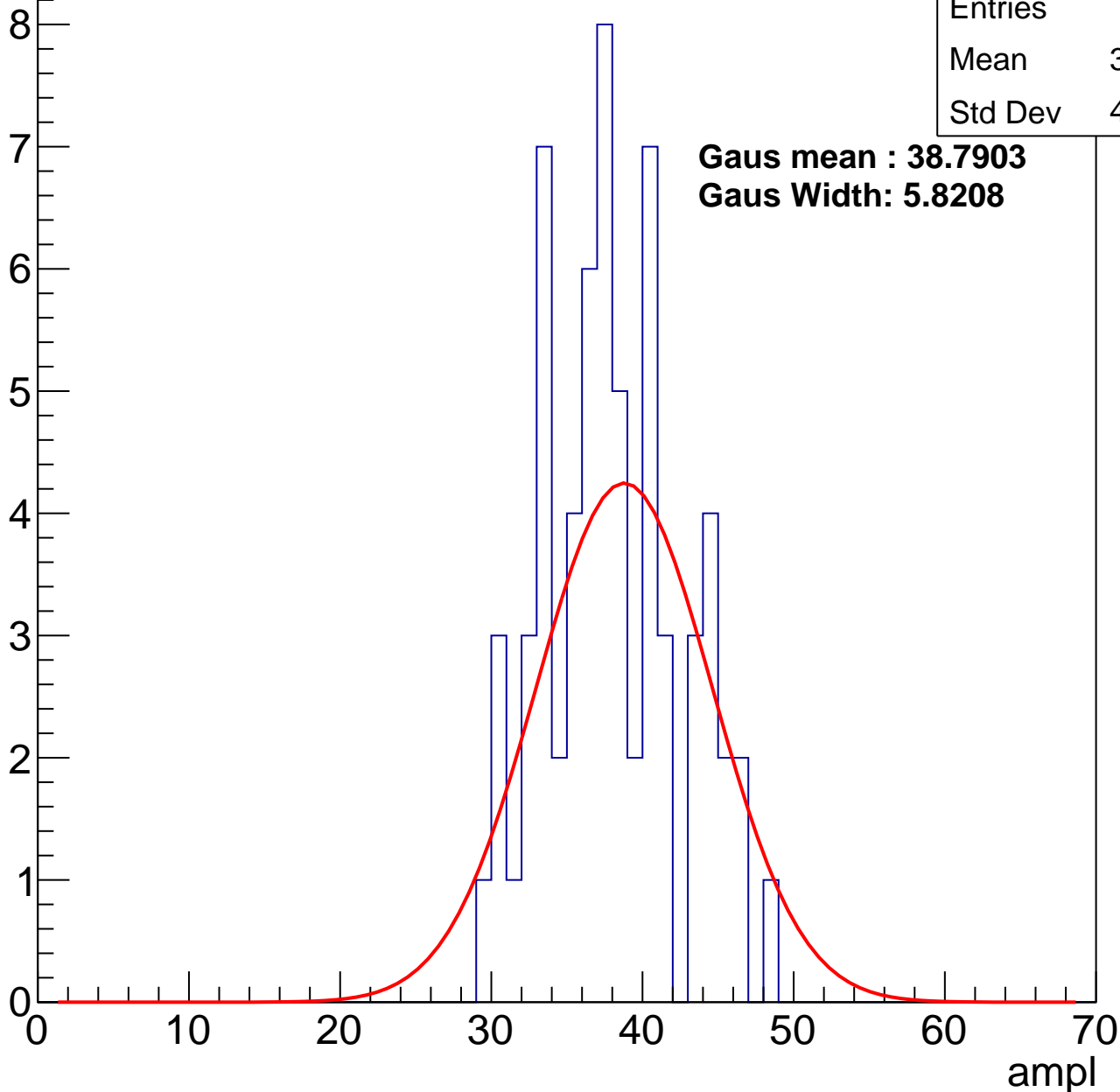
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	37.55
Std Dev	4.496

Gaus mean : 38.7903

Gaus Width: 5.8208



B0L102S, U12-ch126, adc2

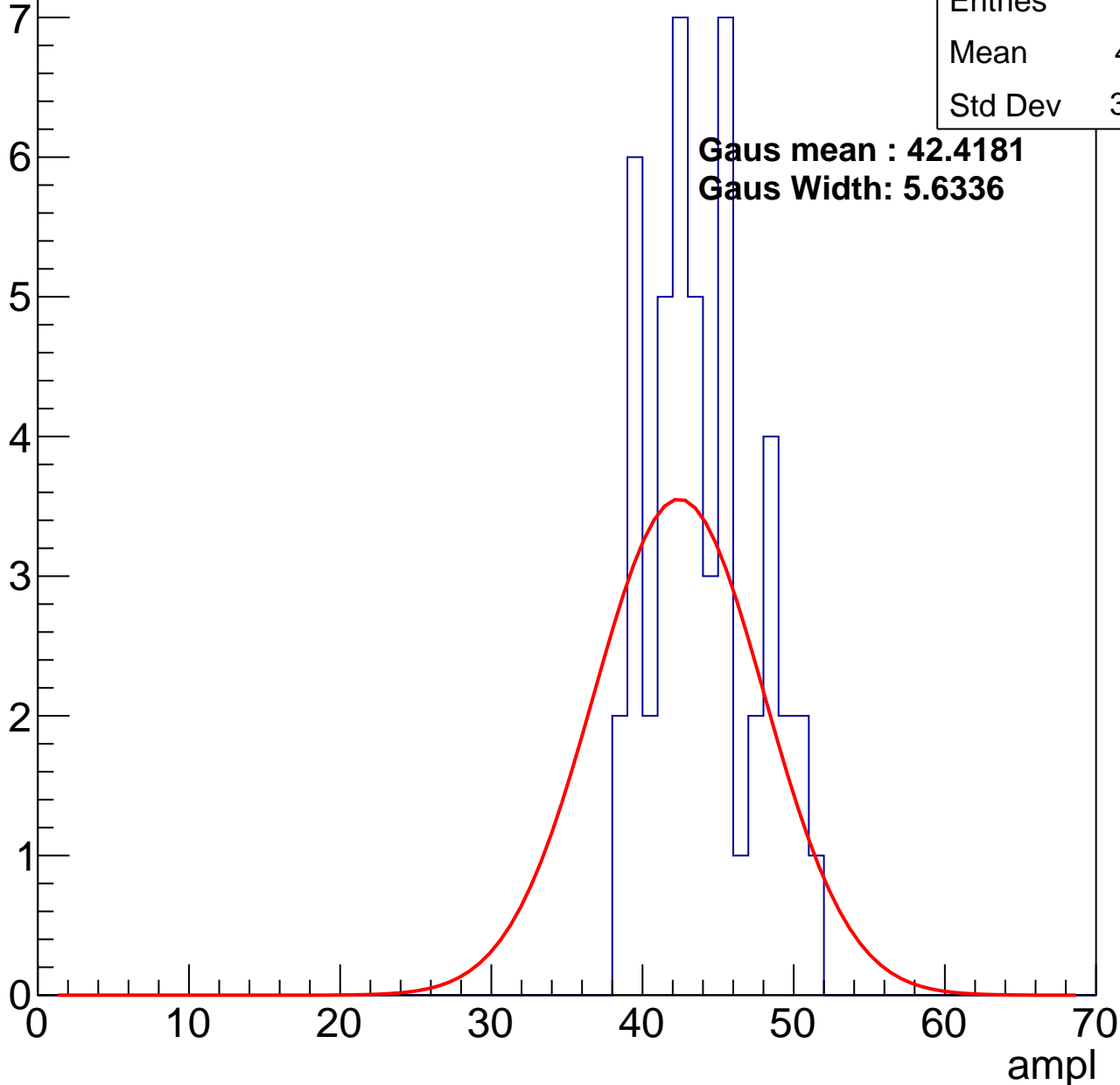
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	43.51
Std Dev	3.453

Gaus mean : 42.4181

Gaus Width: 5.6336

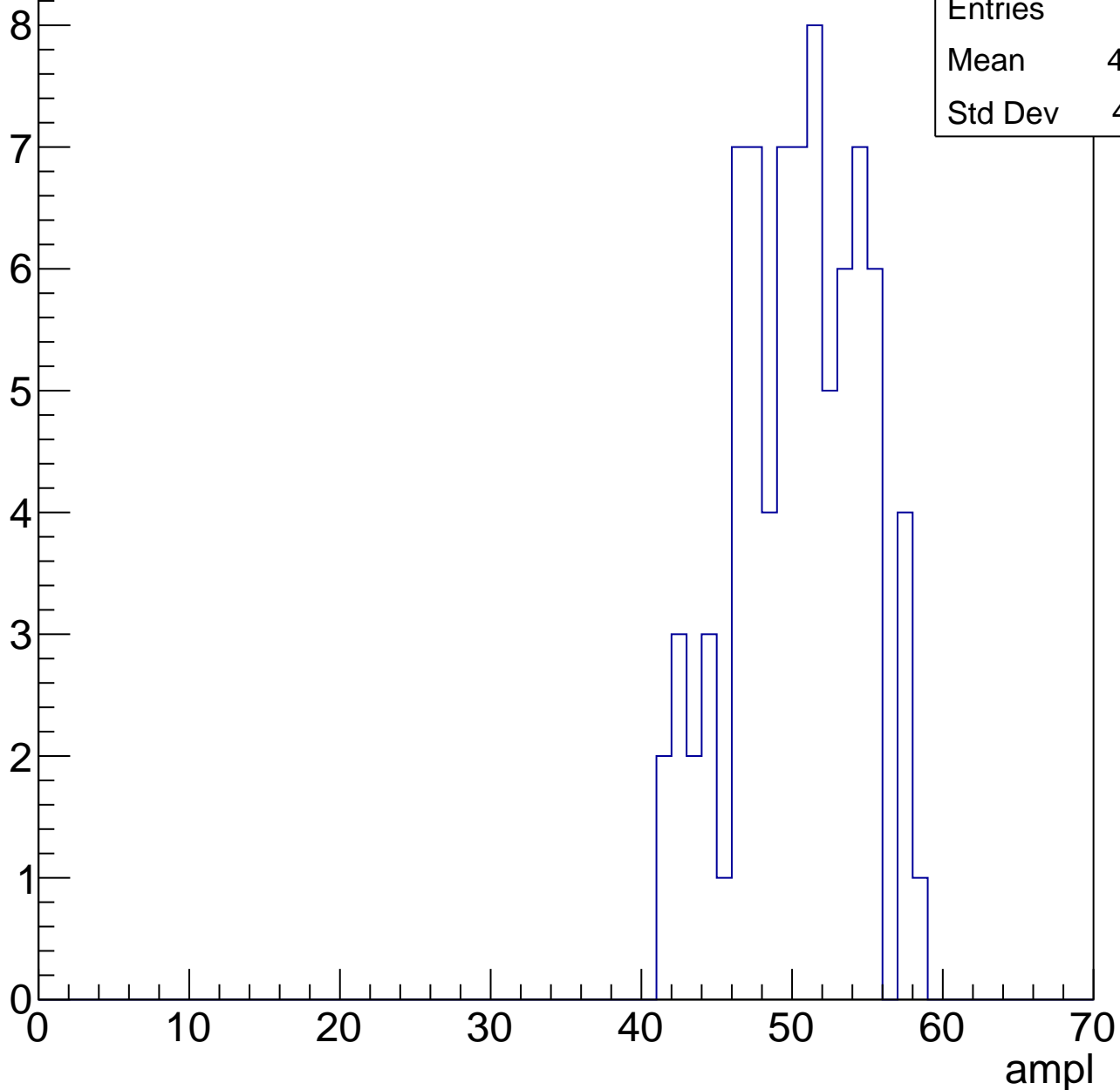


B0L102S, U12-ch126, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	49.84
Std Dev	4.161

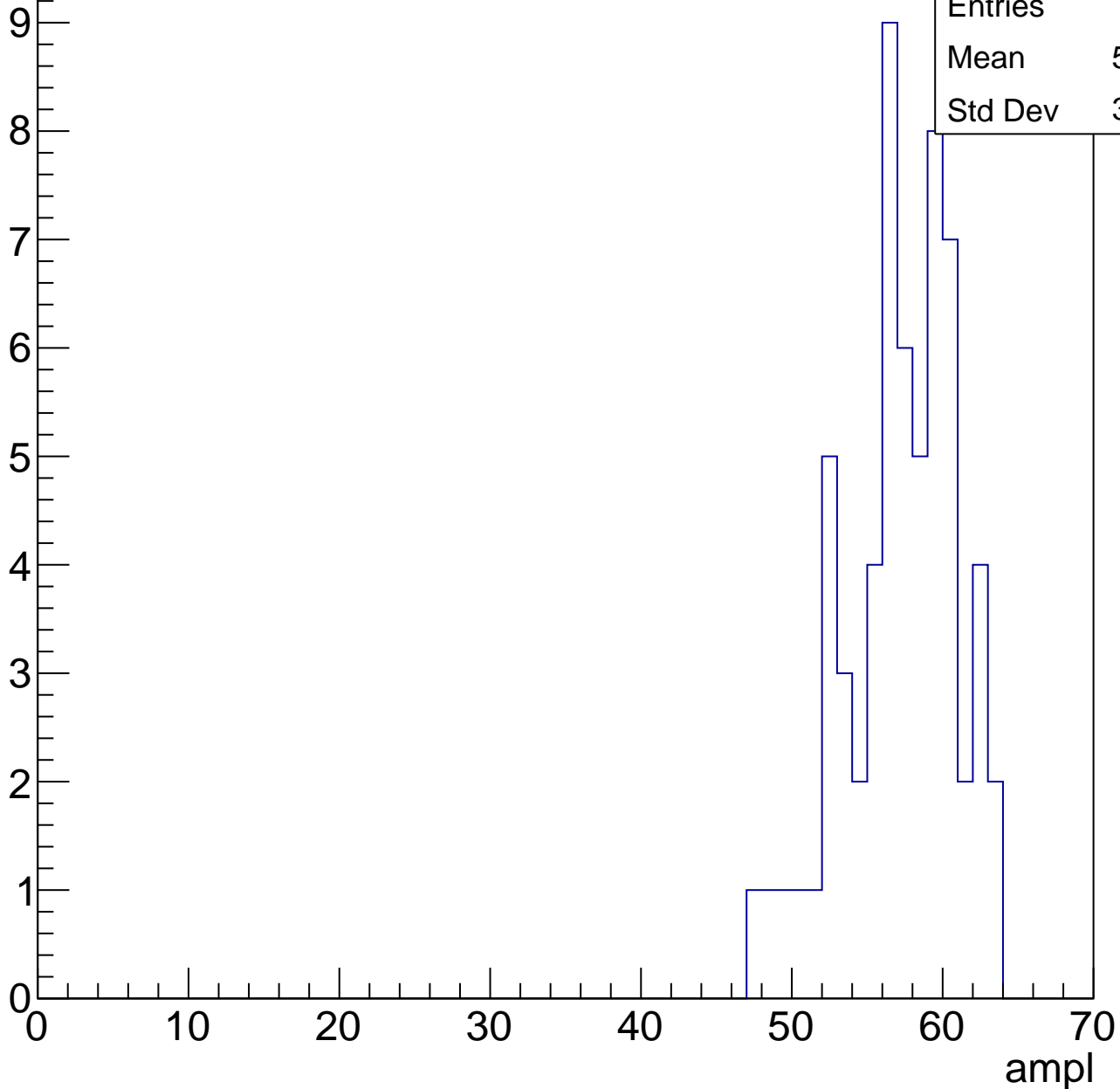


B0L102S, U12-ch126, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	56.71
Std Dev	3.691

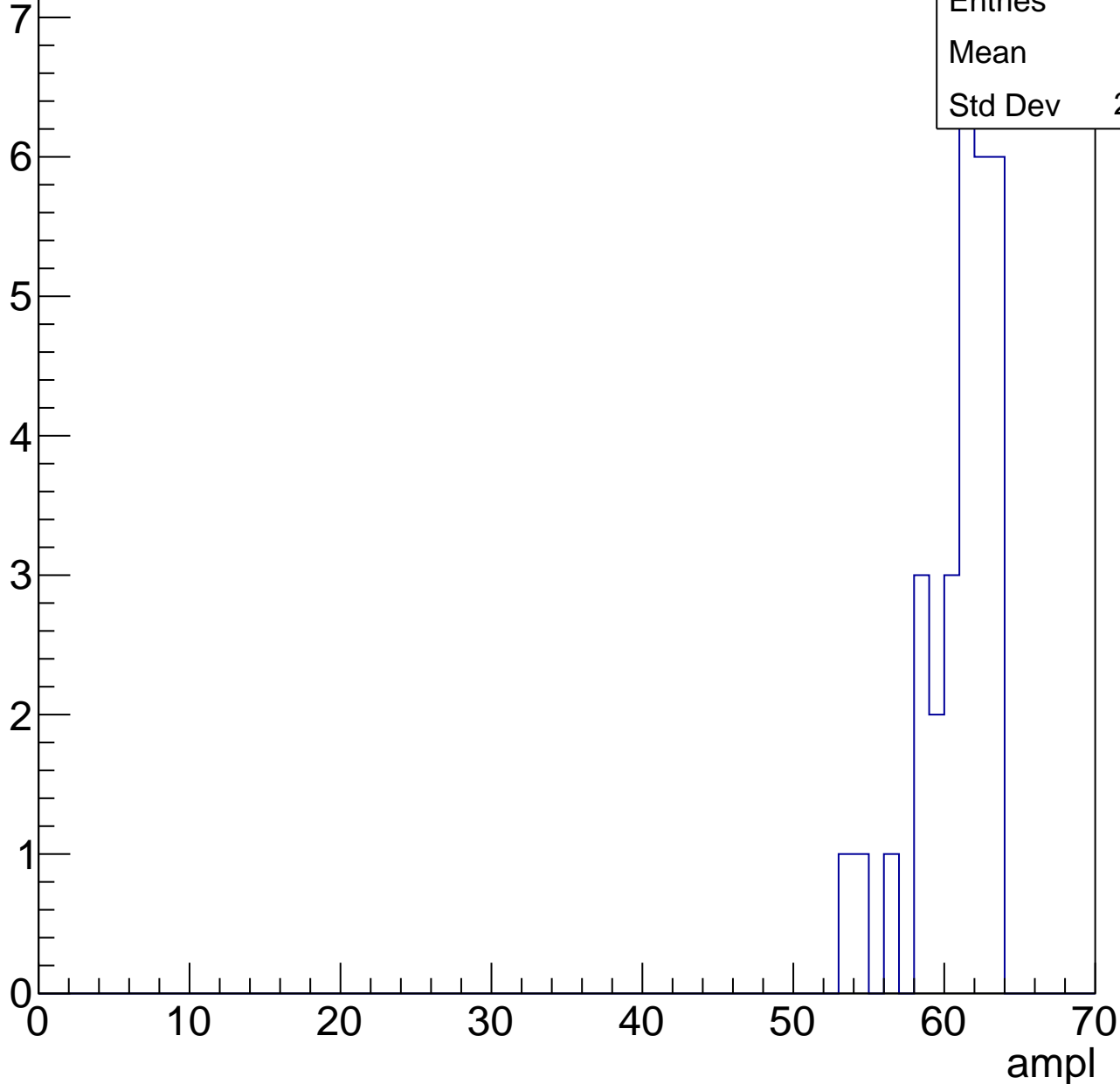


B0L102S, U12-ch126, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

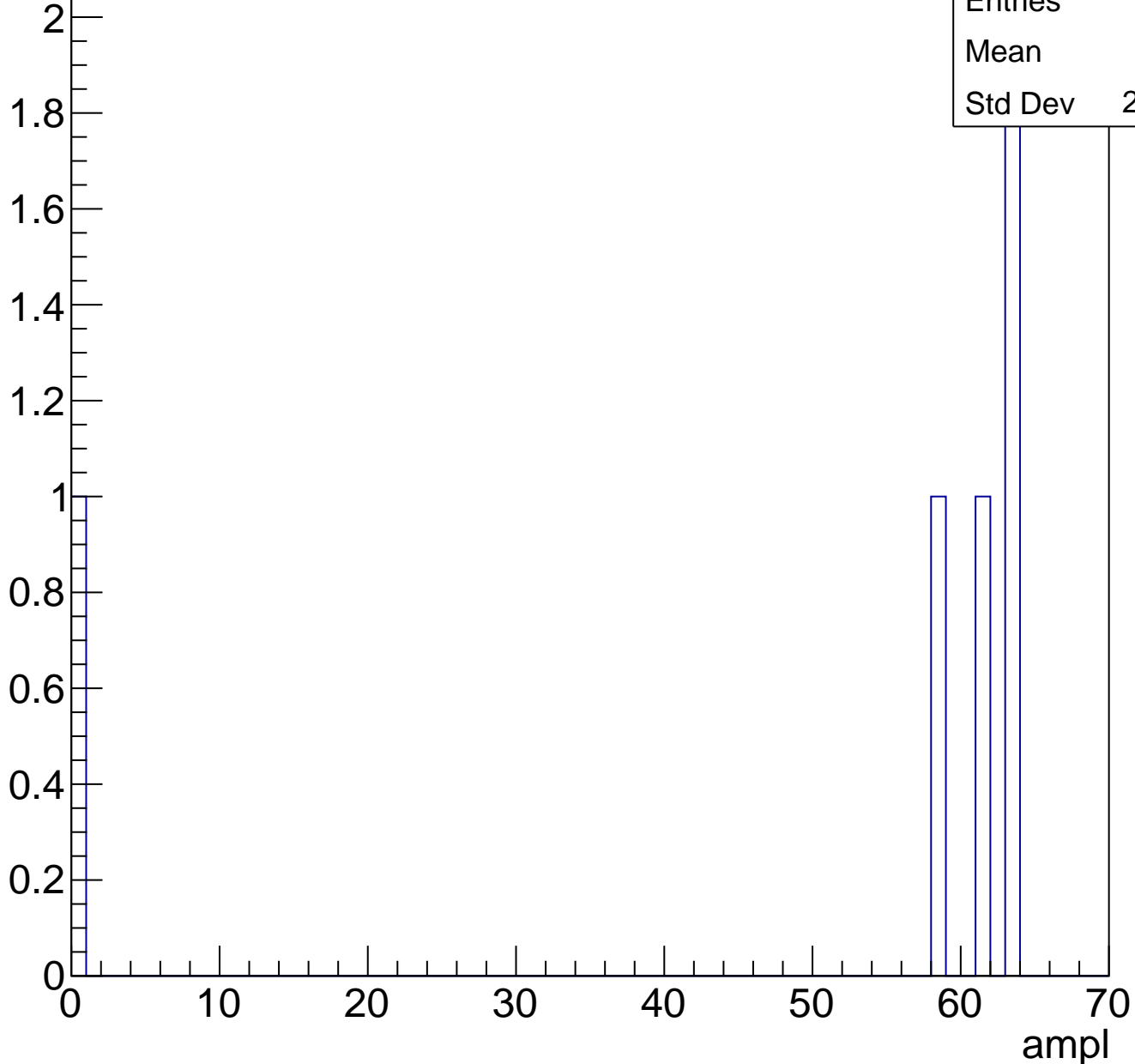
Entries	30
Mean	60.4
Std Dev	2.551



B0L102S, U12-ch126, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	49
Std Dev	24.57

B0L102S, U12-ch126, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U12-ch127, adc0

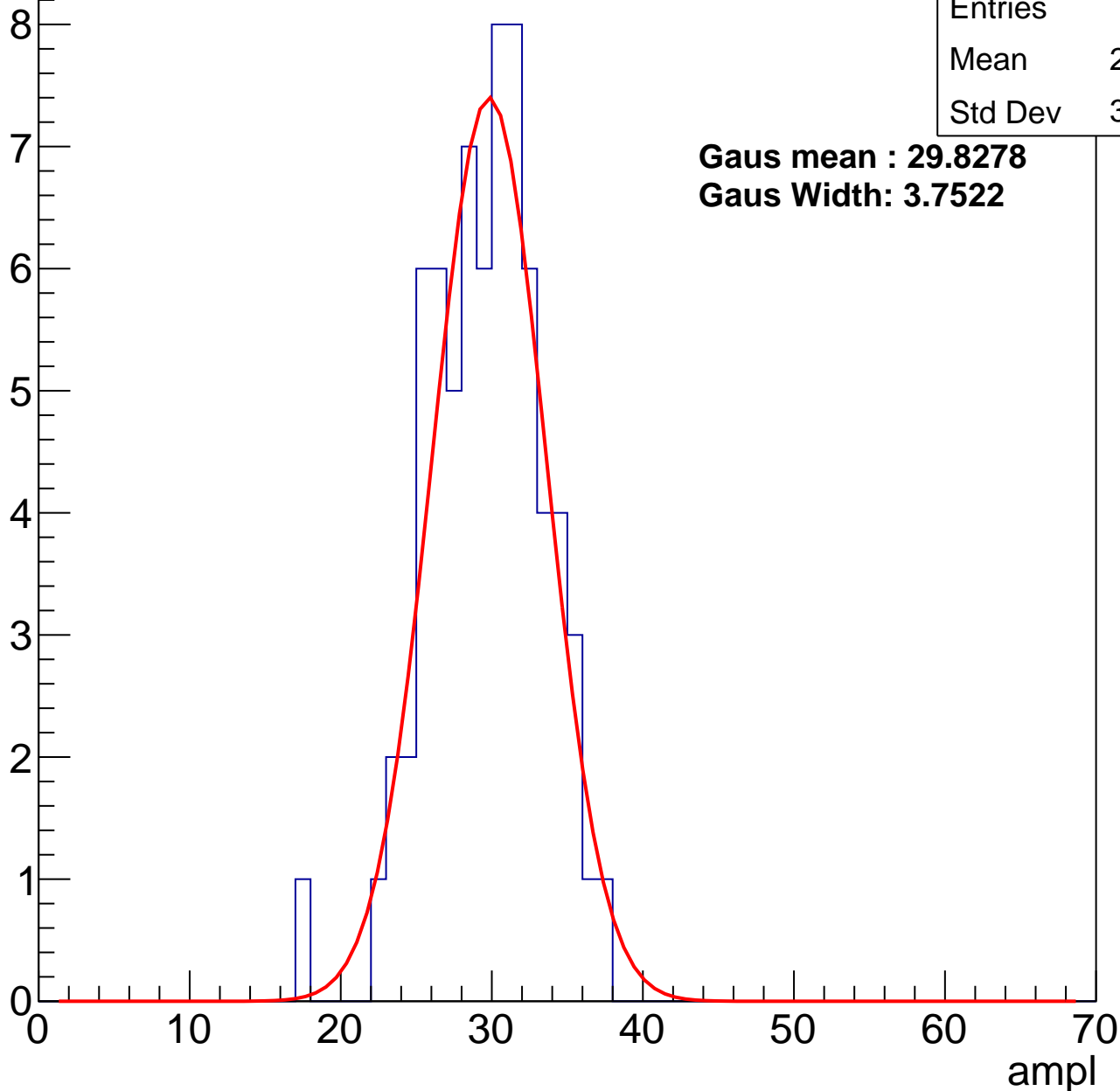
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.15
Std Dev	3.695

Gaus mean : 29.8278

Gaus Width: 3.7522



B0L102S, U12-ch127, adc1

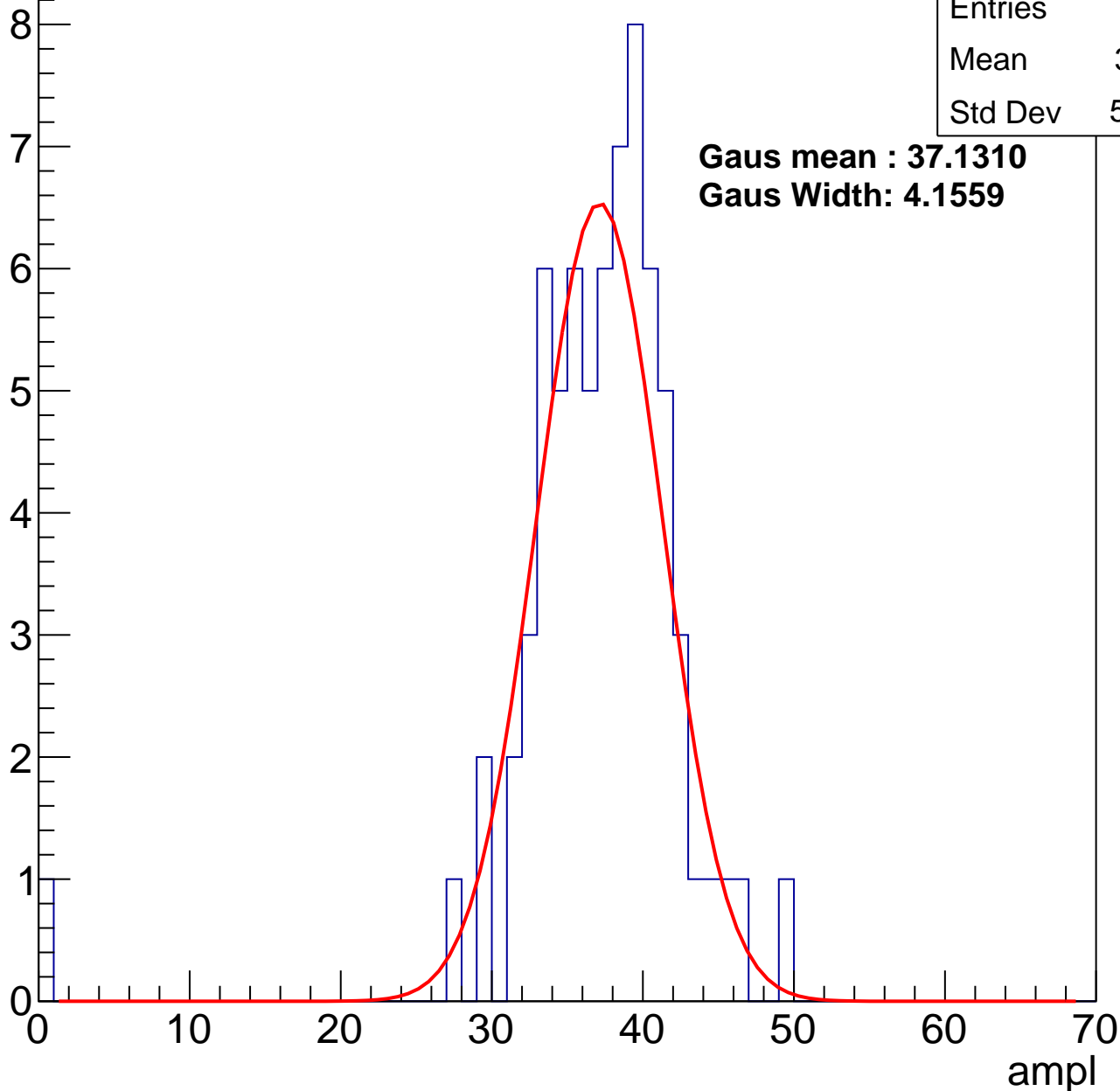
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.61
Std Dev	5.963

Gaus mean : 37.1310

Gaus Width: 4.1559



B0L102S, U12-ch127, adc2

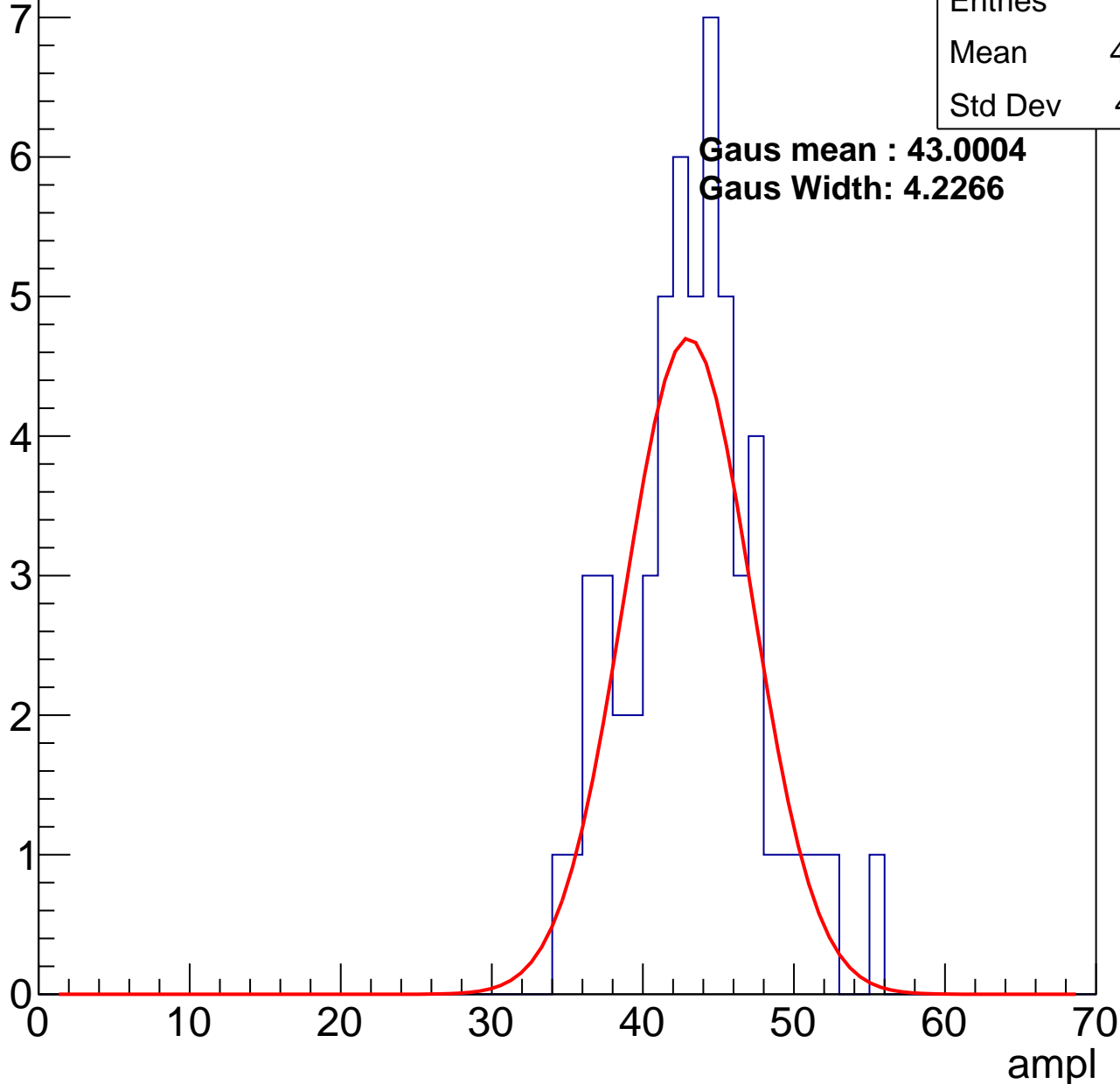
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	42.82
Std Dev	4.331

Gaus mean : 43.0004

Gaus Width: 4.2266

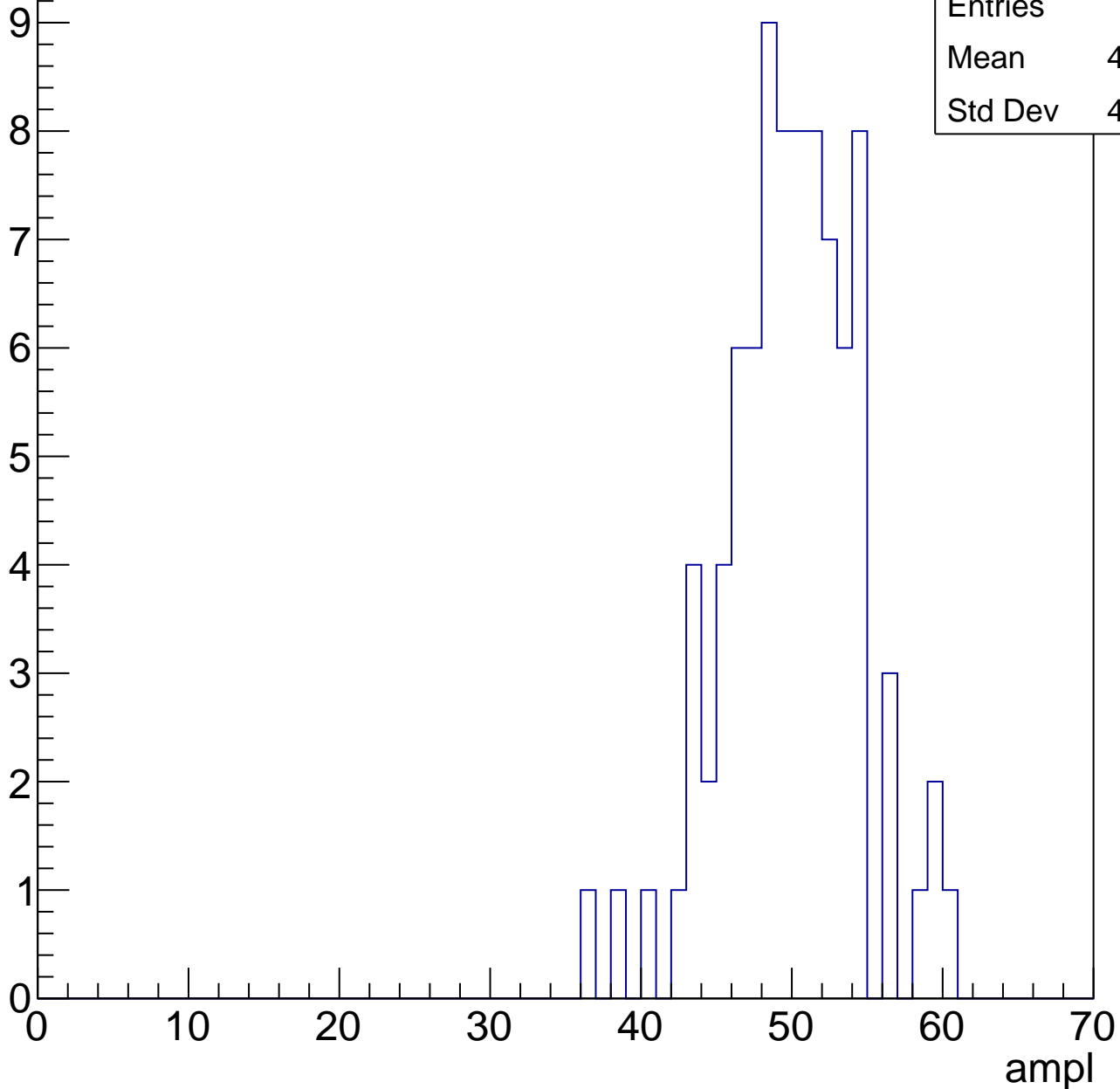


B0L102S, U12-ch127, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	49.47
Std Dev	4.412

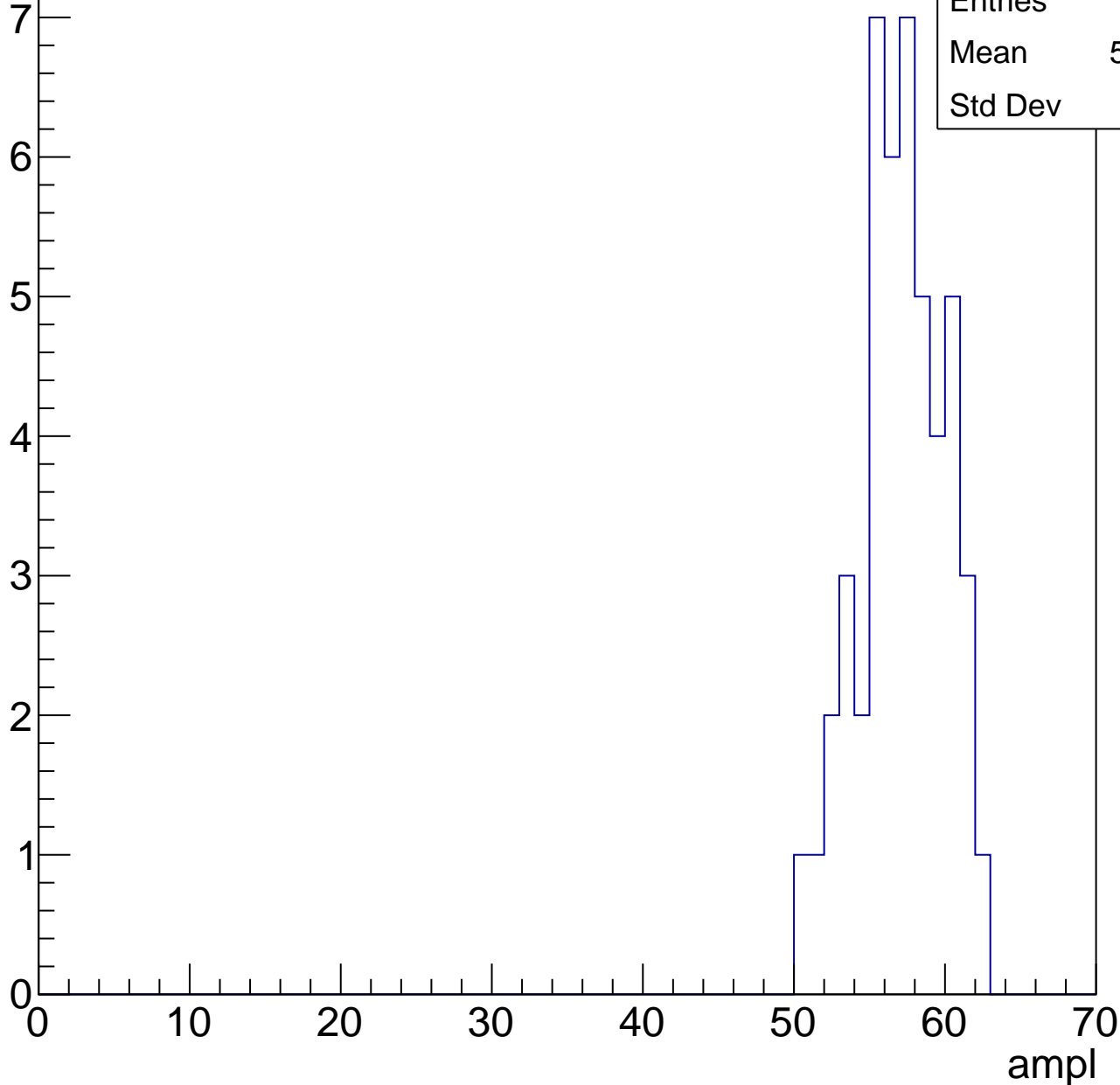


B0L102S, U12-ch127, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	56.66
Std Dev	2.8

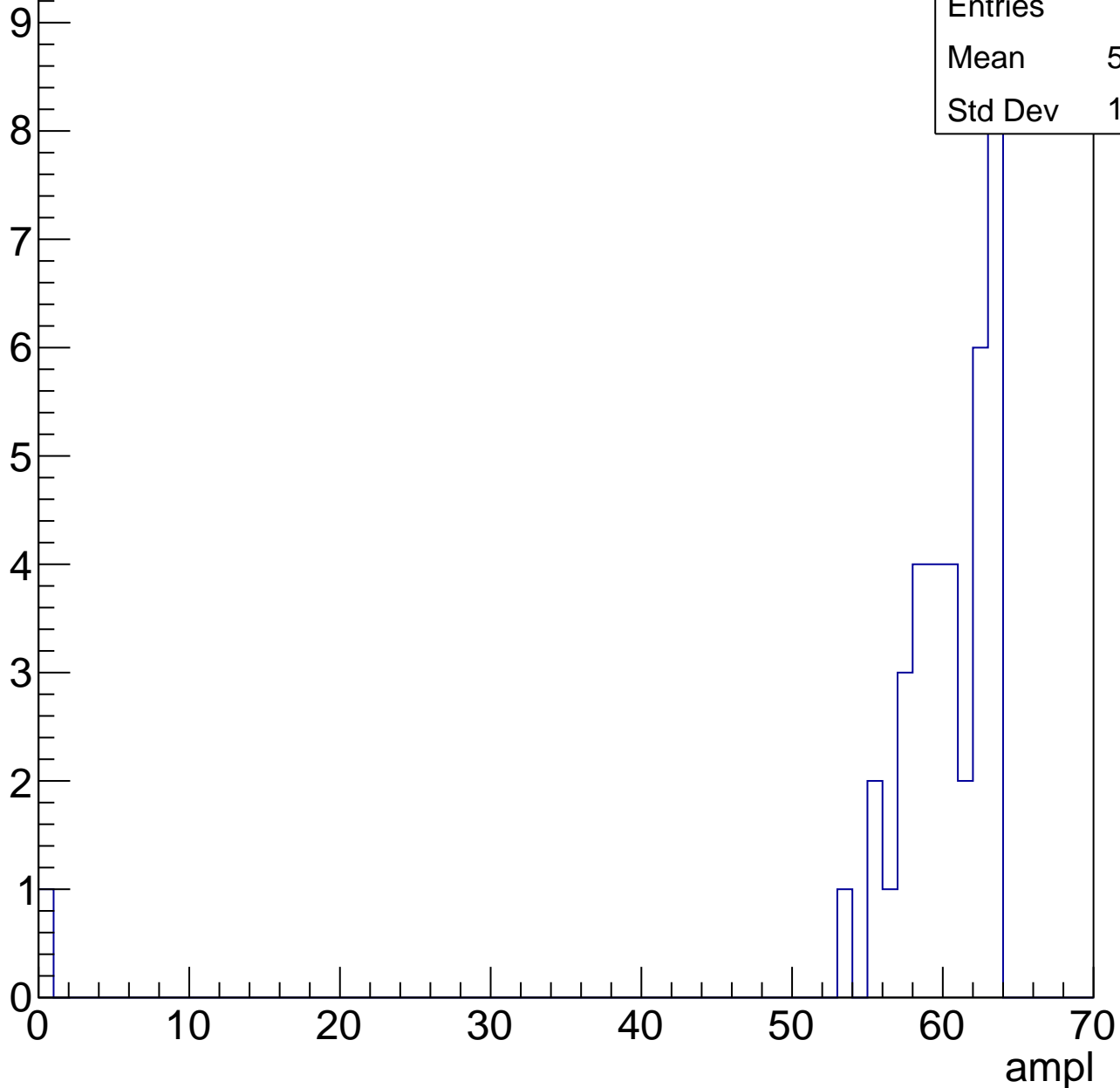


B0L102S, U12-ch127, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	37
Mean	58.35
Std Dev	10.09

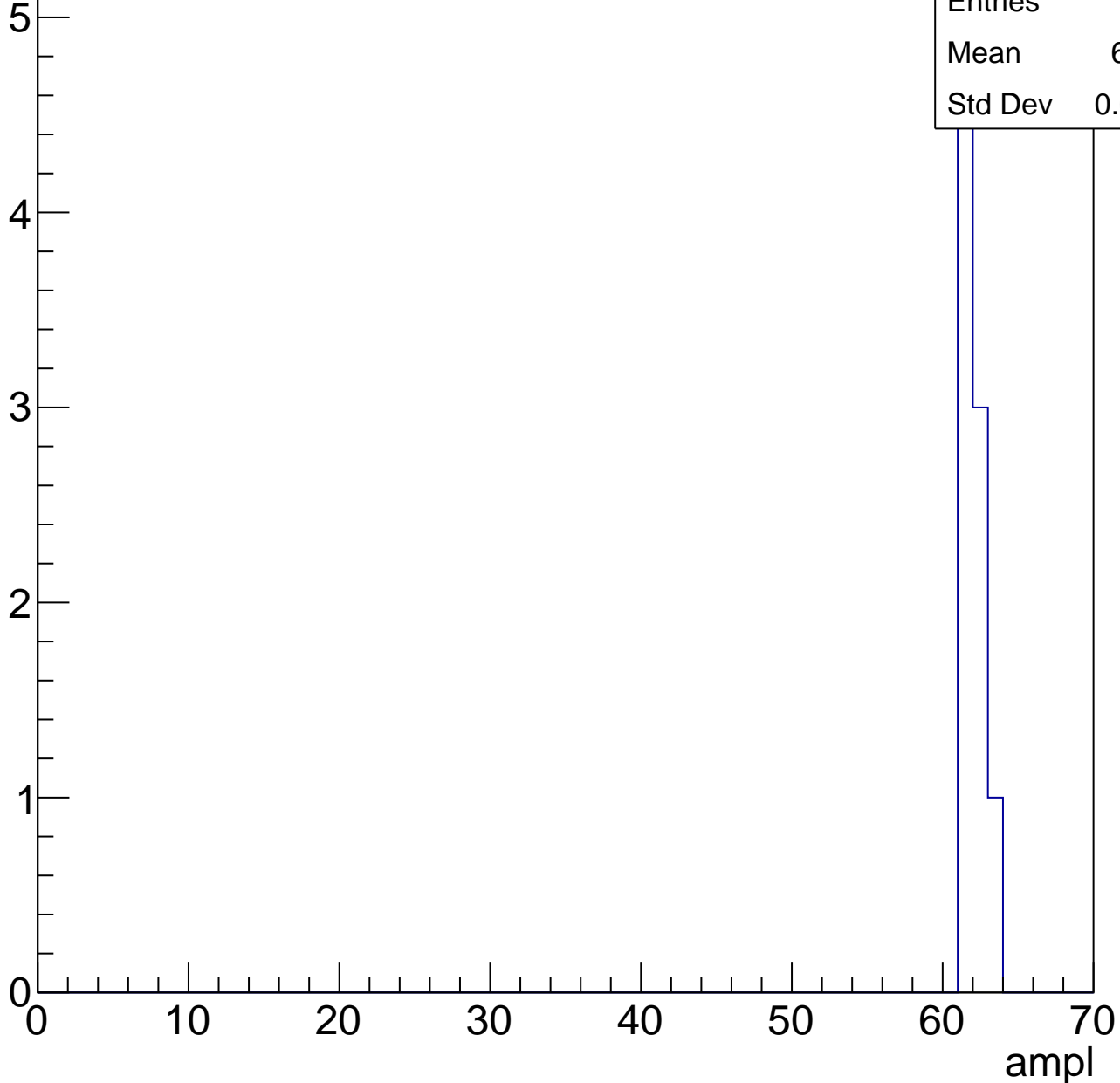


B0L102S, U12-ch127, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	9
Mean	61.56
Std Dev	0.6849



B0L102S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

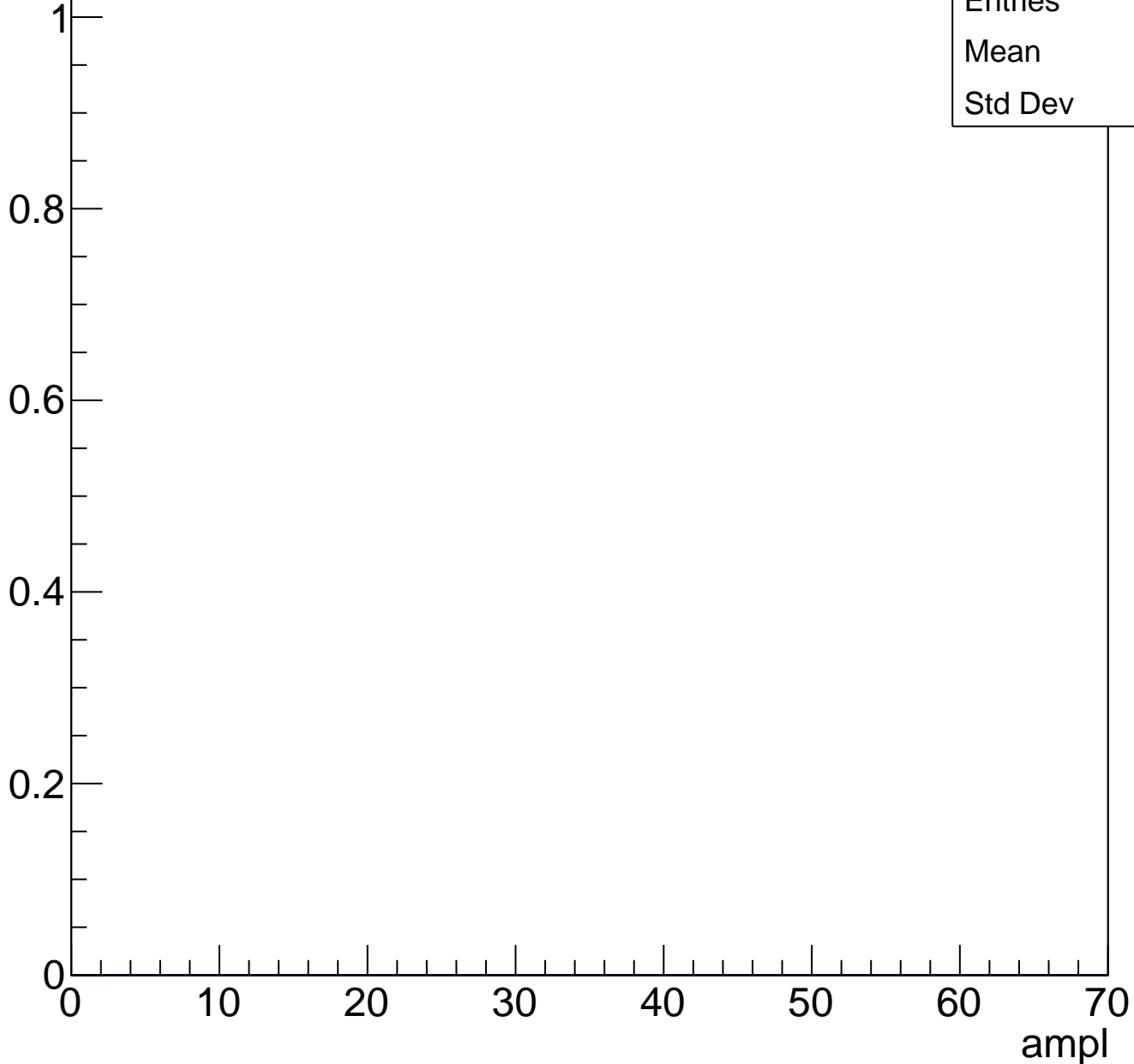


Entries	0
Mean	0
Std Dev	0

B0L102S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0