

B0L000S, U10-ch0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.68
Std Dev	11.47

Turn on : 27.7754

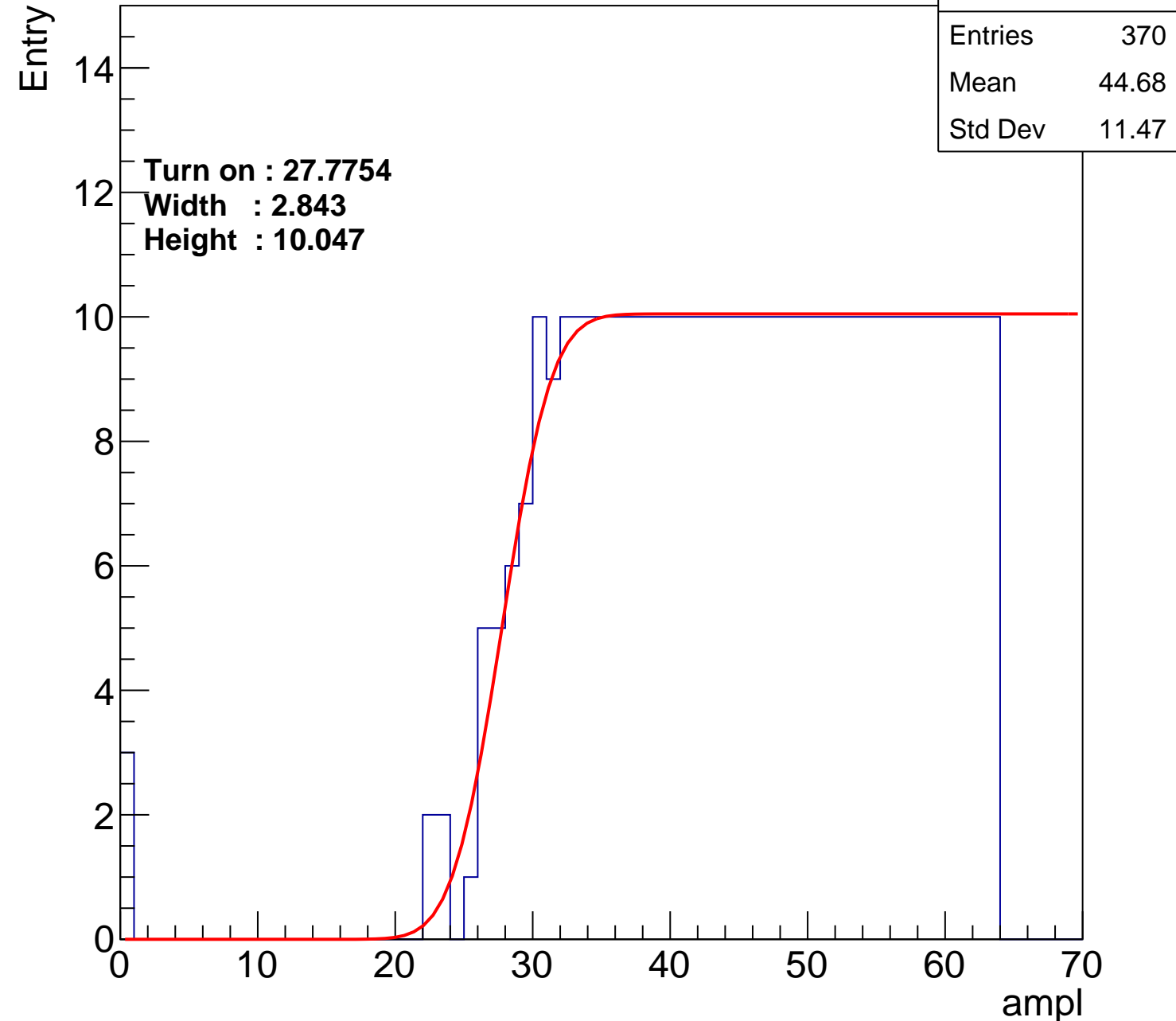
Width : 2.843

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 27.3135

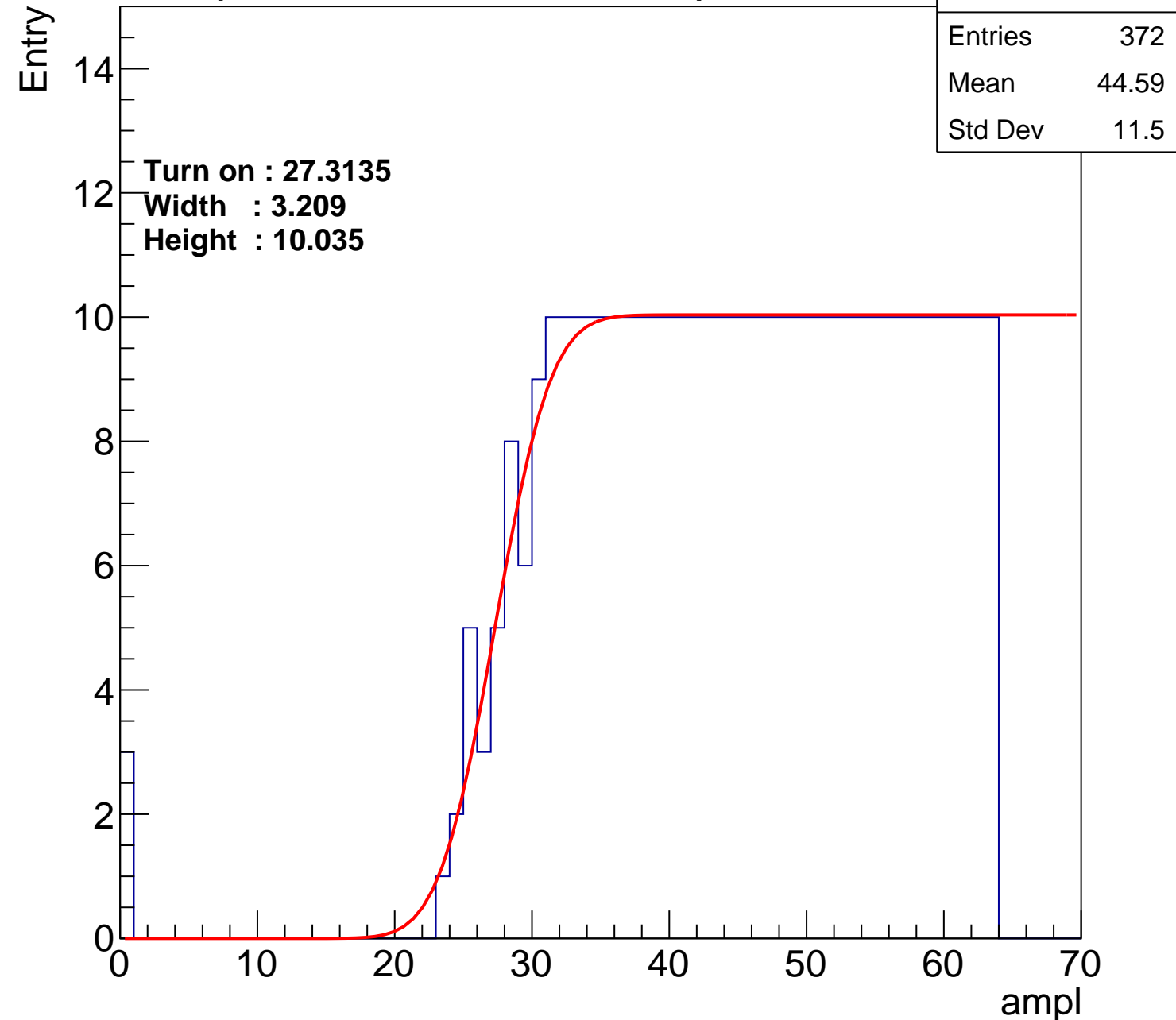
Width : 3.209

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch2

calib_packv5_042523_0143.root, FC#5, port B1

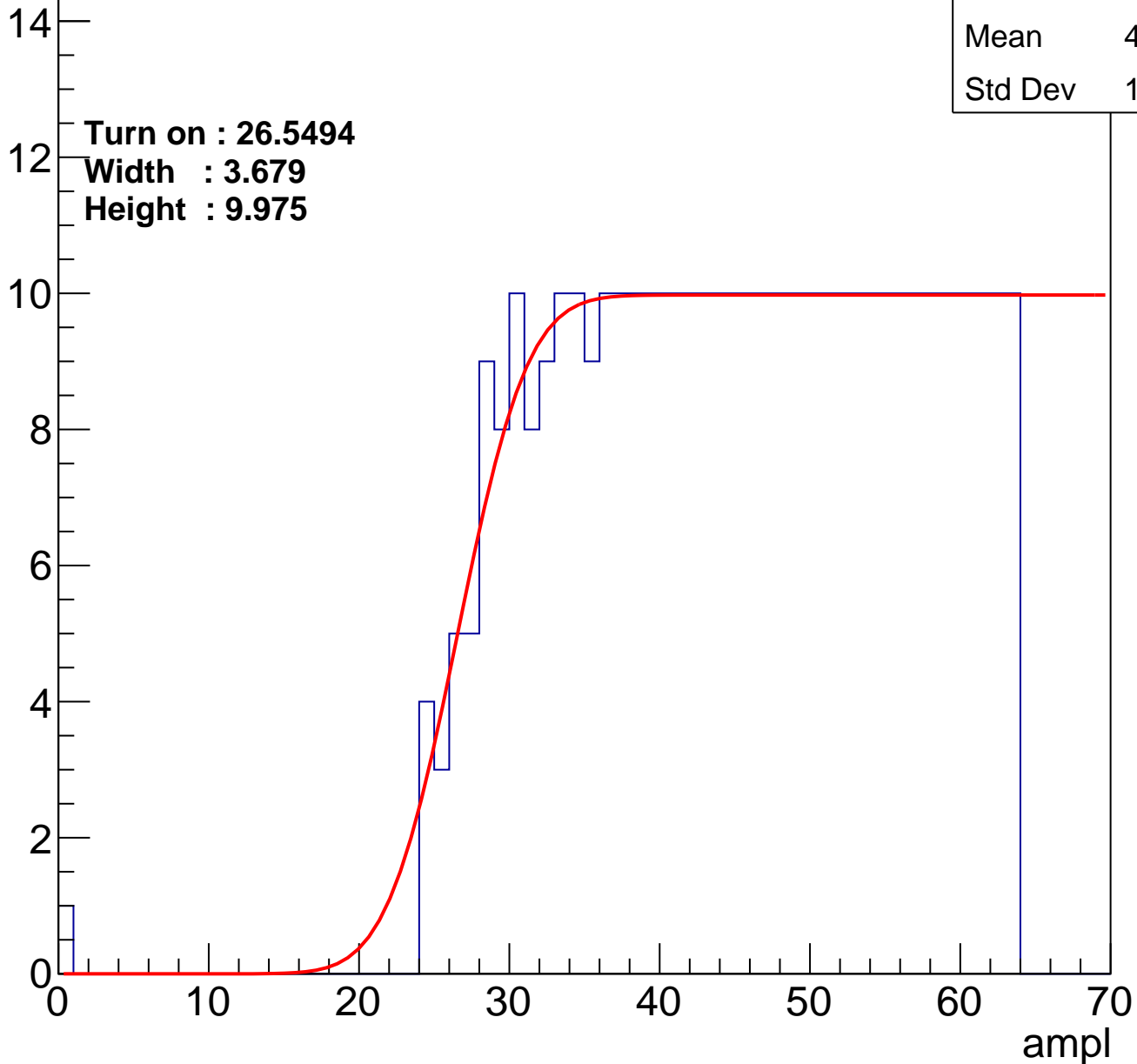
Entries	371
Mean	44.75
Std Dev	11.12

Turn on : 26.5494

Width : 3.679

Height : 9.975

Entry



B0L000S, U10-ch3

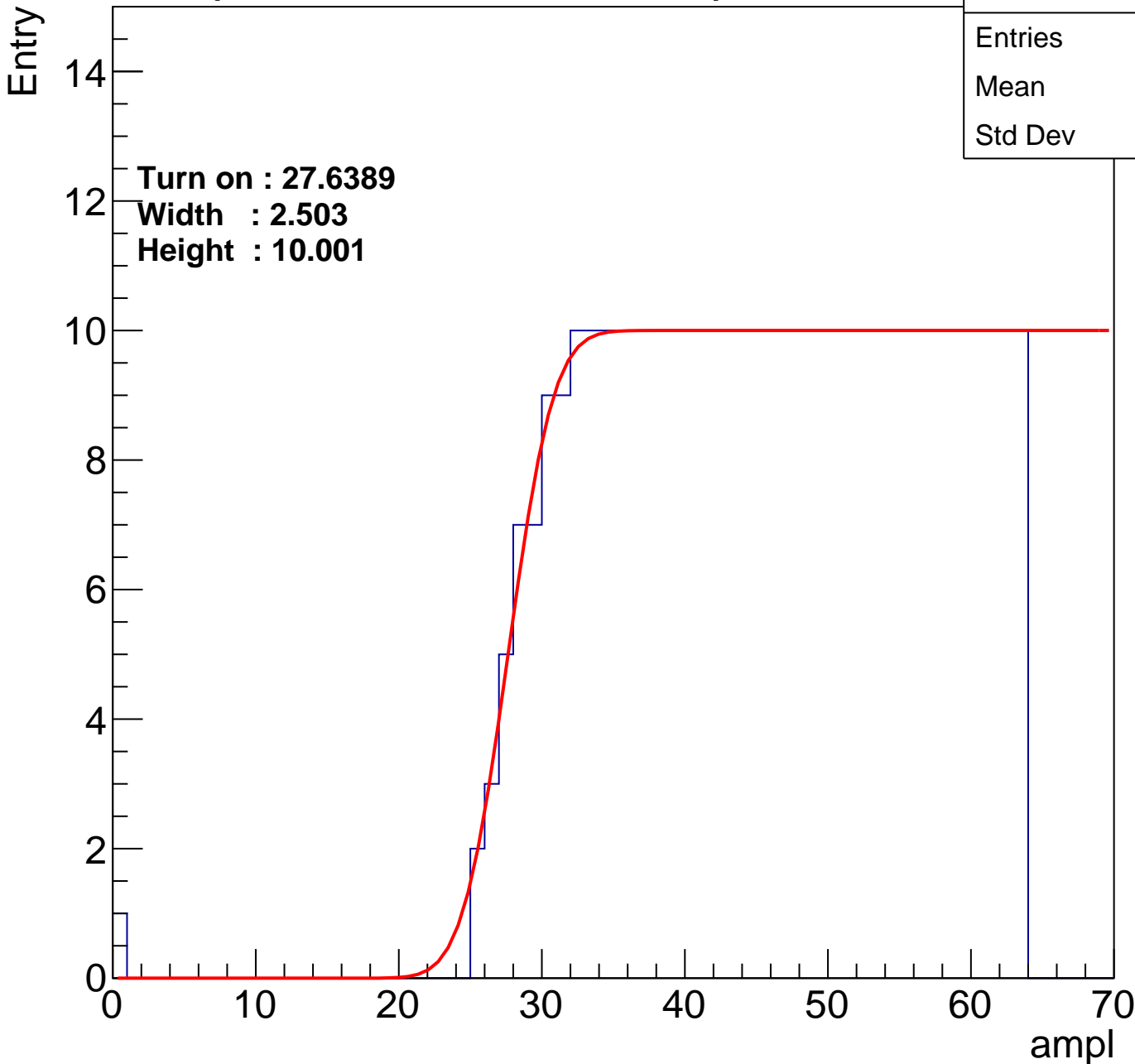
calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.21
Std Dev	10.81

Turn on : 27.6389

Width : 2.503

Height : 10.001



B0L000S, U10-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.45
Std Dev	11.32

Turn on : 29.5359

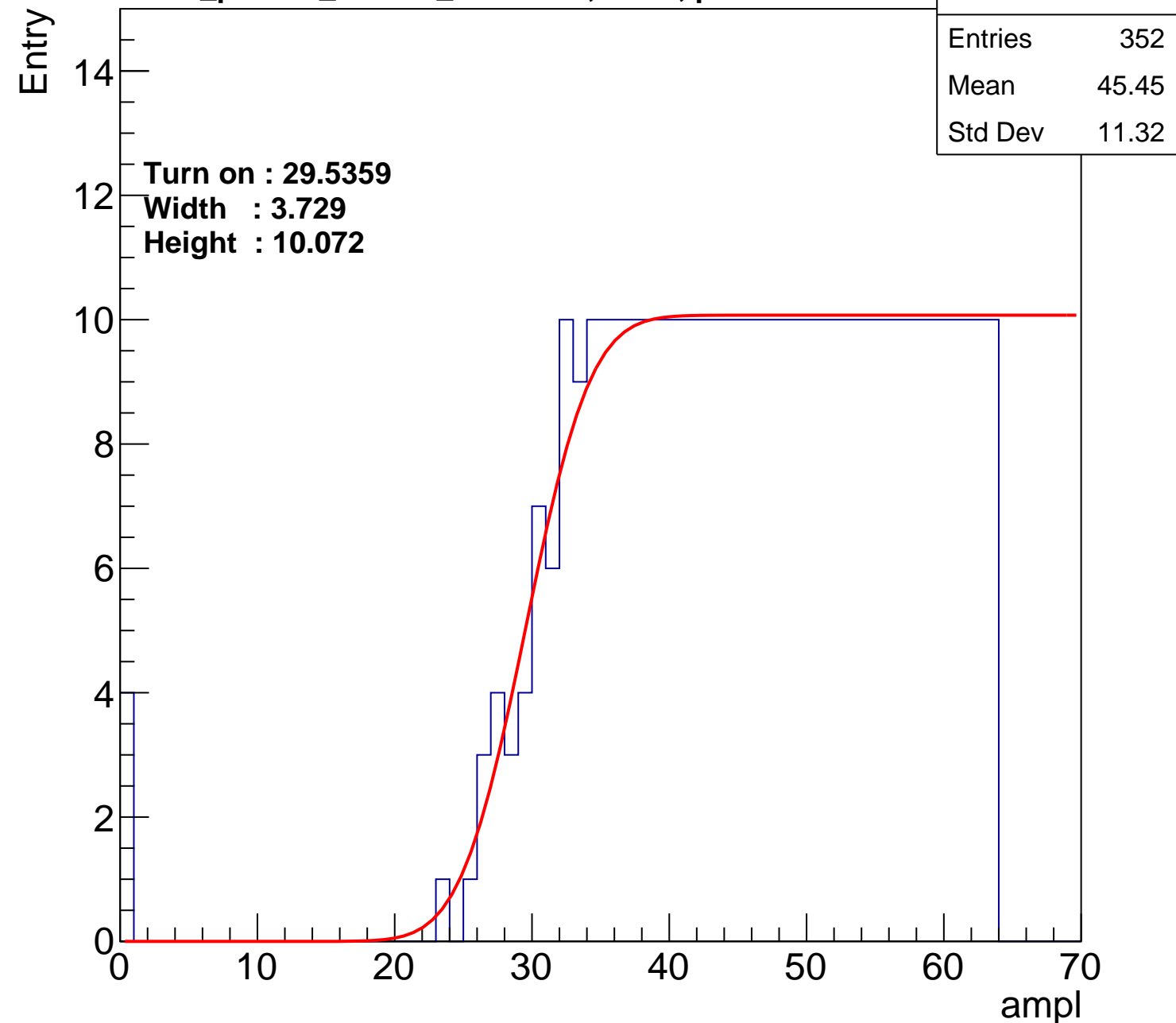
Width : 3.729

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	344
Mean	46.05
Std Dev	10.46

Turn on : 29.5649

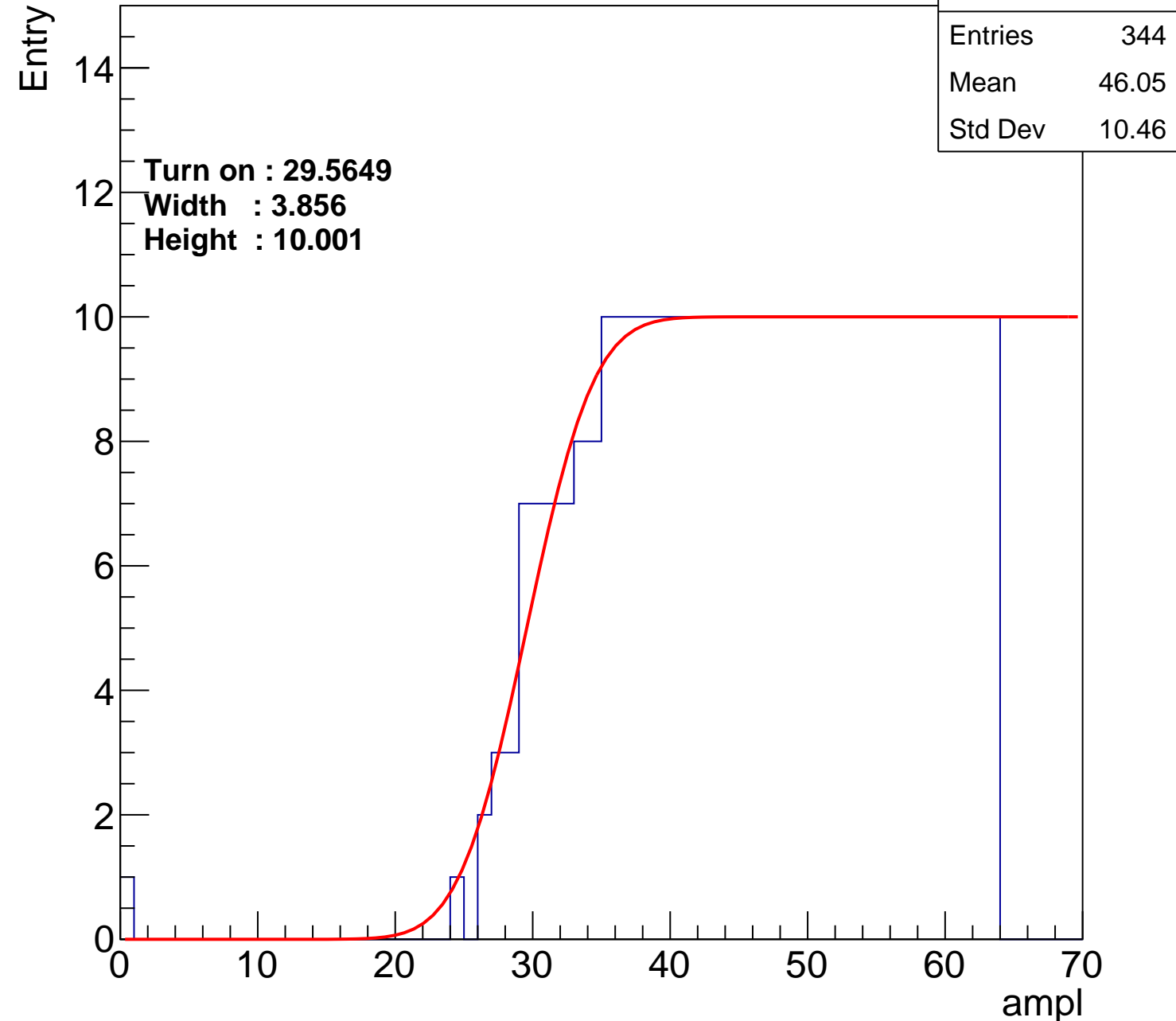
Width : 3.856

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch6

calib_packv5_042523_0143.root, FC#5, port B1

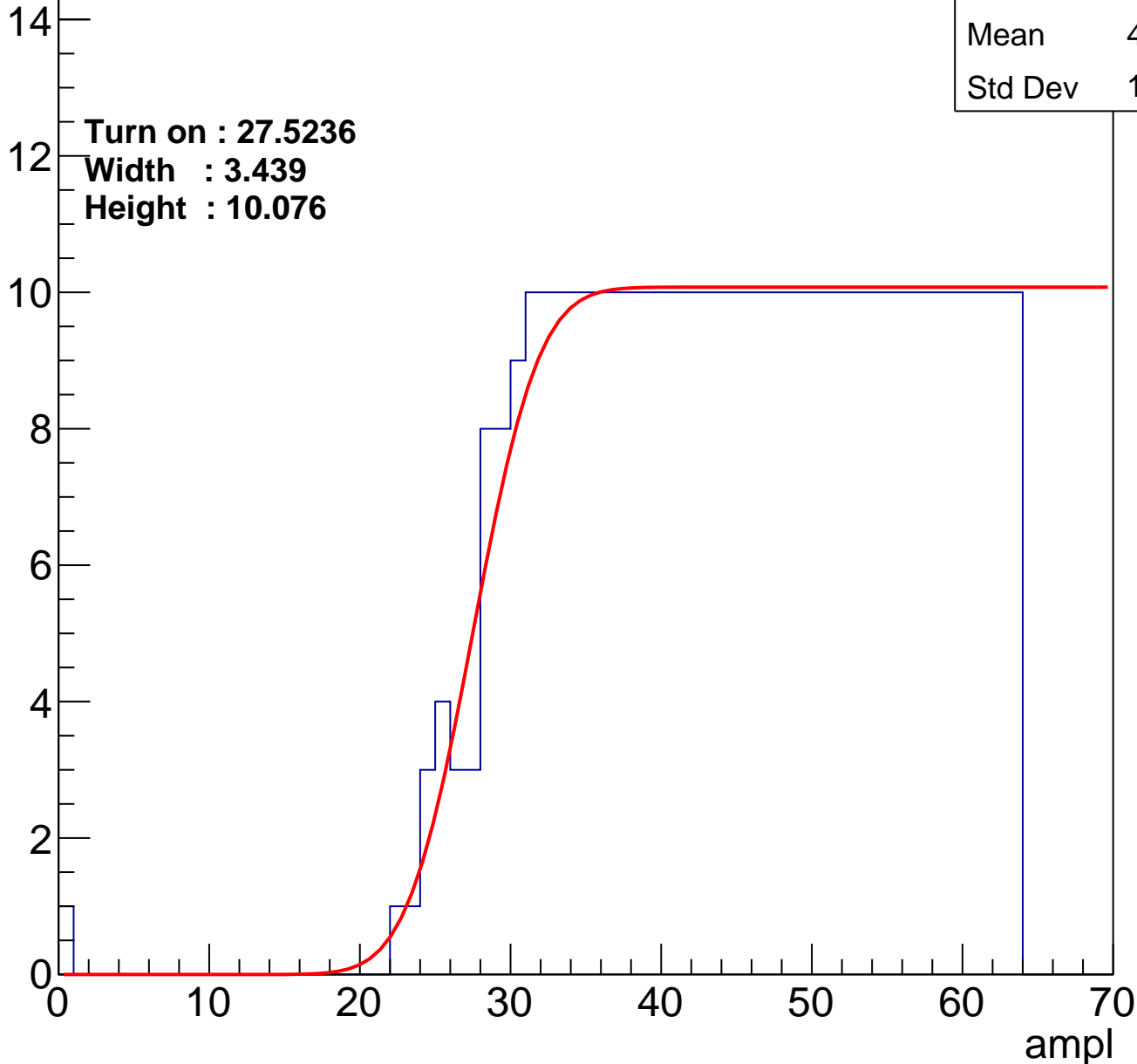
Entry

Entries	371
Mean	44.78
Std Dev	11.09

Turn on : 27.5236

Width : 3.439

Height : 10.076



B0L000S, U10-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.06
Std Dev	11.24

Turn on : 28.0759

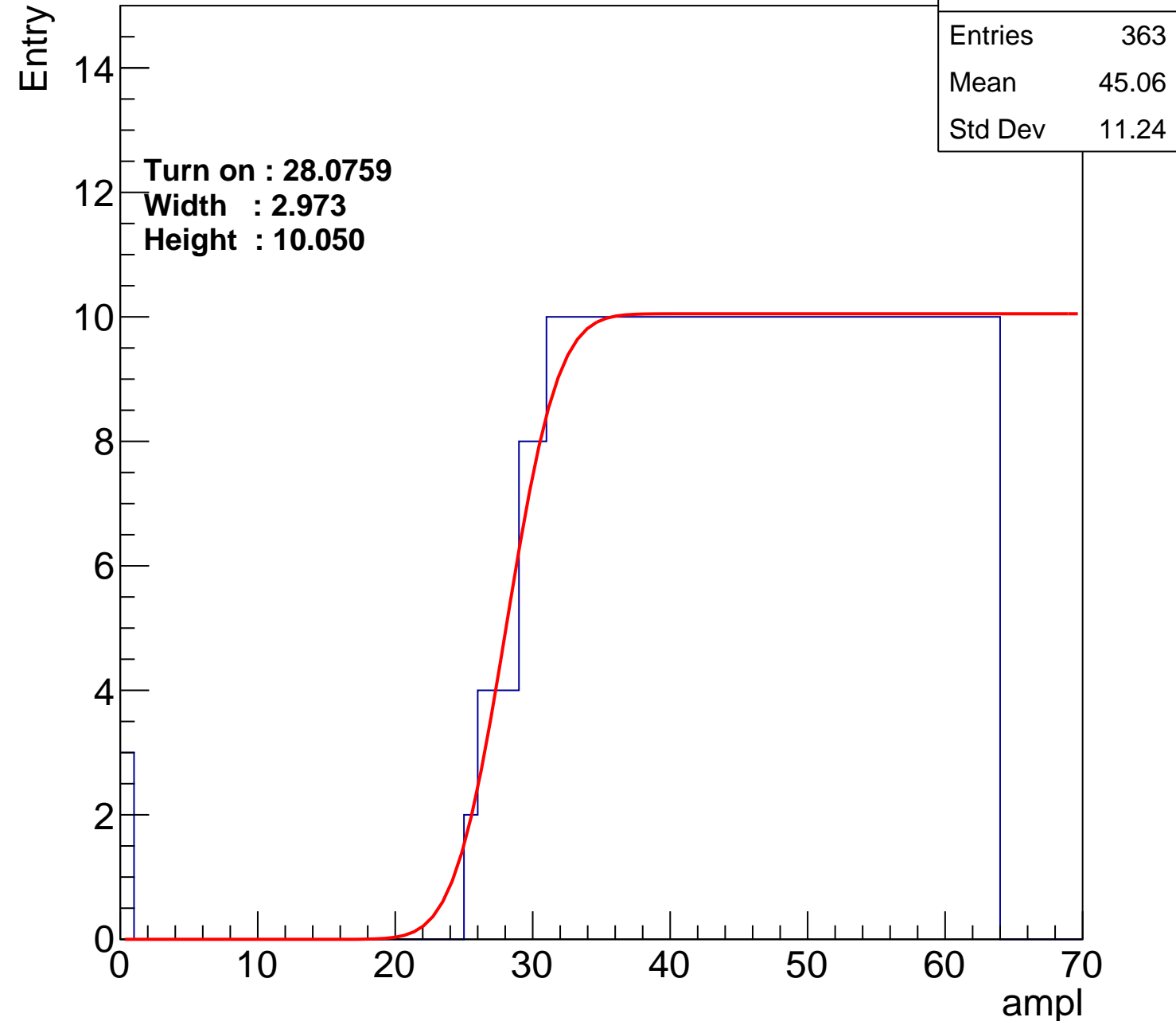
Width : 2.973

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.08
Std Dev	11.46

Turn on : 28.5644

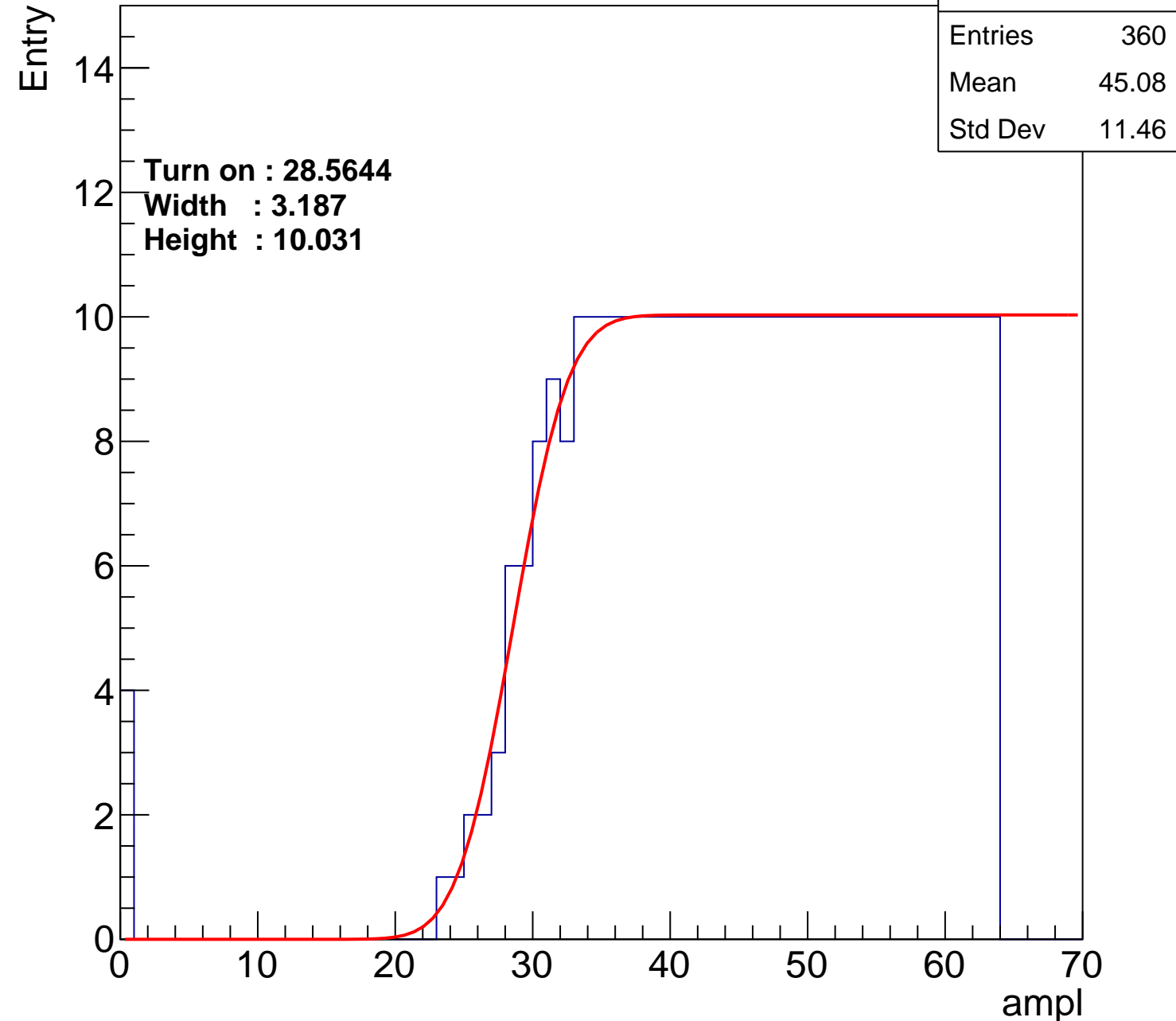
Width : 3.187

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.82
Std Dev	11.35

Turn on : 28.1798

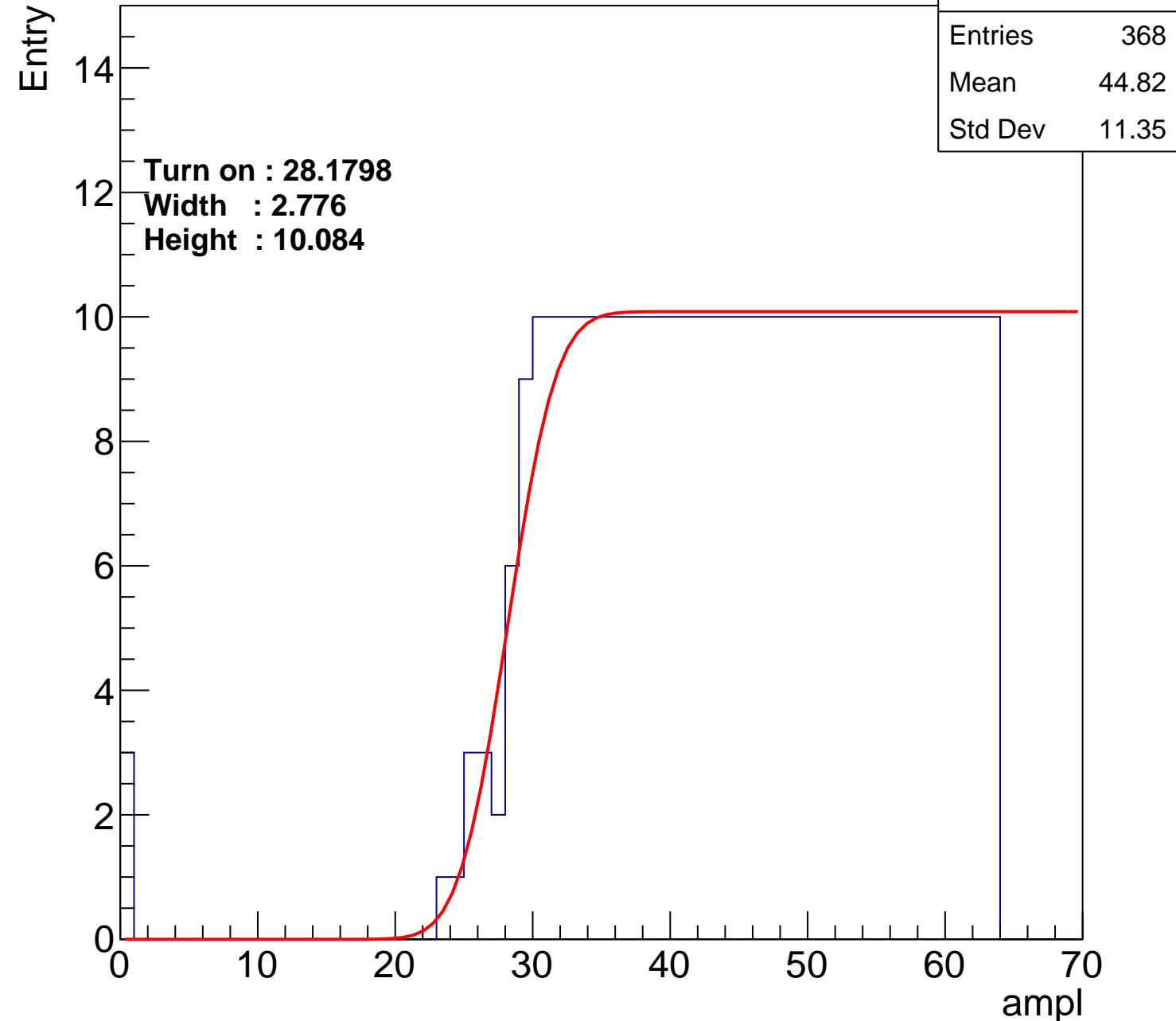
Width : 2.776

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.37
Std Dev	10.75

Turn on : 28.3021

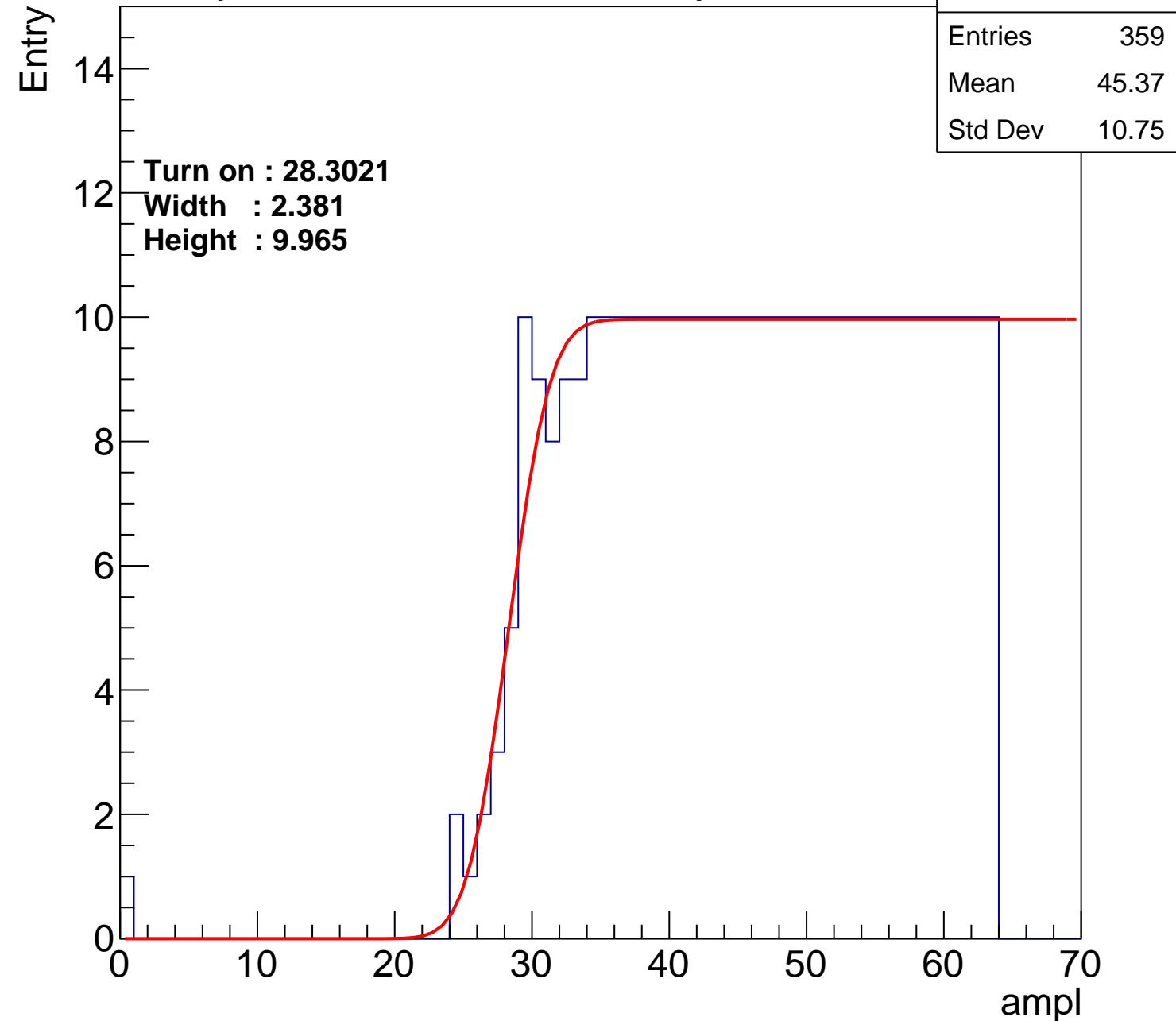
Width : 2.381

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.39
Std Dev	10.94

Turn on : 28.8799

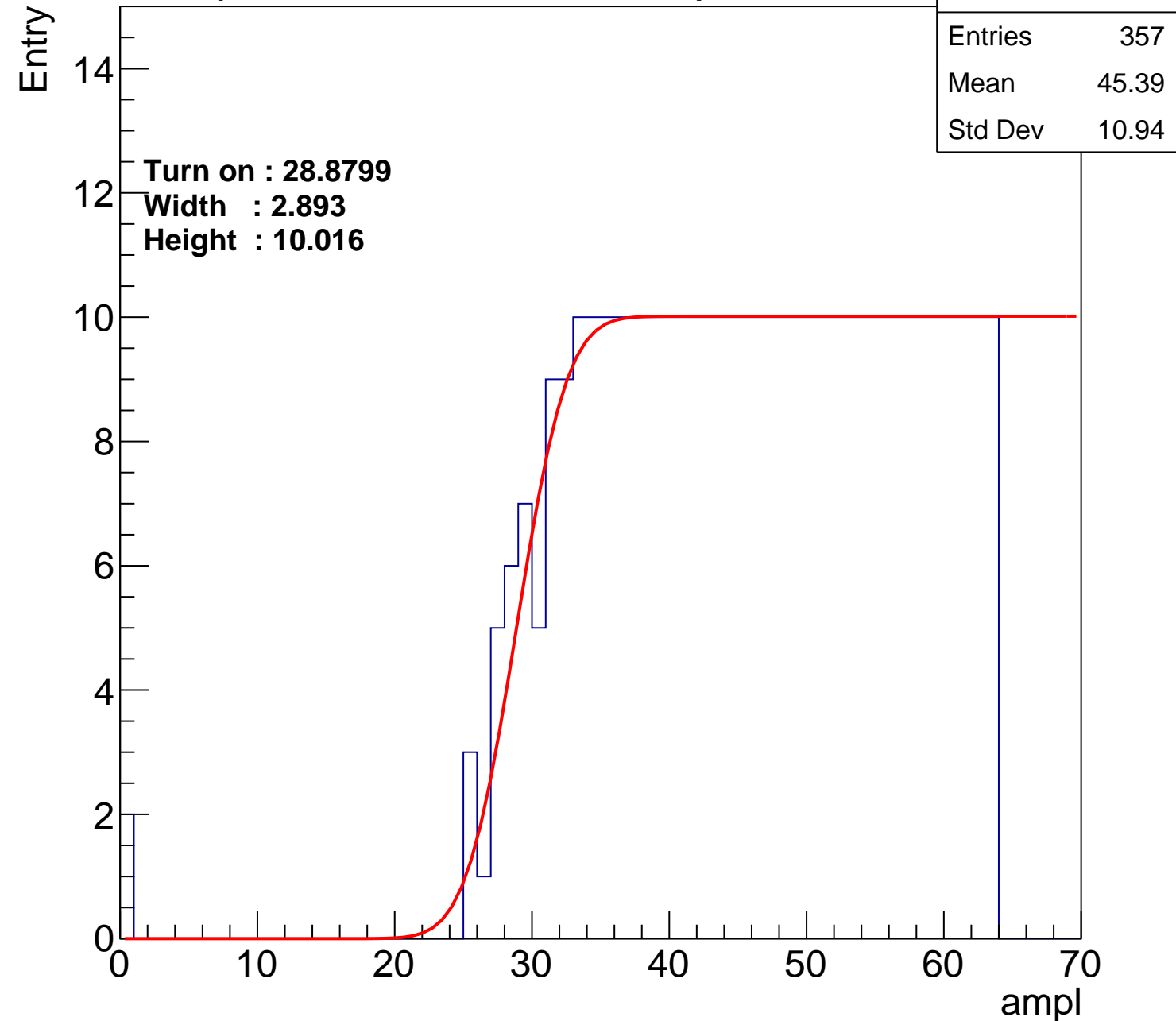
Width : 2.893

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.65
Std Dev	11.51

Turn on : 27.6593

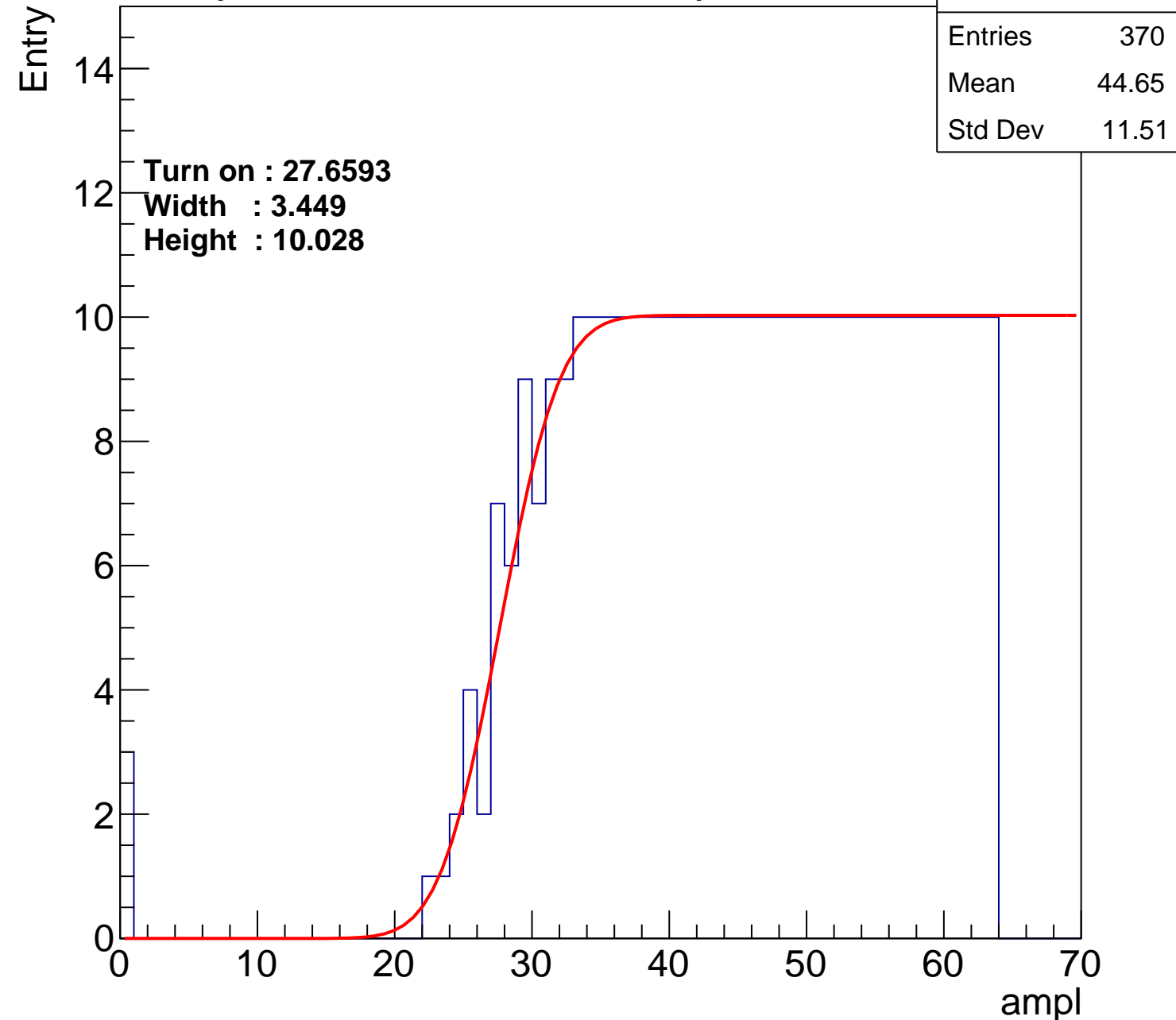
Width : 3.449

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch13

calib_packv5_042523_0143.root, FC#5, port B1

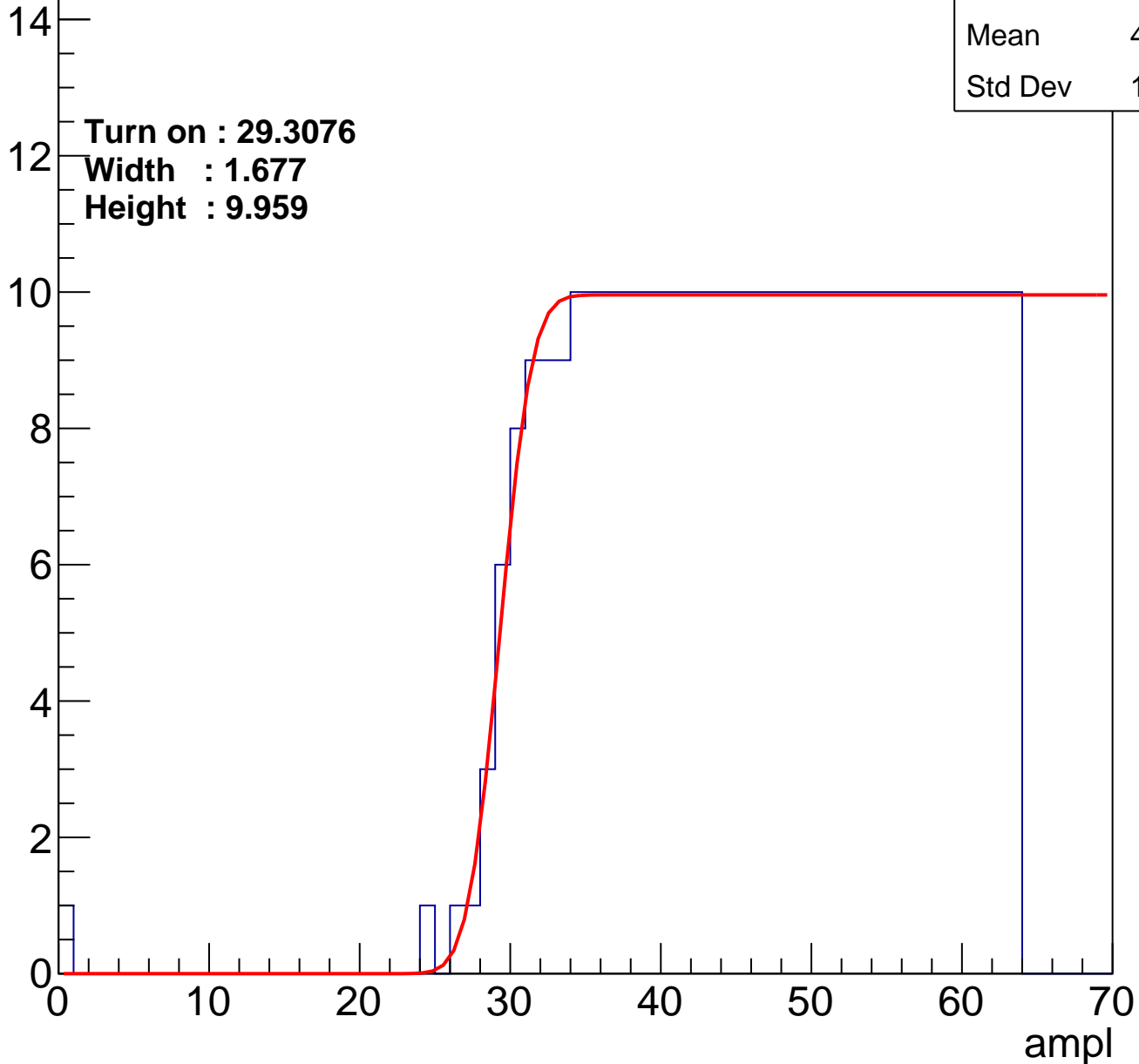
Entries	348
Mean	45.95
Std Dev	10.42

Turn on : 29.3076

Width : 1.677

Height : 9.959

Entry



B0L000S, U10-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.9937

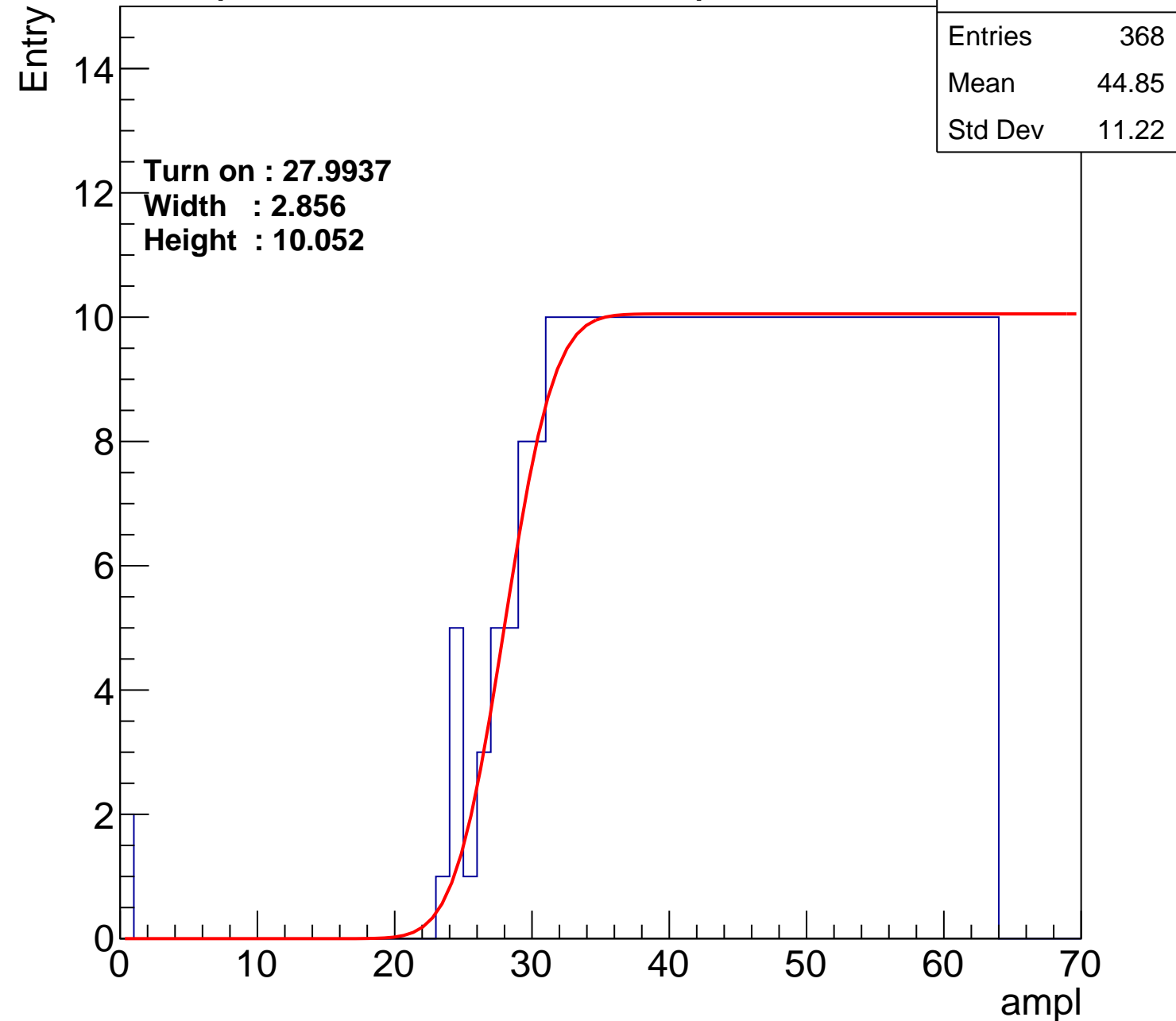
Width : 2.856

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch15

calib_packv5_042523_0143.root, FC#5, port B1

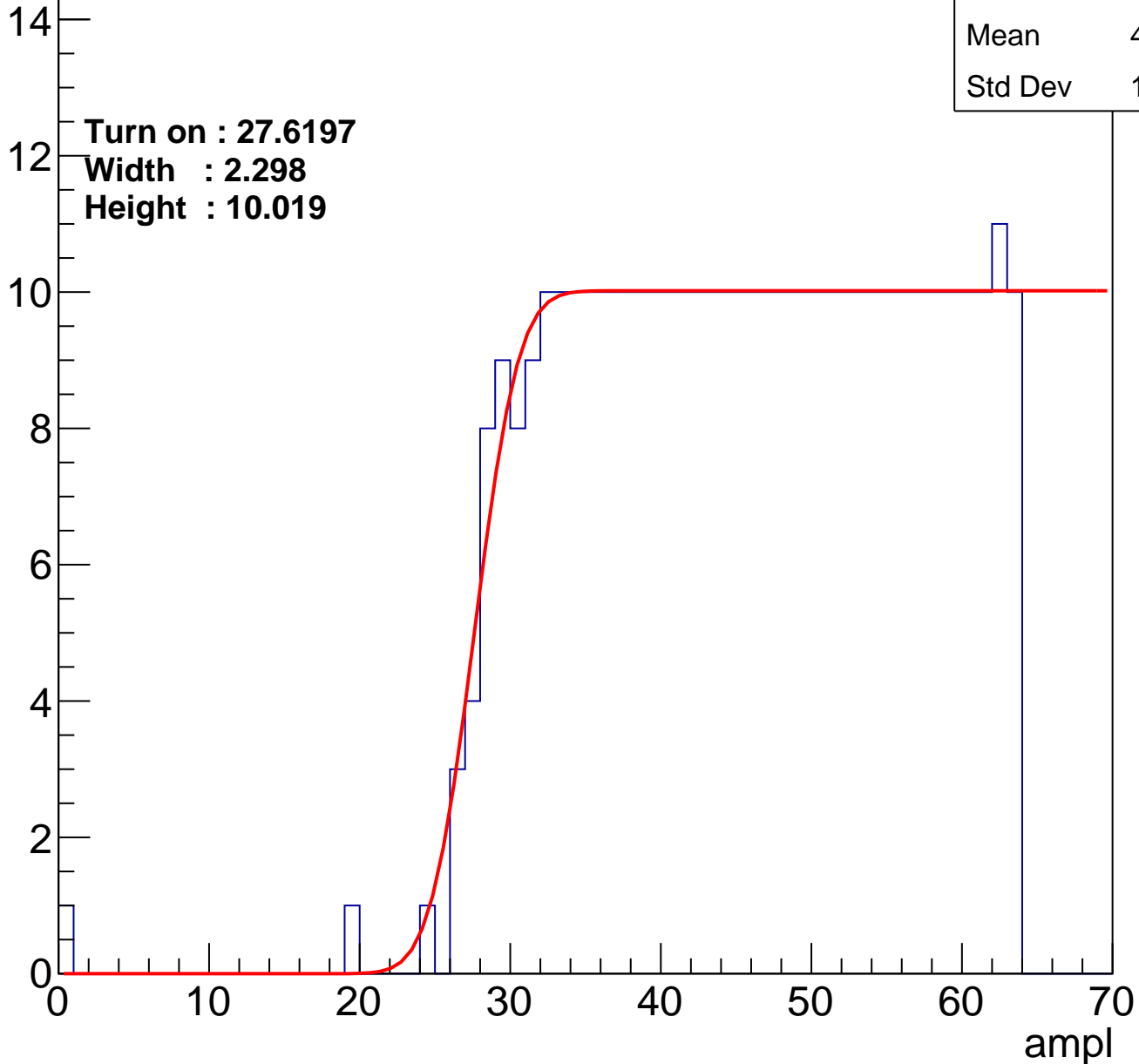
Entries	365
Mean	45.19
Std Dev	10.89

Turn on : 27.6197

Width : 2.298

Height : 10.019

Entry



B0L000S, U10-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.4
Std Dev	11.08

Turn on : 28.0968

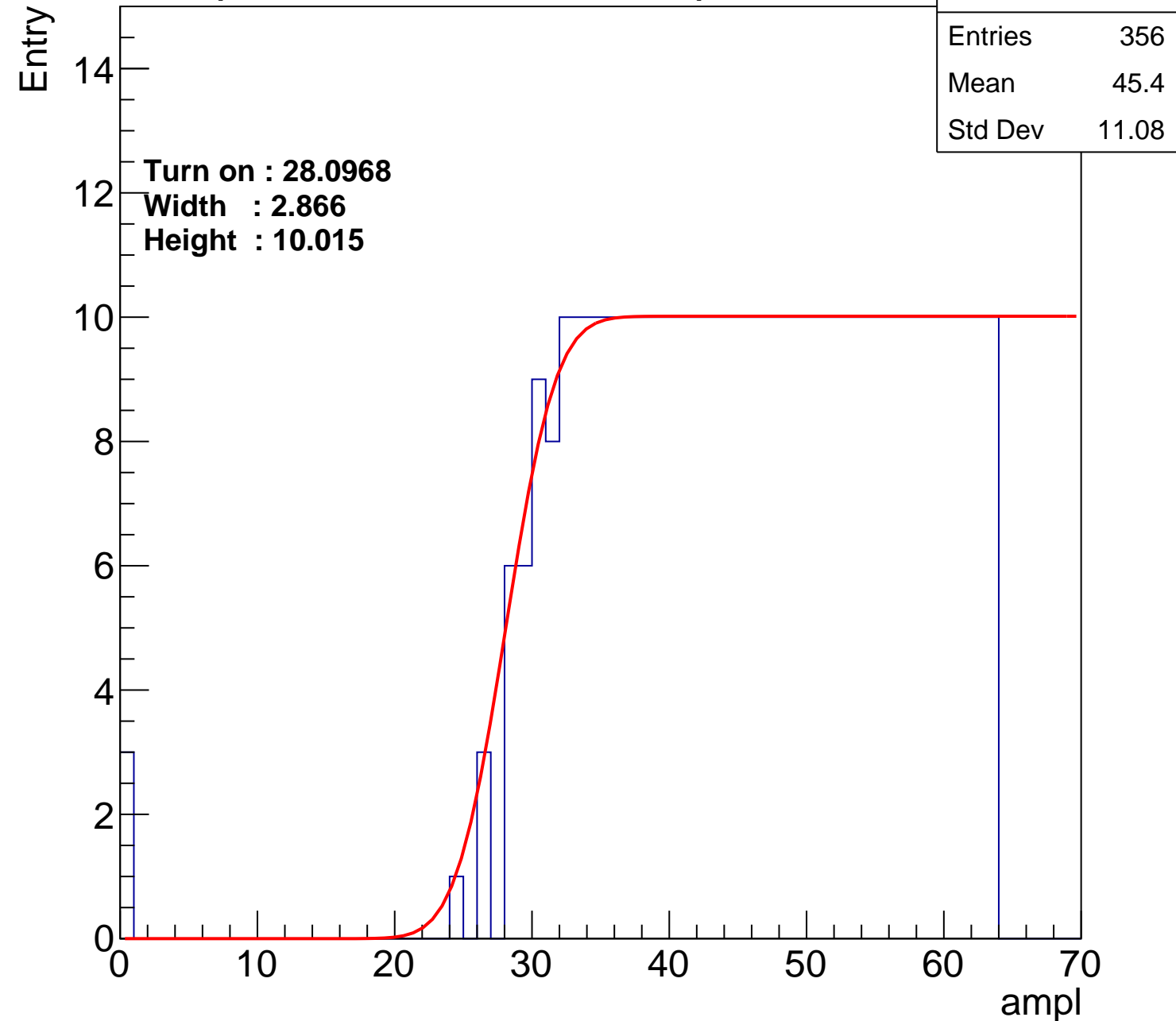
Width : 2.866

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 27.6313

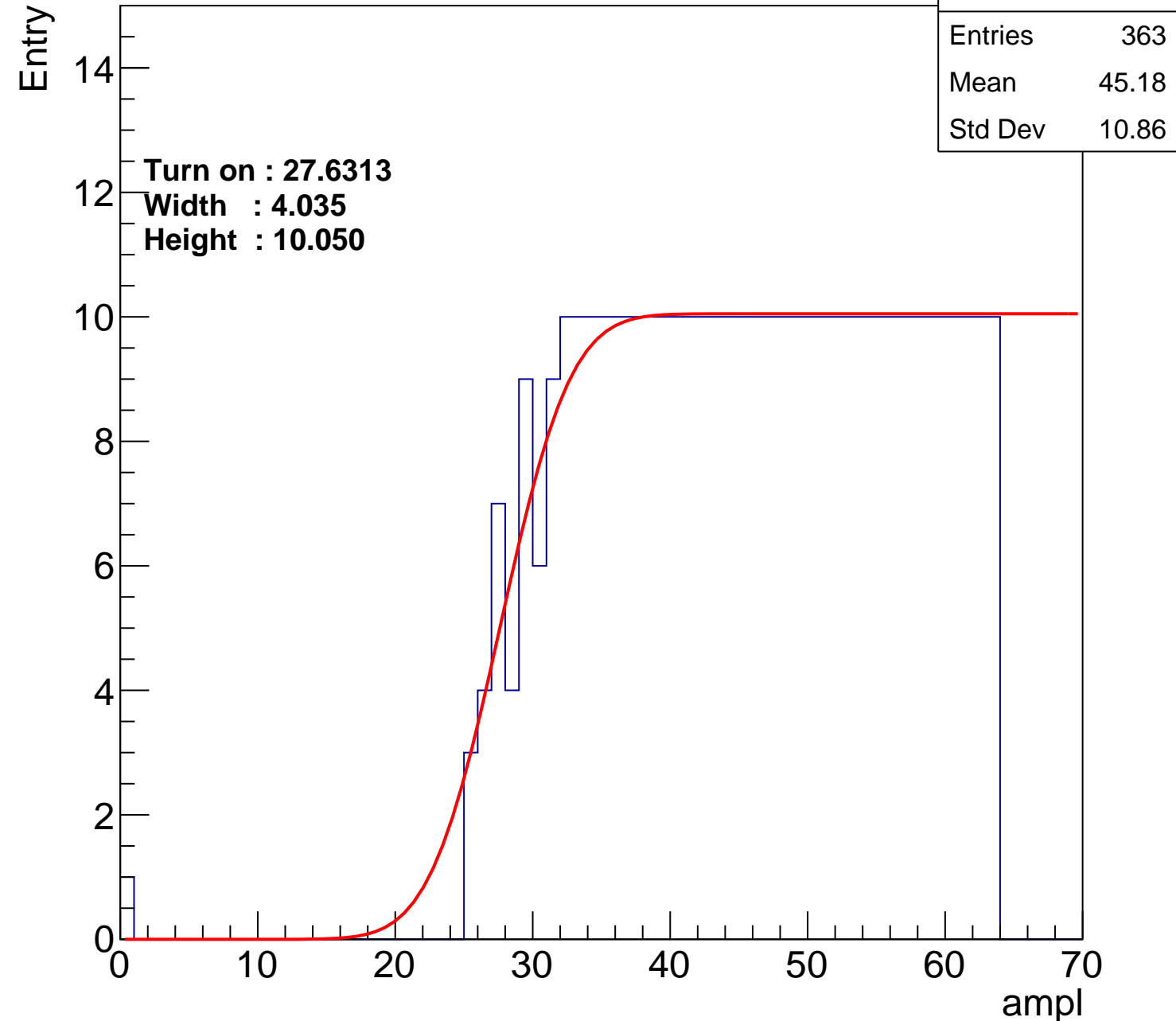
Width : 4.035

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch18

calib_packv5_042523_0143.root, FC#5, port B1

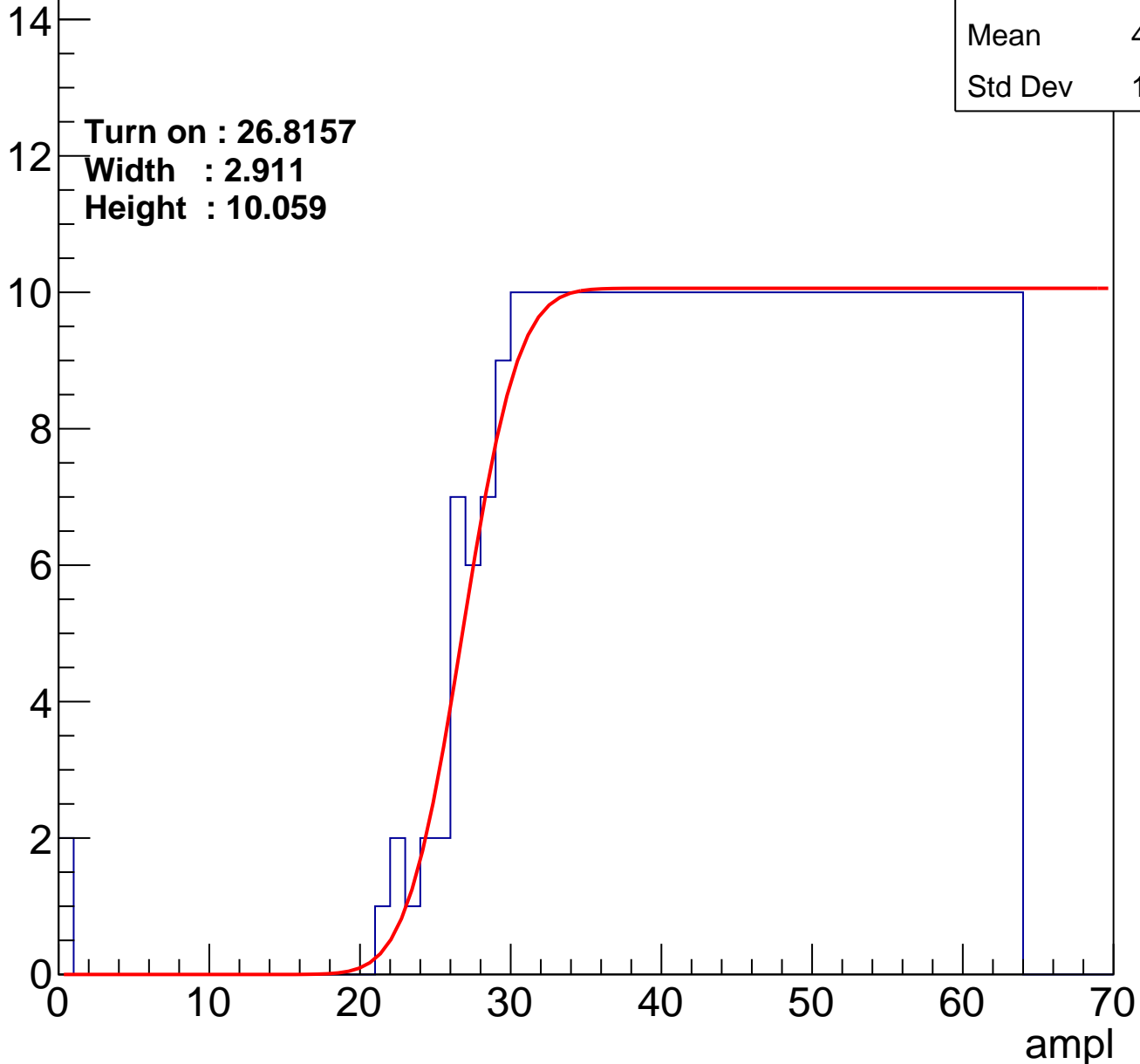
Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.8157

Width : 2.911

Height : 10.059

Entry

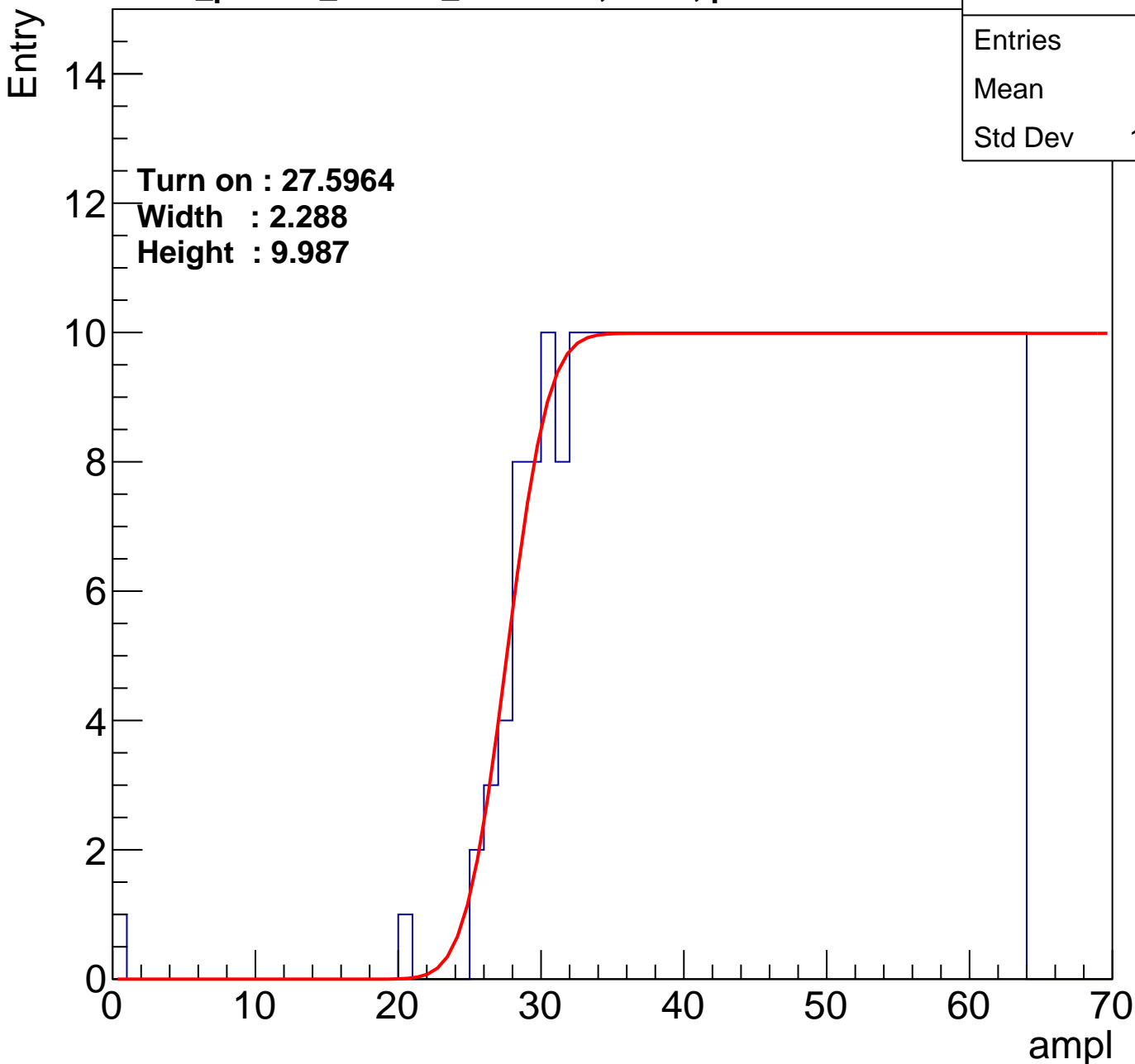


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.1
Std Dev	10.89

Height : 9.987



B0L000S, U10-ch20

calib_packv5_042523_0143.root, FC#5, port B1

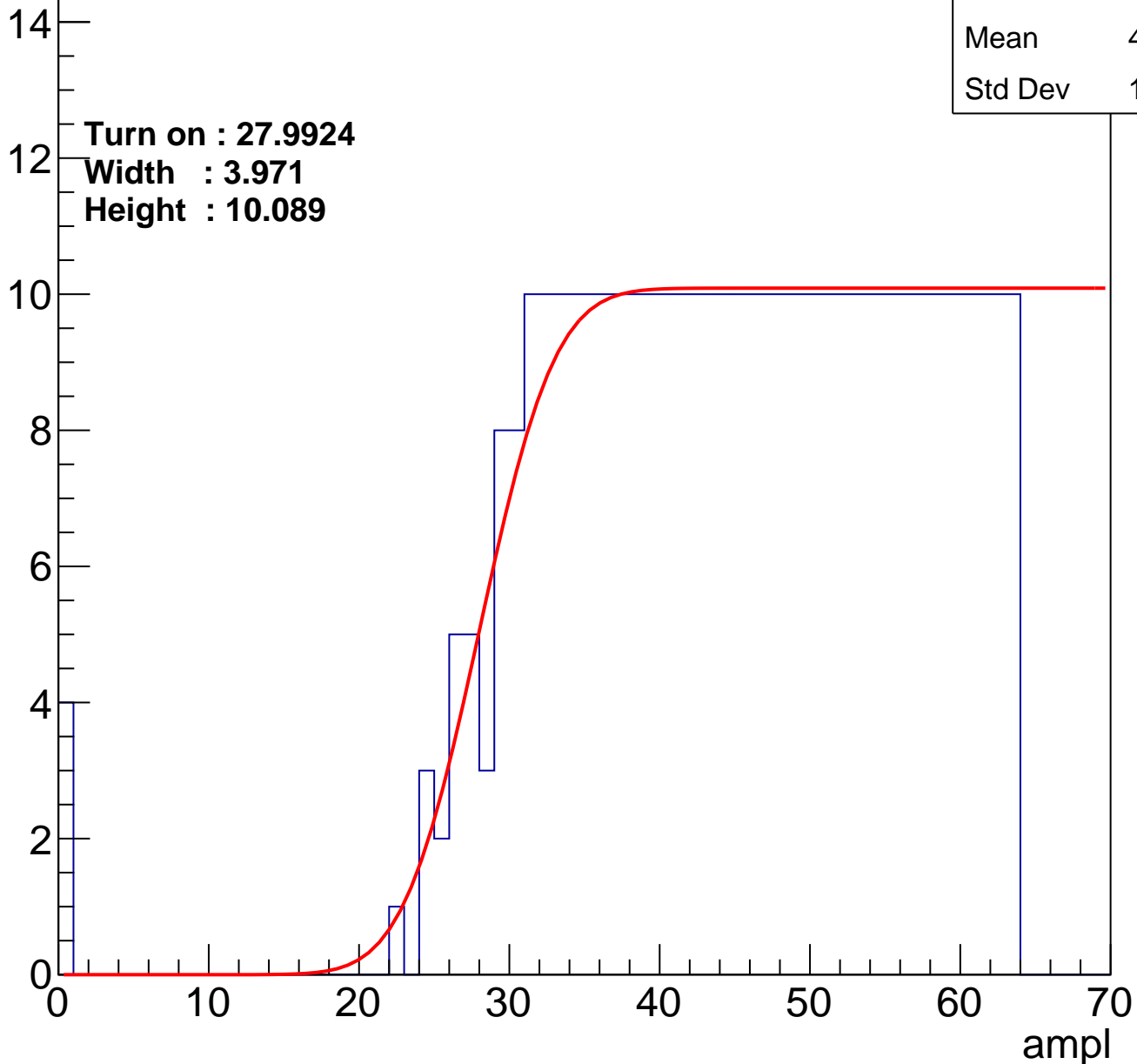
Entries	369
Mean	44.65
Std Dev	11.65

Turn on : 27.9924

Width : 3.971

Height : 10.089

Entry



B0L000S, U10-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	46.15
Std Dev	10.36

Turn on : 29.9226

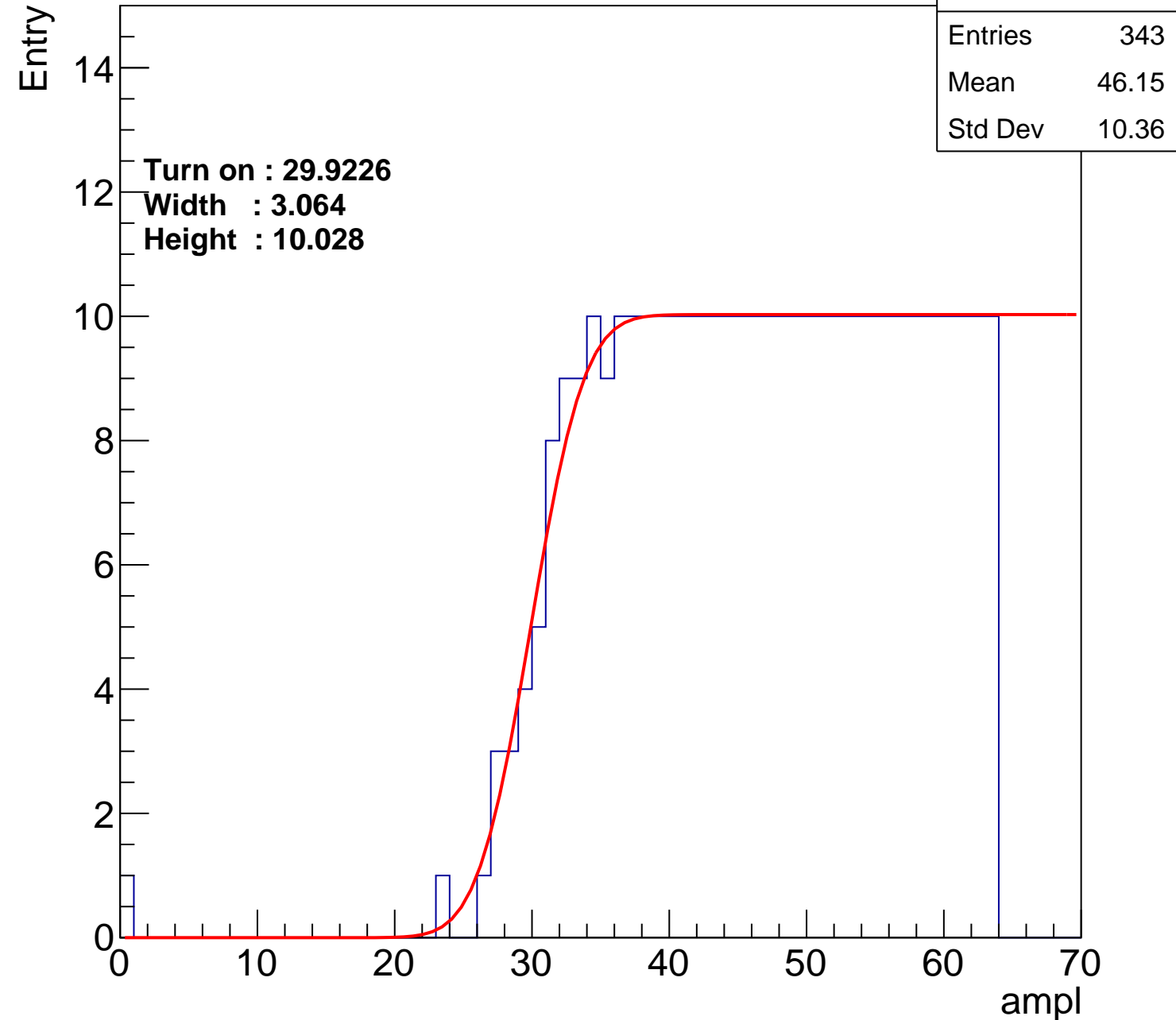
Width : 3.064

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch22

calib_packv5_042523_0143.root, FC#5, port B1

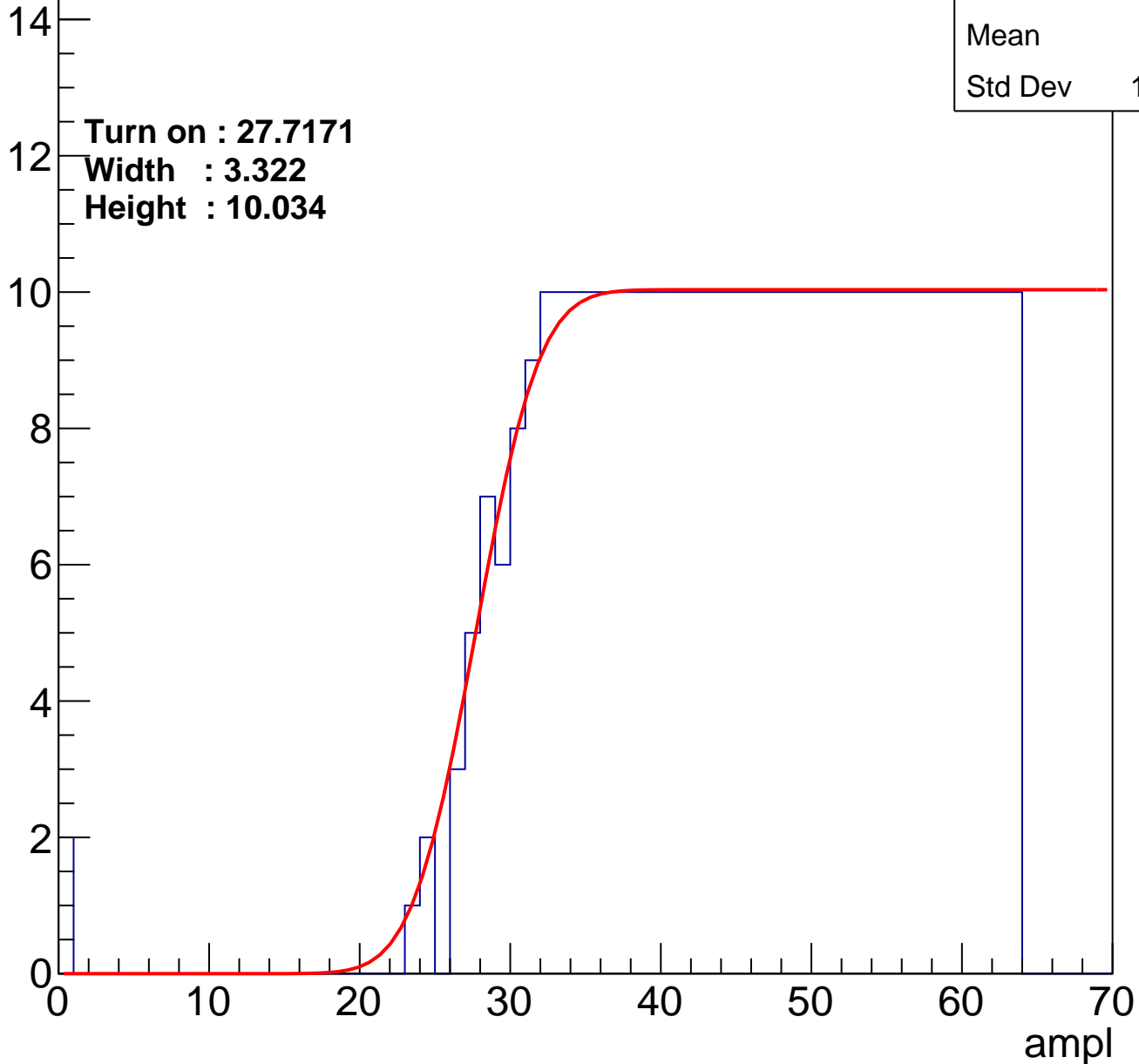
Entries	363
Mean	45.1
Std Dev	11.07

Turn on : 27.7171

Width : 3.322

Height : 10.034

Entry



B0L000S, U10-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.5662

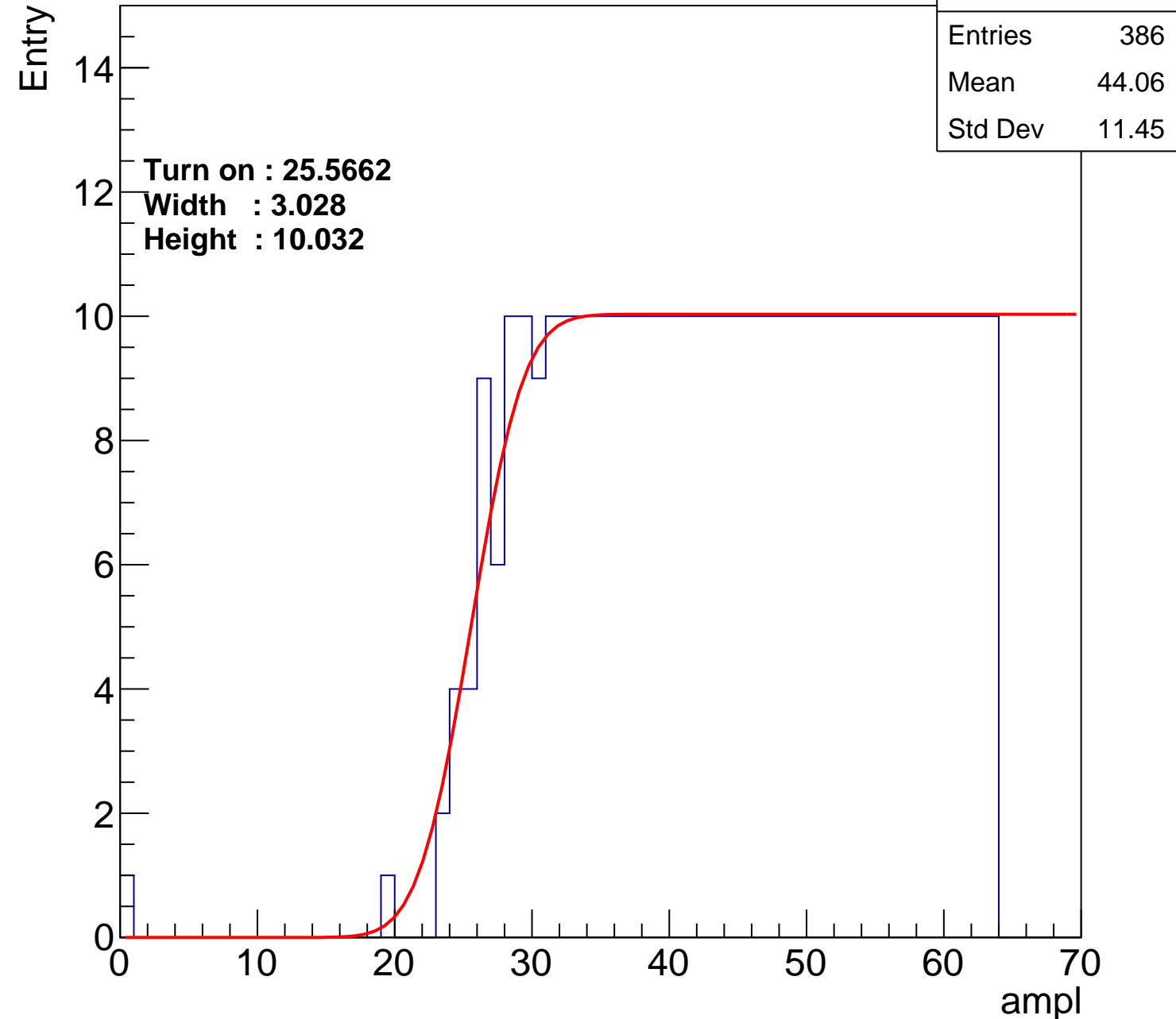
Width : 3.028

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch24

calib_packv5_042523_0143.root, FC#5, port B1

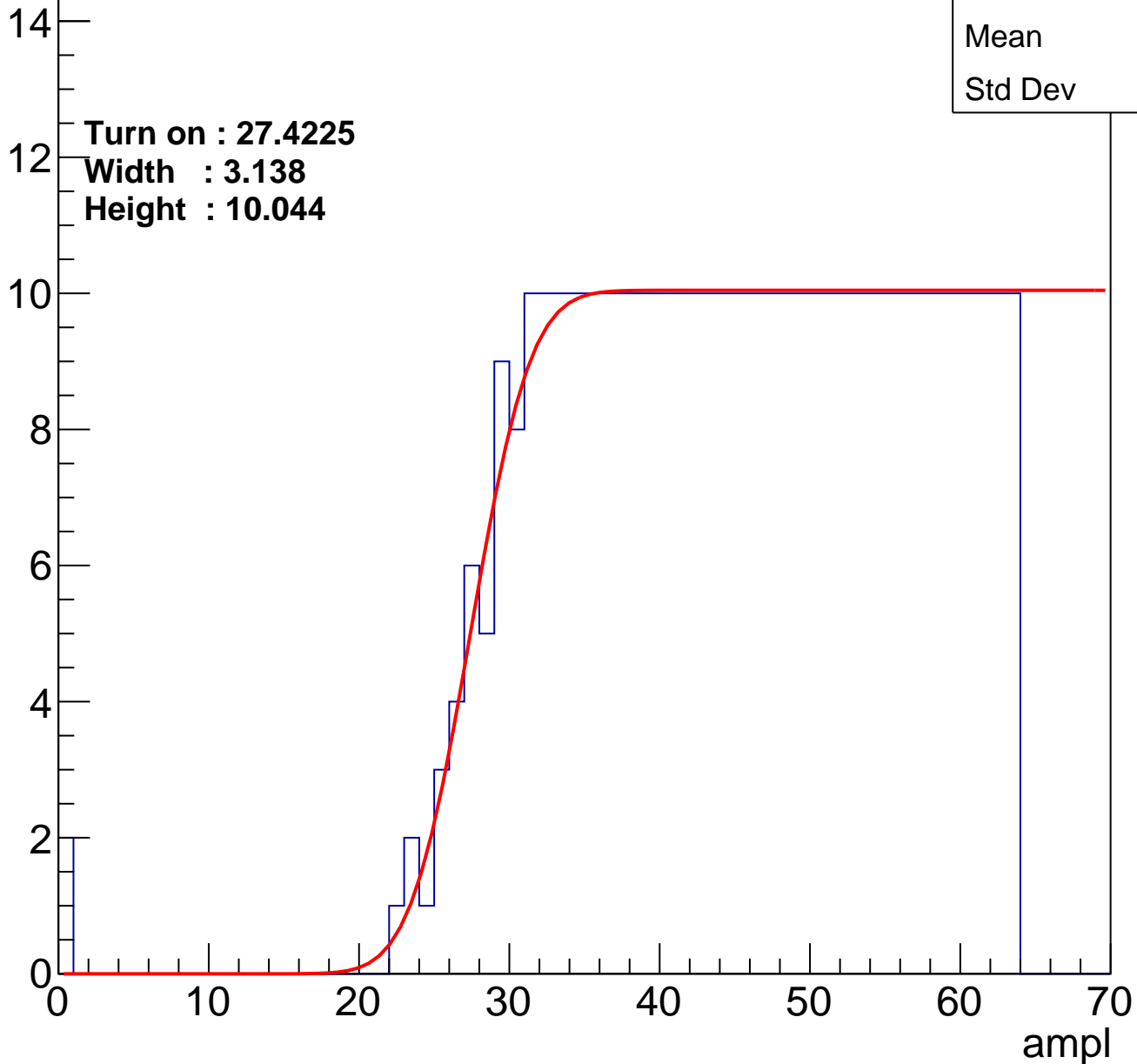
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.4225

Width : 3.138

Height : 10.044

Entry



B0L000S, U10-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.34
Std Dev	10.76

Turn on : 28.0602

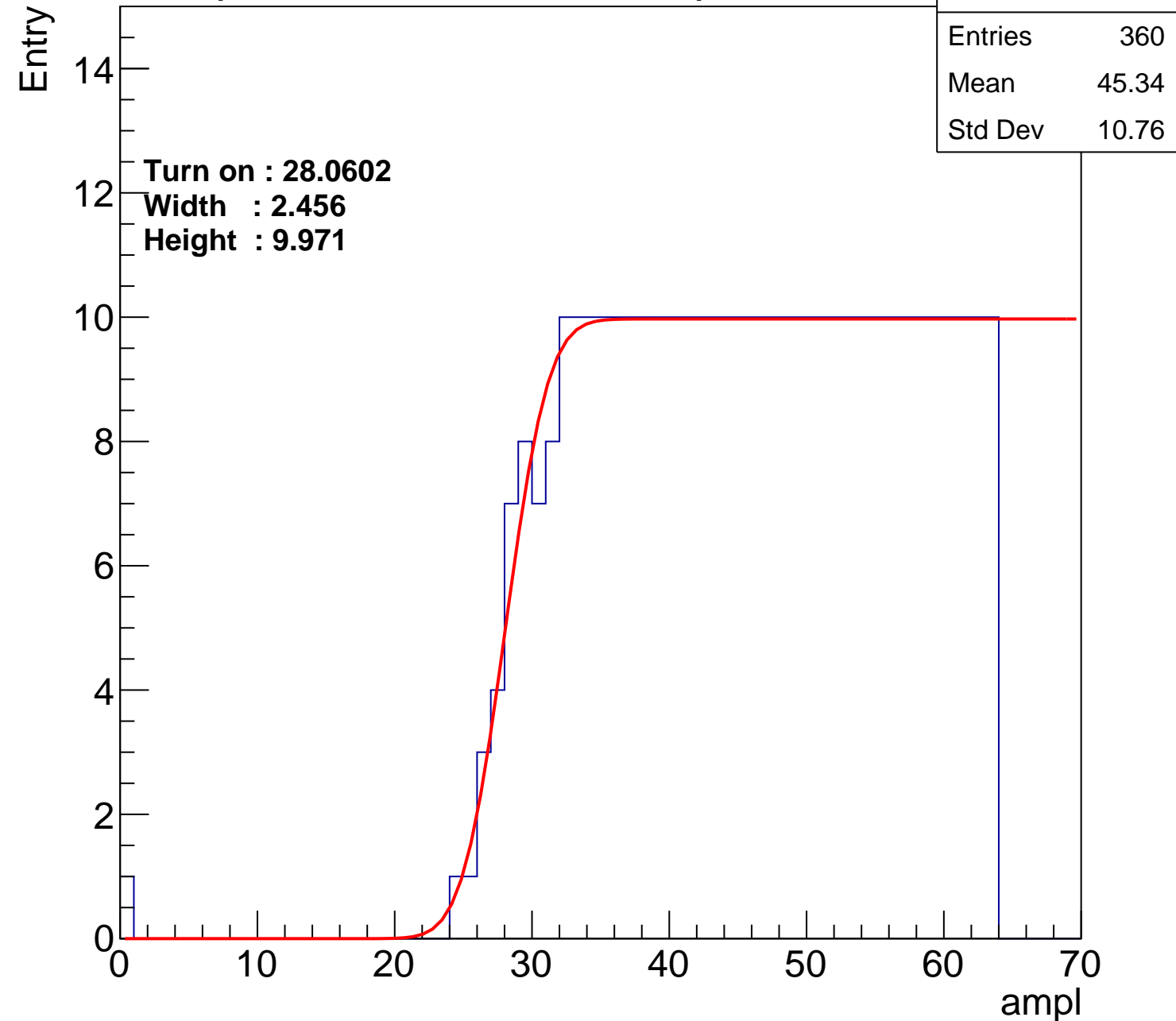
Width : 2.456

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch26

calib_packv5_042523_0143.root, FC#5, port B1

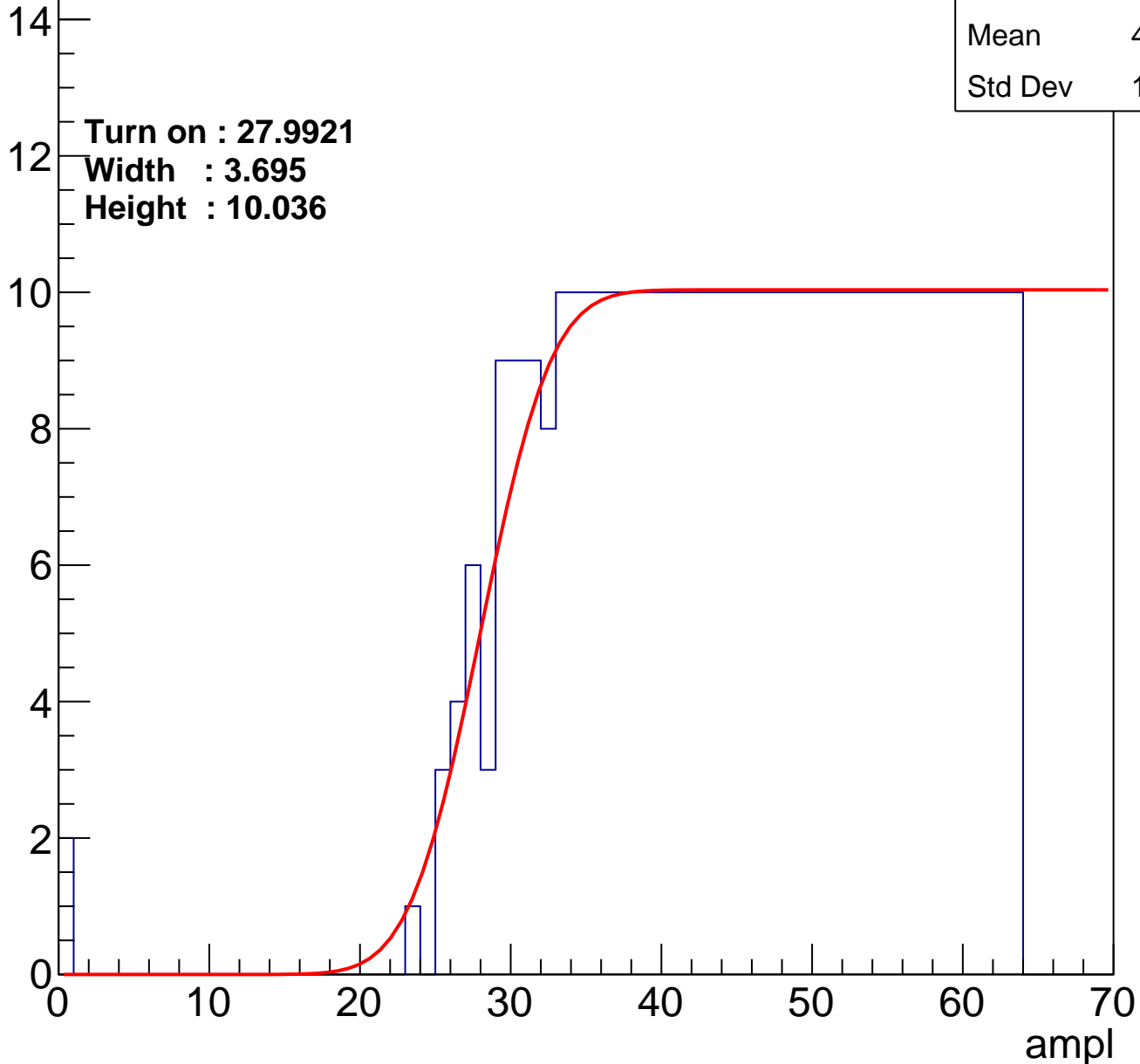
Entries	364
Mean	45.04
Std Dev	11.12

Turn on : 27.9921

Width : 3.695

Height : 10.036

Entry



B0L000S, U10-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.46
Std Dev	10.73

Turn on : 28.7084

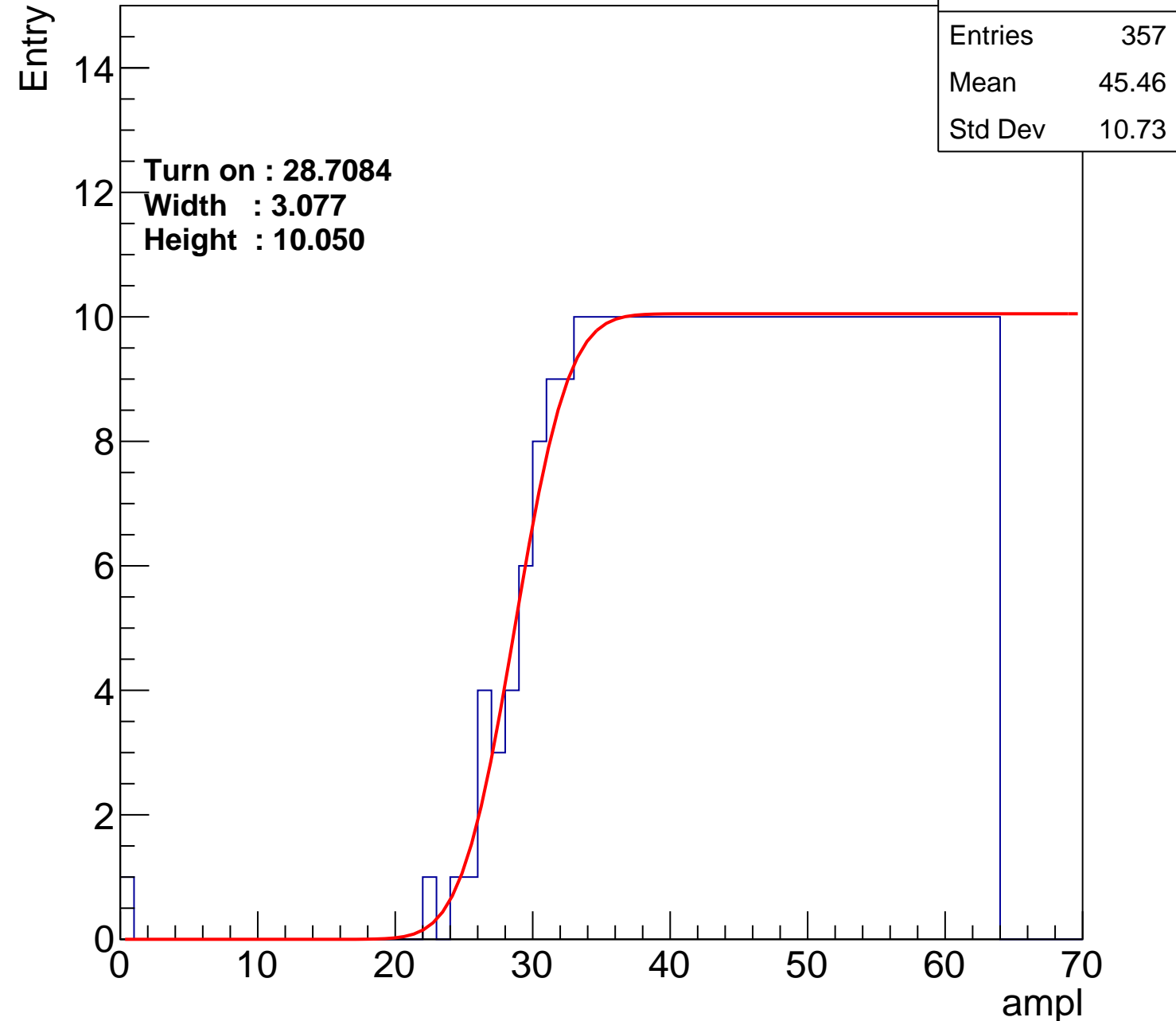
Width : 3.077

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl

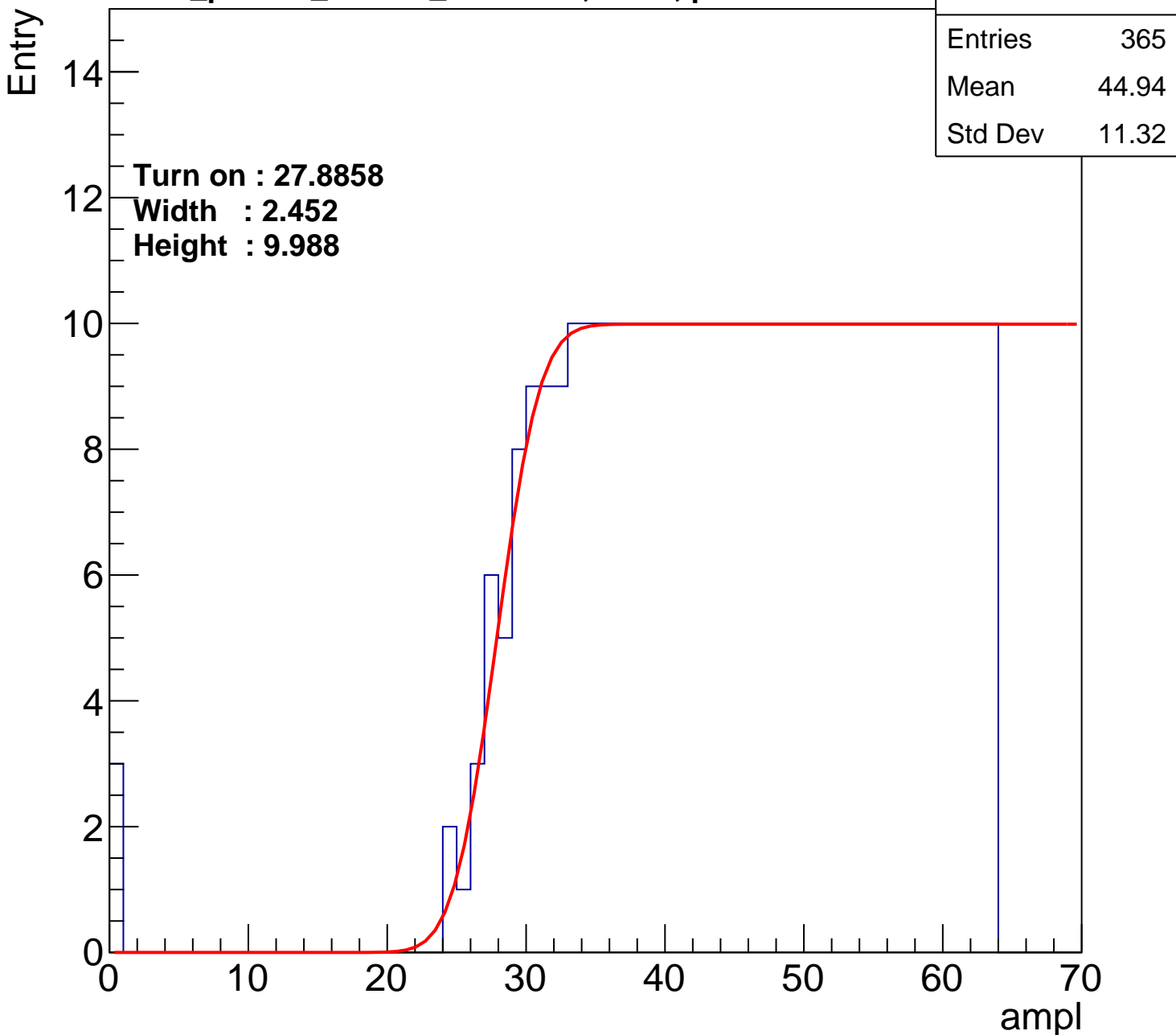


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.94
Std Dev	11.32

Height : 9.988



calib_packv5_042523_0143.root, FC#5, port B1

Entries	342
Mean	46.04
Std Dev	10.82

Height : 10.048



B0L000S, U10-ch30

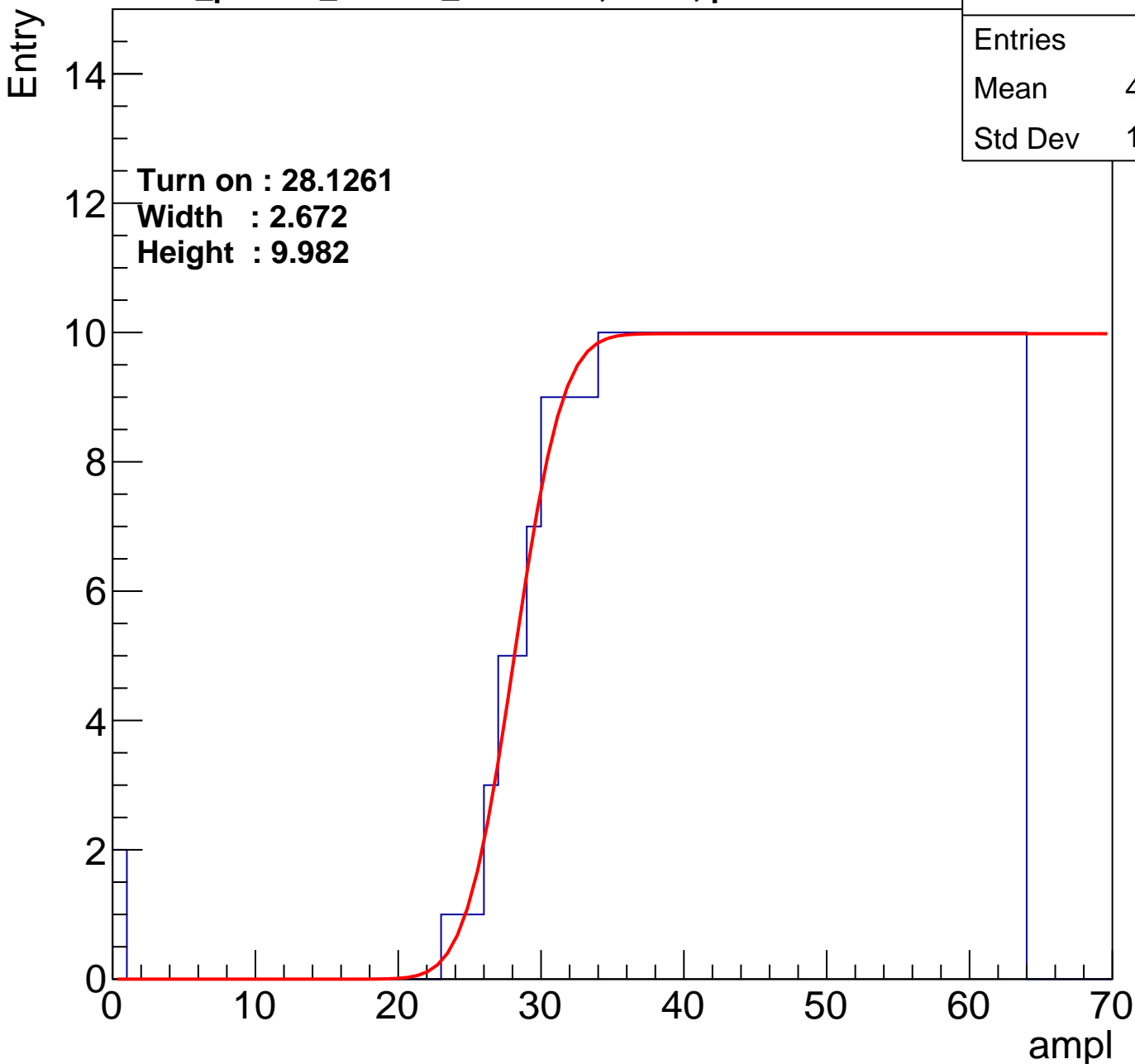
calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 28.1261

Width : 2.672

Height : 9.982



B0L000S, U10-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
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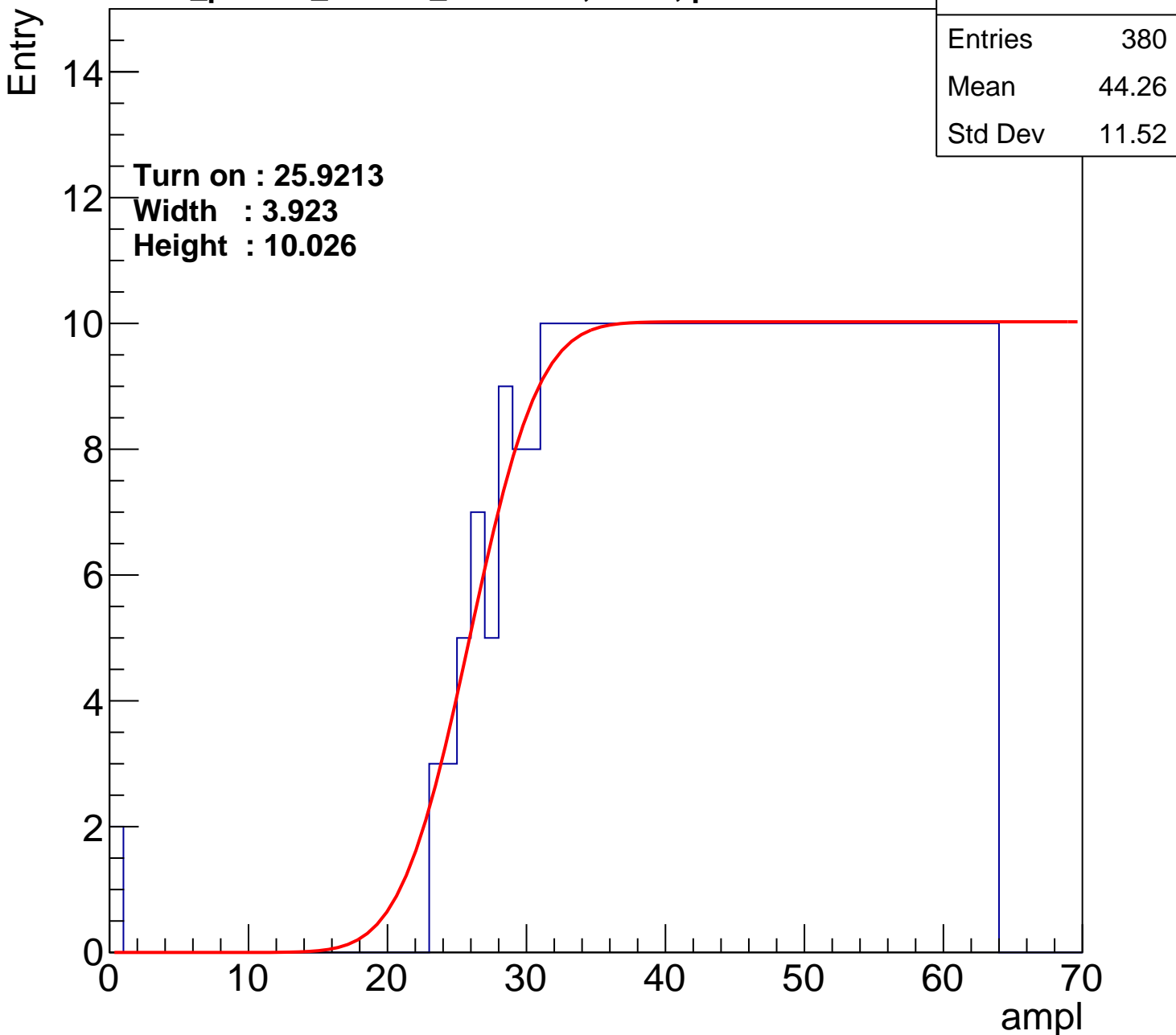
Mean	44.26
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Std Dev	11.52
---------	-------

Turn on : 25.9213

Width : 3.923

Height : 10.026



B0L000S, U10-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	43.92
Std Dev	12.24

Turn on : 27.1964

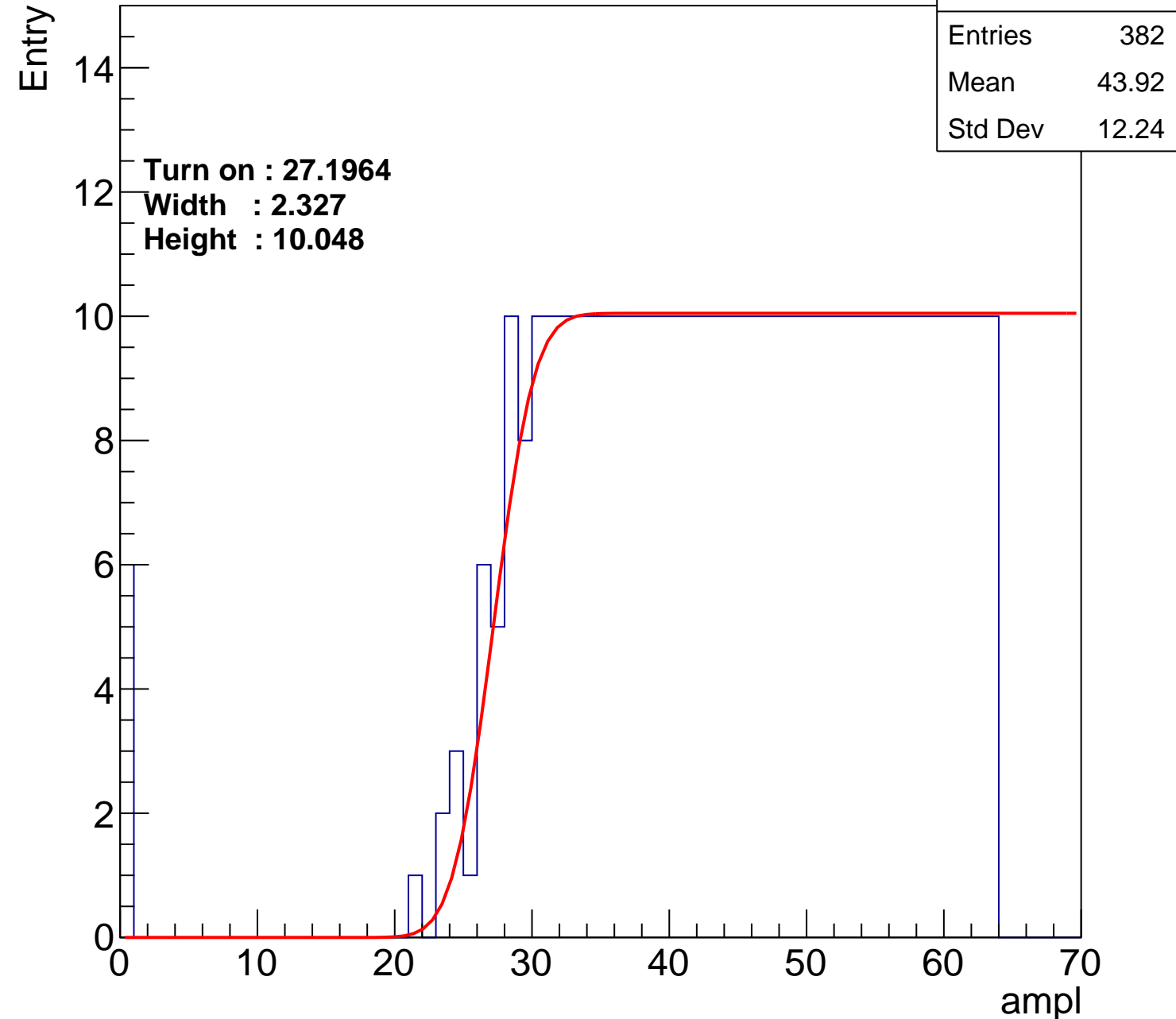
Width : 2.327

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch33

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.96
Std Dev	11.13

Turn on : 27.9215

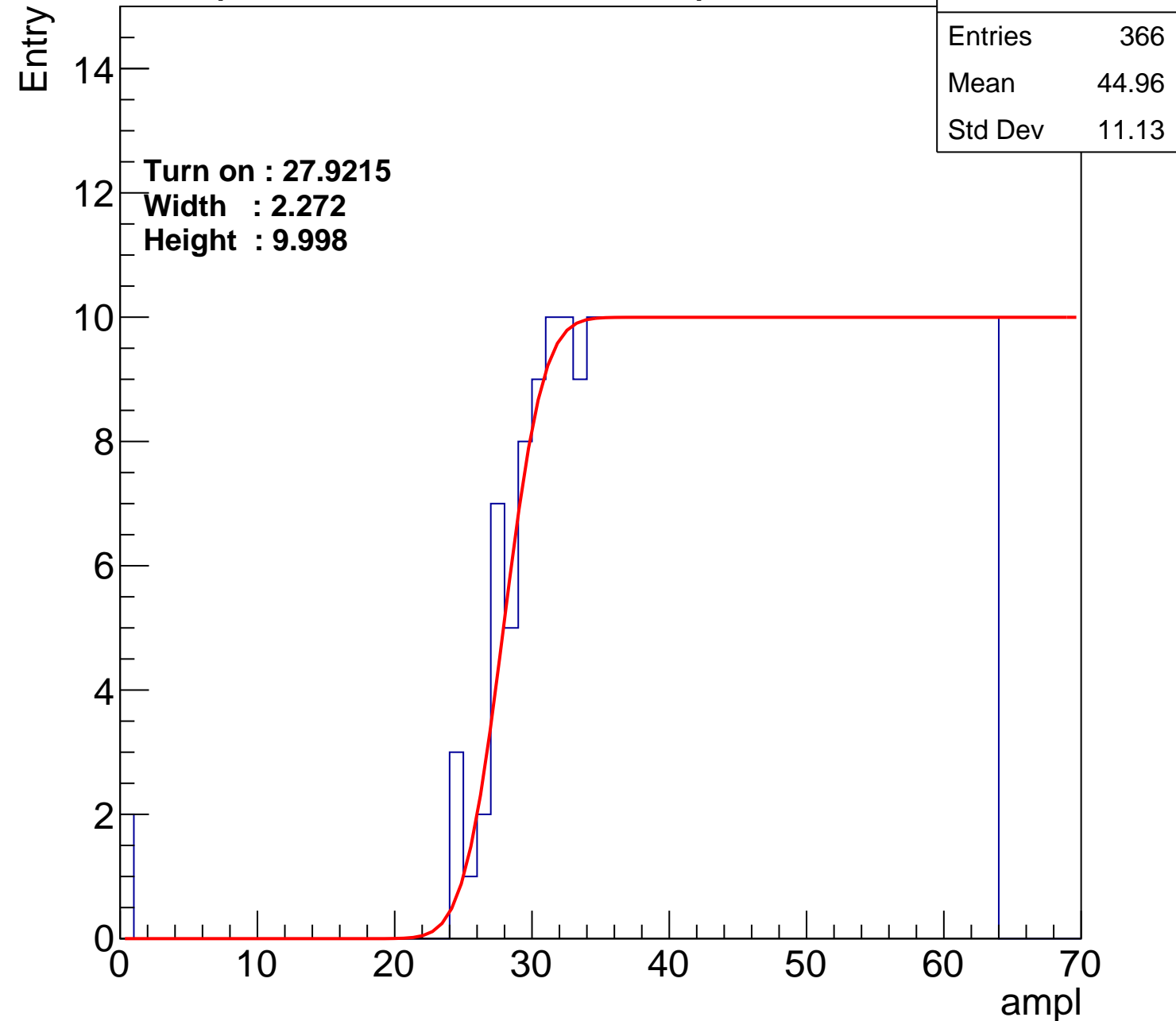
Width : 2.272

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.29
Std Dev	10.84

Turn on : 27.7924

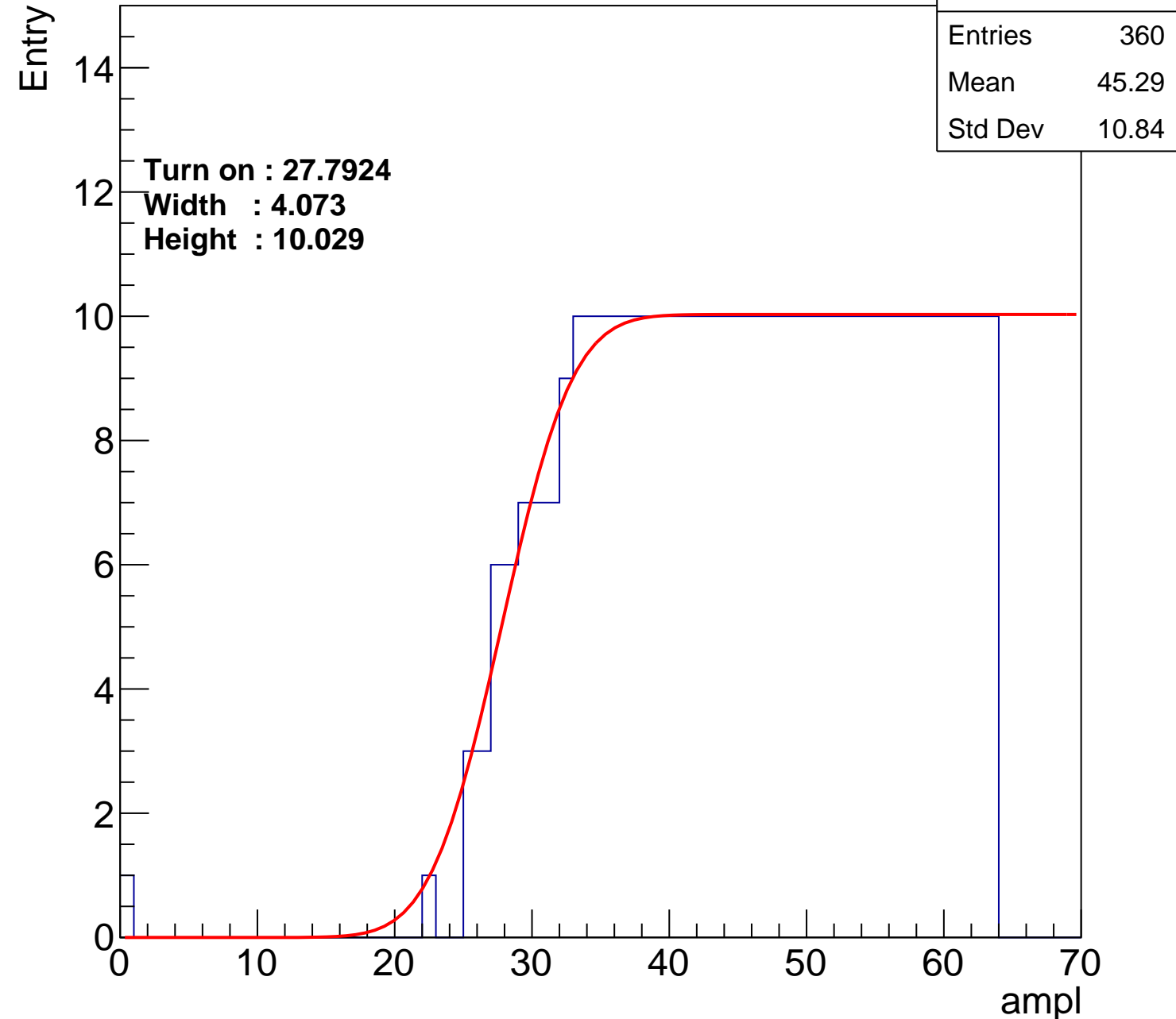
Width : 4.073

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch35

calib_packv5_042523_0143.root, FC#5, port B1

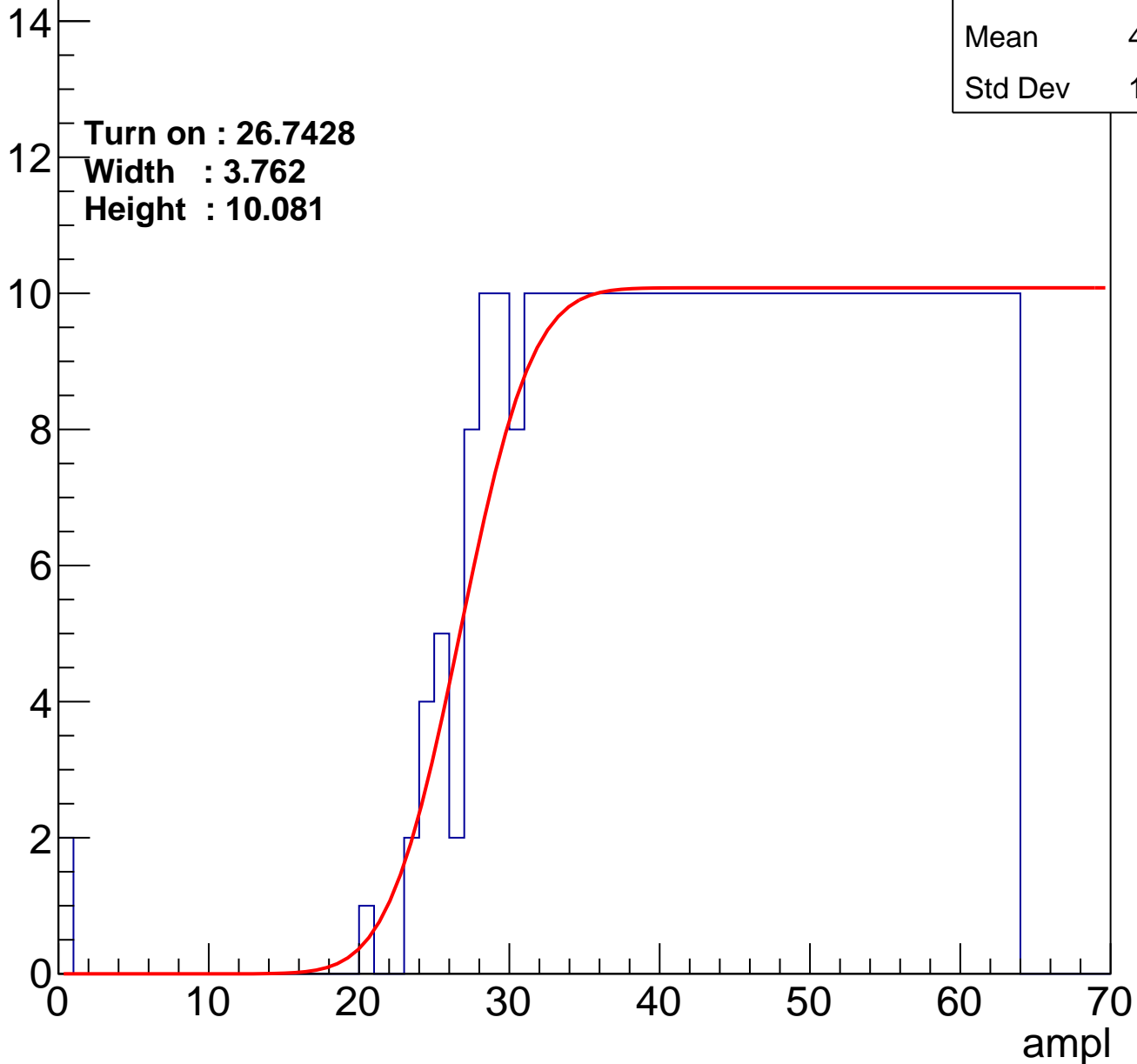
Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.7428

Width : 3.762

Height : 10.081

Entry



B0L000S, U10-ch36

calib_packv5_042523_0143.root, FC#5, port B1

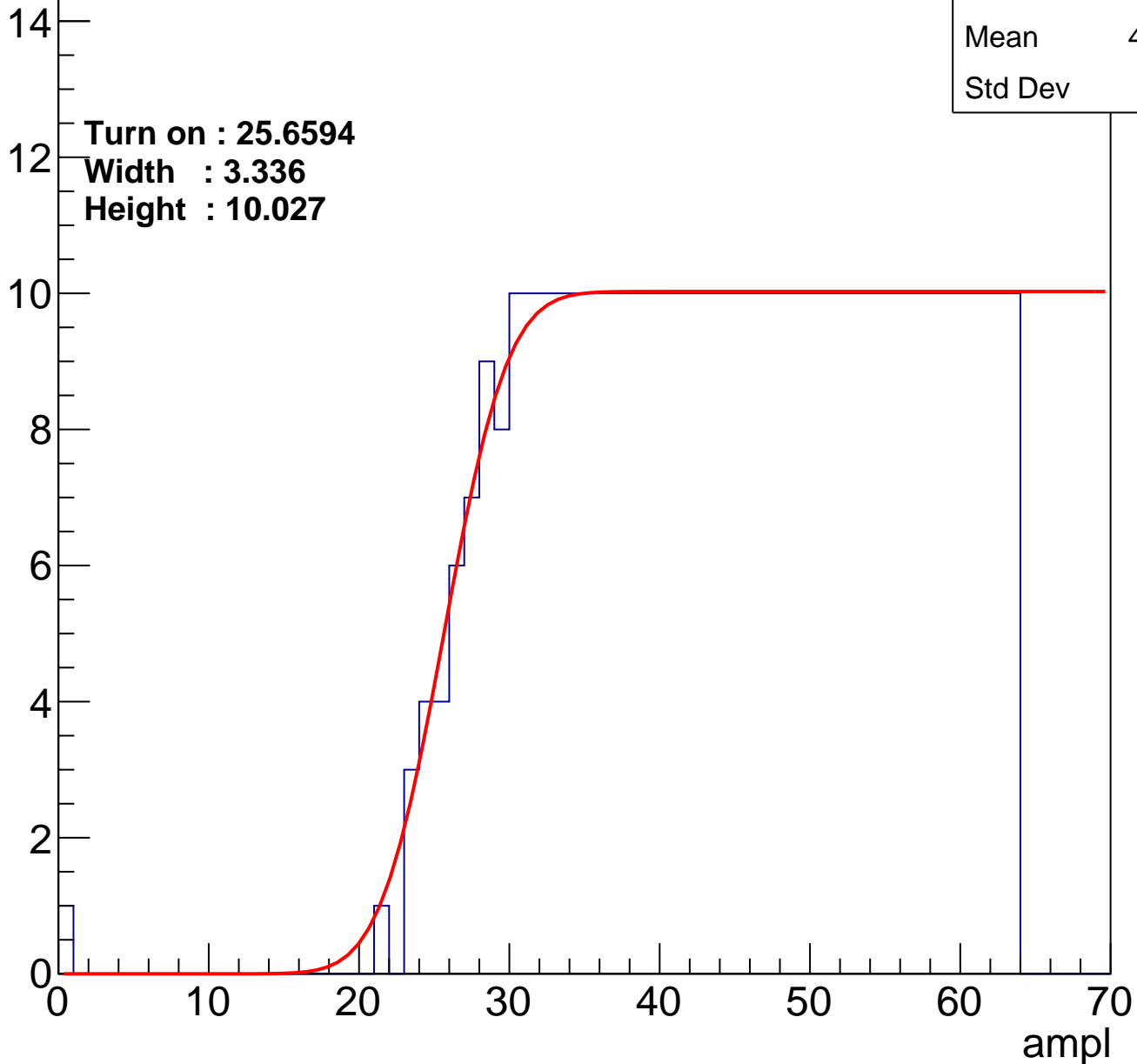
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.6594

Width : 3.336

Height : 10.027

Entry



B0L000S, U10-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.67
Std Dev	11.03

Turn on : 29.9688

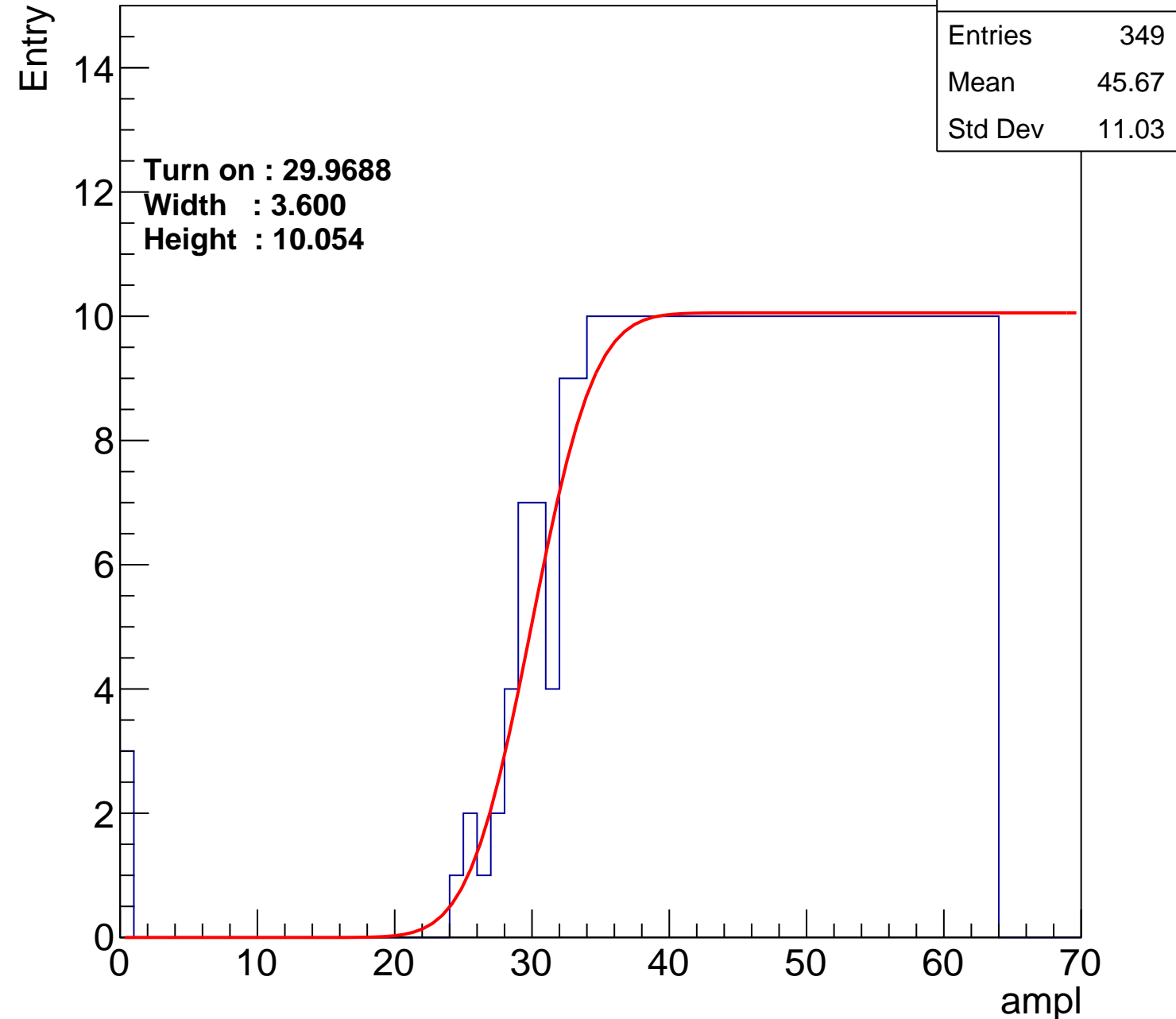
Width : 3.600

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.48
Std Dev	10.68

Turn on : 28.4559

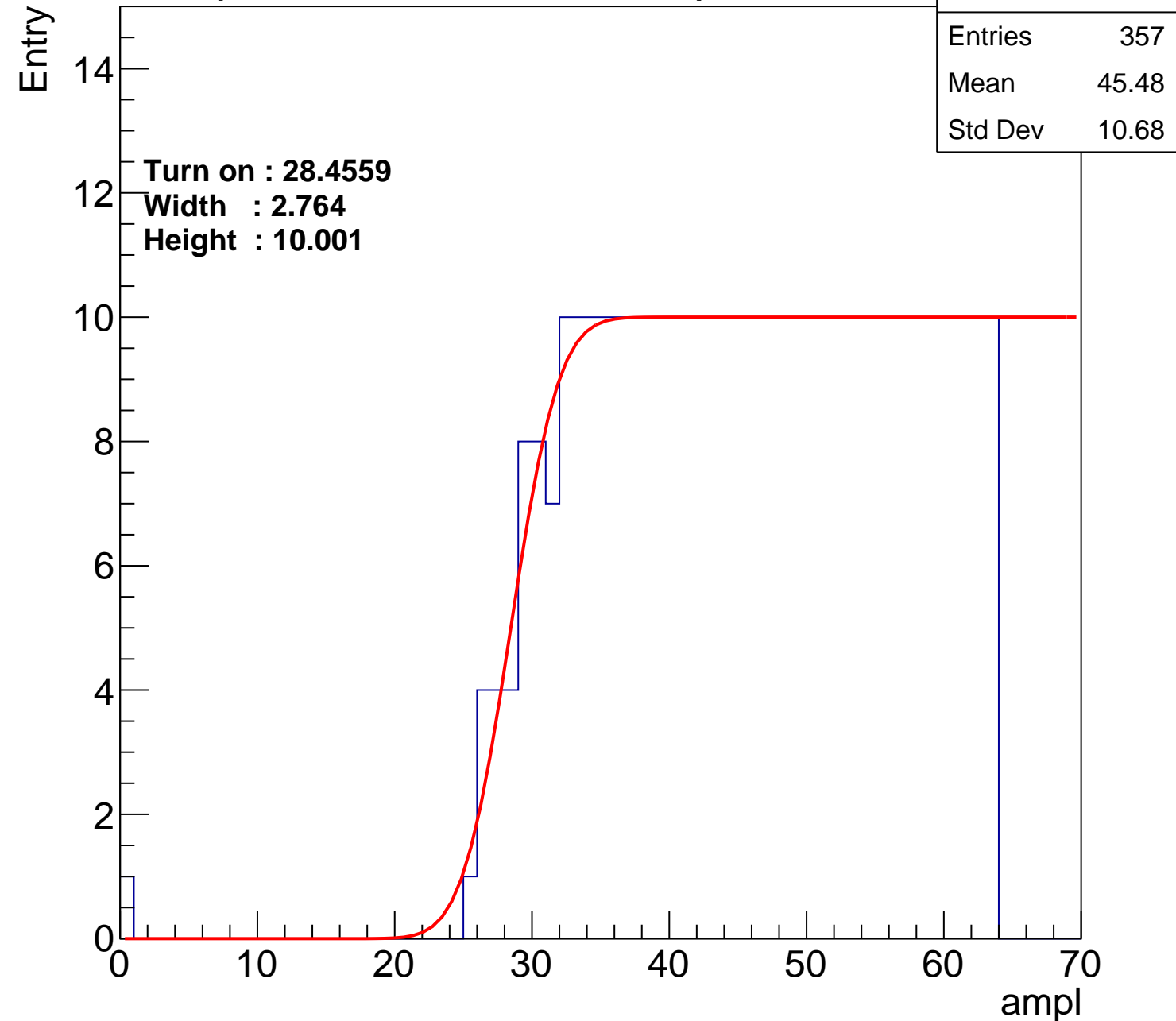
Width : 2.764

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch39

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.37
Std Dev	10.77

Turn on : 28.7399

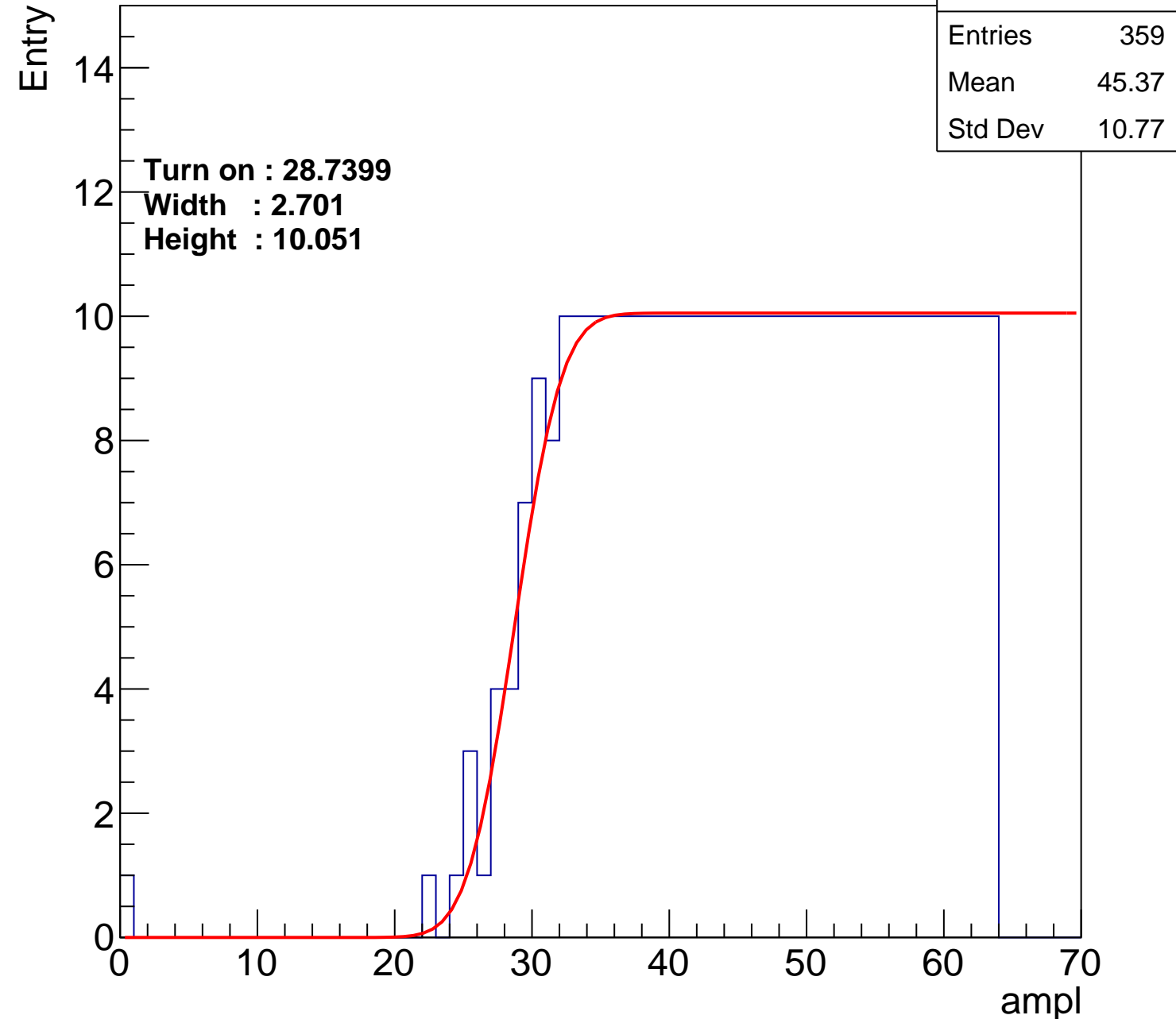
Width : 2.701

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch40

calib_packv5_042523_0143.root, FC#5, port B1

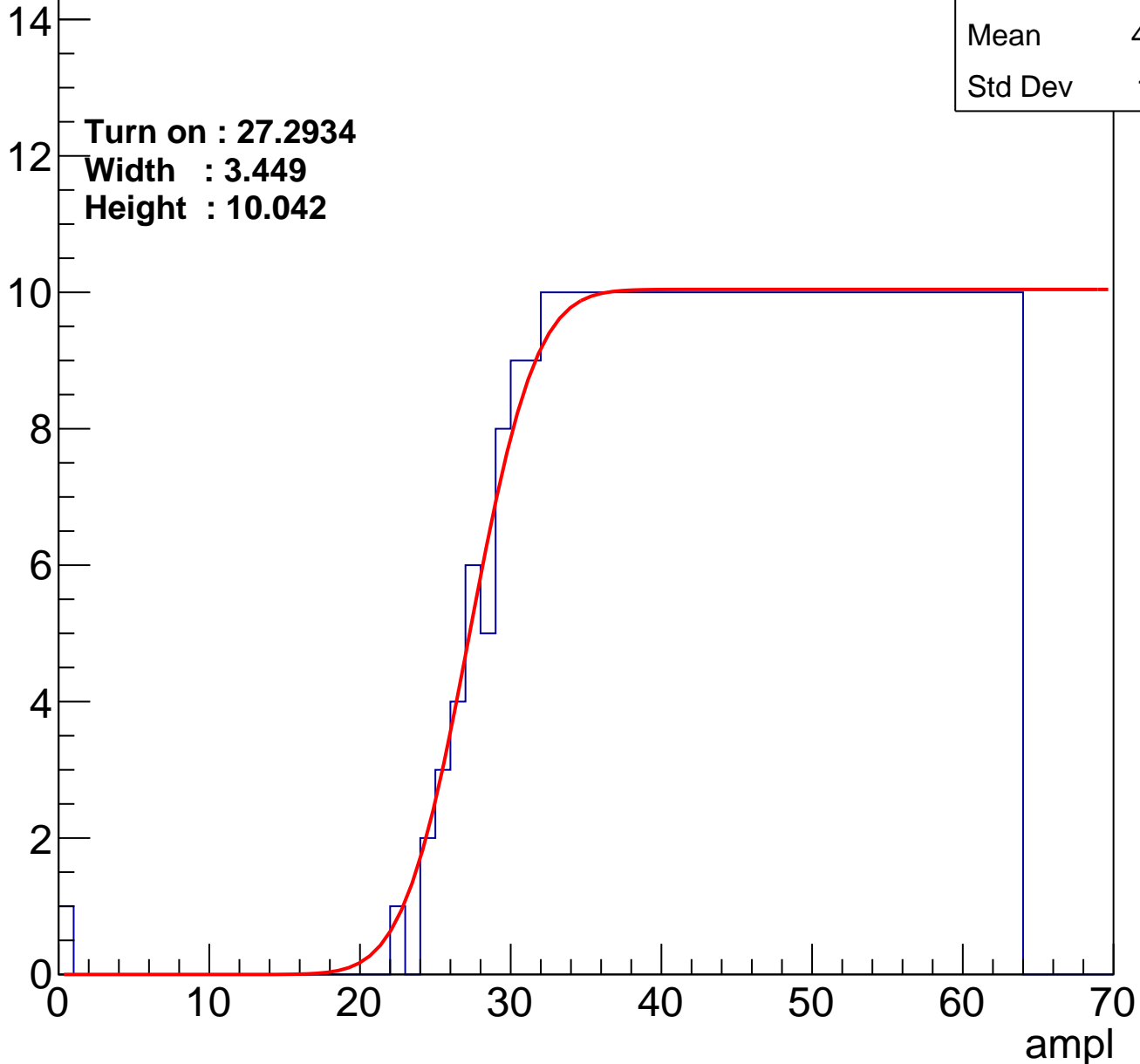
Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.2934

Width : 3.449

Height : 10.042

Entry



B0L000S, U10-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.28
Std Dev	11.16

Turn on : 28.4694

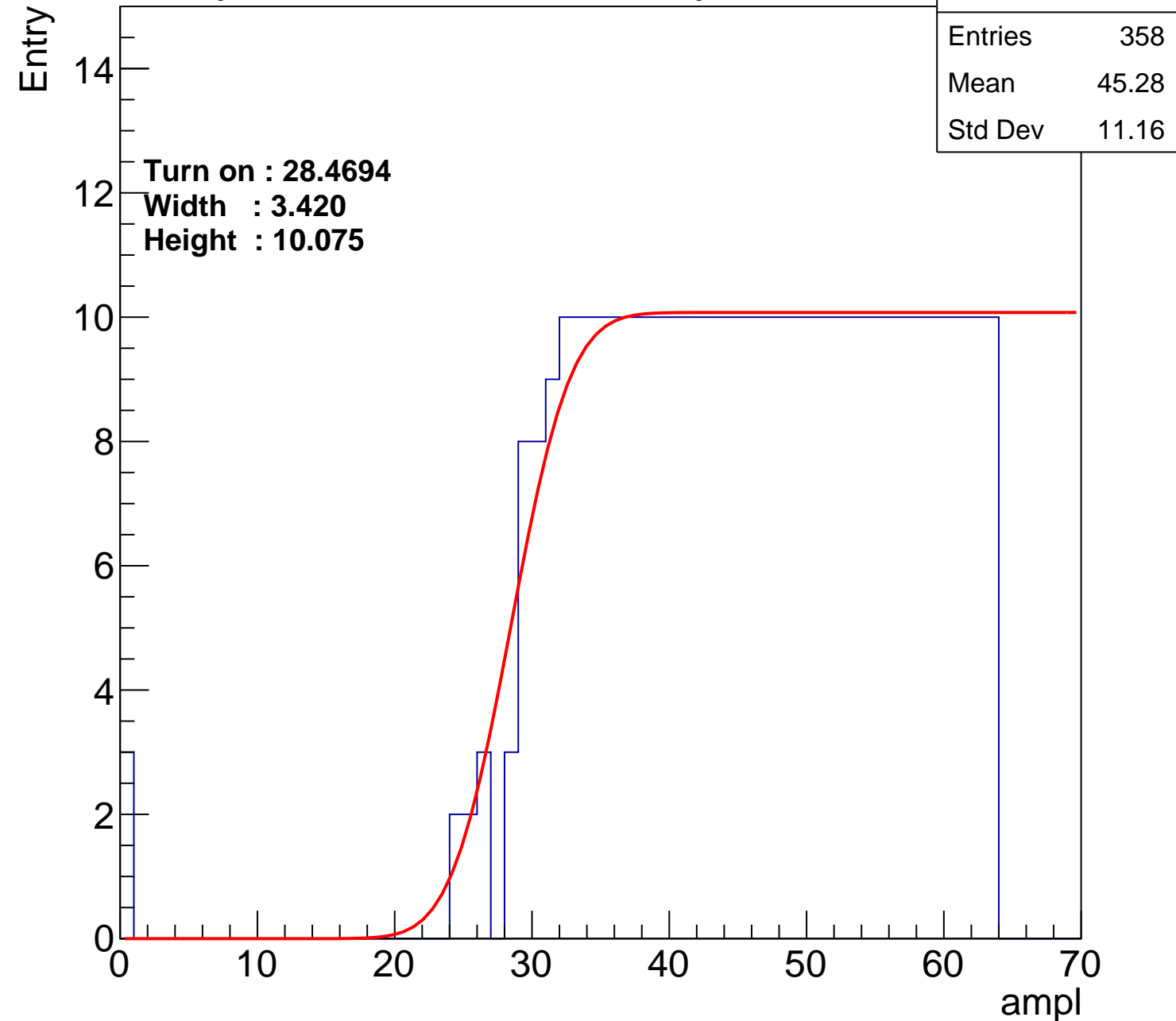
Width : 3.420

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	45.97
Std Dev	10.88

Turn on : 30.6171

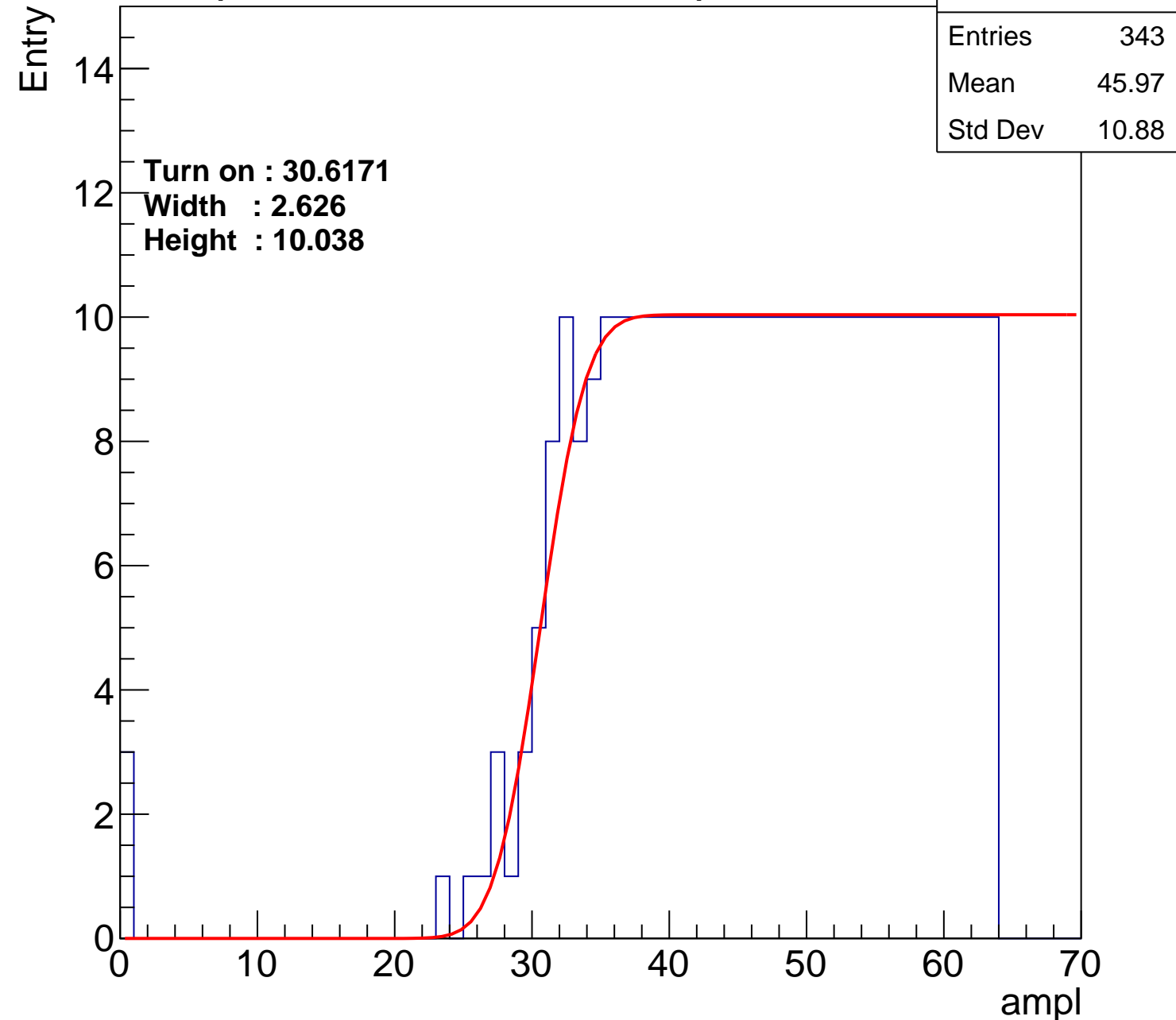
Width : 2.626

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch43

calib_packv5_042523_0143.root, FC#5, port B1

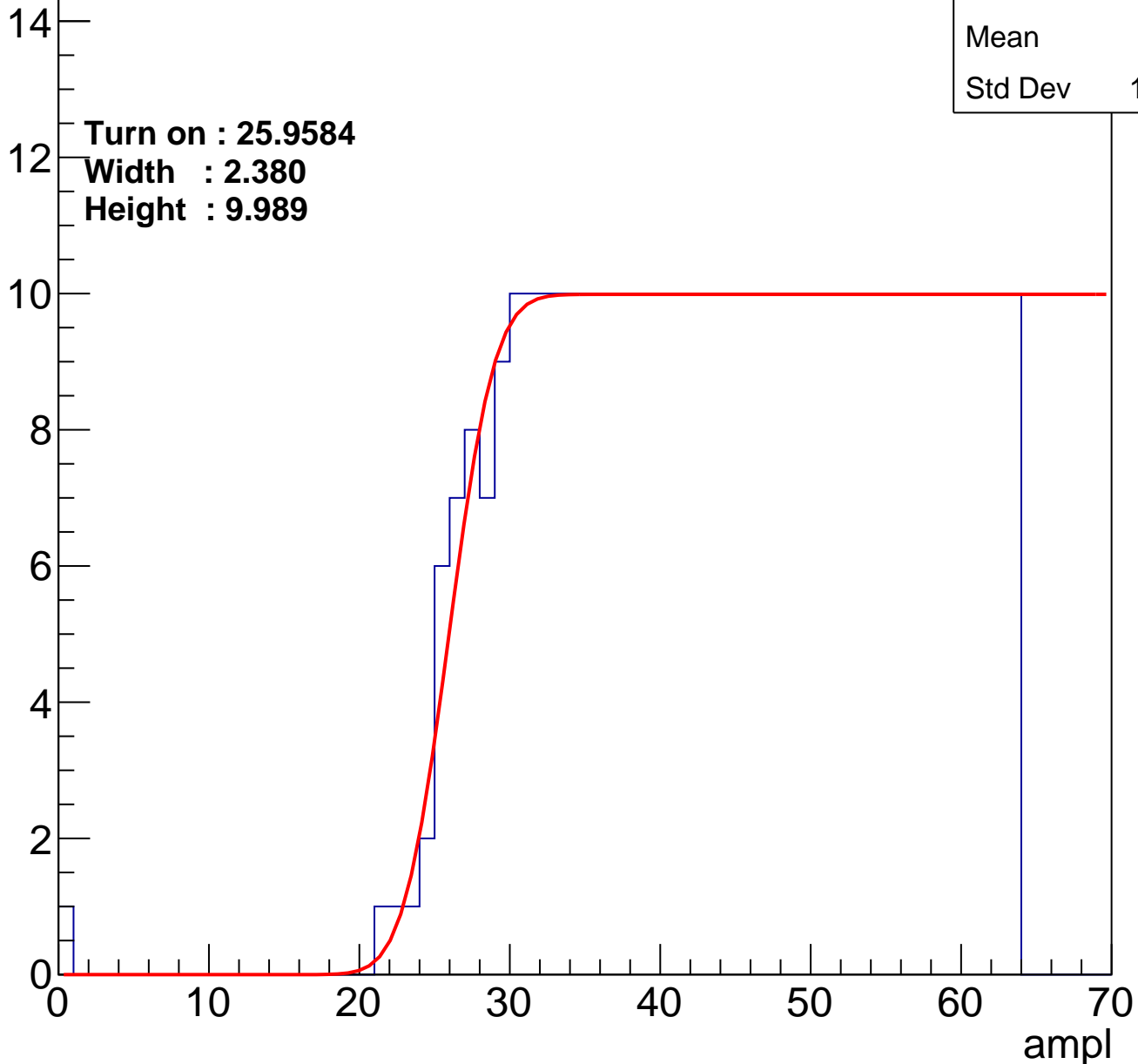
Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.9584

Width : 2.380

Height : 9.989

Entry



B0L000S, U10-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.16
Std Dev	10.95

Turn on : 28.2181

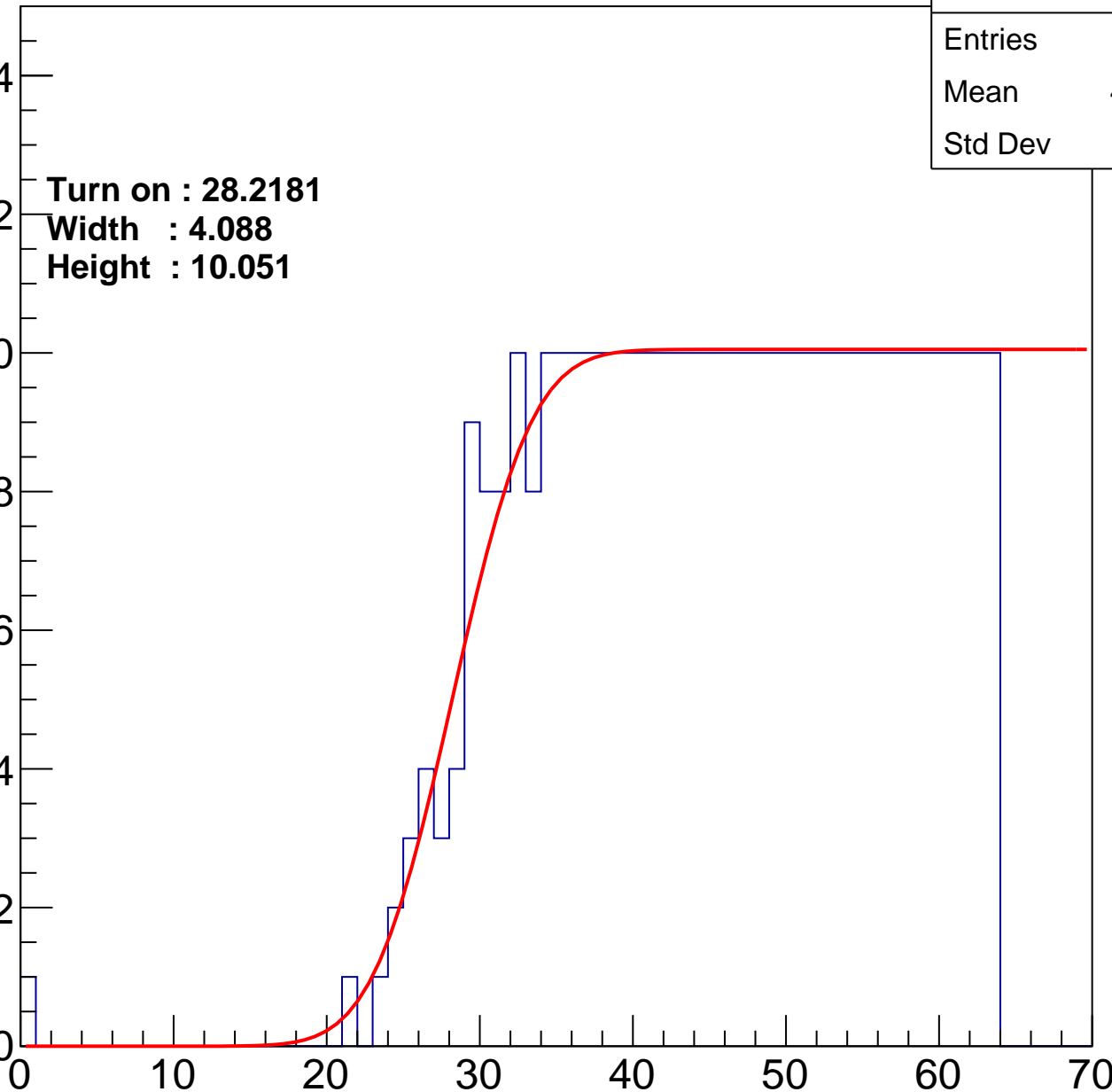
Width : 4.088

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.56
Std Dev	10.6

Turn on : 28.4713

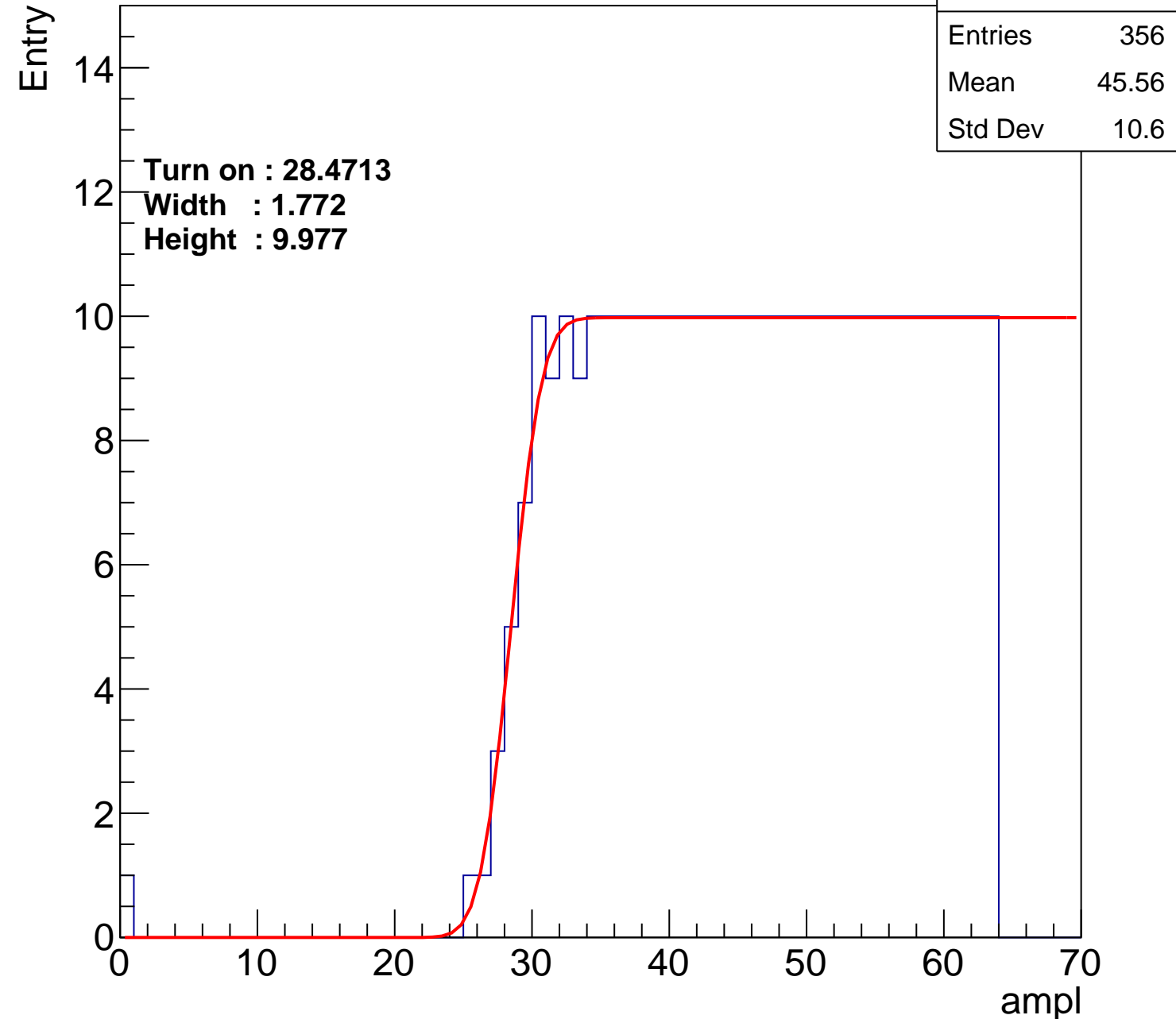
Width : 1.772

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch46

calib_packv5_042523_0143.root, FC#5, port B1

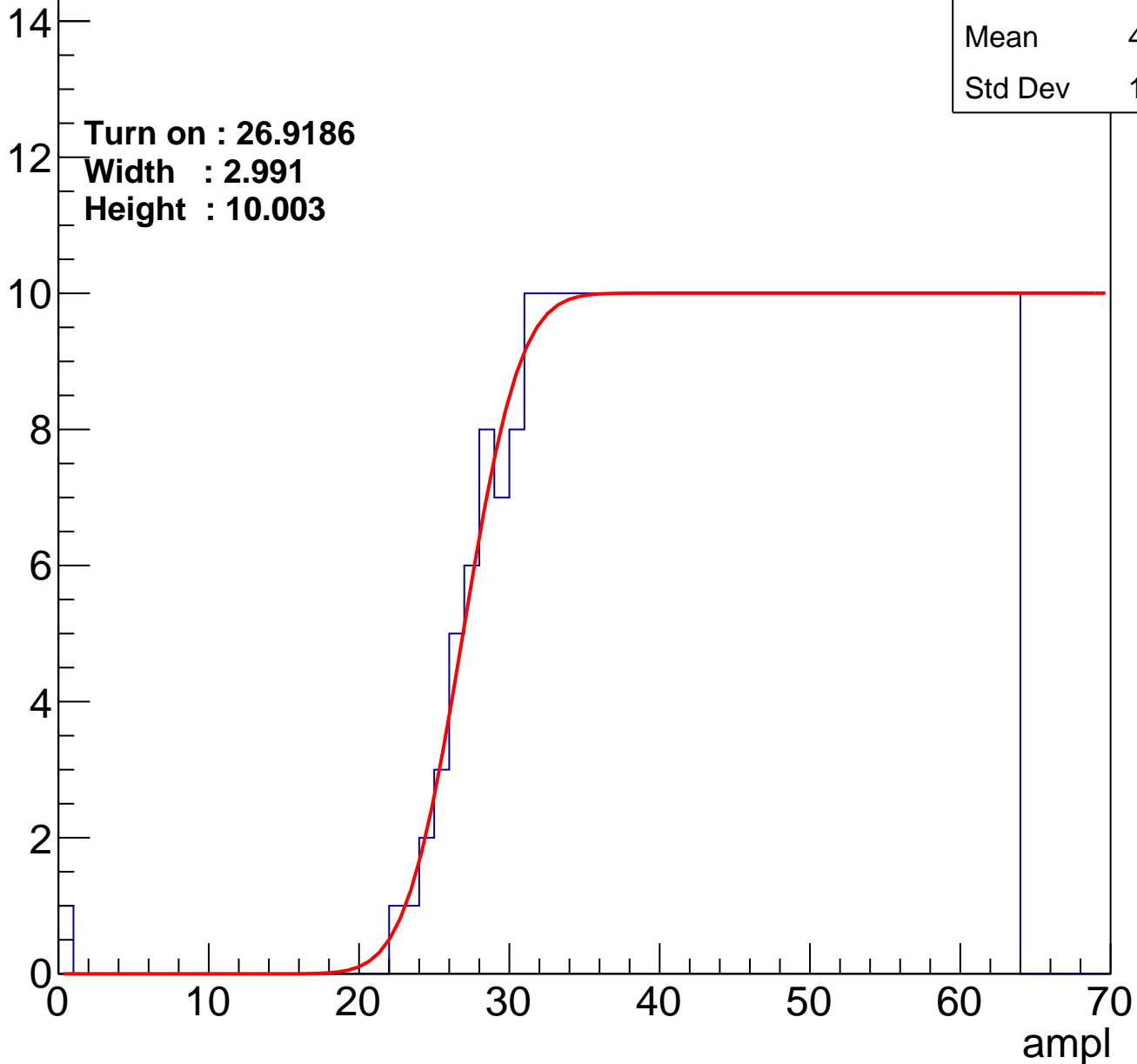
Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 26.9186

Width : 2.991

Height : 10.003

Entry



B0L000S, U10-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.8480

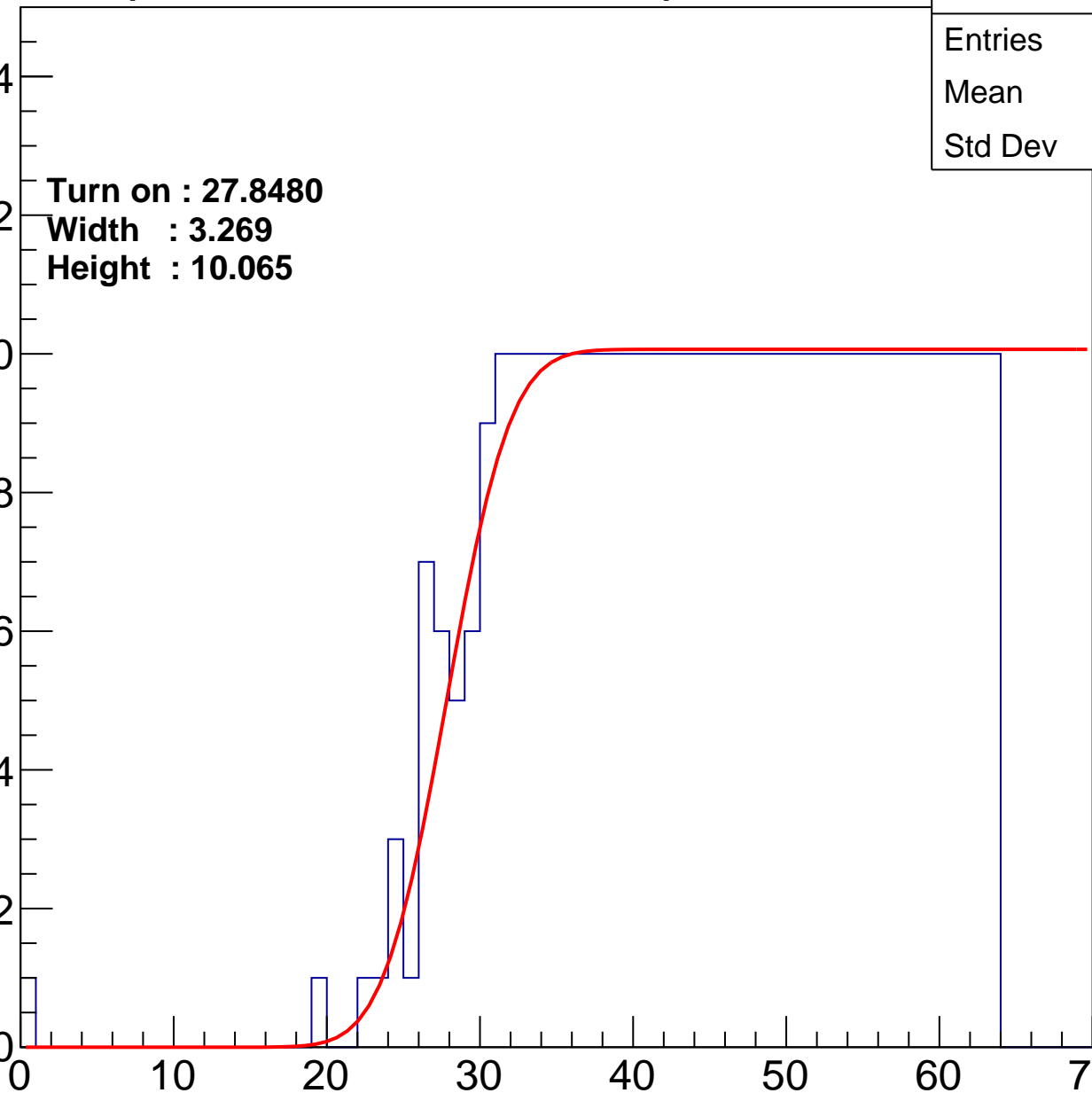
Width : 3.269

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.16
Std Dev	10.88

Turn on : 27.2210

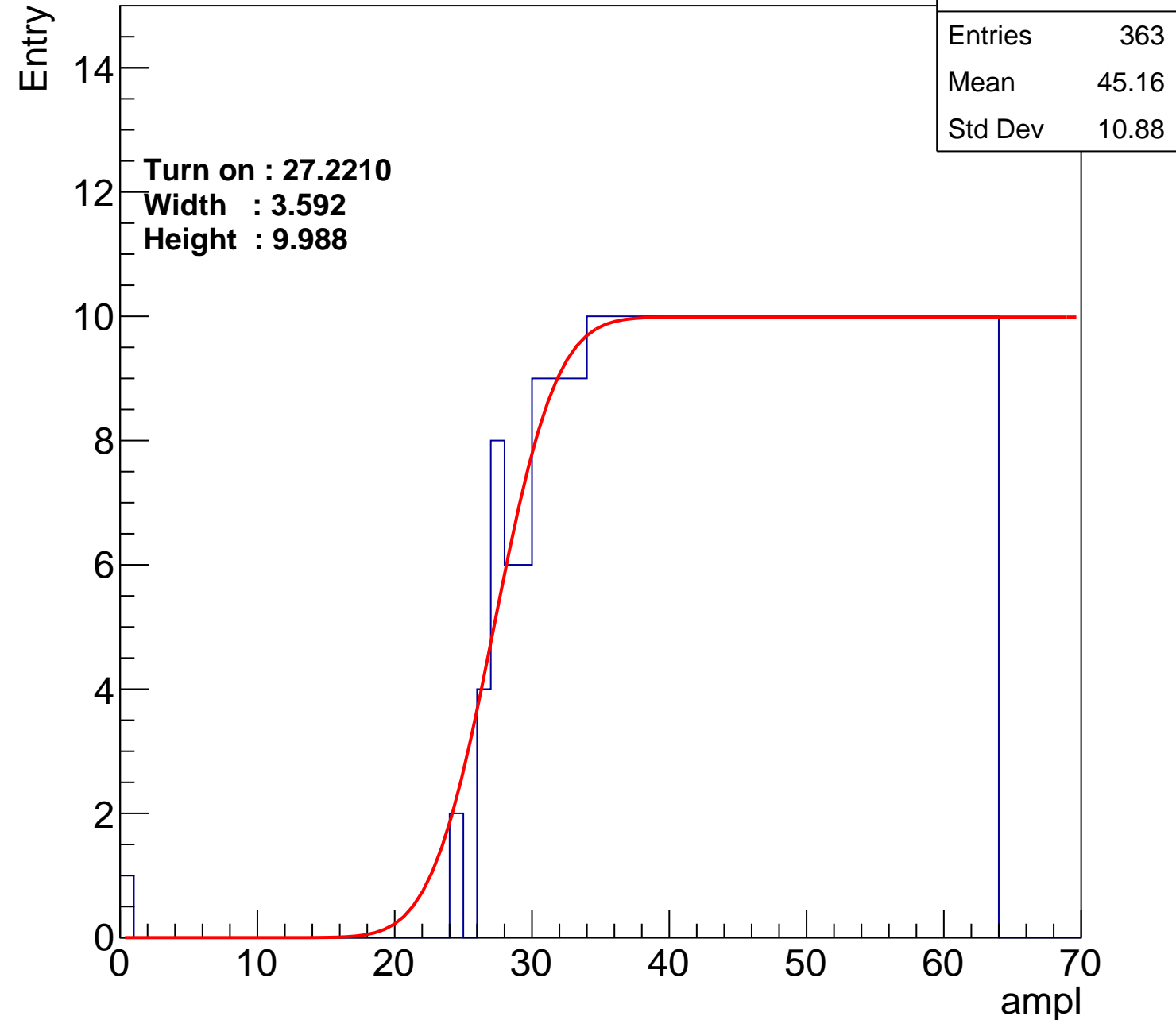
Width : 3.592

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl

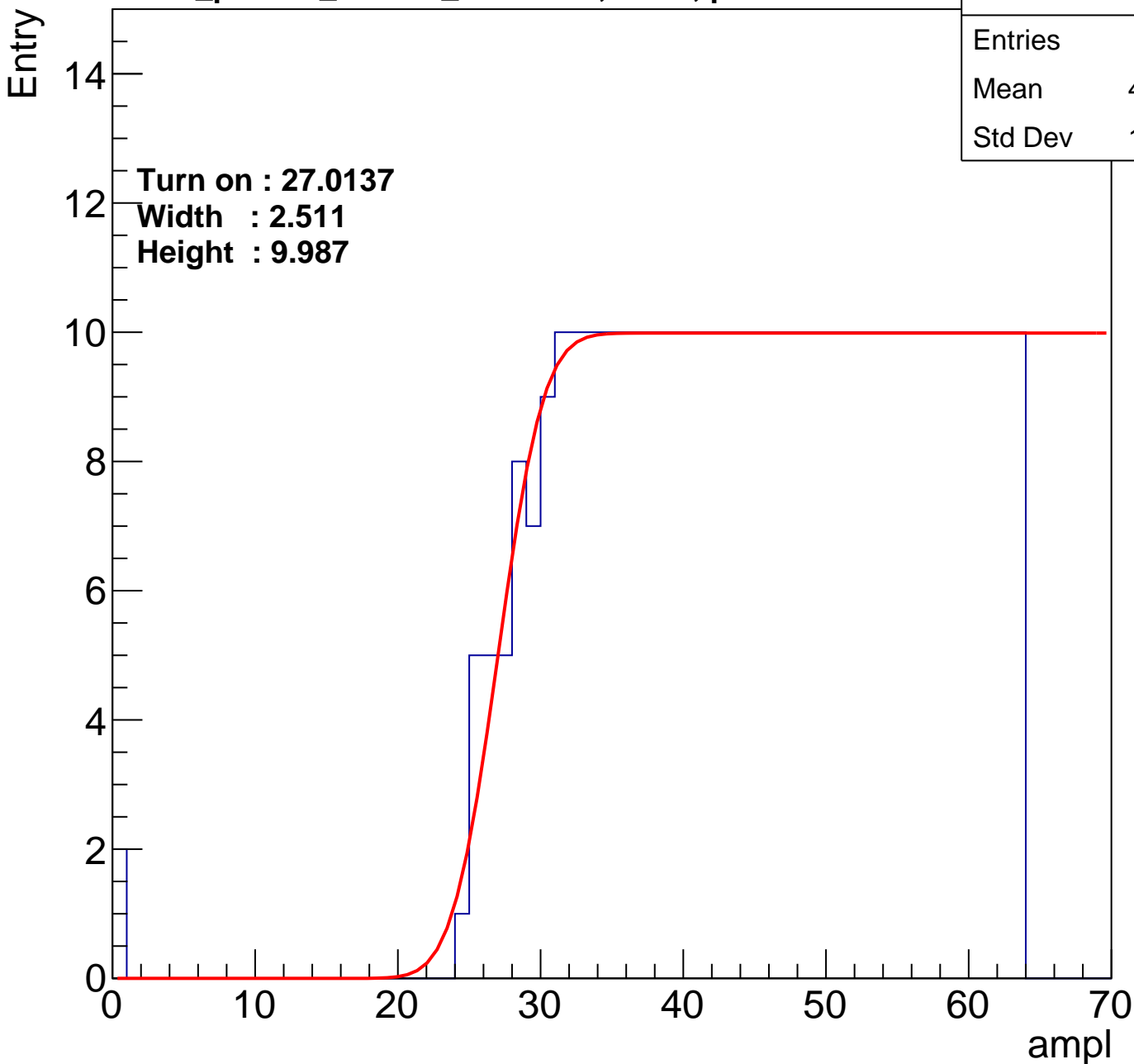


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.68
Std Dev	11.27

Height : 9.987



B0L000S, U10-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	44.67
Std Dev	12.17

Turn on : 28.6866

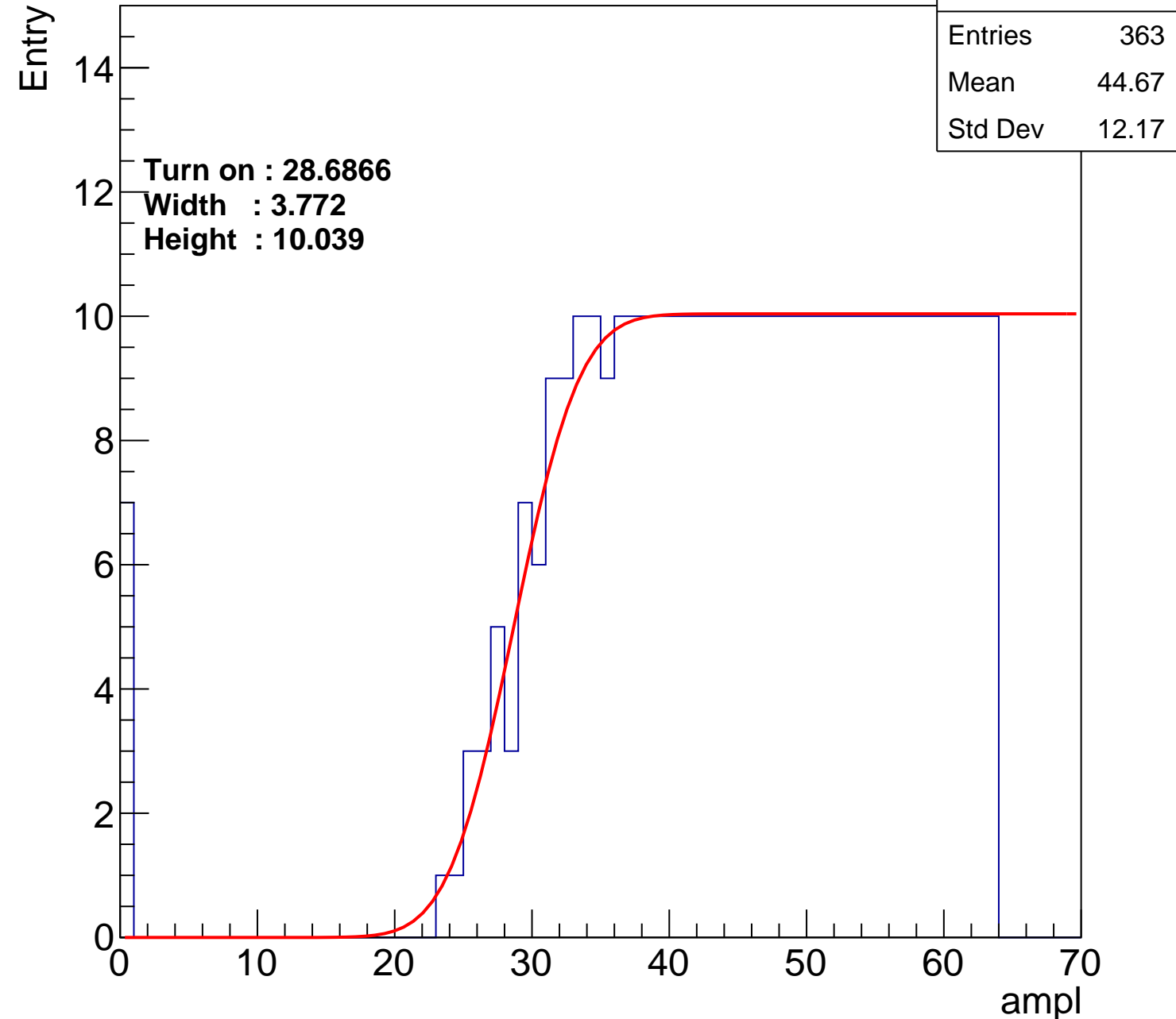
Width : 3.772

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.97
Std Dev	11.27

Turn on : 27.6570

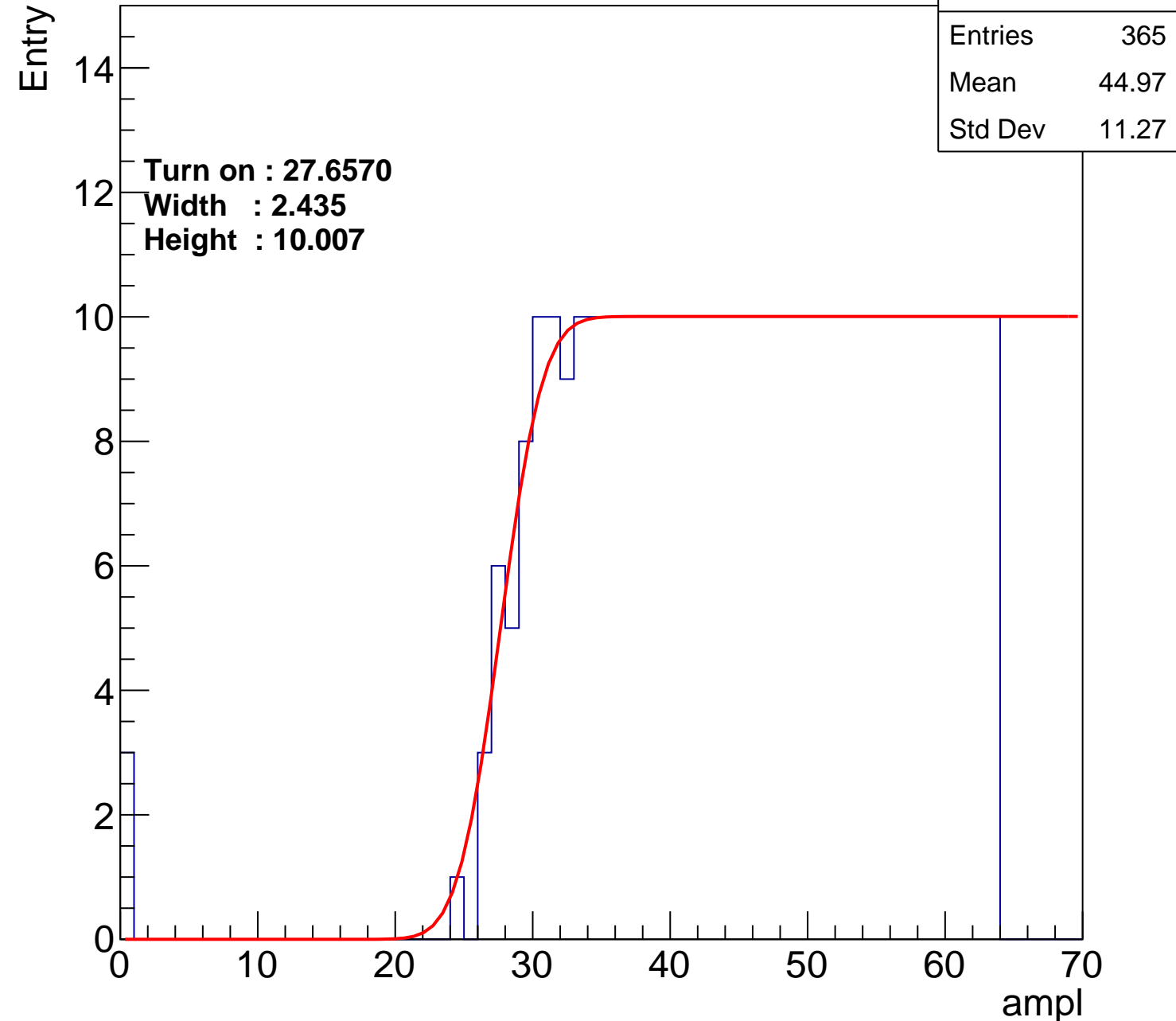
Width : 2.435

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.57
Std Dev	10.66

Turn on : 29.0157

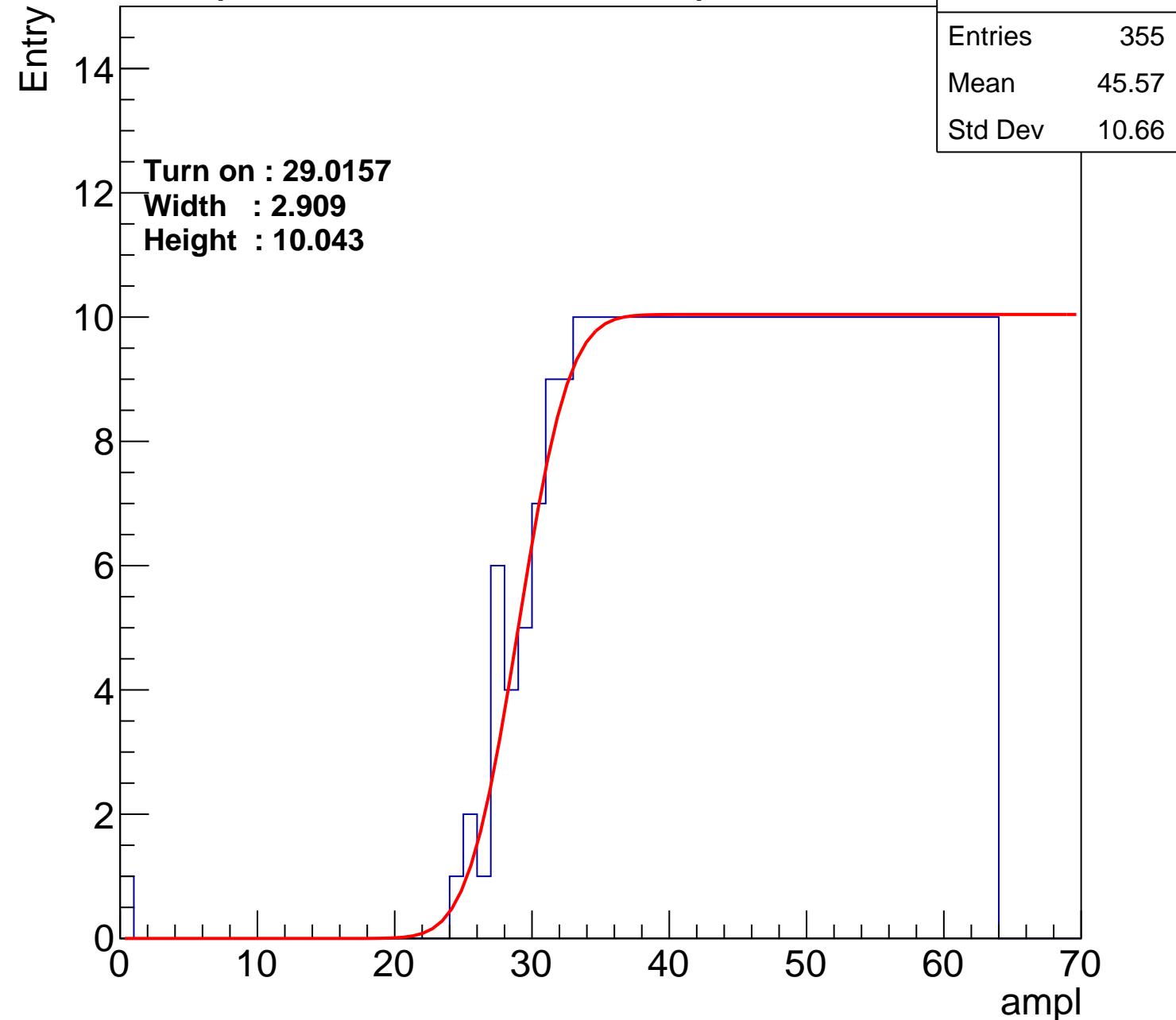
Width : 2.909

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.1704

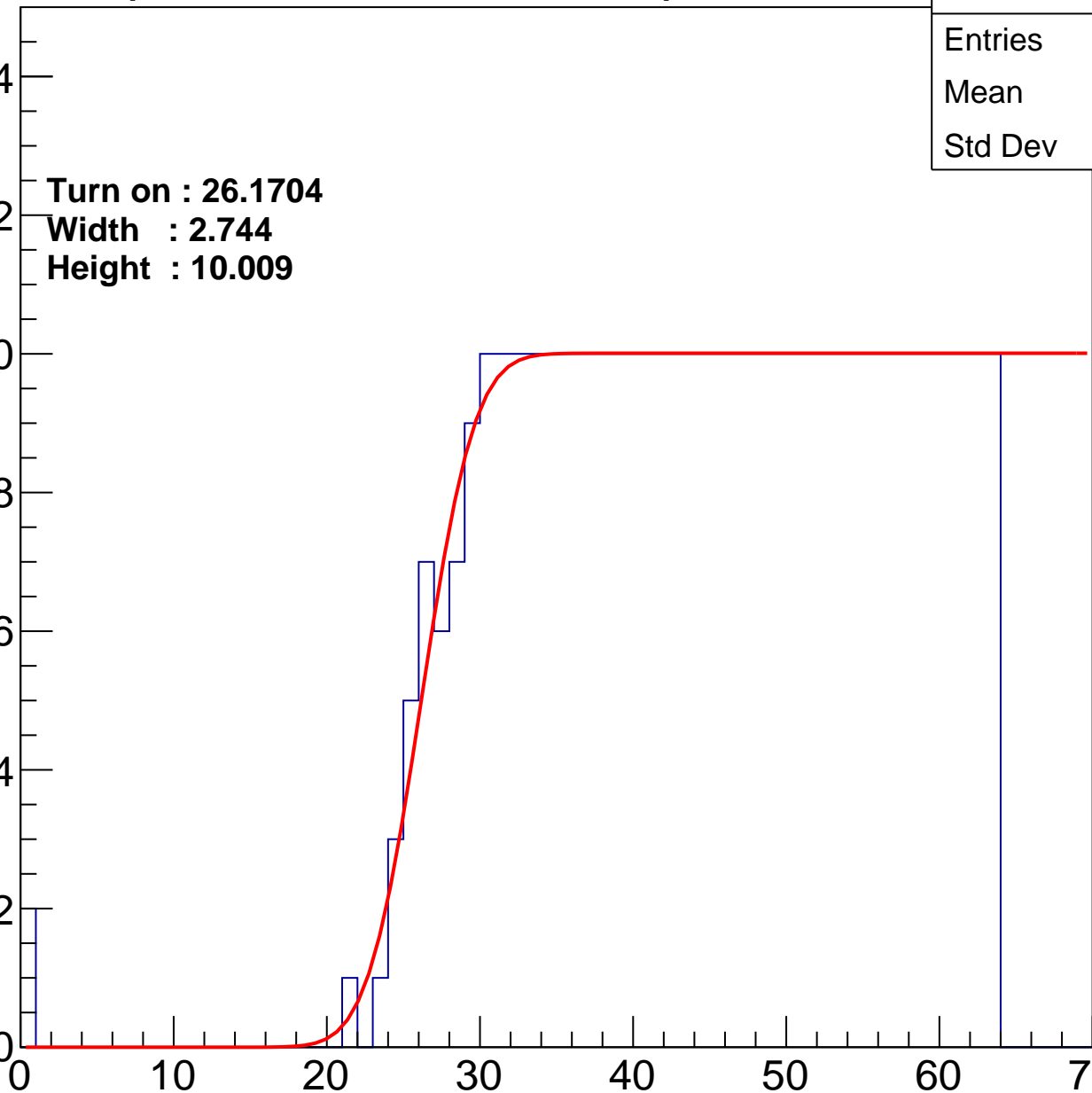
Width : 2.744

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch54

calib_packv5_042523_0143.root, FC#5, port B1

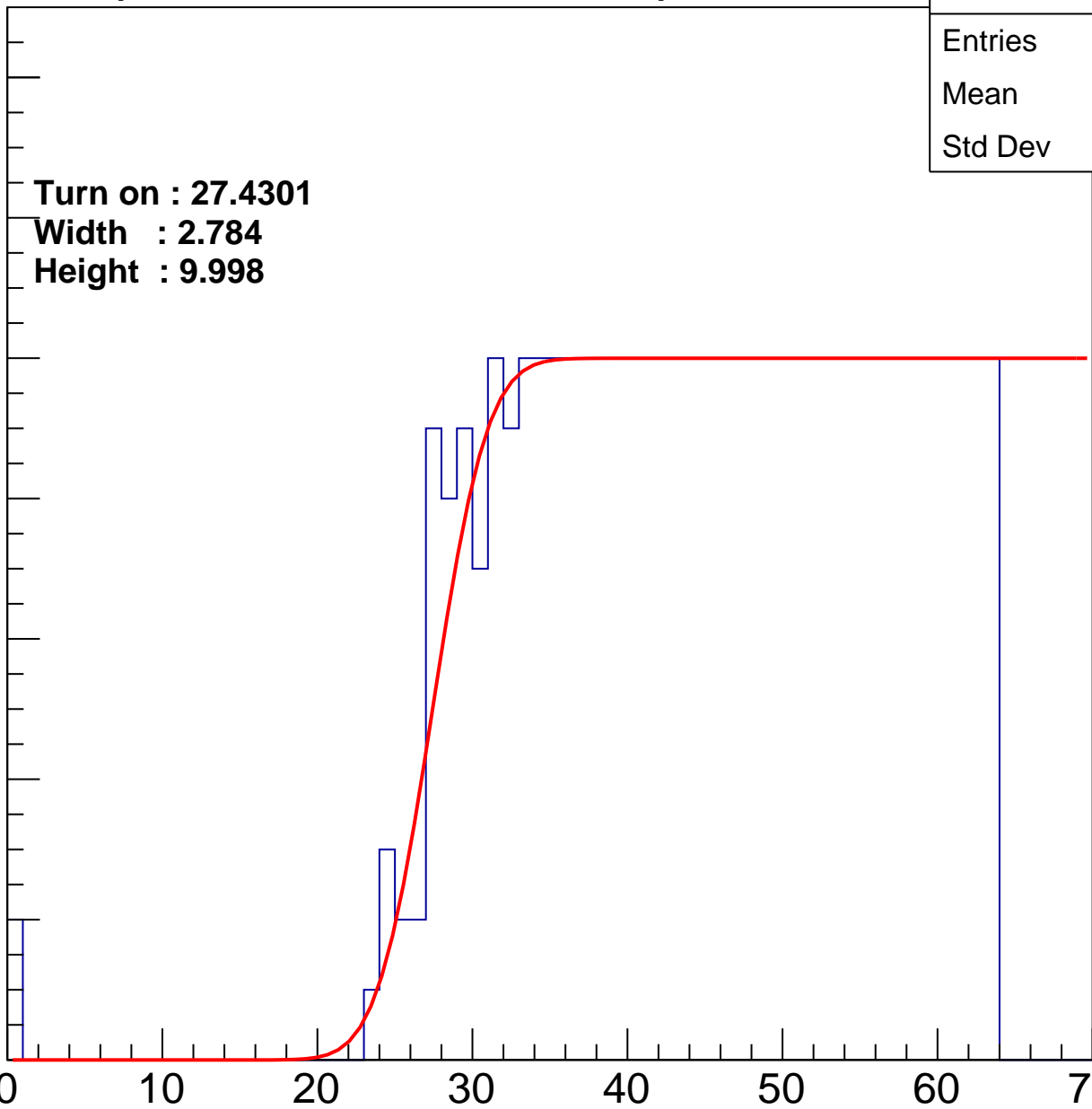
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4301
Width : 2.784
Height : 9.998

Entries	372
Mean	44.66
Std Dev	11.3

ampl



B0L000S, U10-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.07
Std Dev	11.09

Turn on : 28.0551

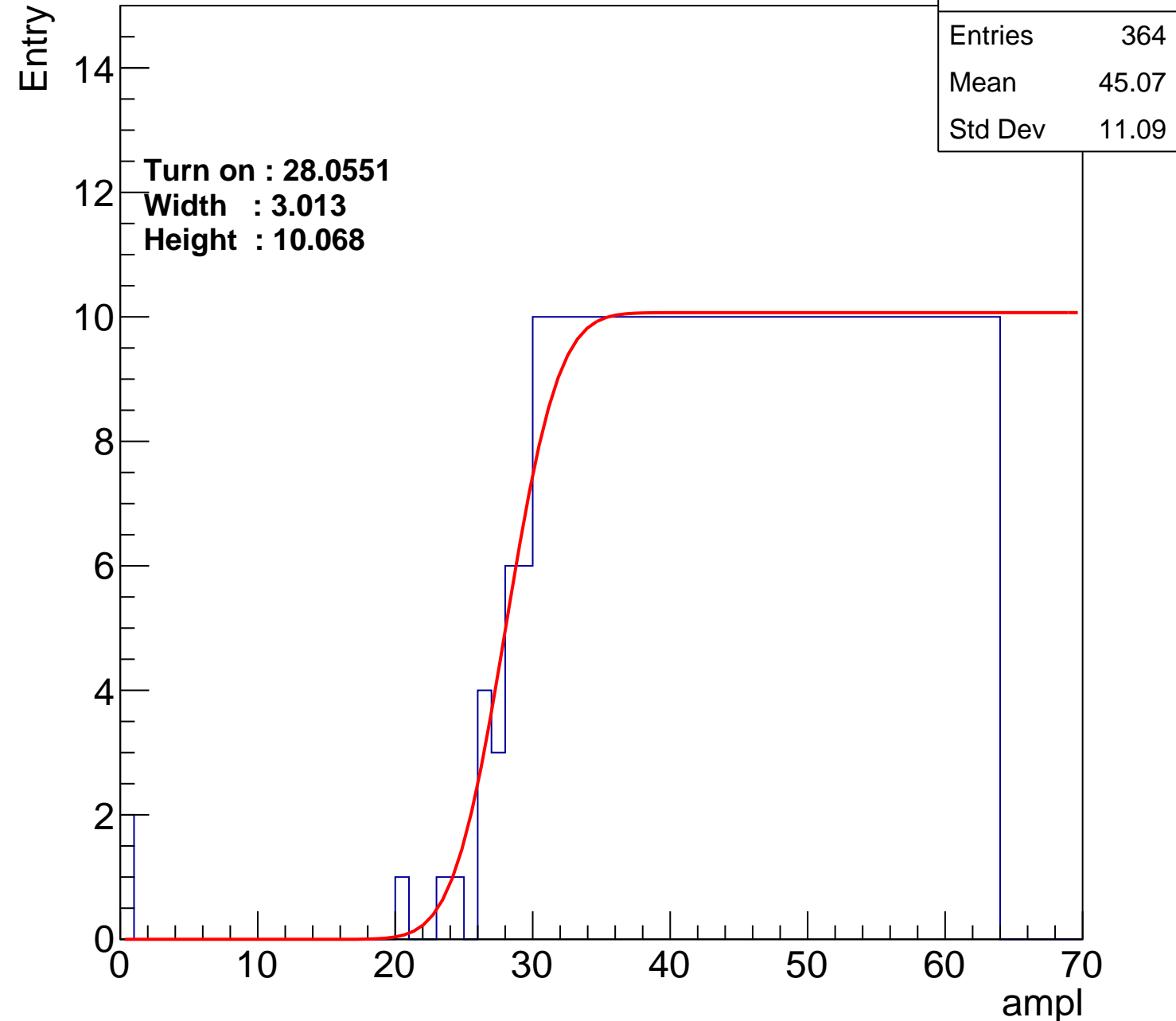
Width : 3.013

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch56

calib_packv5_042523_0143.root, FC#5, port B1

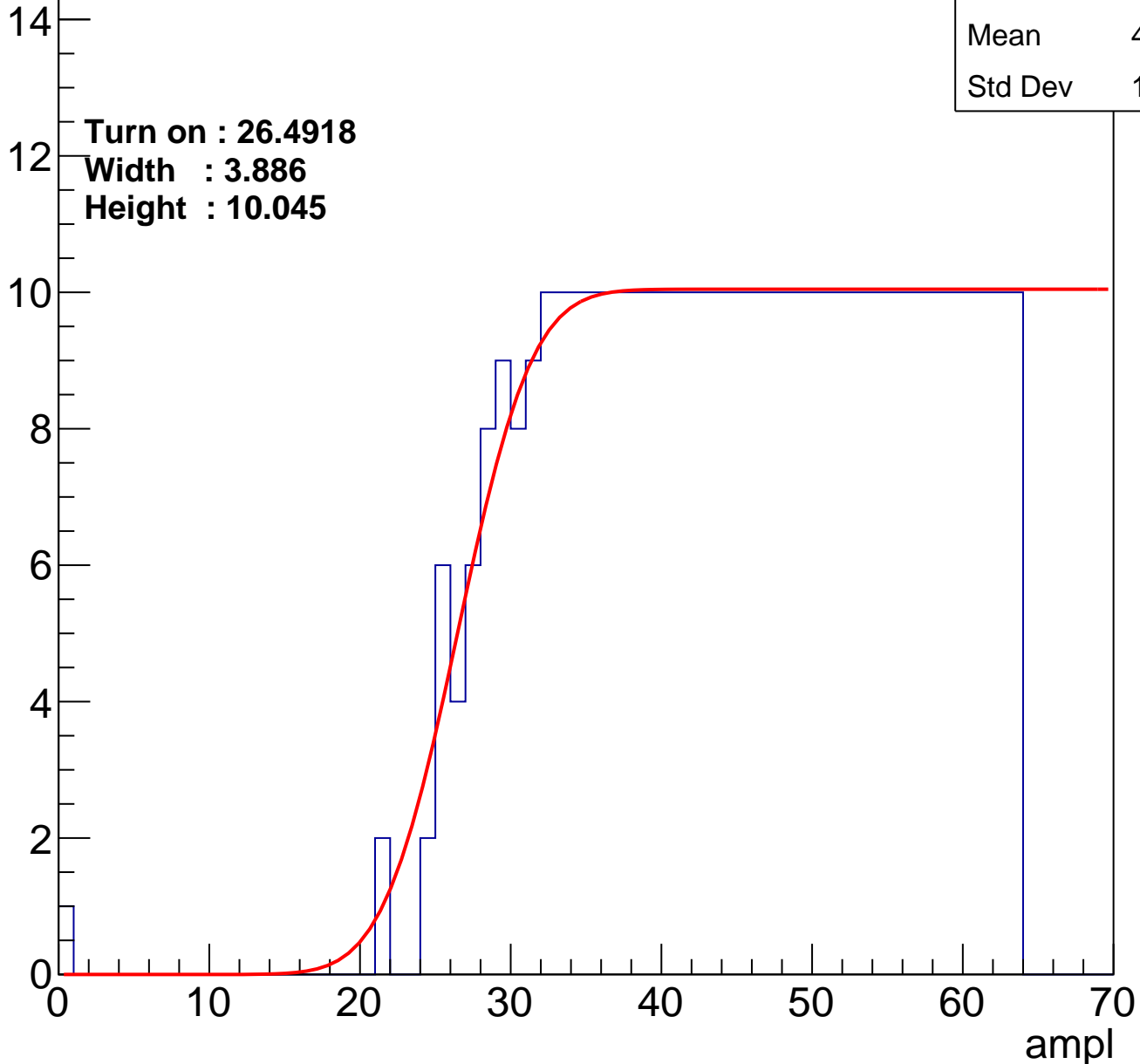
Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.4918

Width : 3.886

Height : 10.045

Entry



B0L000S, U10-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.05
Std Dev	11.16

Turn on : 28.1935

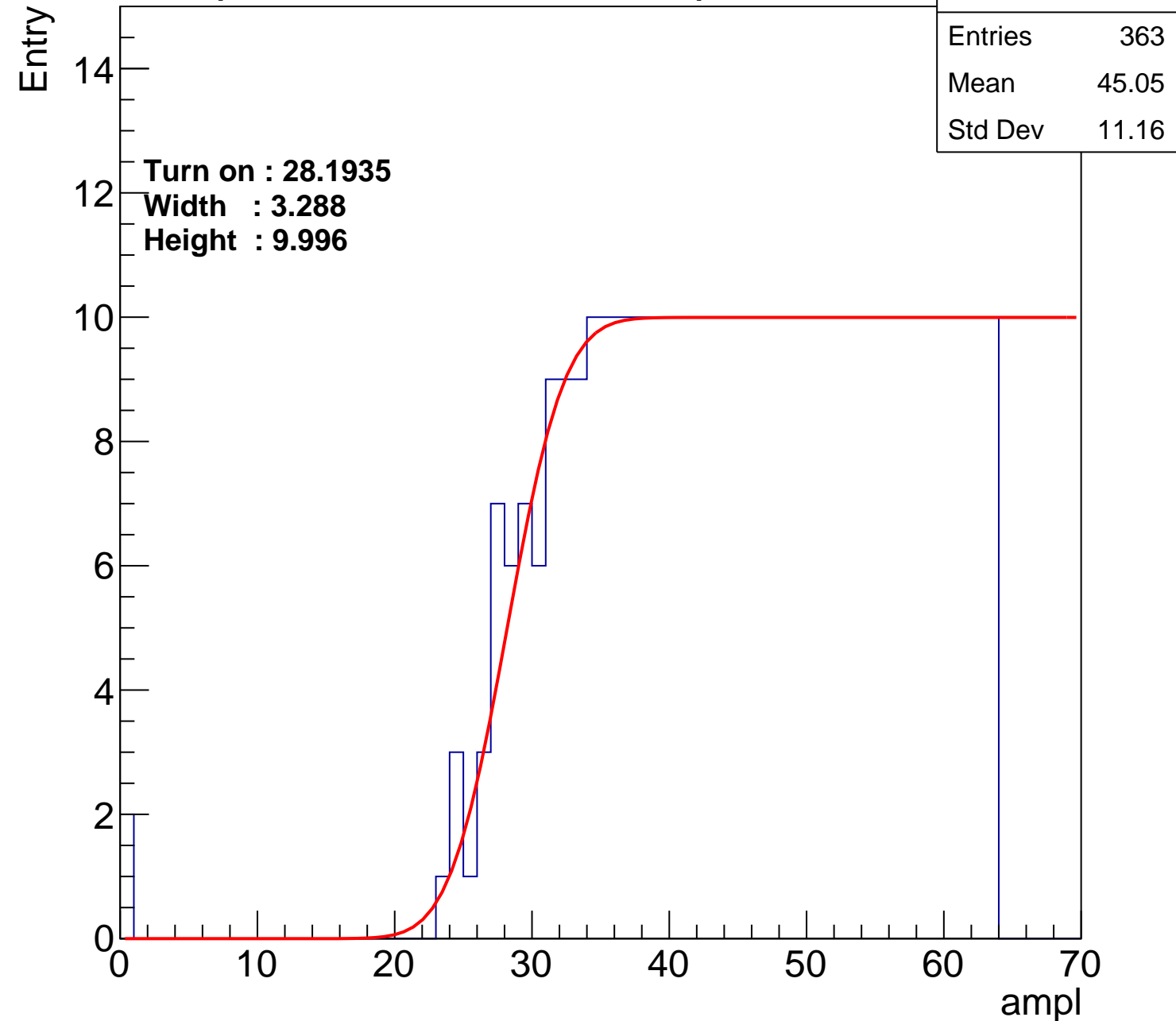
Width : 3.288

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch58

calib_packv5_042523_0143.root, FC#5, port B1

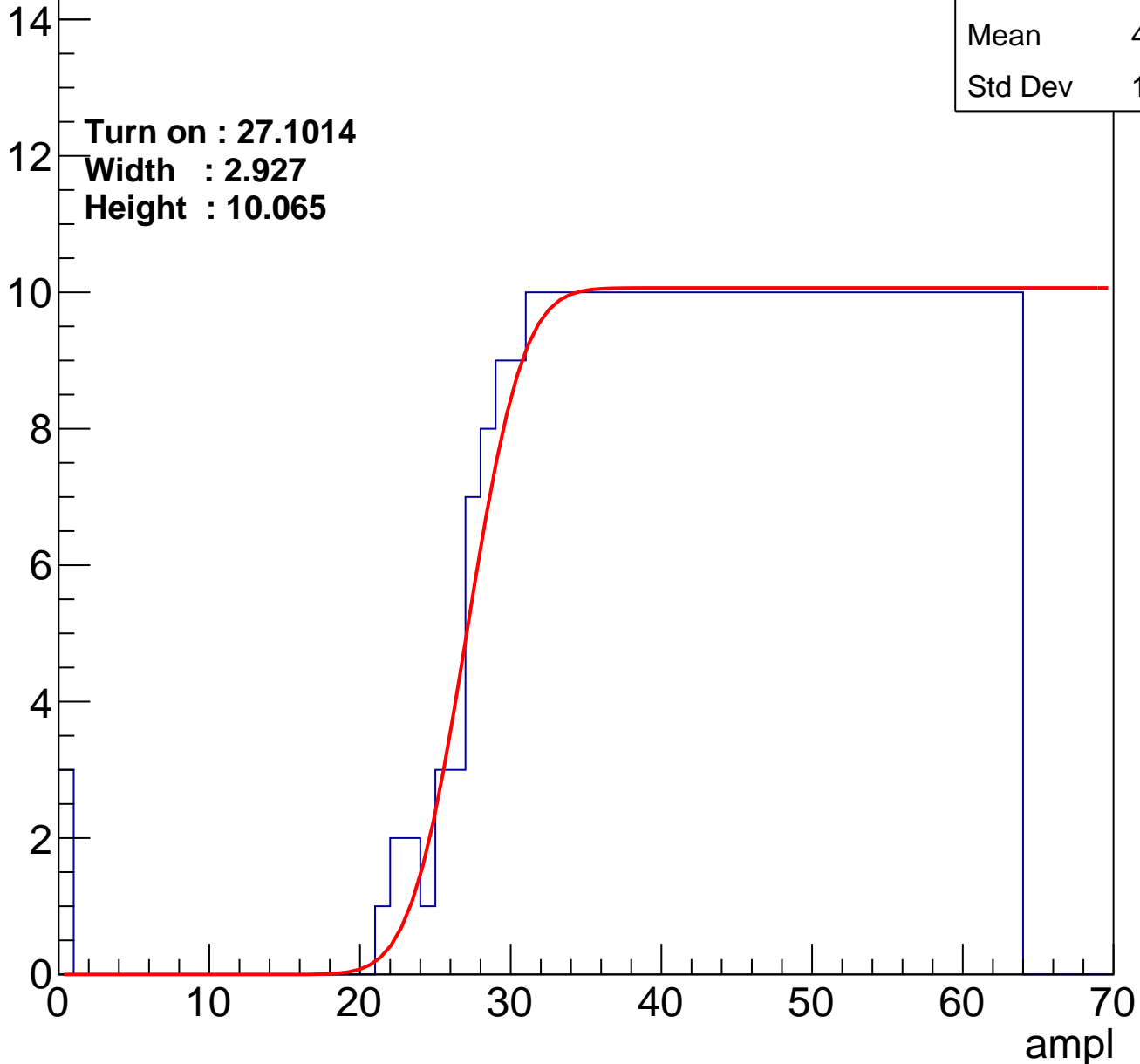
Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 27.1014

Width : 2.927

Height : 10.065

Entry



B0L000S, U10-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.3244

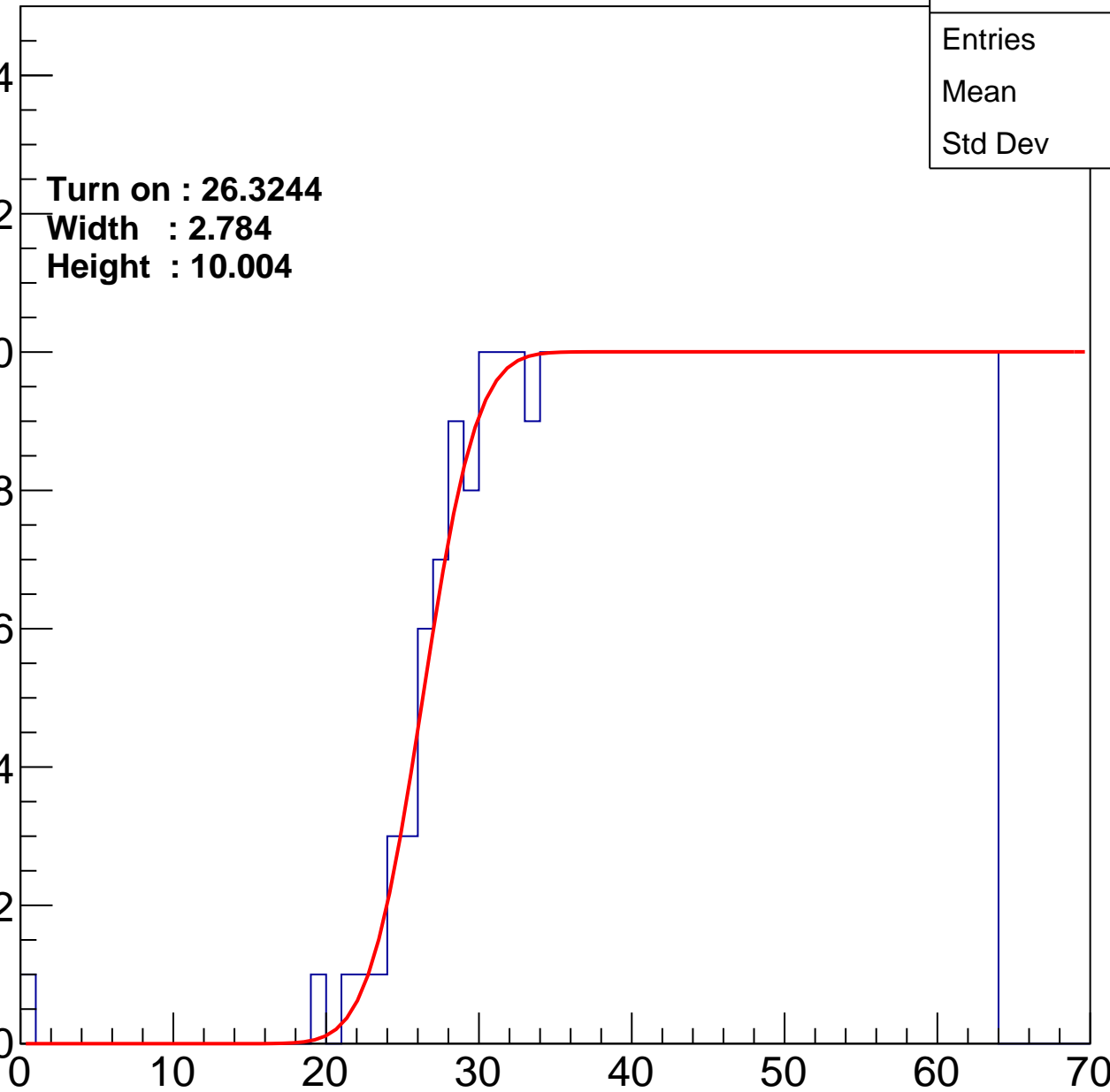
Width : 2.784

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.61
Std Dev	11.19

Turn on : 27.0518

Width : 3.207

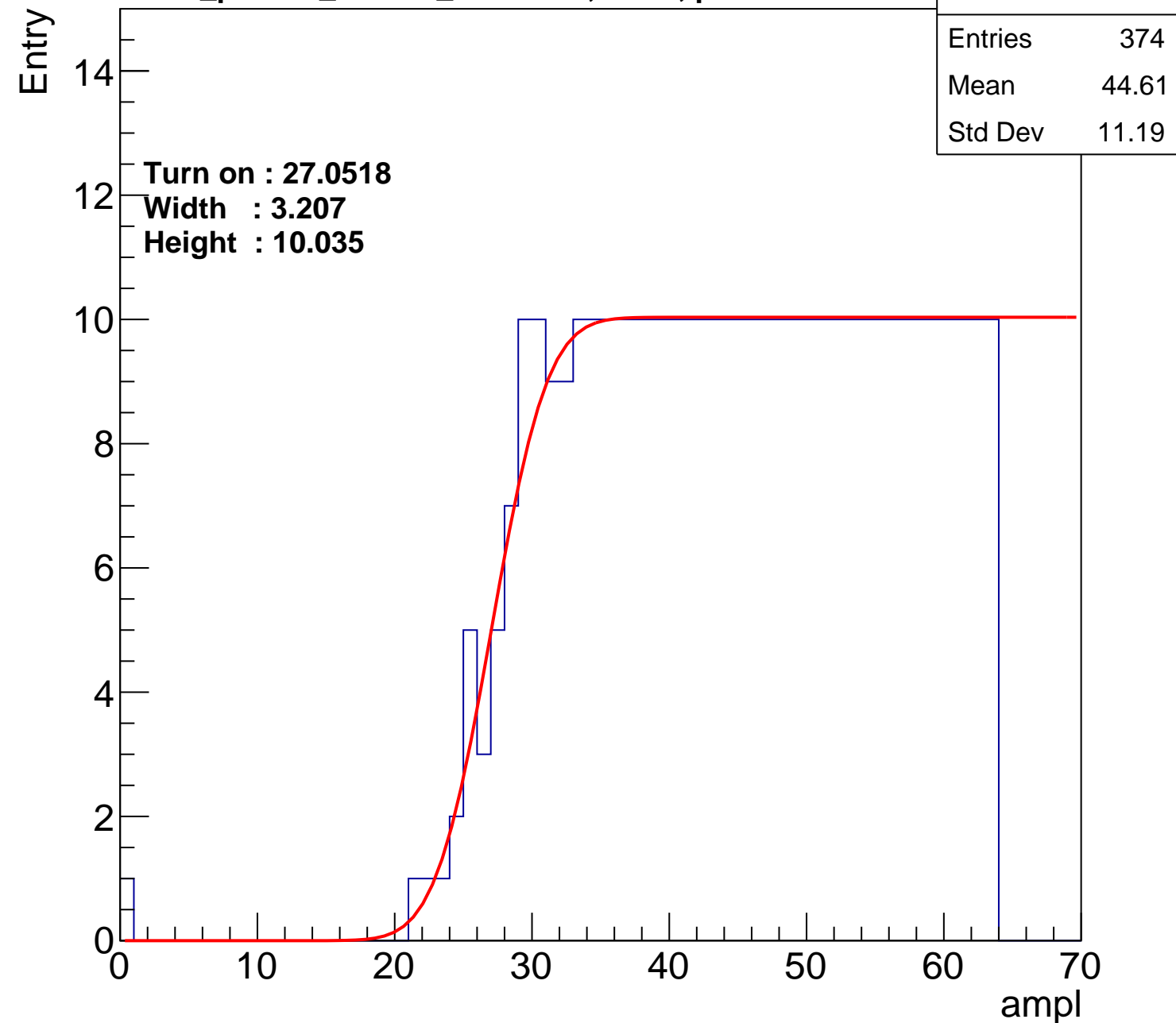
Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U10-ch61

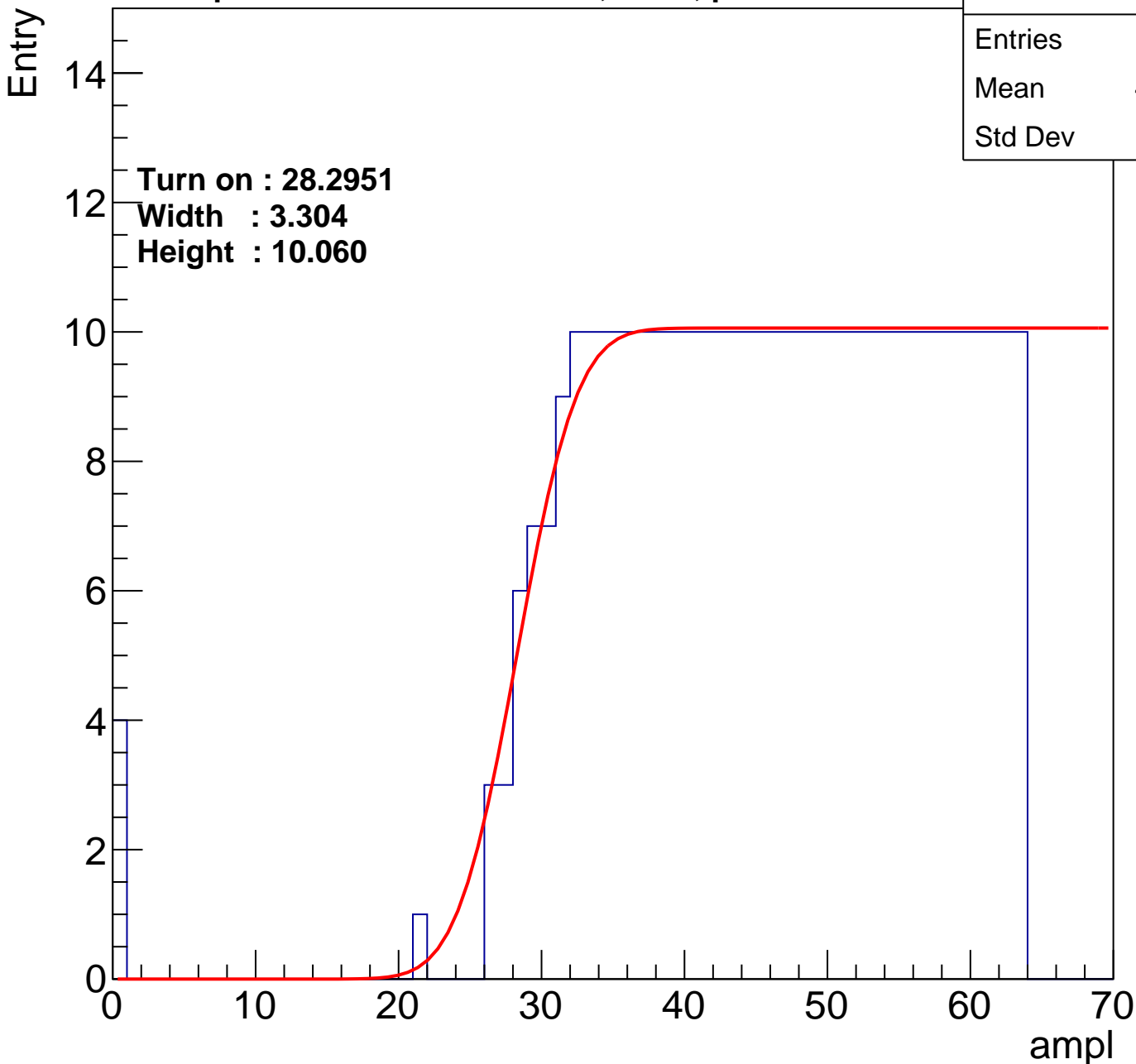
calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.11
Std Dev	11.41

Turn on : 28.2951

Width : 3.304

Height : 10.060



B0L000S, U10-ch62

calib_packv5_042523_0143.root, FC#5, port B1

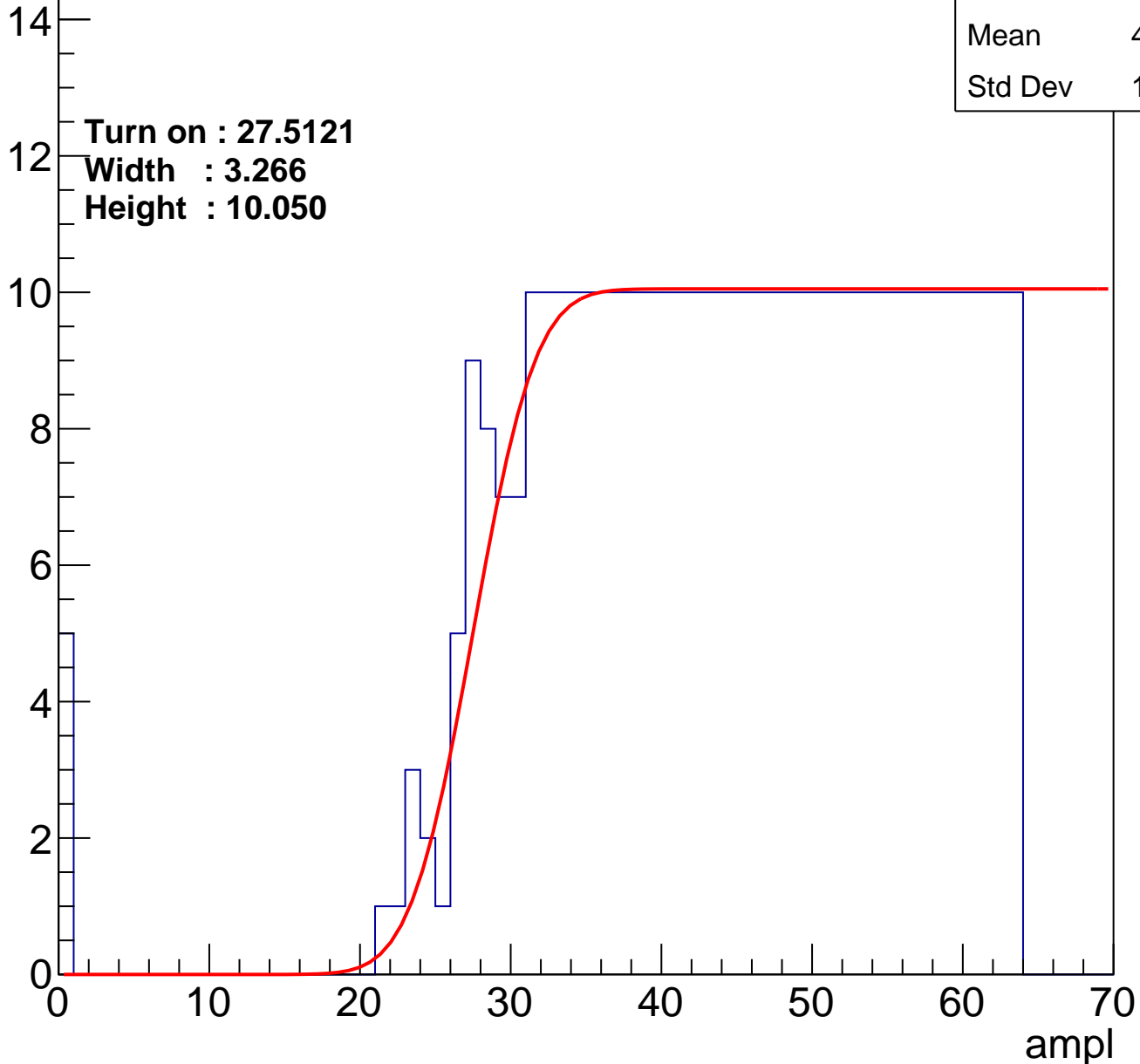
Entries	379
Mean	44.08
Std Dev	12.08

Turn on : 27.5121

Width : 3.266

Height : 10.050

Entry



B0L000S, U10-ch63

calib_packv5_042523_0143.root, FC#5, port B1

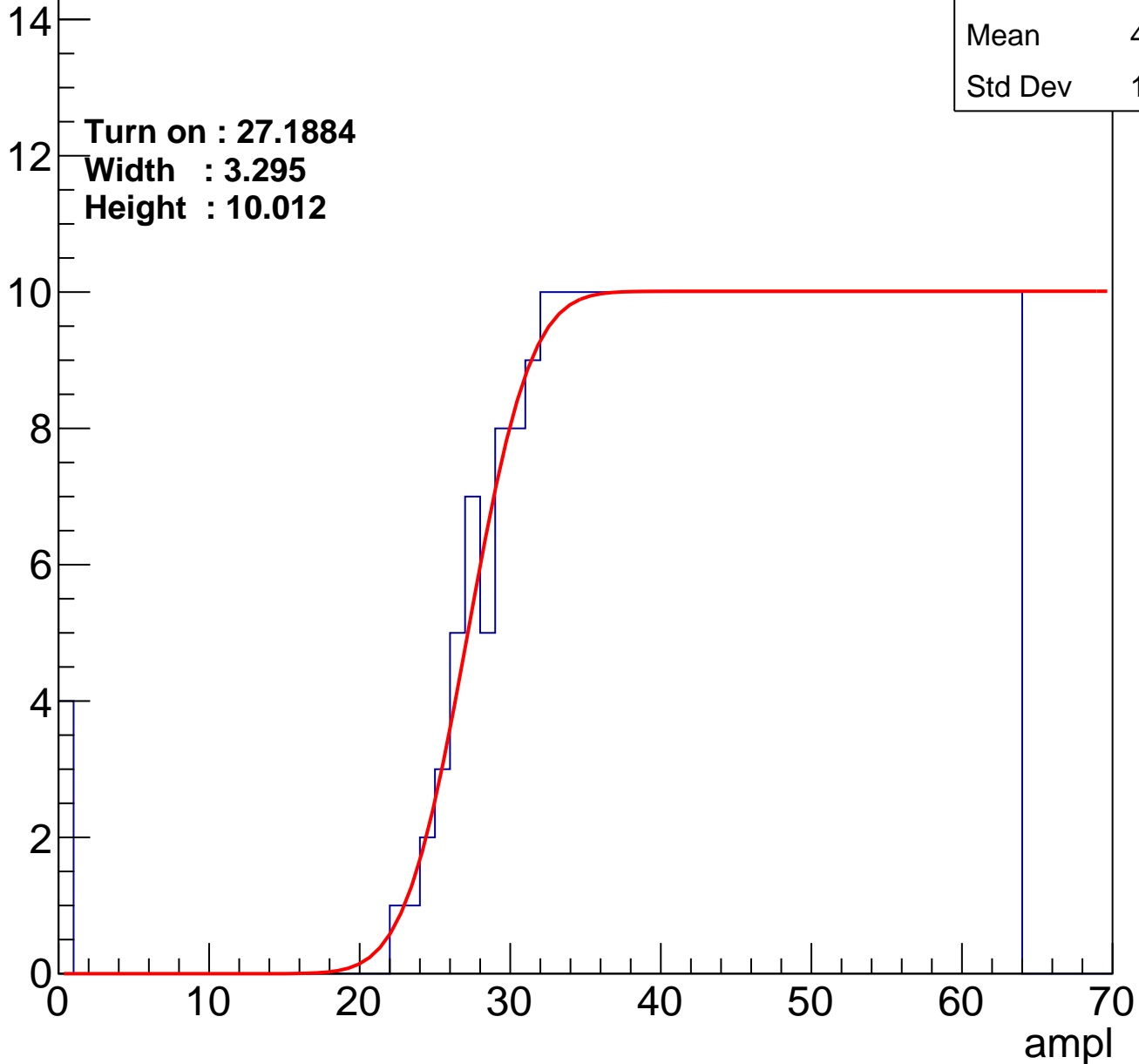
Entries	373
Mean	44.45
Std Dev	11.75

Turn on : 27.1884

Width : 3.295

Height : 10.012

Entry



B0L000S, U10-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.08
Std Dev	11.26

Turn on : 28.1585

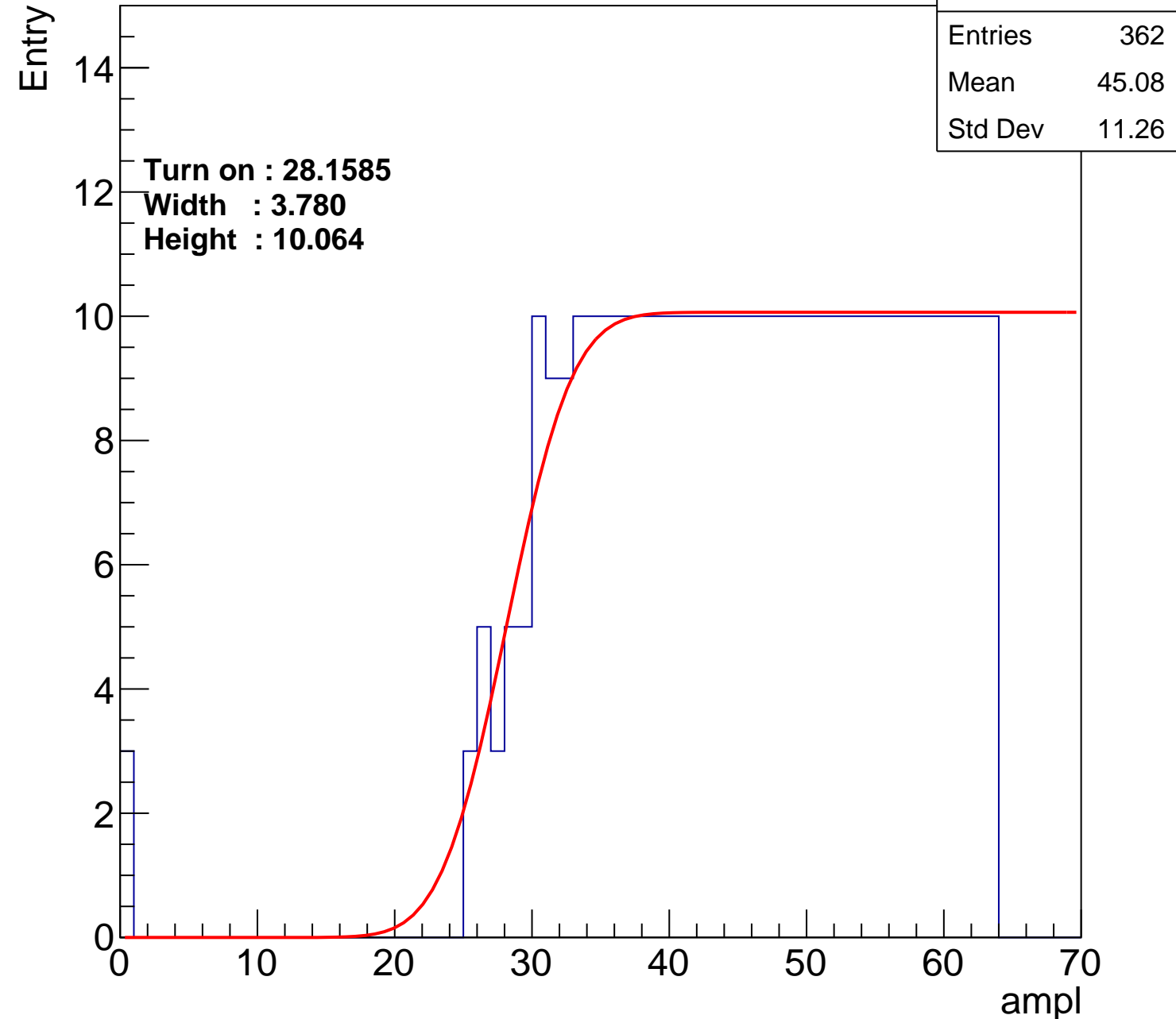
Width : 3.780

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch65

calib_packv5_042523_0143.root, FC#5, port B1

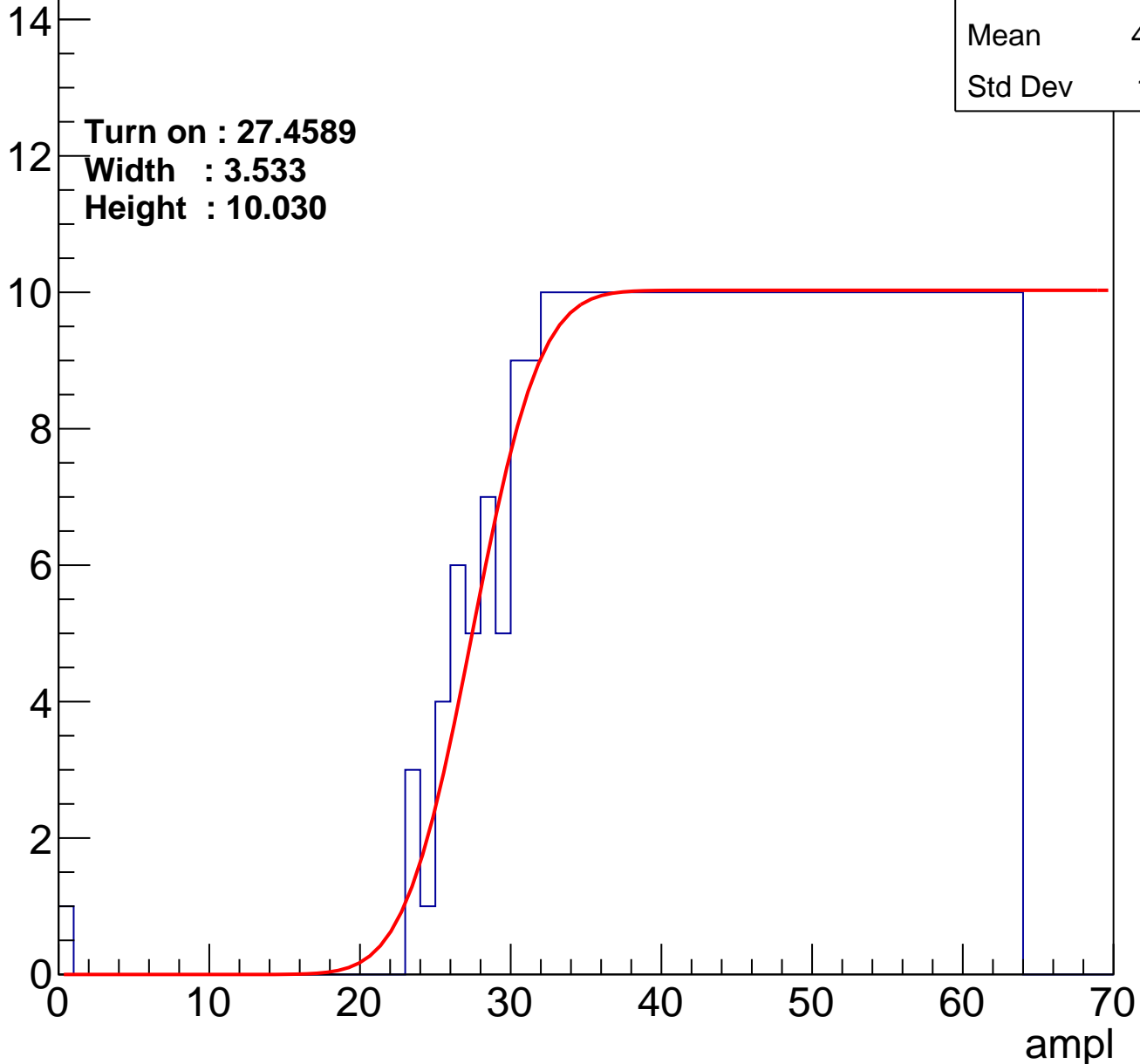
Entries	370
Mean	44.79
Std Dev	11.11

Turn on : 27.4589

Width : 3.533

Height : 10.030

Entry



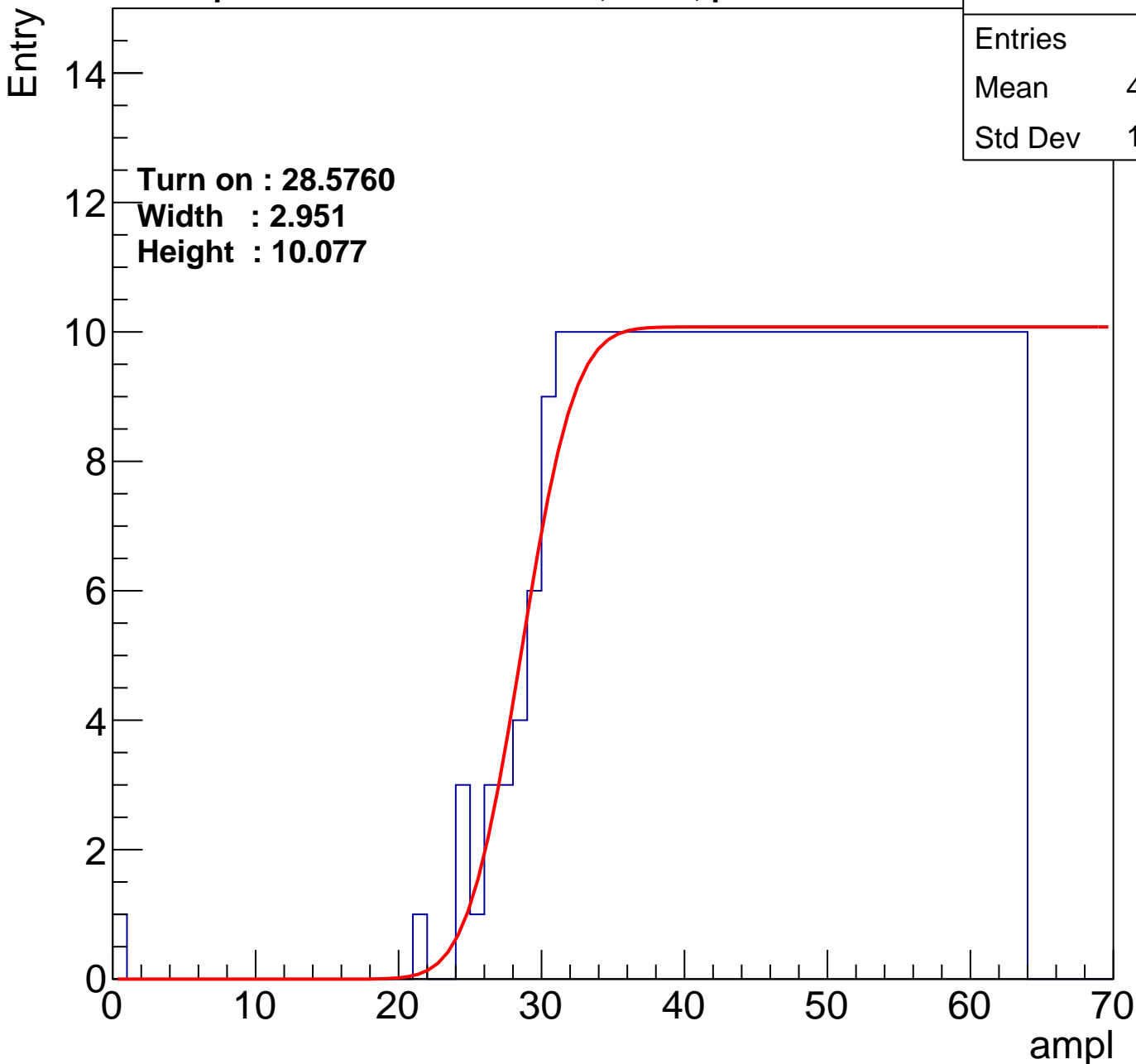
calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.27
Std Dev	10.83

Turn on : 28.5760

Width : 2.951

Height : 10.077



B0L000S, U10-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.77
Std Dev	11.1

Turn on : 27.5562

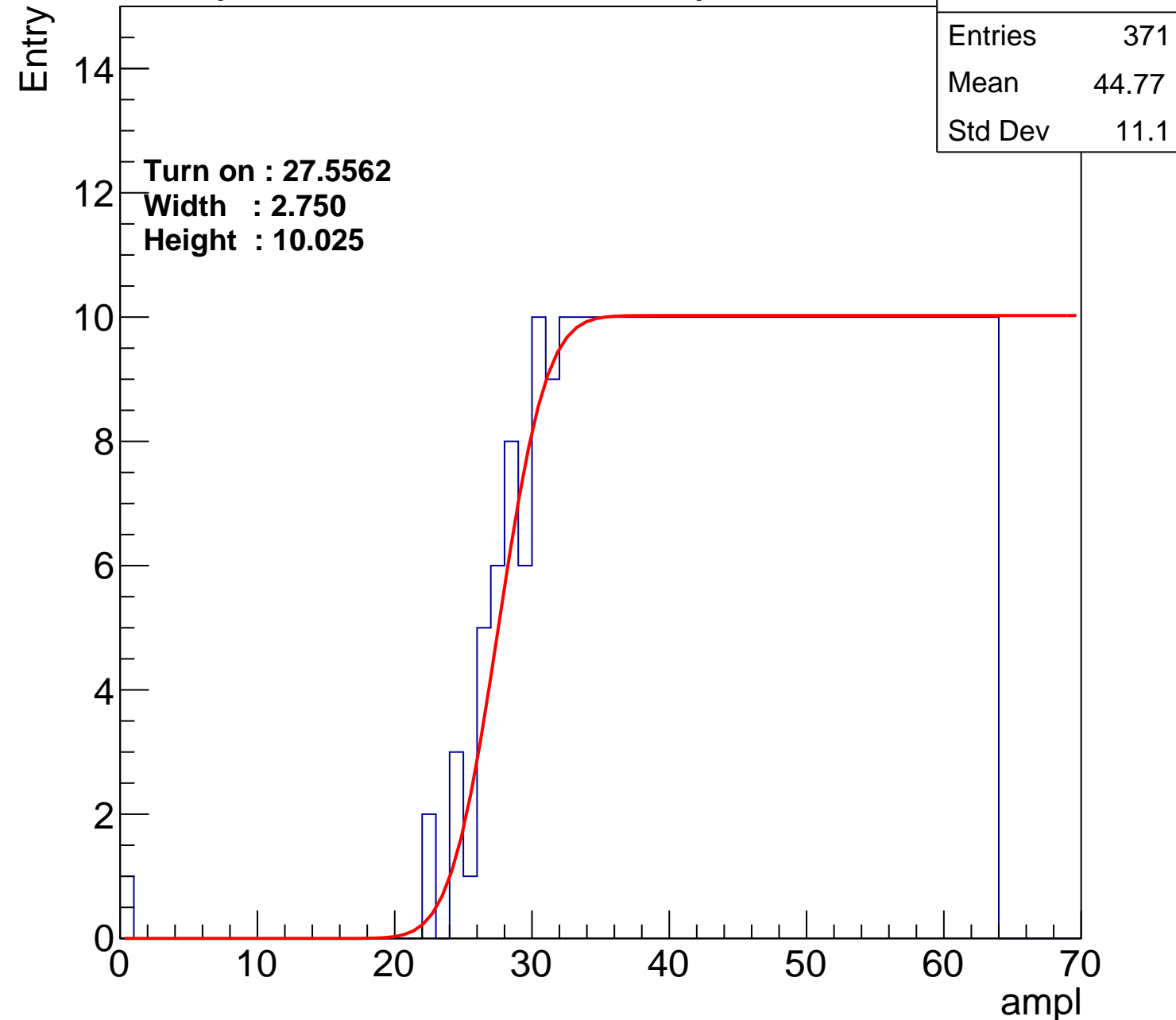
Width : 2.750

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.9685

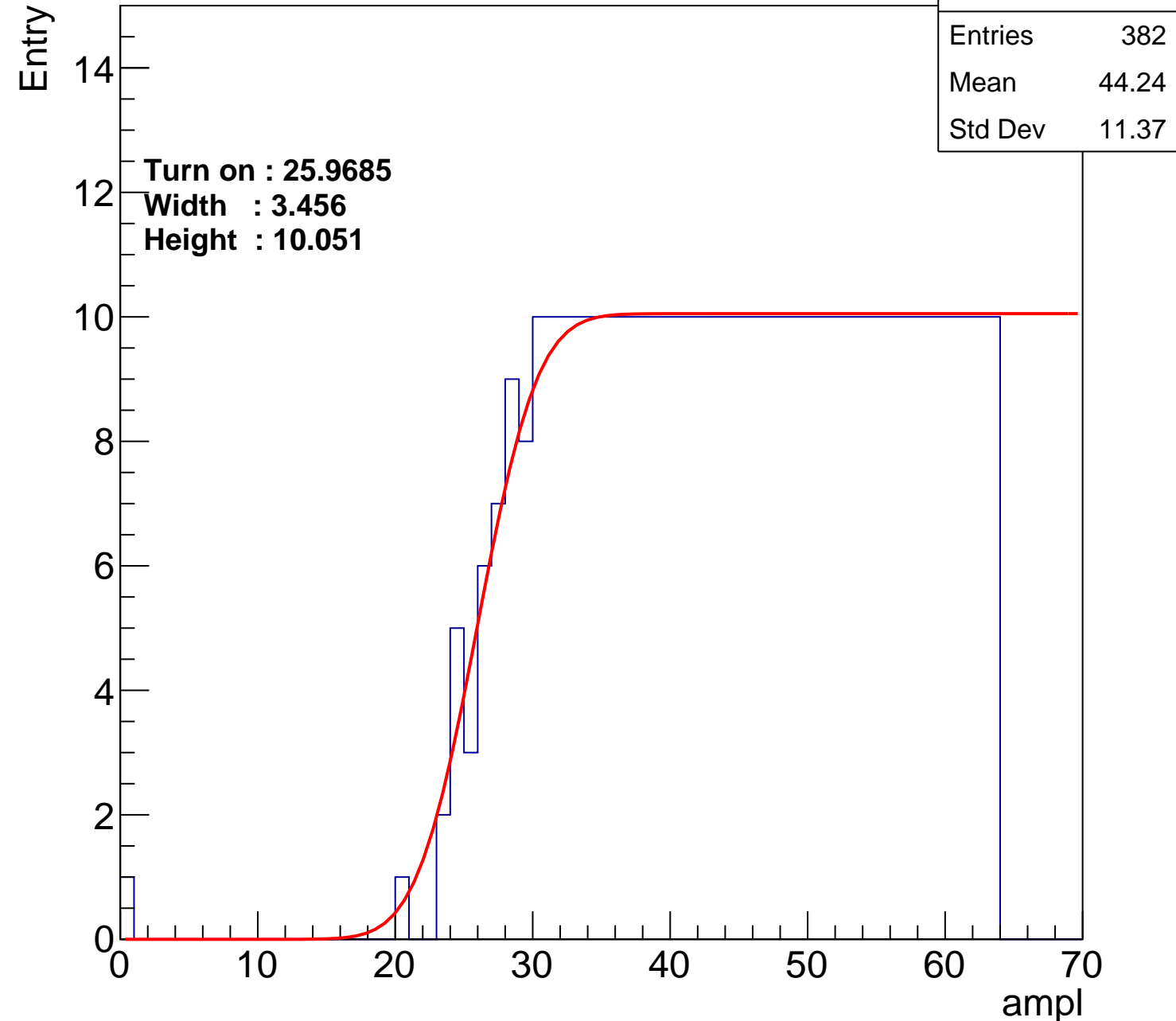
Width : 3.456

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.0783

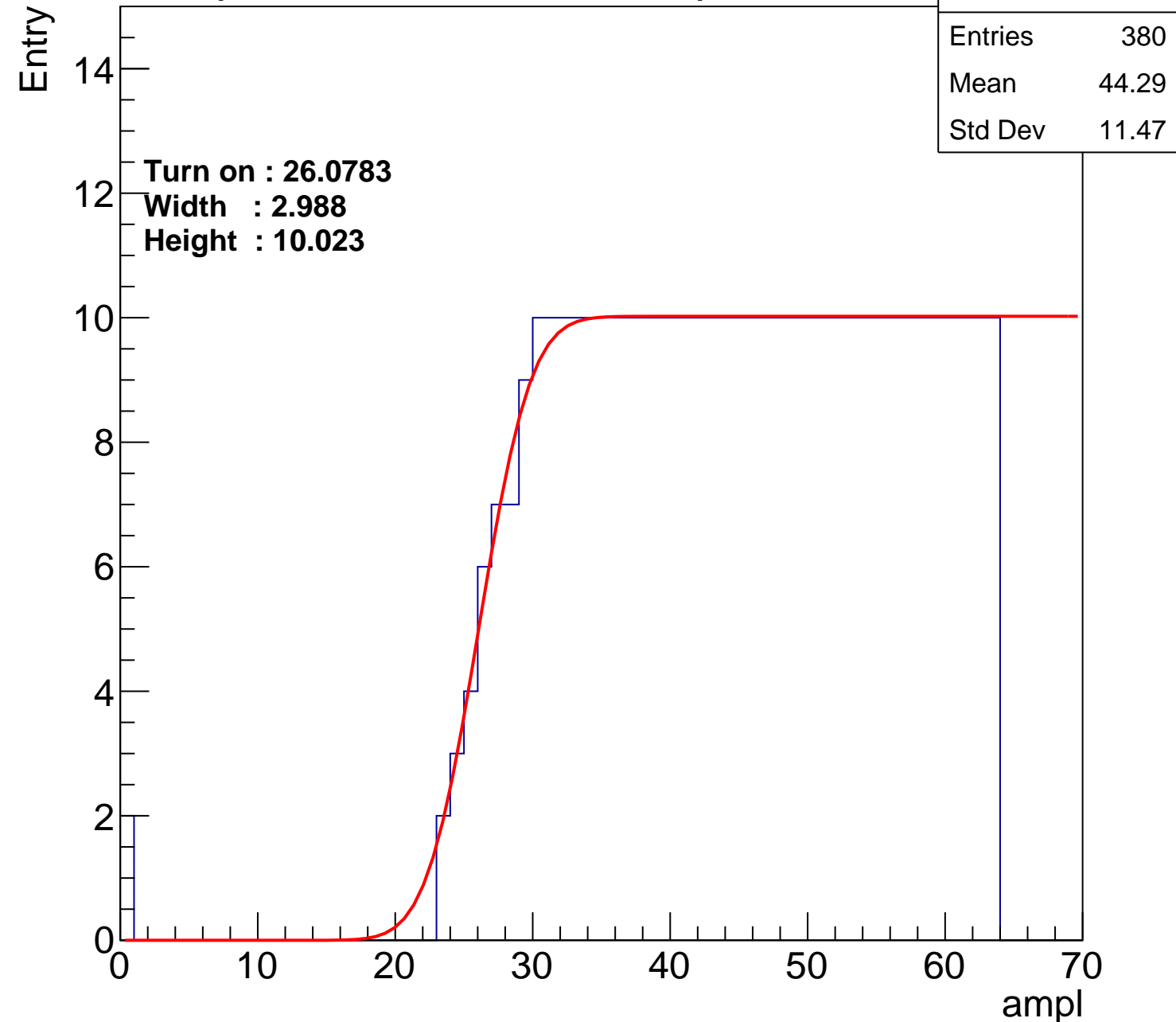
Width : 2.988

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.1824

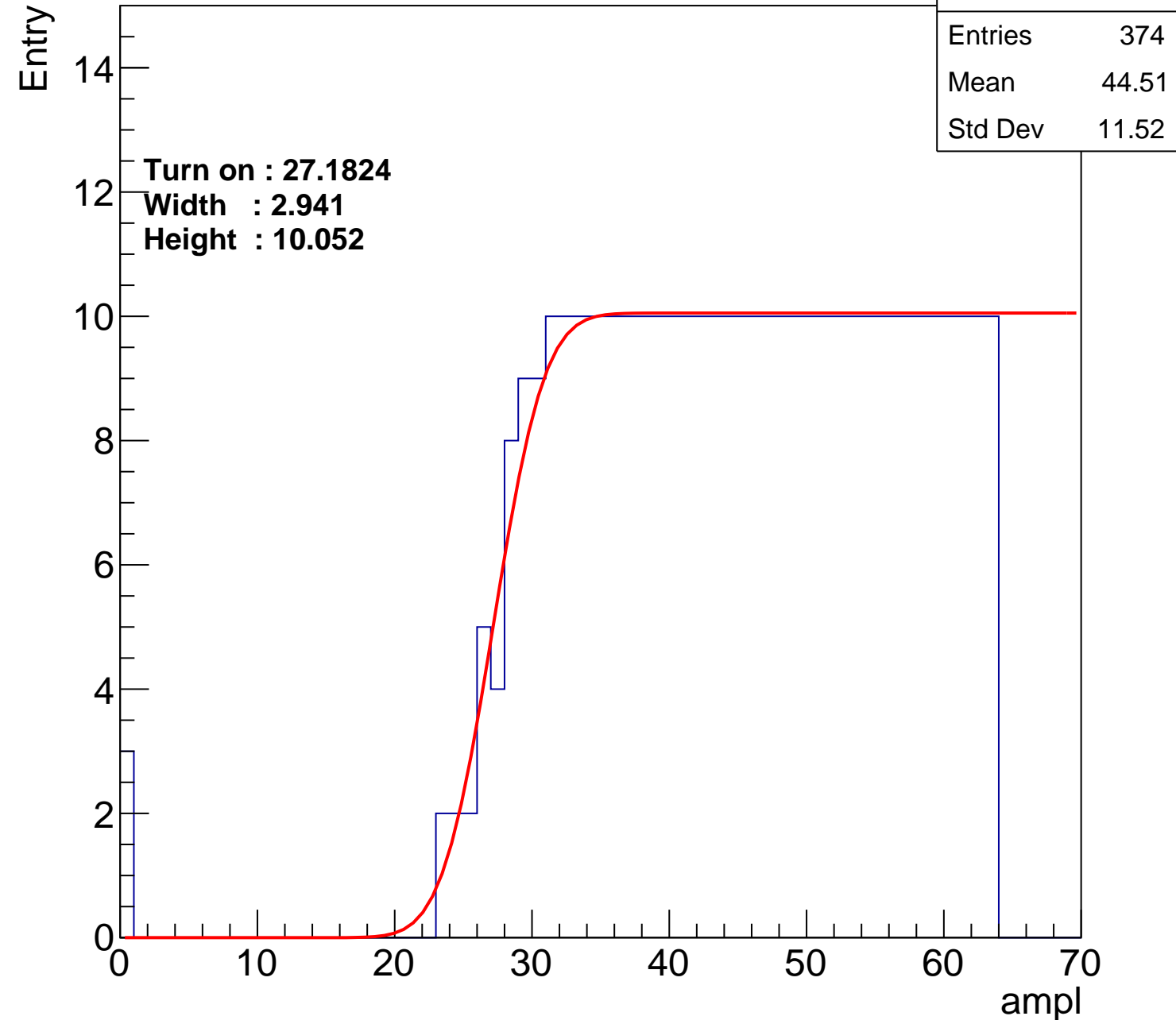
Width : 2.941

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch71

calib_packv5_042523_0143.root, FC#5, port B1

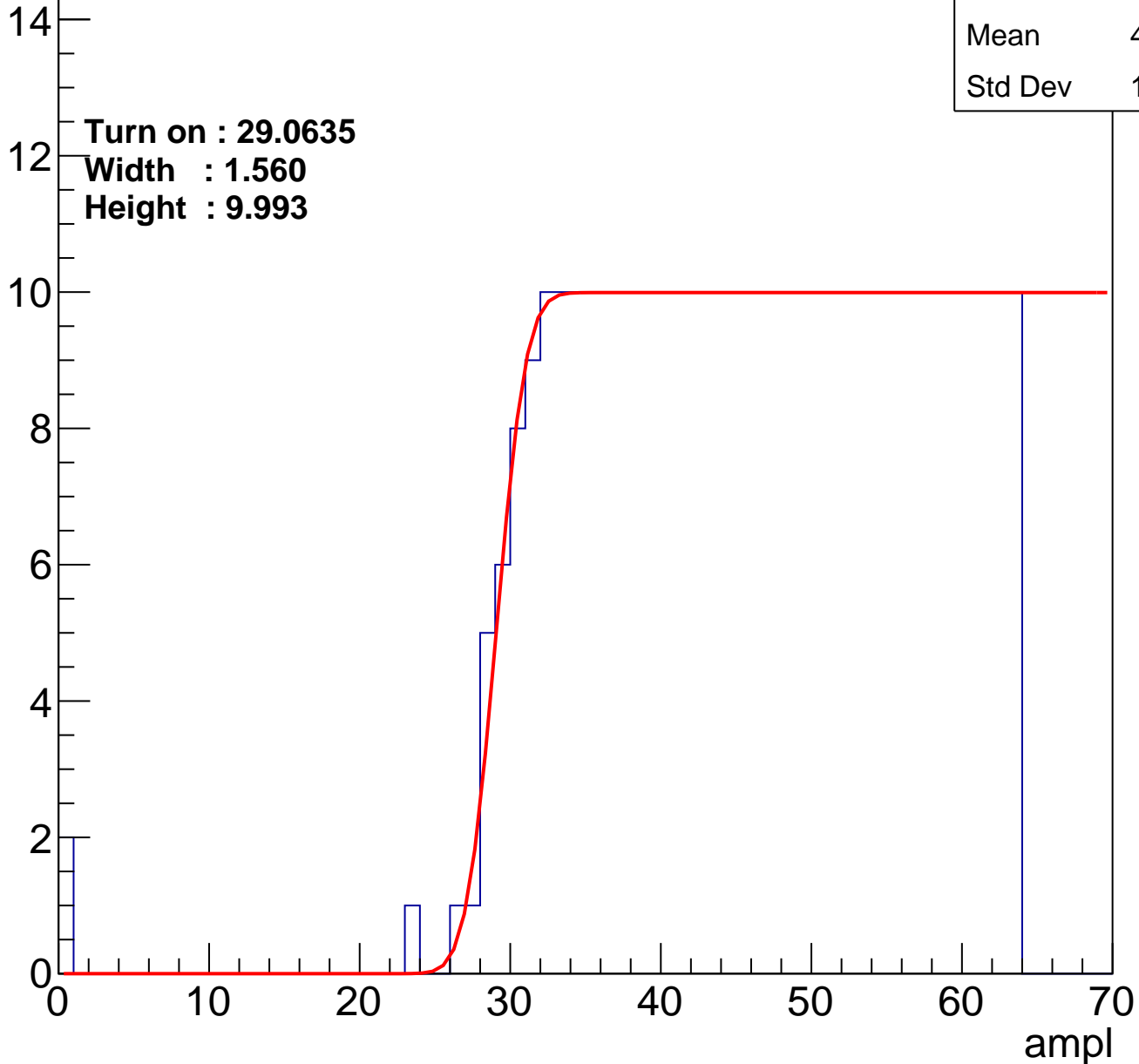
Entries	353
Mean	45.63
Std Dev	10.76

Turn on : 29.0635

Width : 1.560

Height : 9.993

Entry



B0L000S, U10-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.7484

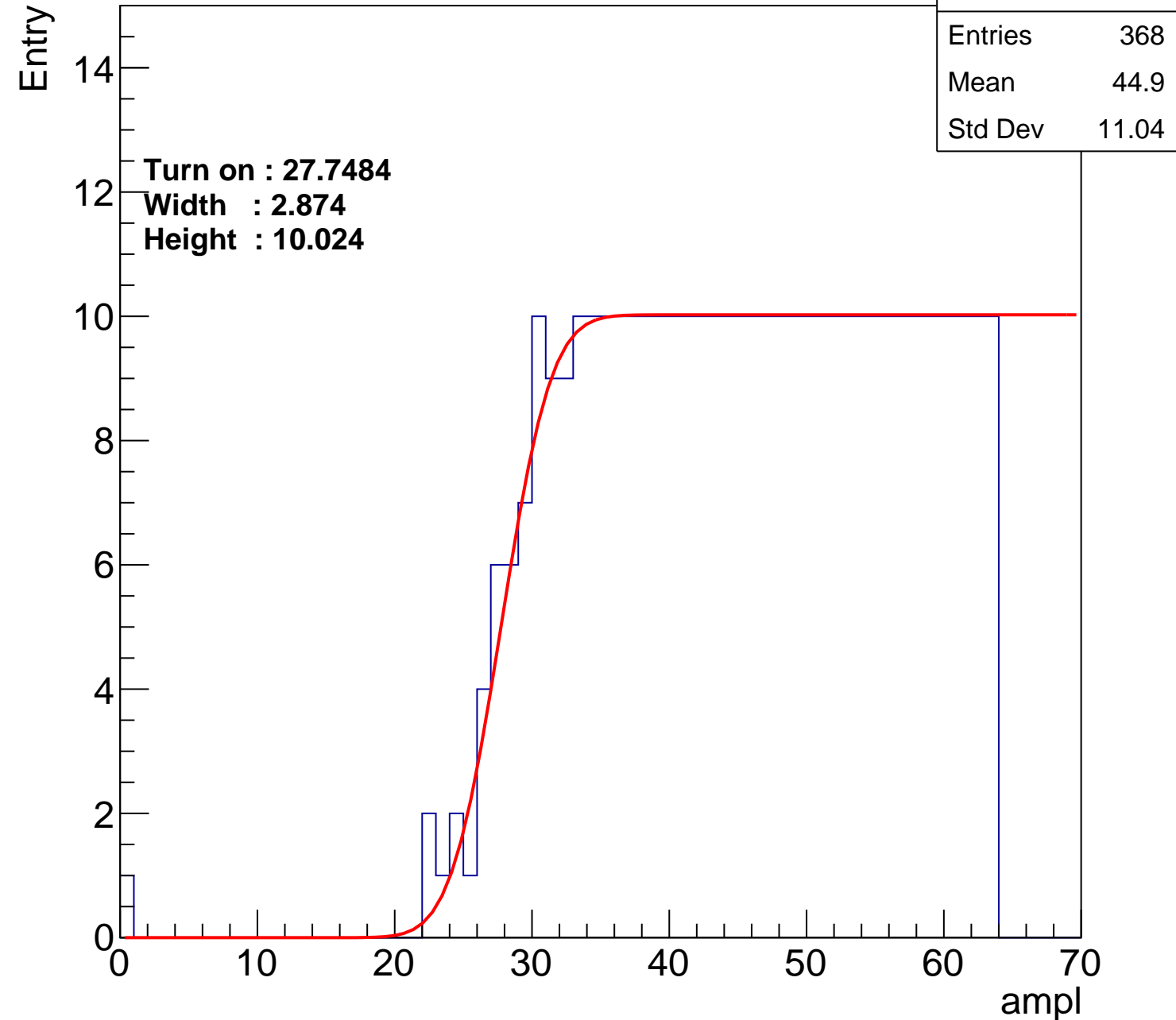
Width : 2.874

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.05
Std Dev	11.78

Turn on : 26.8371

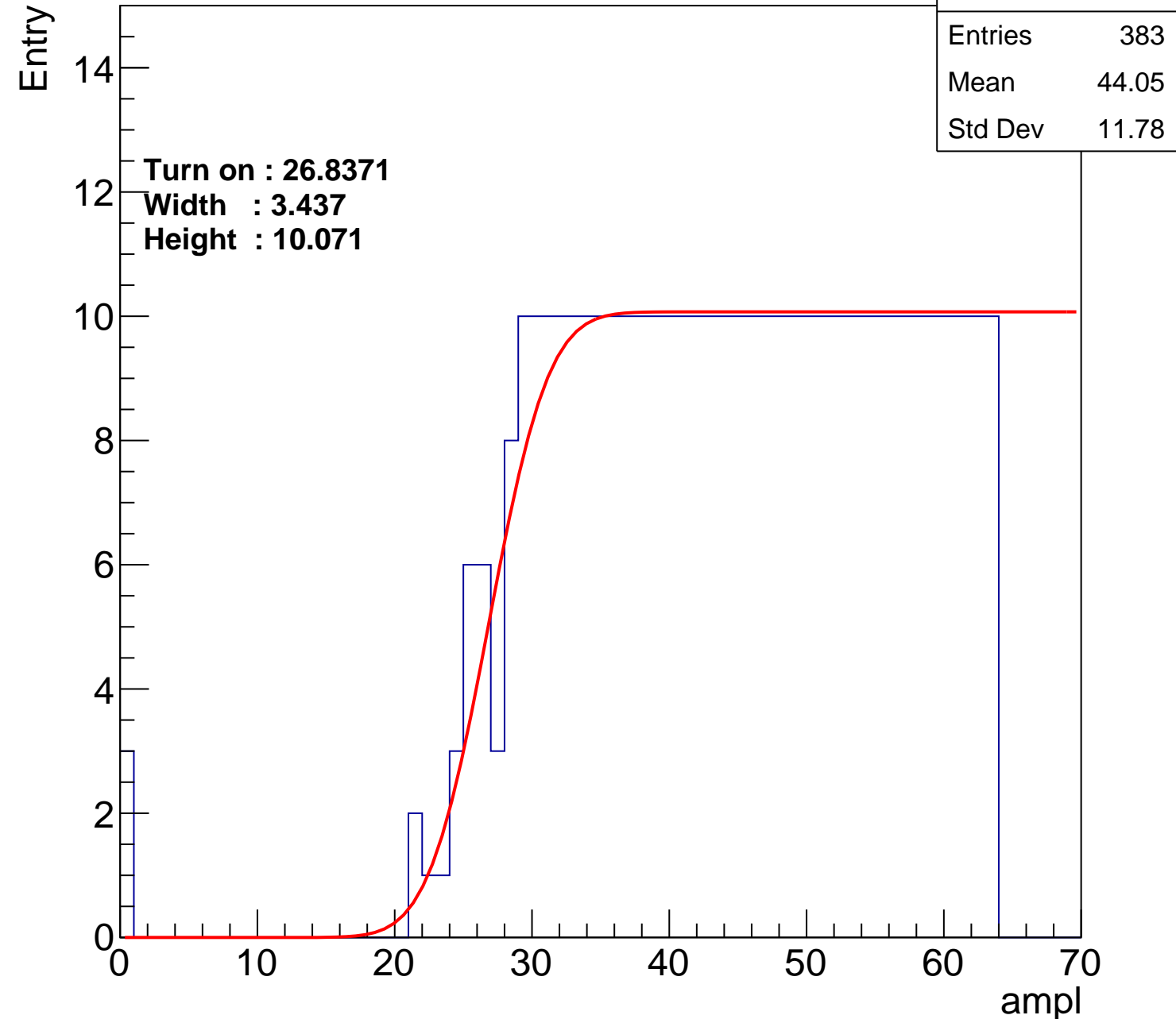
Width : 3.437

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch74

calib_packv5_042523_0143.root, FC#5, port B1

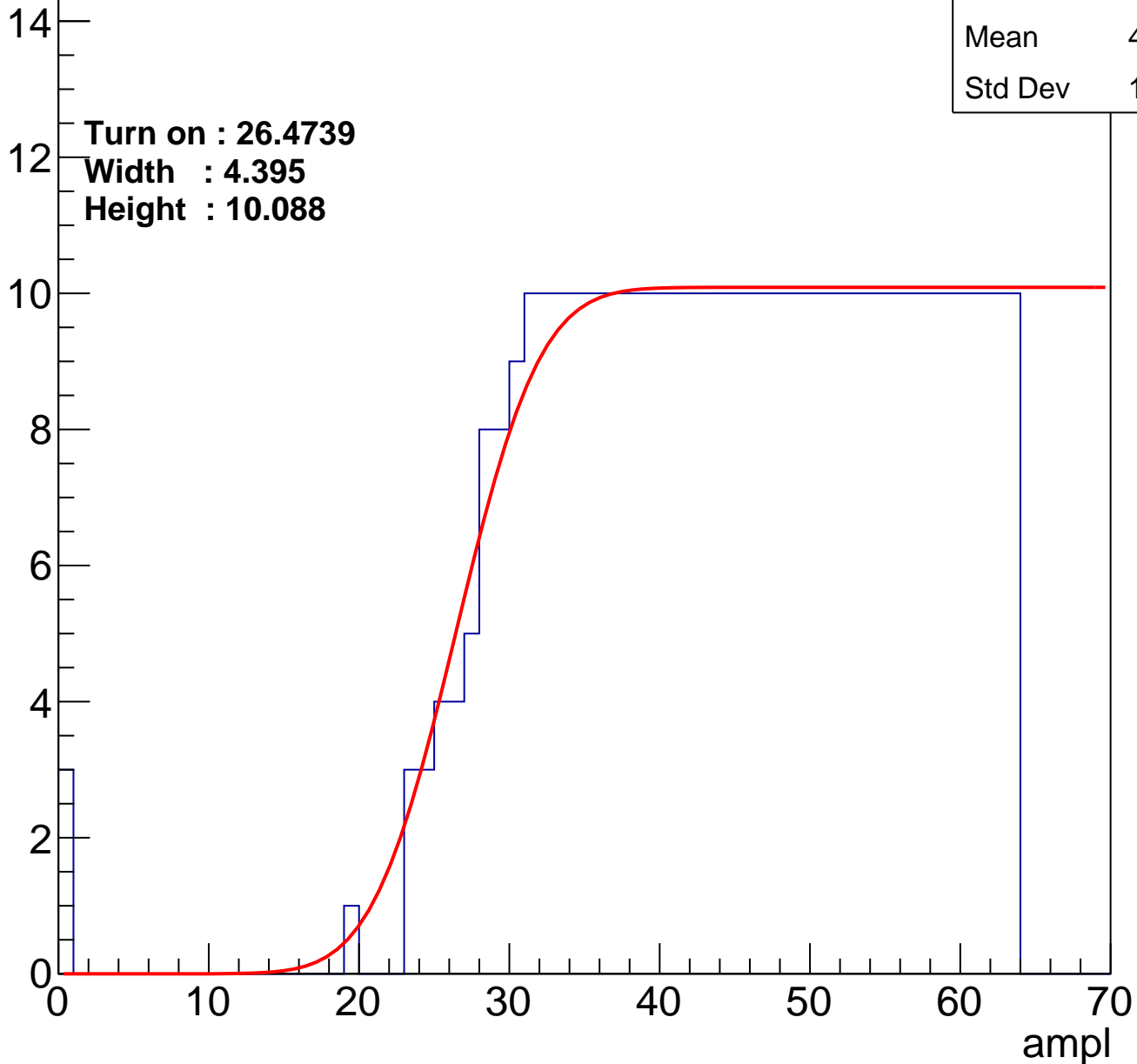
Entries	378
Mean	44.27
Std Dev	11.69

Turn on : 26.4739

Width : 4.395

Height : 10.088

Entry



B0L000S, U10-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.88
Std Dev	10.46

Turn on : 29.5471

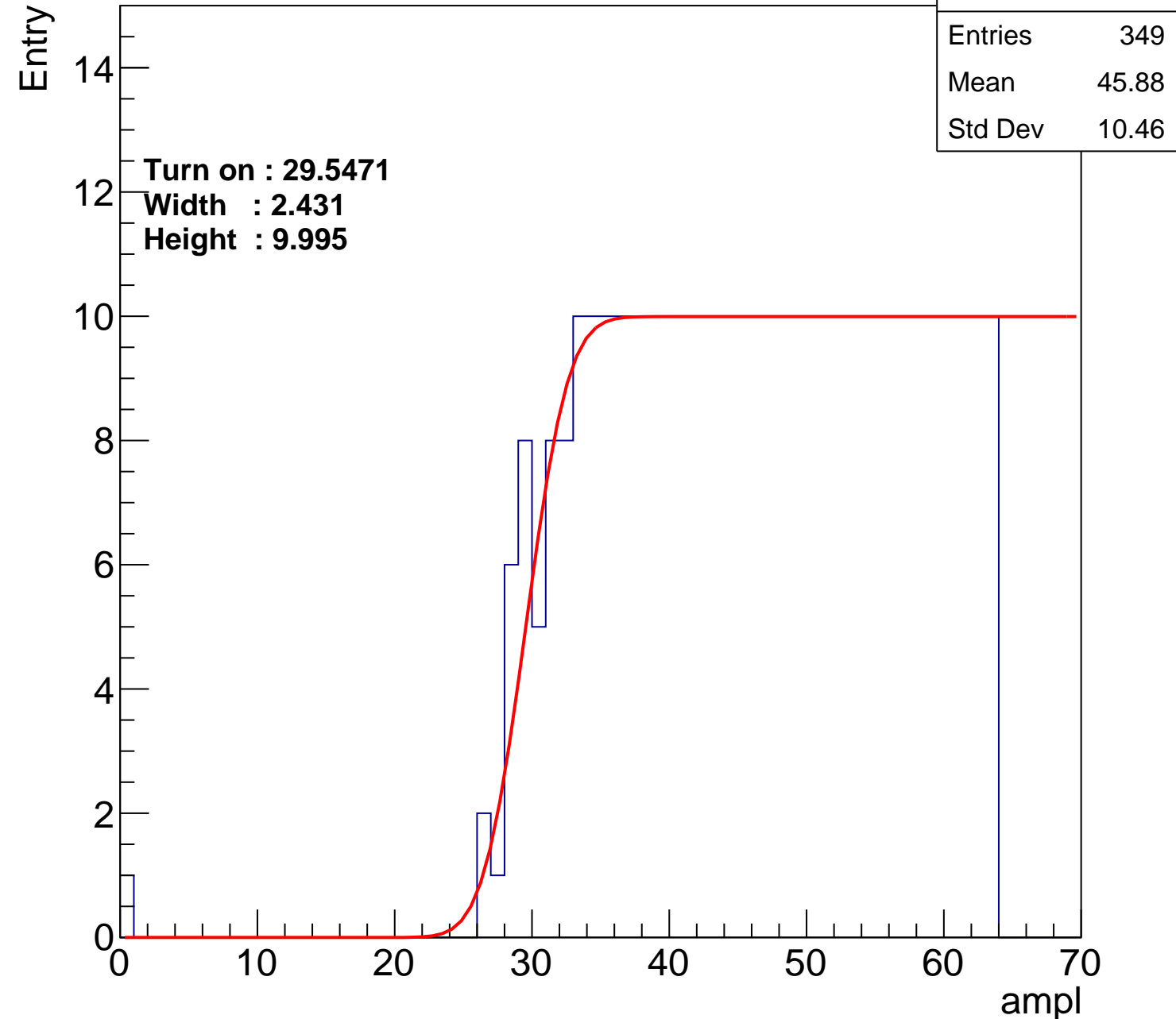
Width : 2.431

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch76

calib_packv5_042523_0143.root, FC#5, port B1

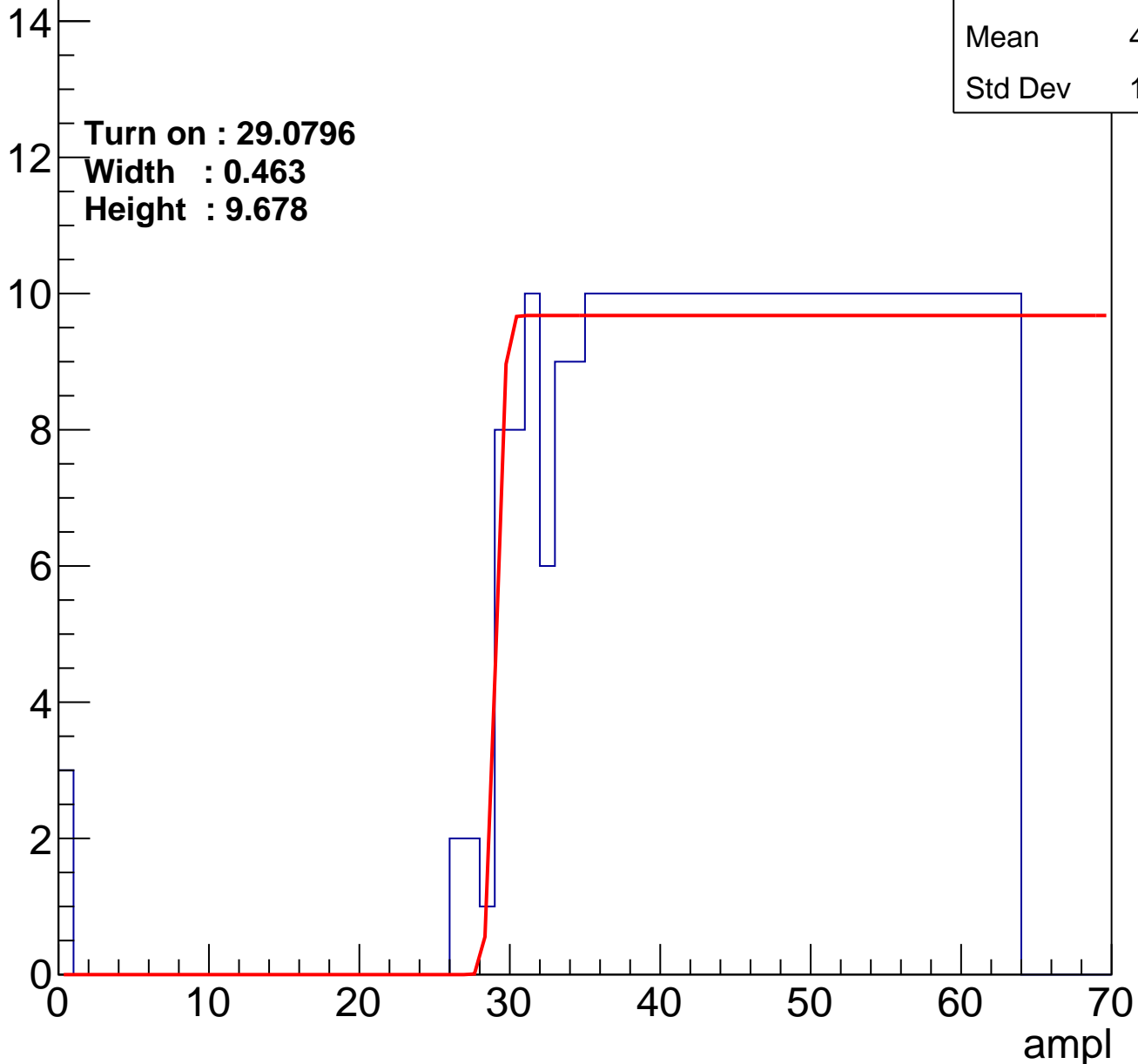
Entries	348
Mean	45.75
Std Dev	10.94

Turn on : 29.0796

Width : 0.463

Height : 9.678

Entry



B0L000S, U10-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	348
Mean	45.7
Std Dev	11.03

Turn on : 29.8202

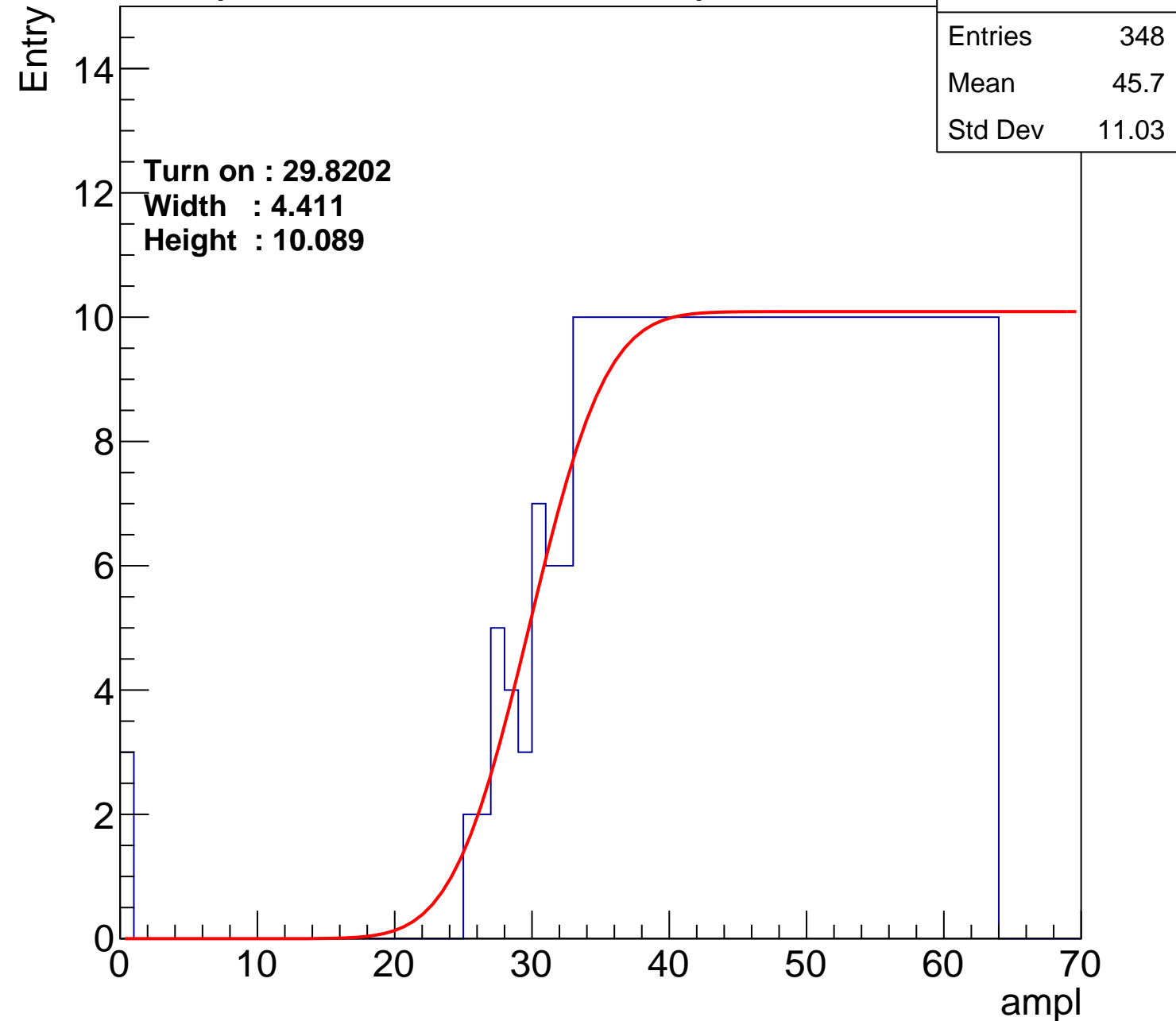
Width : 4.411

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.13
Std Dev	11.3

Turn on : 29.4592

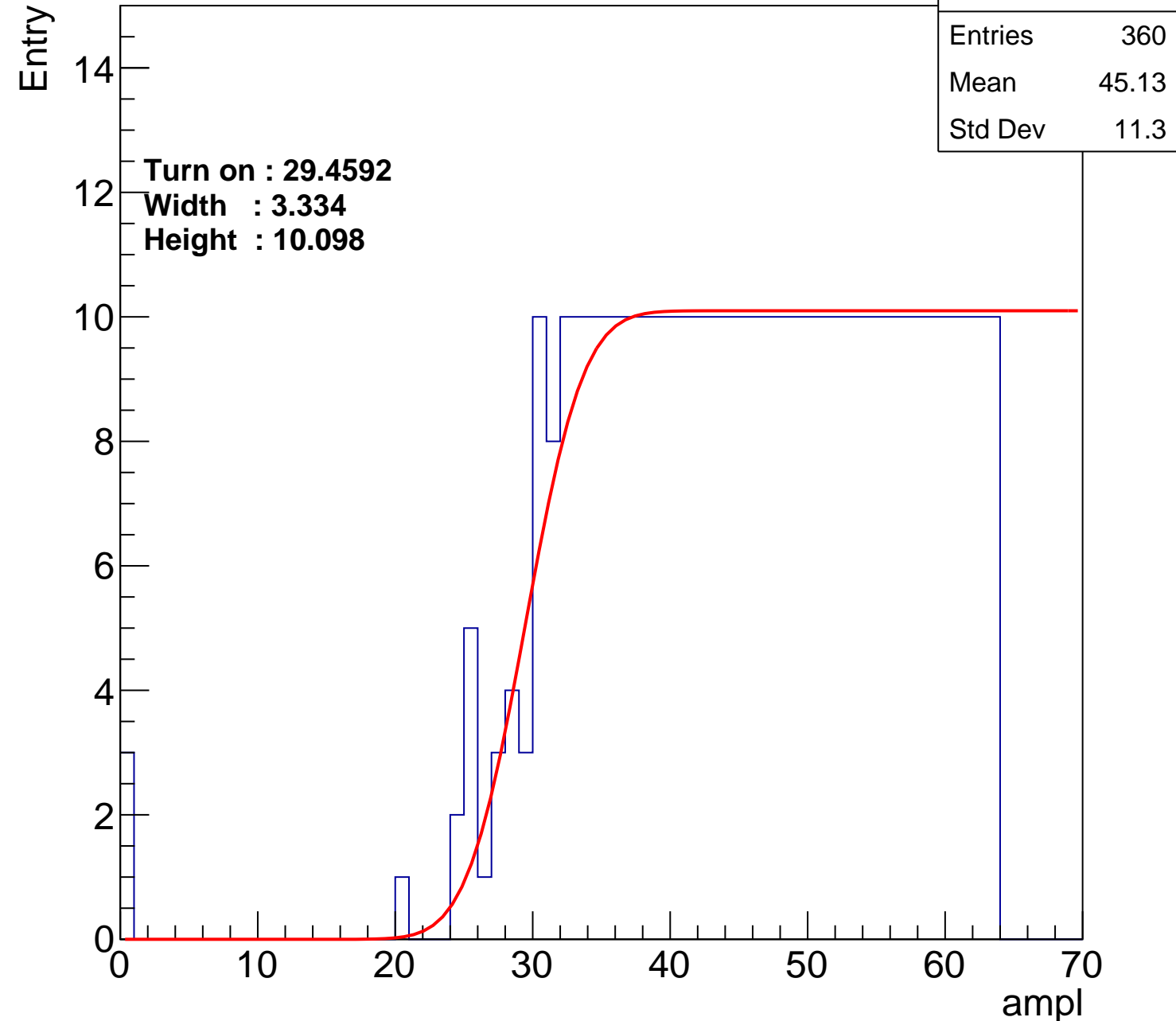
Width : 3.334

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 26.7052

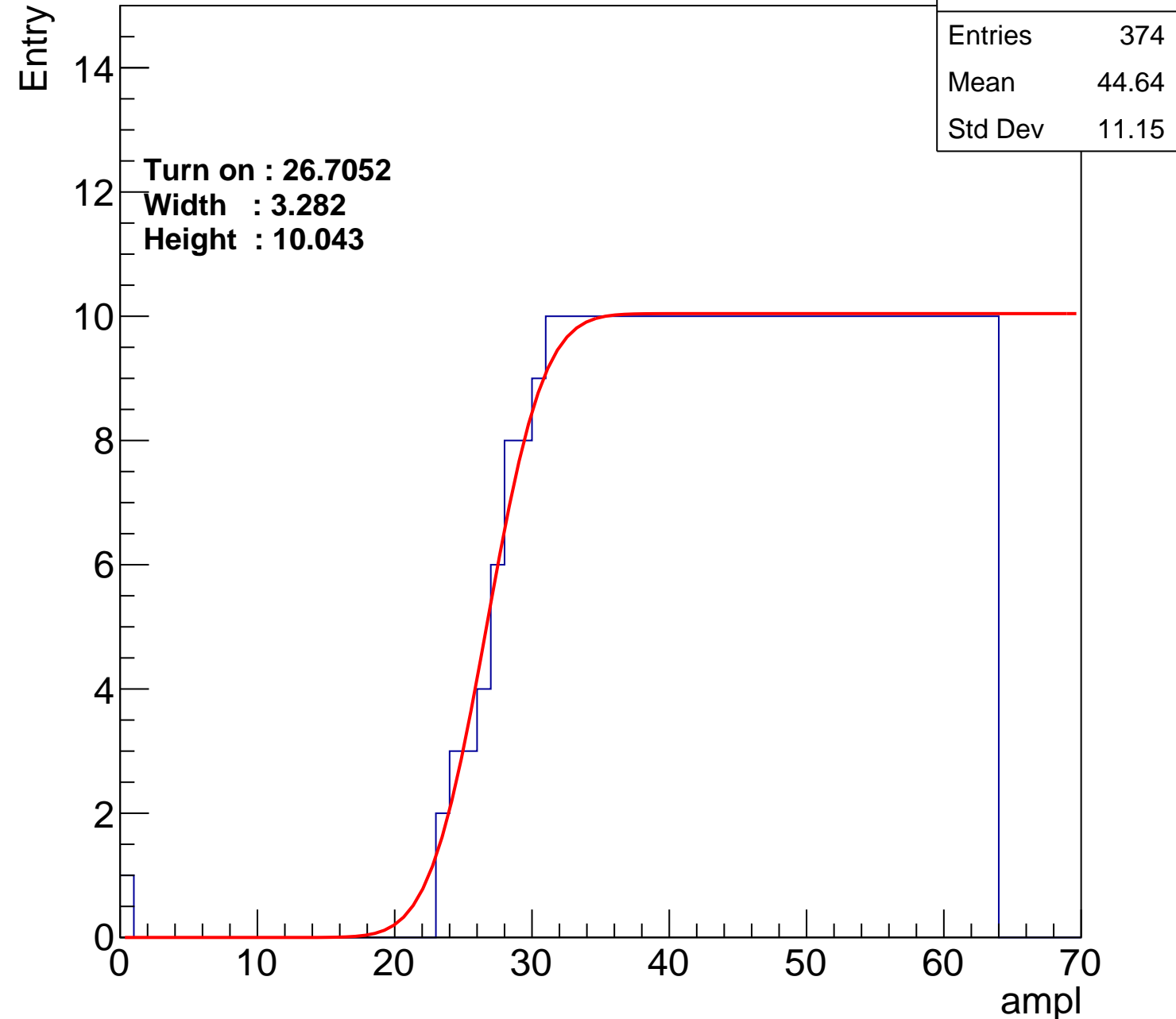
Width : 3.282

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch80

calib_packv5_042523_0143.root, FC#5, port B1

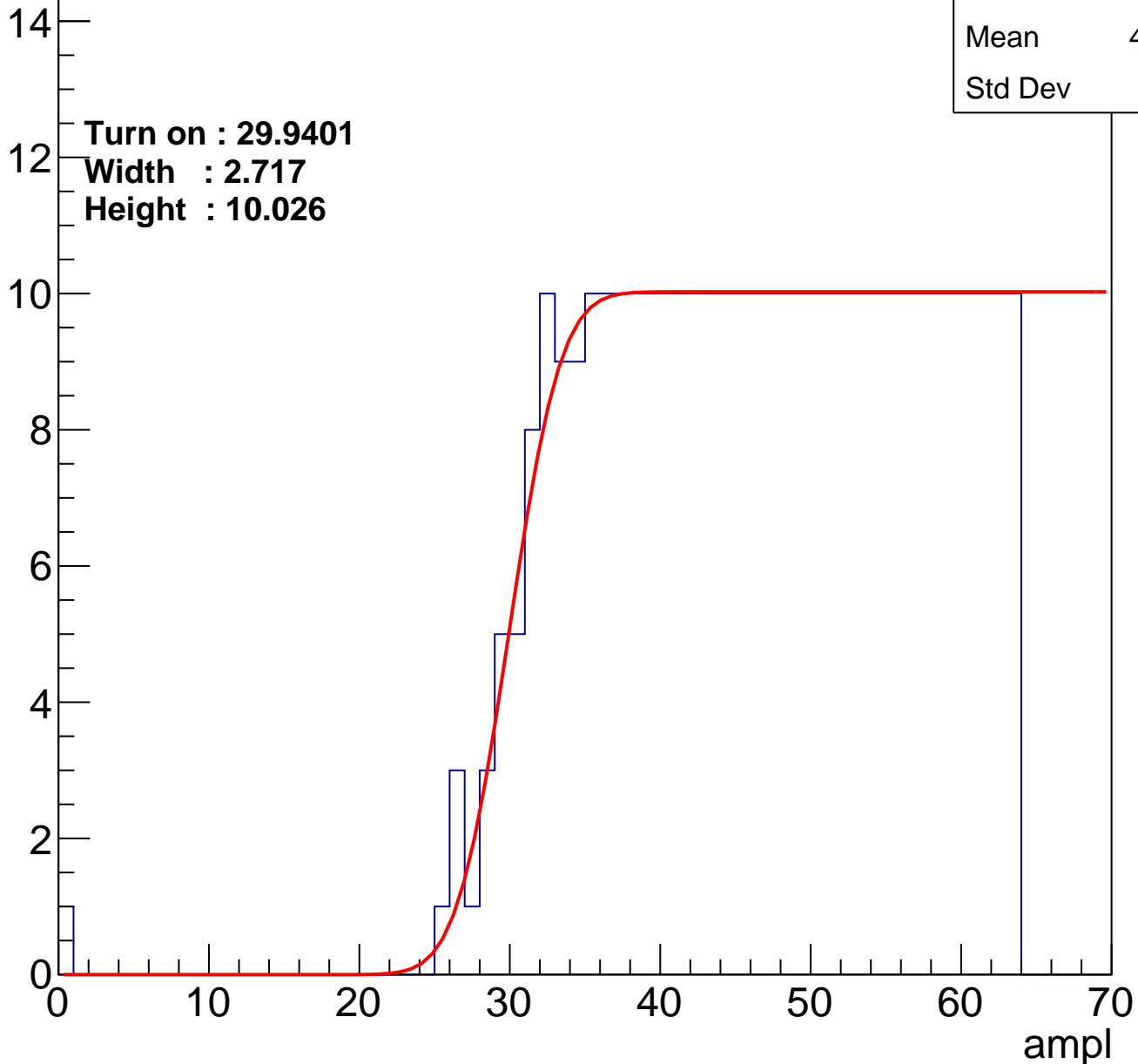
Entries	345
Mean	46.06
Std Dev	10.4

Turn on : 29.9401

Width : 2.717

Height : 10.026

Entry



B0L000S, U10-ch81

calib_packv5_042523_0143.root, FC#5, port B1

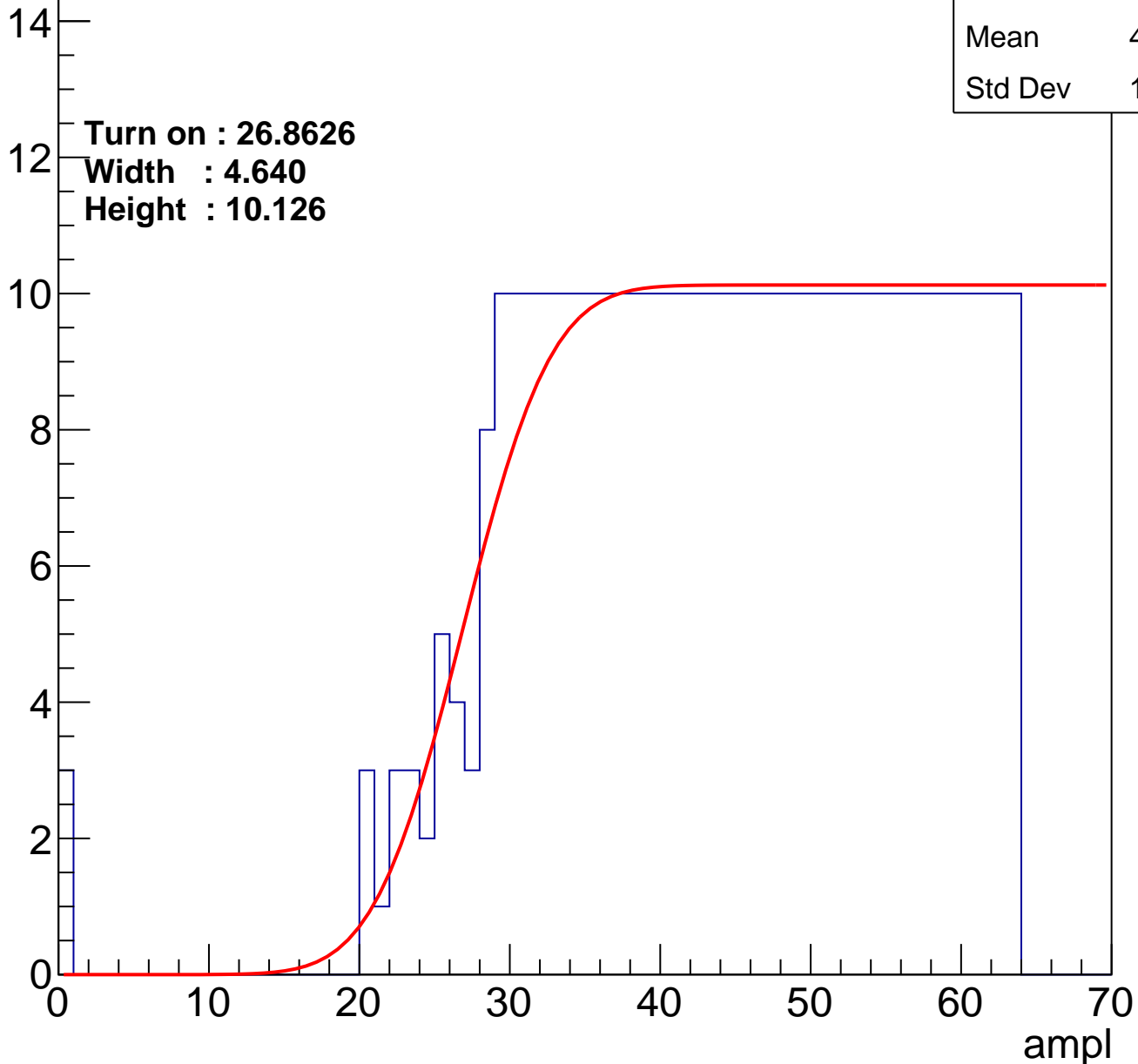
Entries	385
Mean	43.89
Std Dev	11.93

Turn on : 26.8626

Width : 4.640

Height : 10.126

Entry



B0L000S, U10-ch82

calib_packv5_042523_0143.root, FC#5, port B1

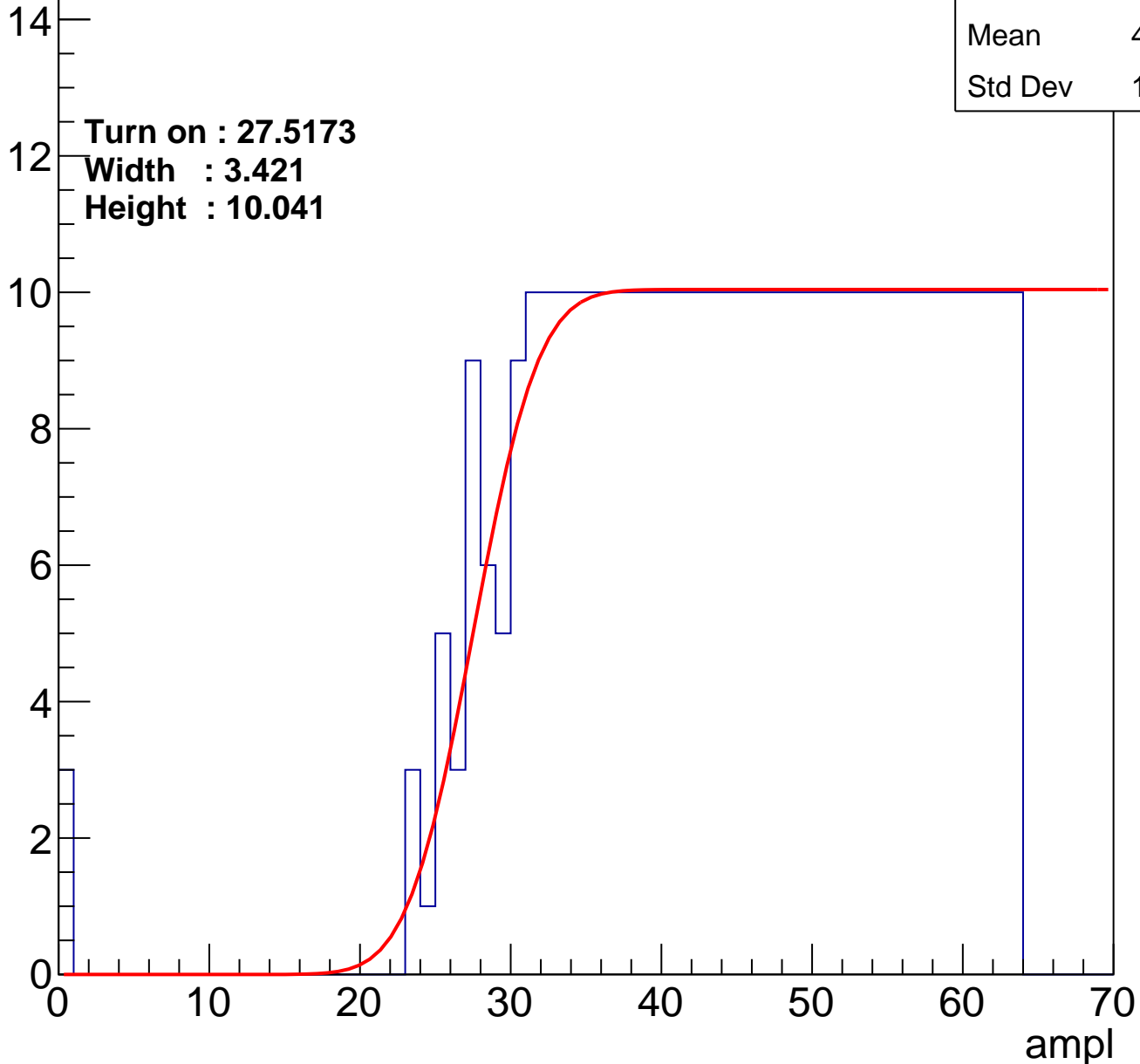
Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 27.5173

Width : 3.421

Height : 10.041

Entry



B0L000S, U10-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.87
Std Dev	11.19

Turn on : 27.4961

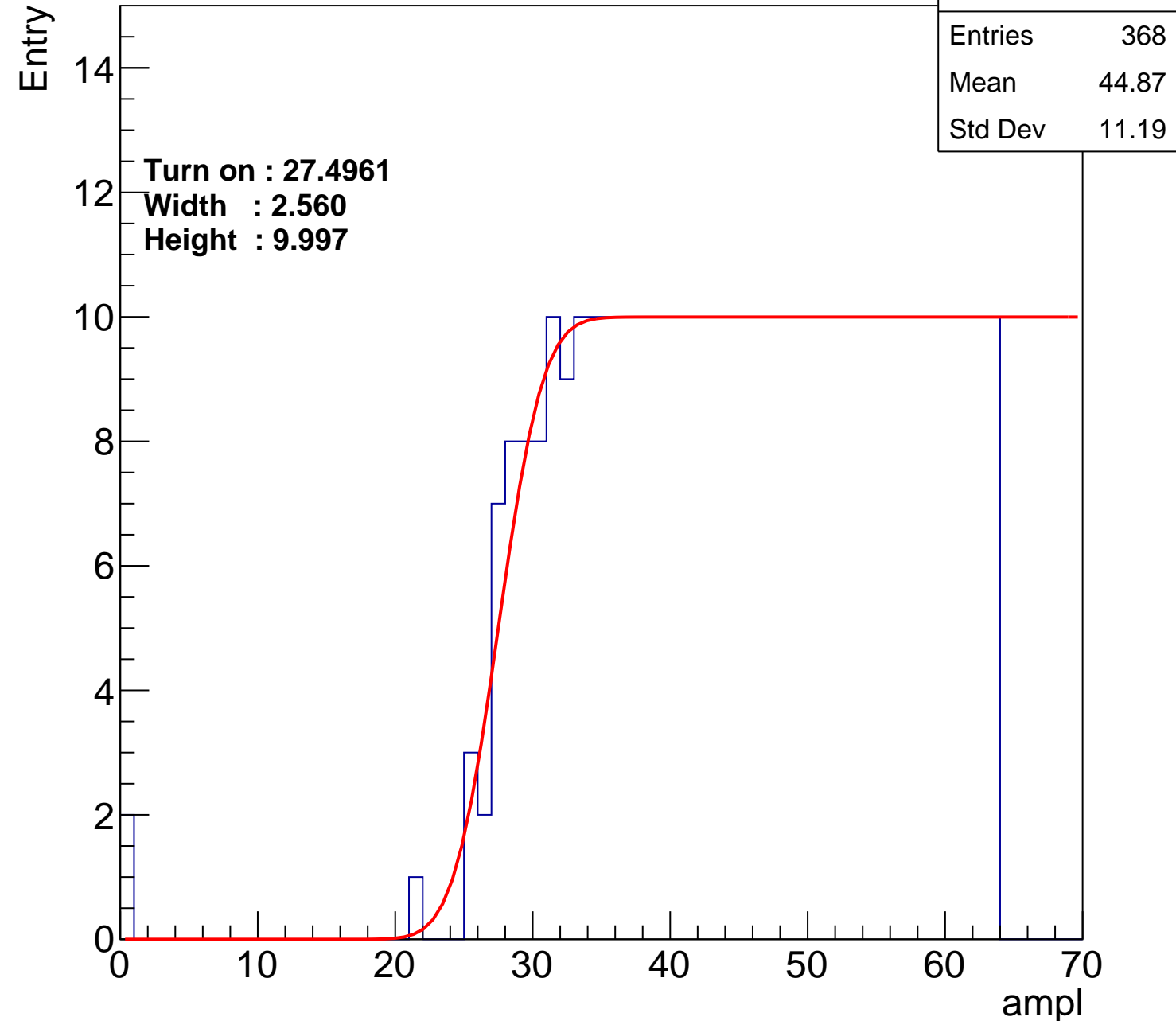
Width : 2.560

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.84
Std Dev	10.48

Turn on : 29.2197

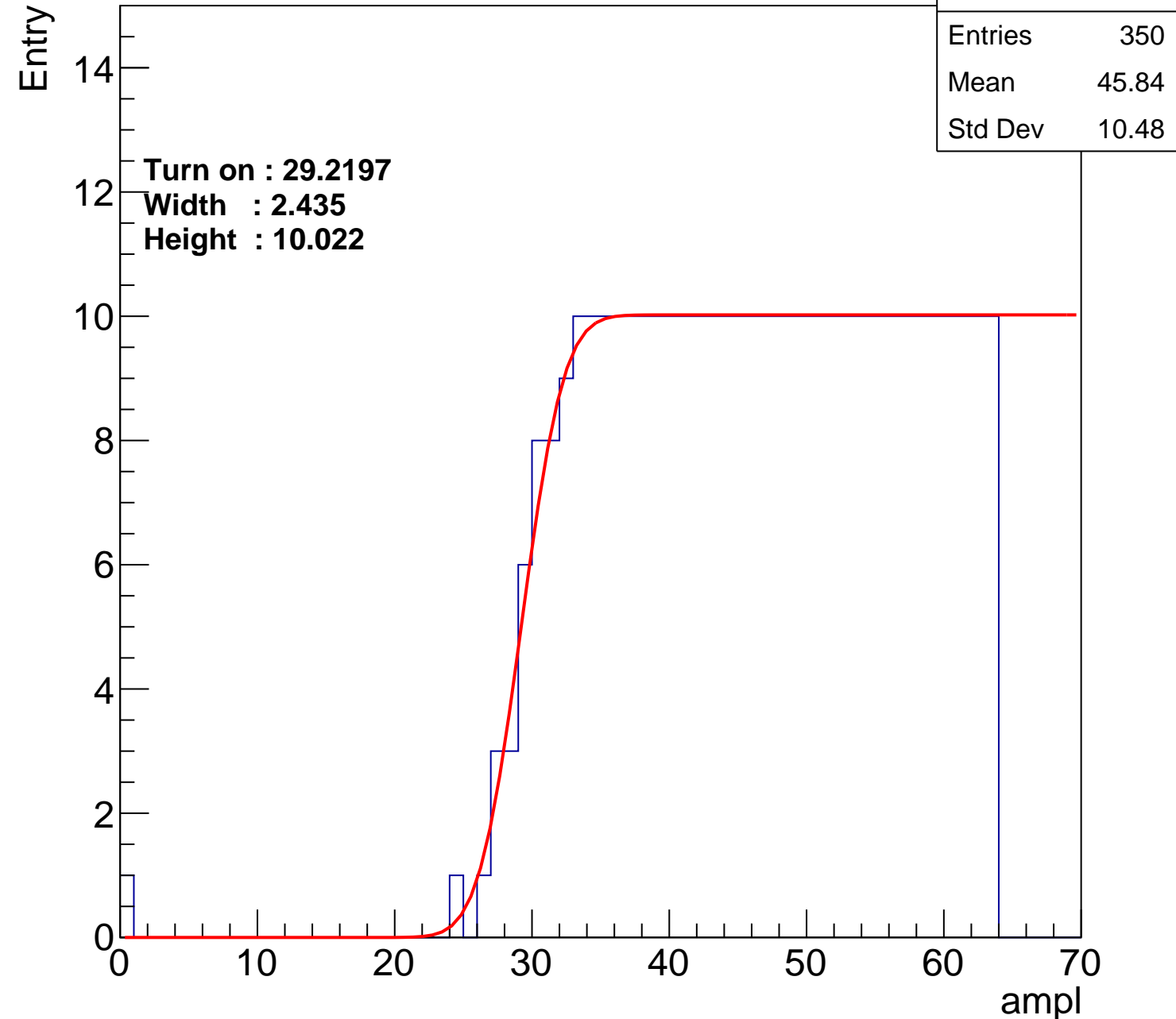
Width : 2.435

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 27.6692

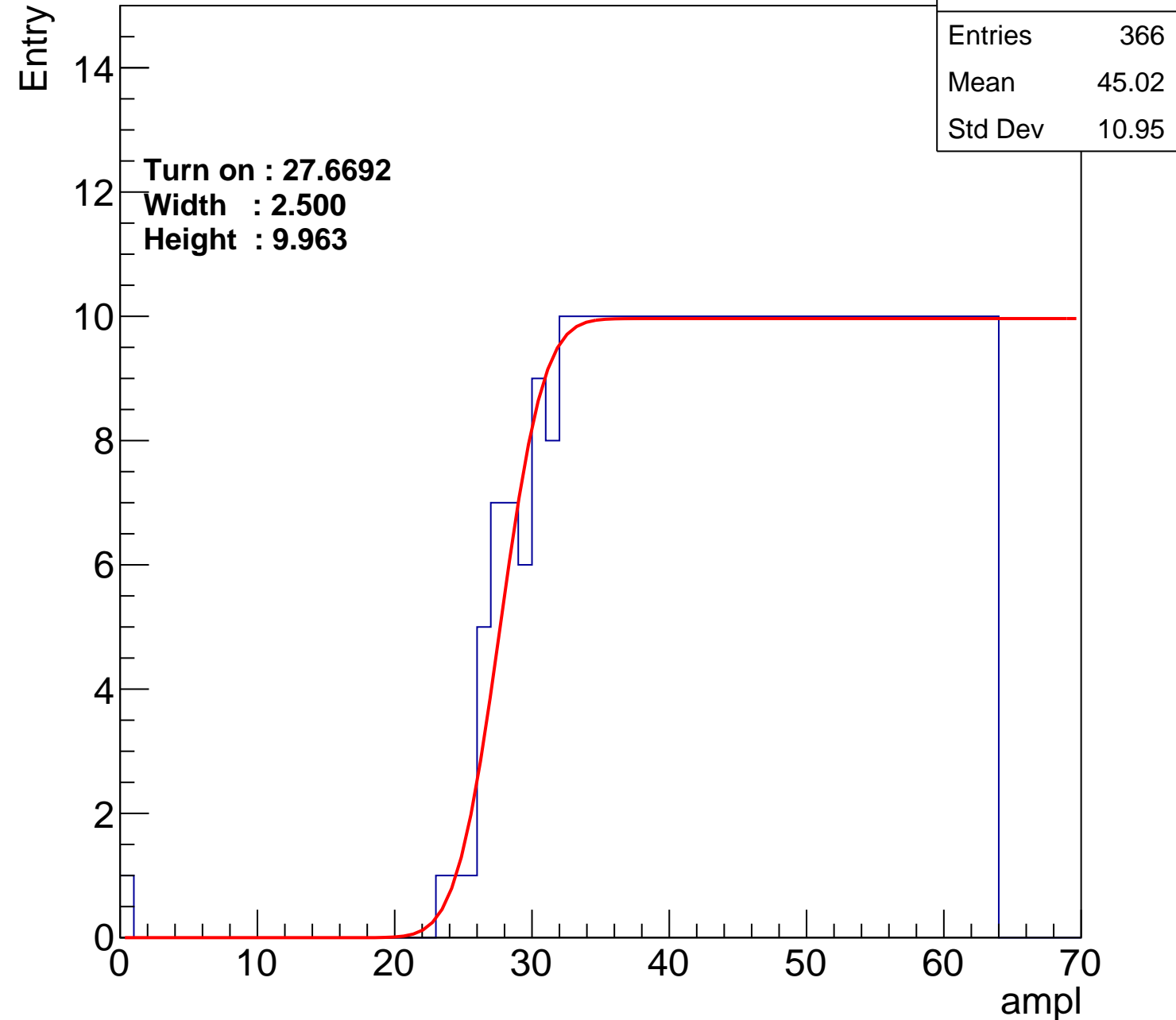
Width : 2.500

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.3986

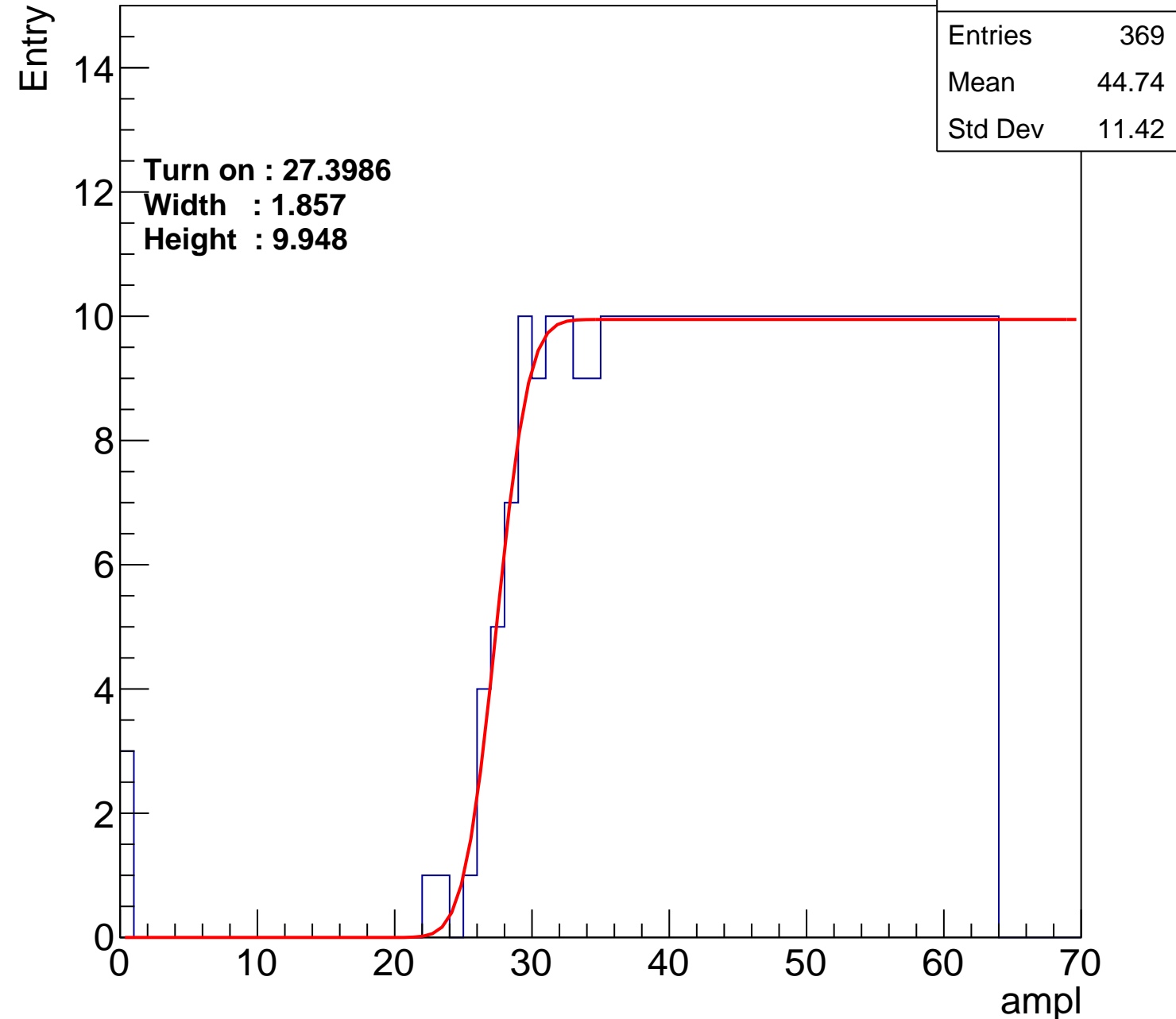
Width : 1.857

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.71
Std Dev	10.8

Turn on : 28.7575

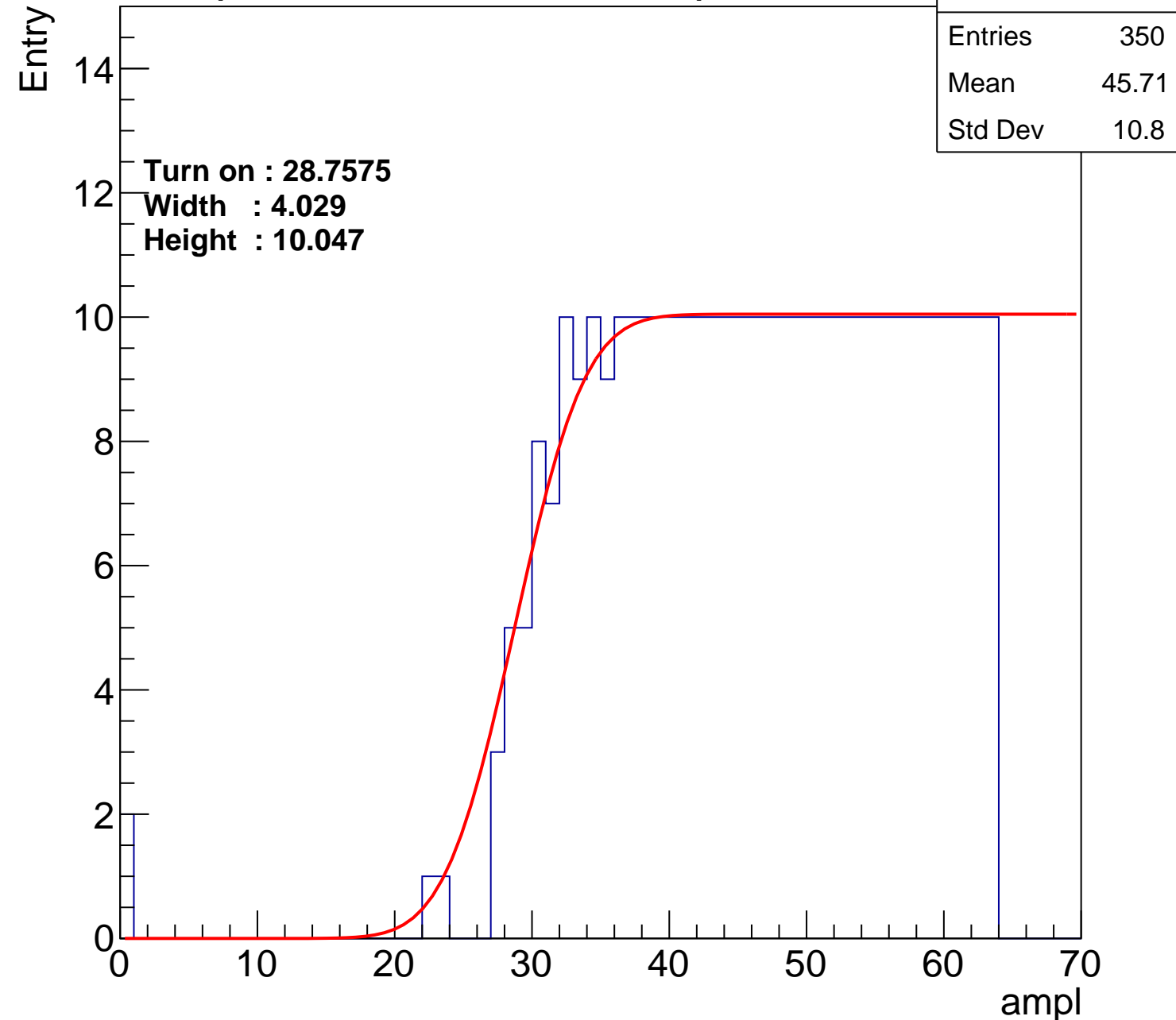
Width : 4.029

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.39
Std Dev	11.47

Turn on : 26.8028

Width : 3.567

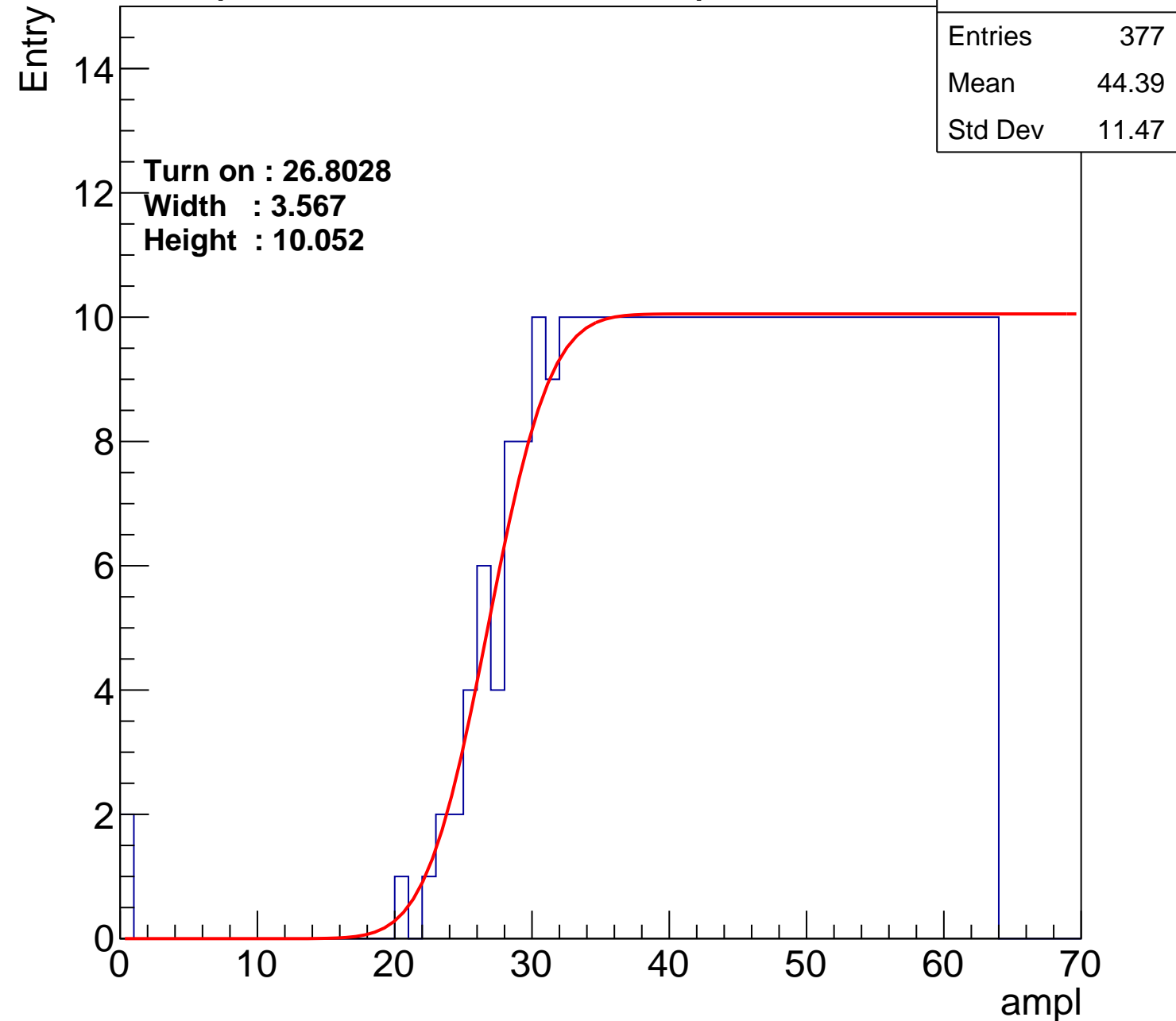
Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U10-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.1
Std Dev	11.87

Turn on : 26.9842

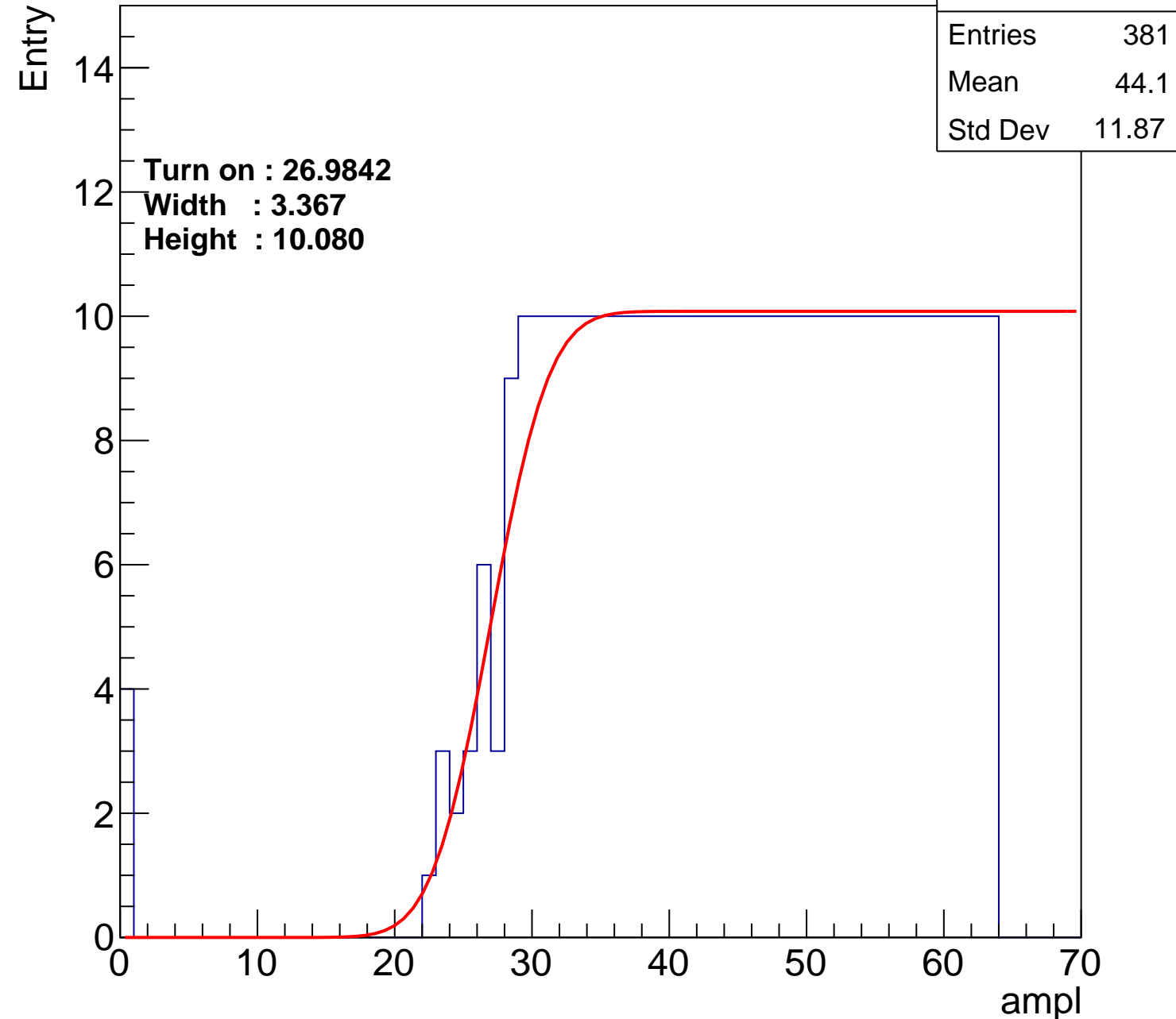
Width : 3.367

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch90

calib_packv5_042523_0143.root, FC#5, port B1

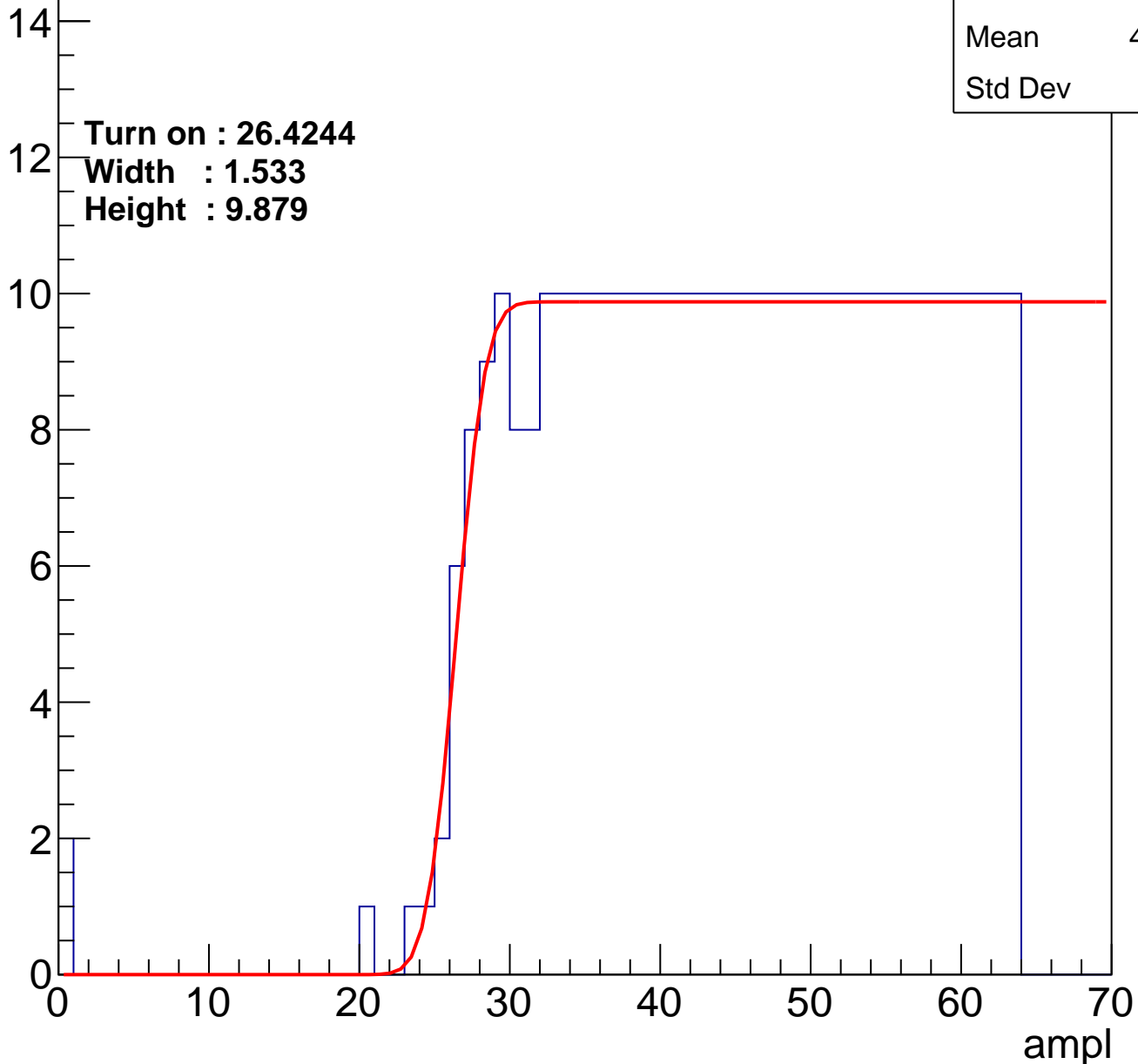
Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.4244

Width : 1.533

Height : 9.879

Entry



B0L000S, U10-ch91

calib_packv5_042523_0143.root, FC#5, port B1

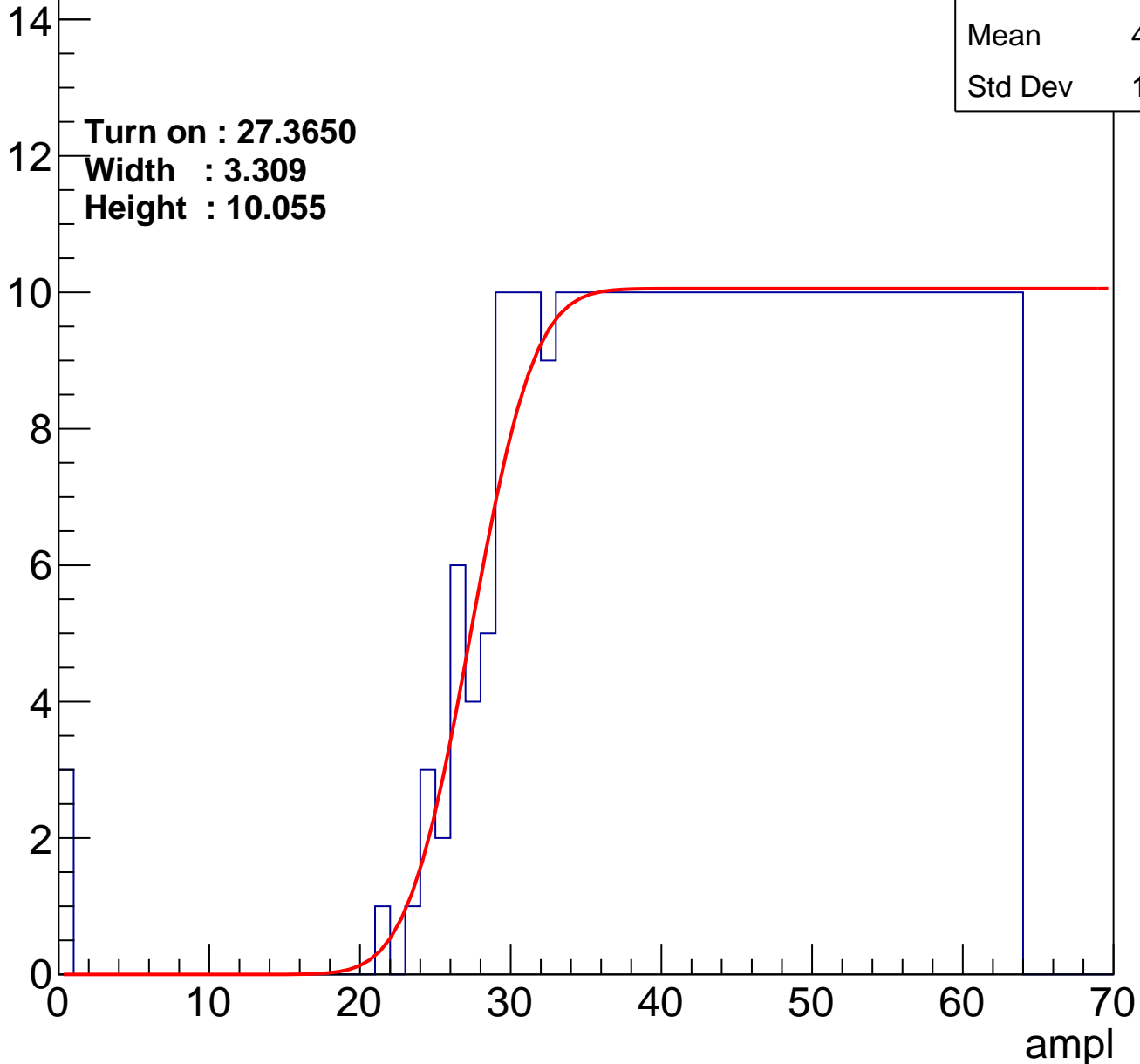
Entries	374
Mean	44.49
Std Dev	11.56

Turn on : 27.3650

Width : 3.309

Height : 10.055

Entry



B0L000S, U10-ch92

calib_packv5_042523_0143.root, FC#5, port B1

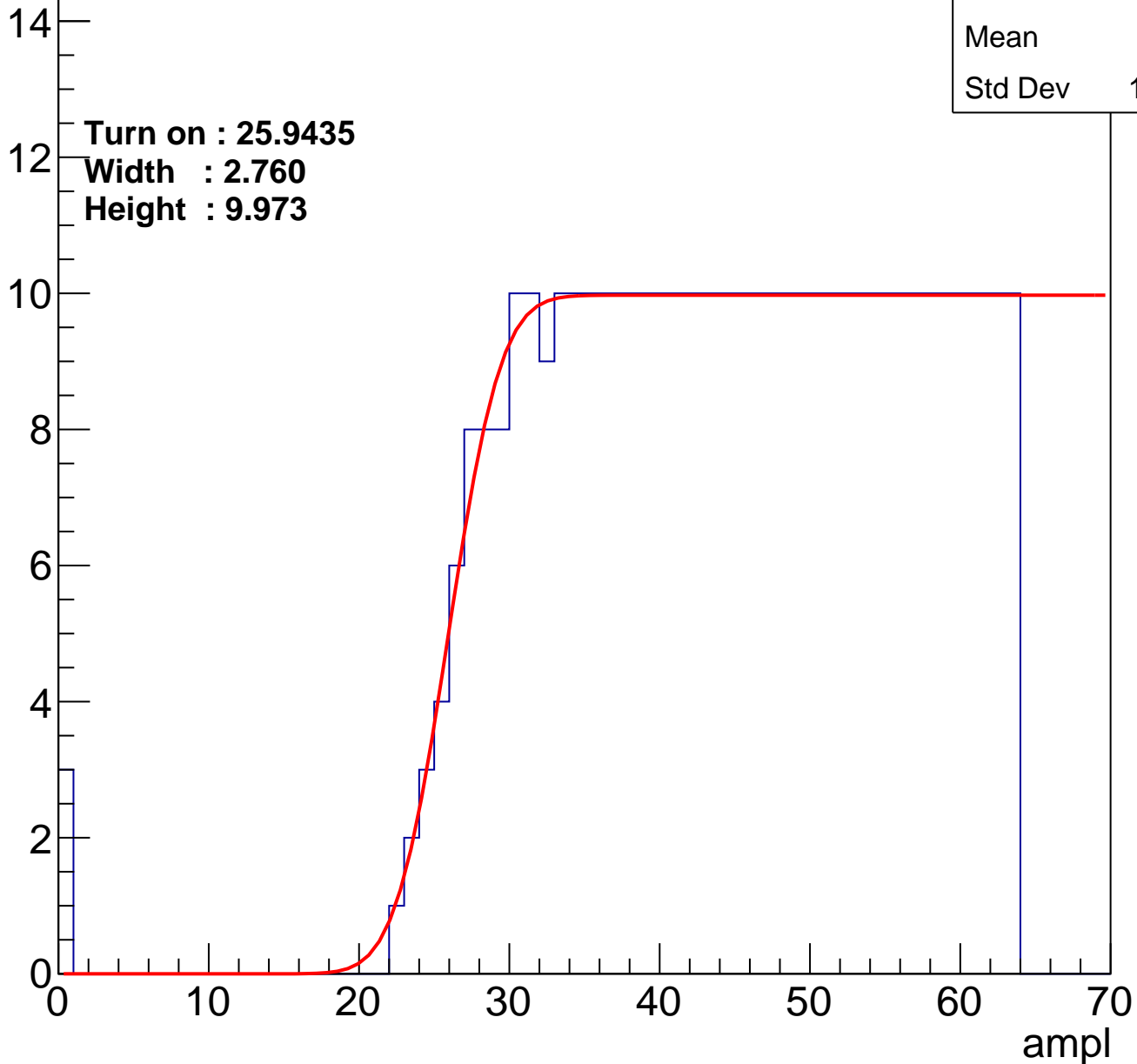
Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 25.9435

Width : 2.760

Height : 9.973

Entry



B0L000S, U10-ch93

calib_packv5_042523_0143.root, FC#5, port B1

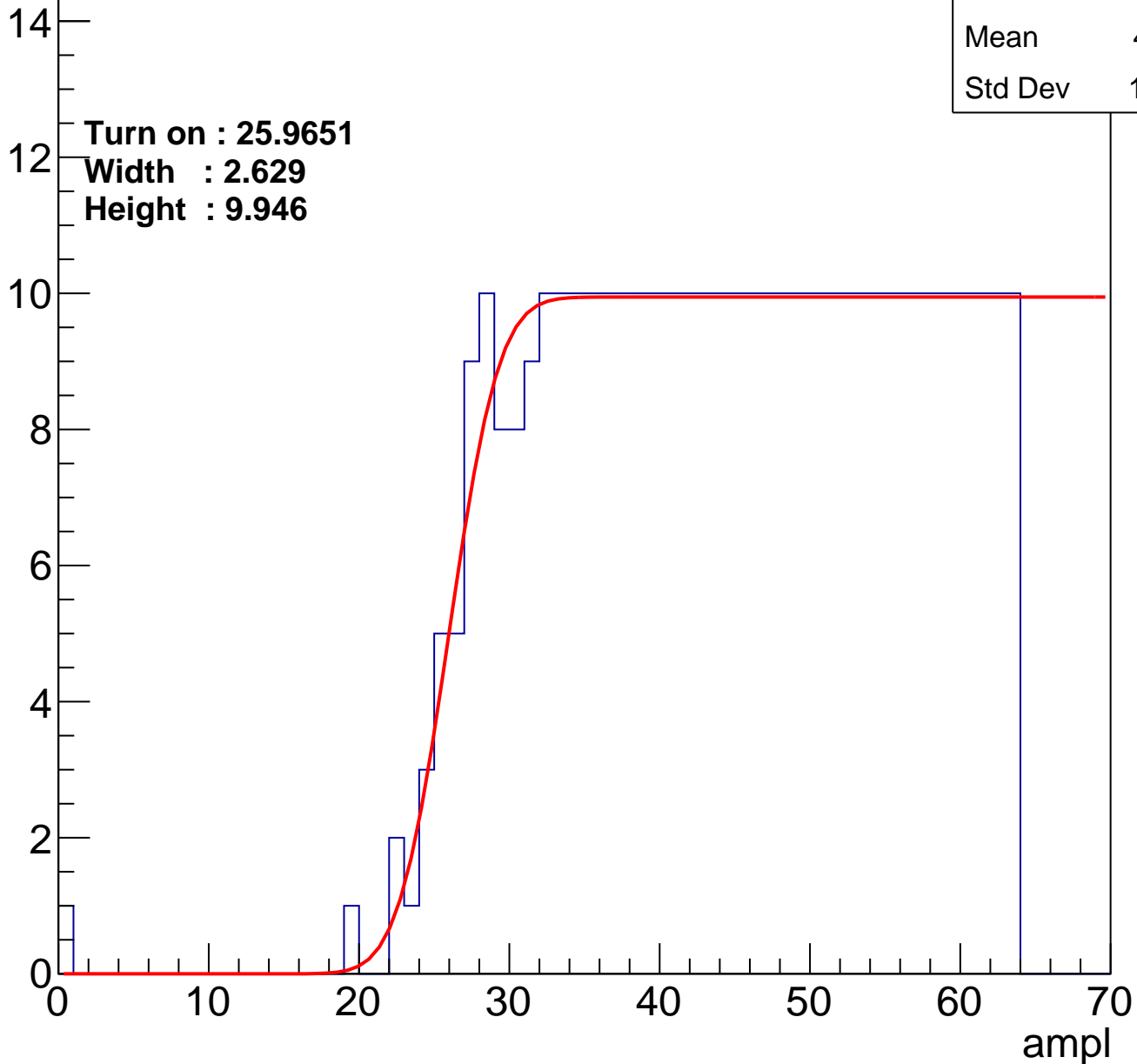
Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.9651

Width : 2.629

Height : 9.946

Entry



B0L000S, U10-ch94

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.8612

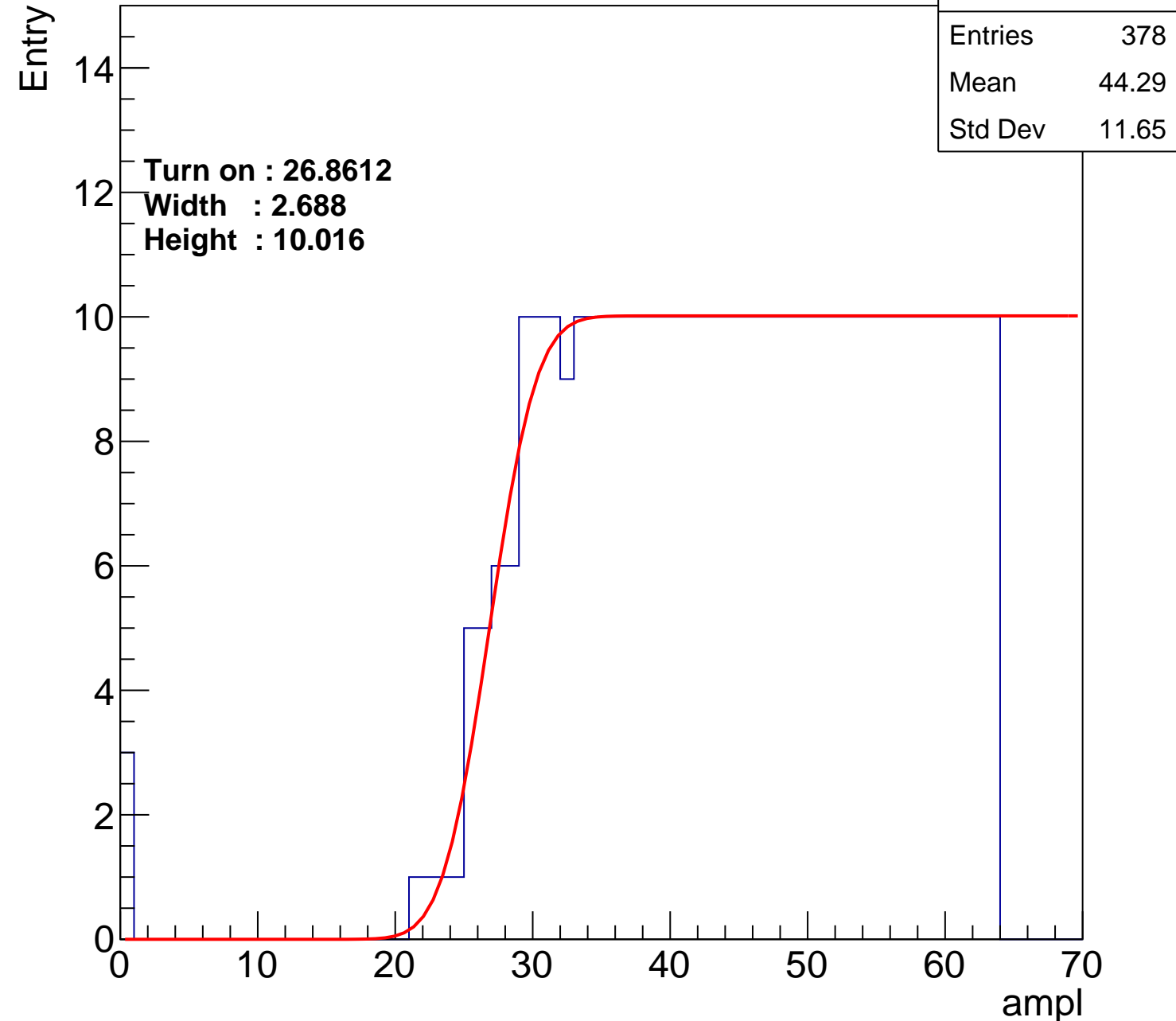
Width : 2.688

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.61
Std Dev	10.86

Turn on : 29.9294

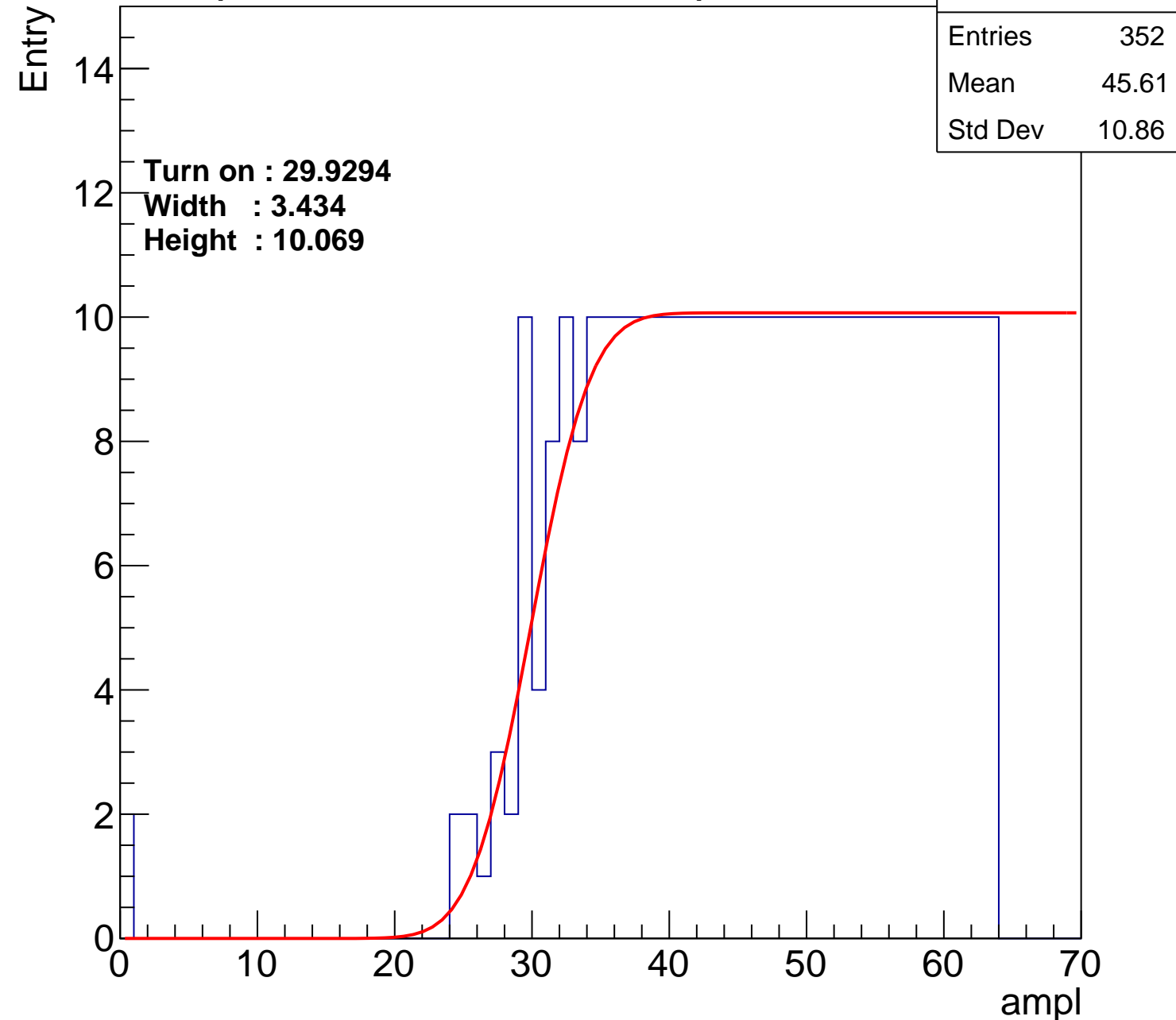
Width : 3.434

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.96
Std Dev	11

Turn on : 27.5448

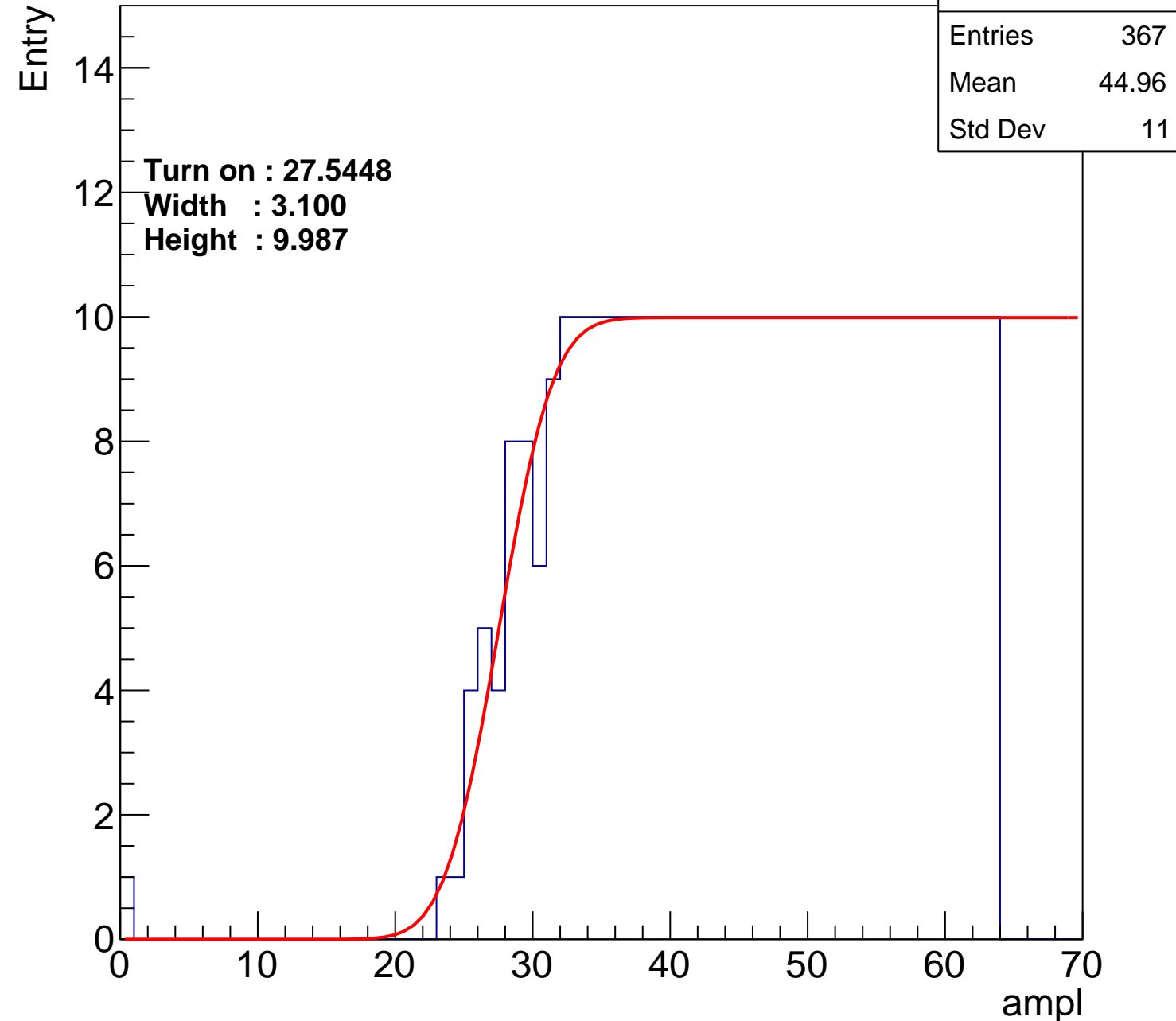
Width : 3.100

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.29
Std Dev	10.99

Turn on : 28.1043

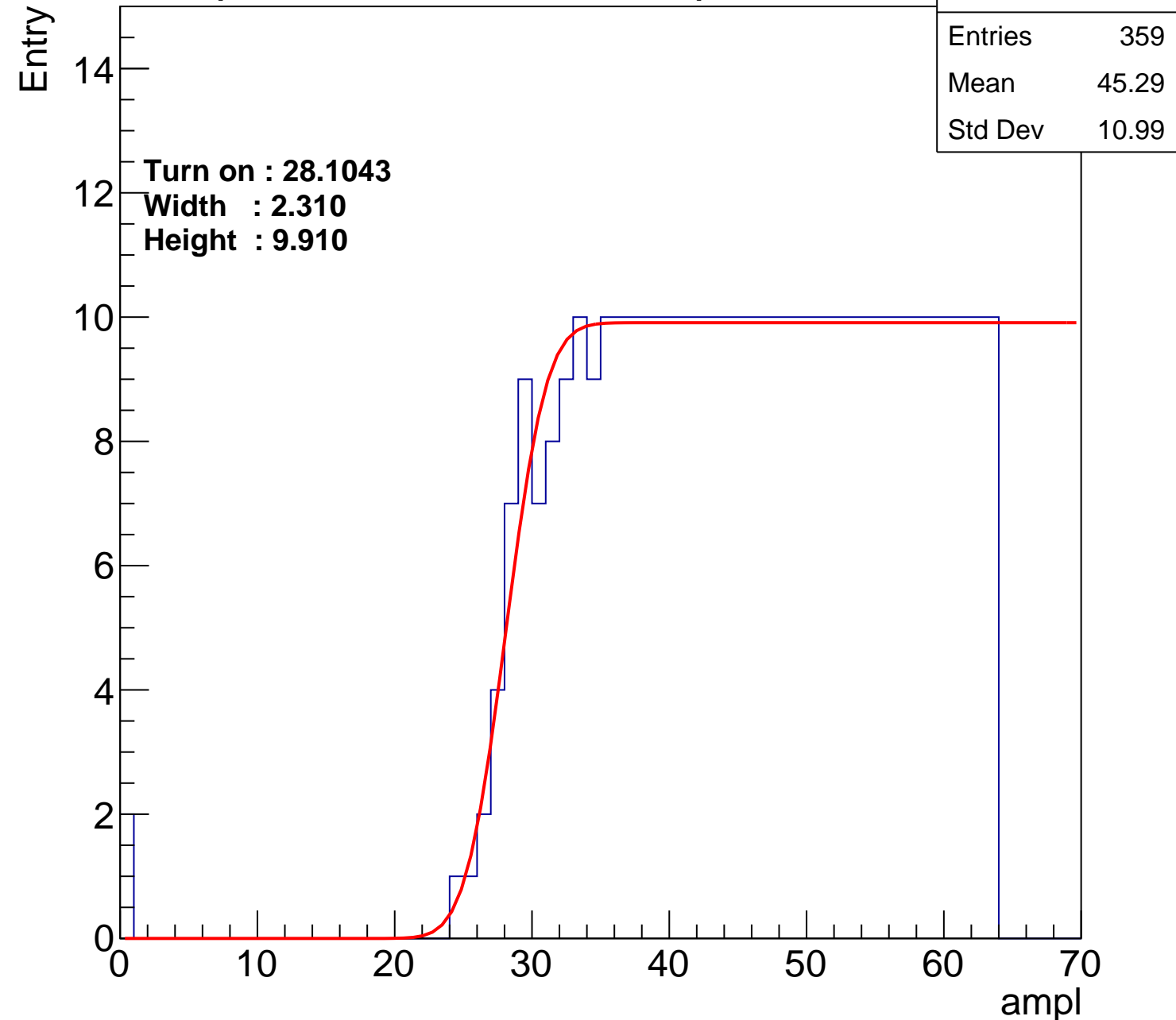
Width : 2.310

Height : 9.910

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.18
Std Dev	10.81

Turn on : 27.7727

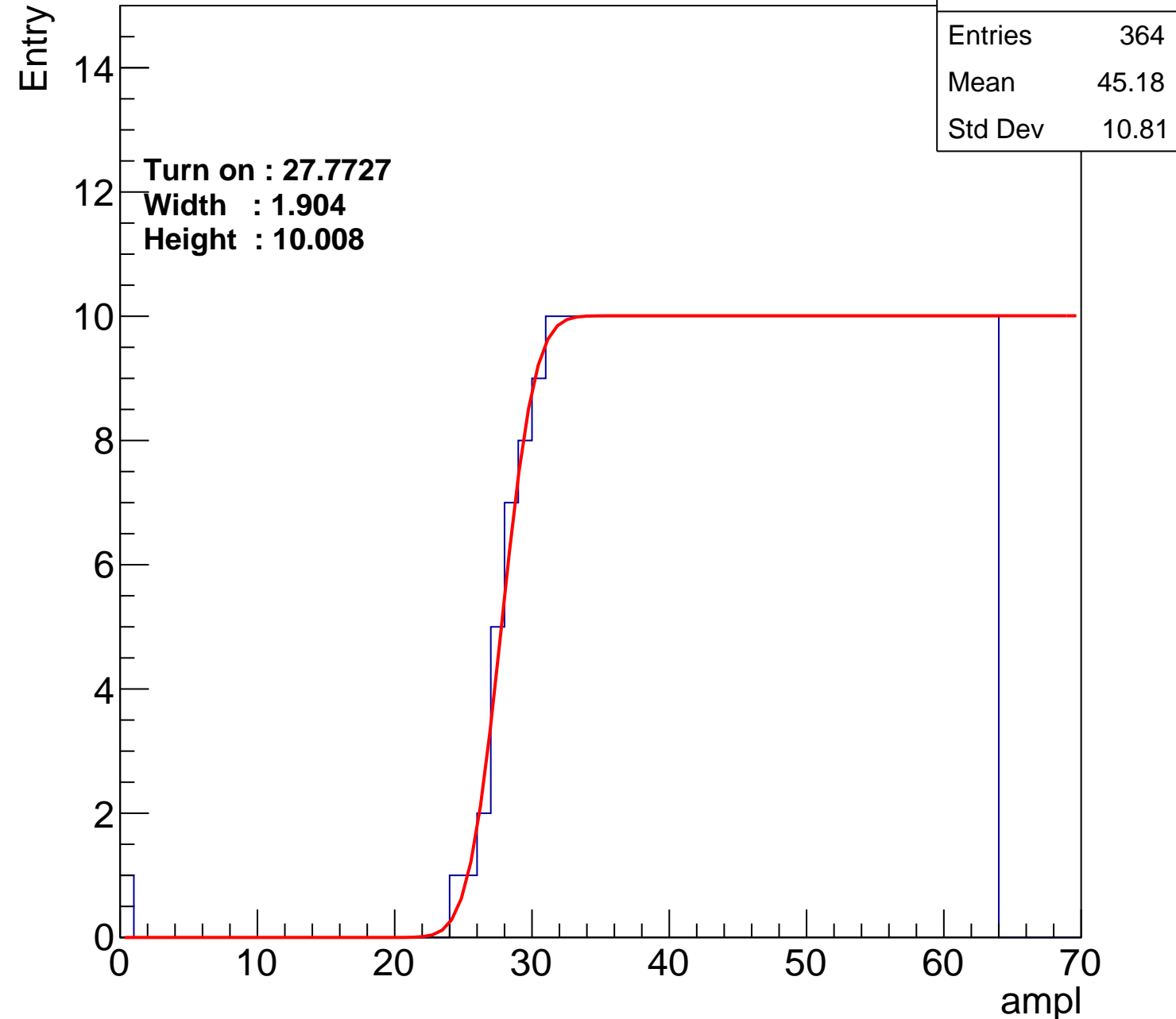
Width : 1.904

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.31
Std Dev	11.31

Turn on : 26.1094

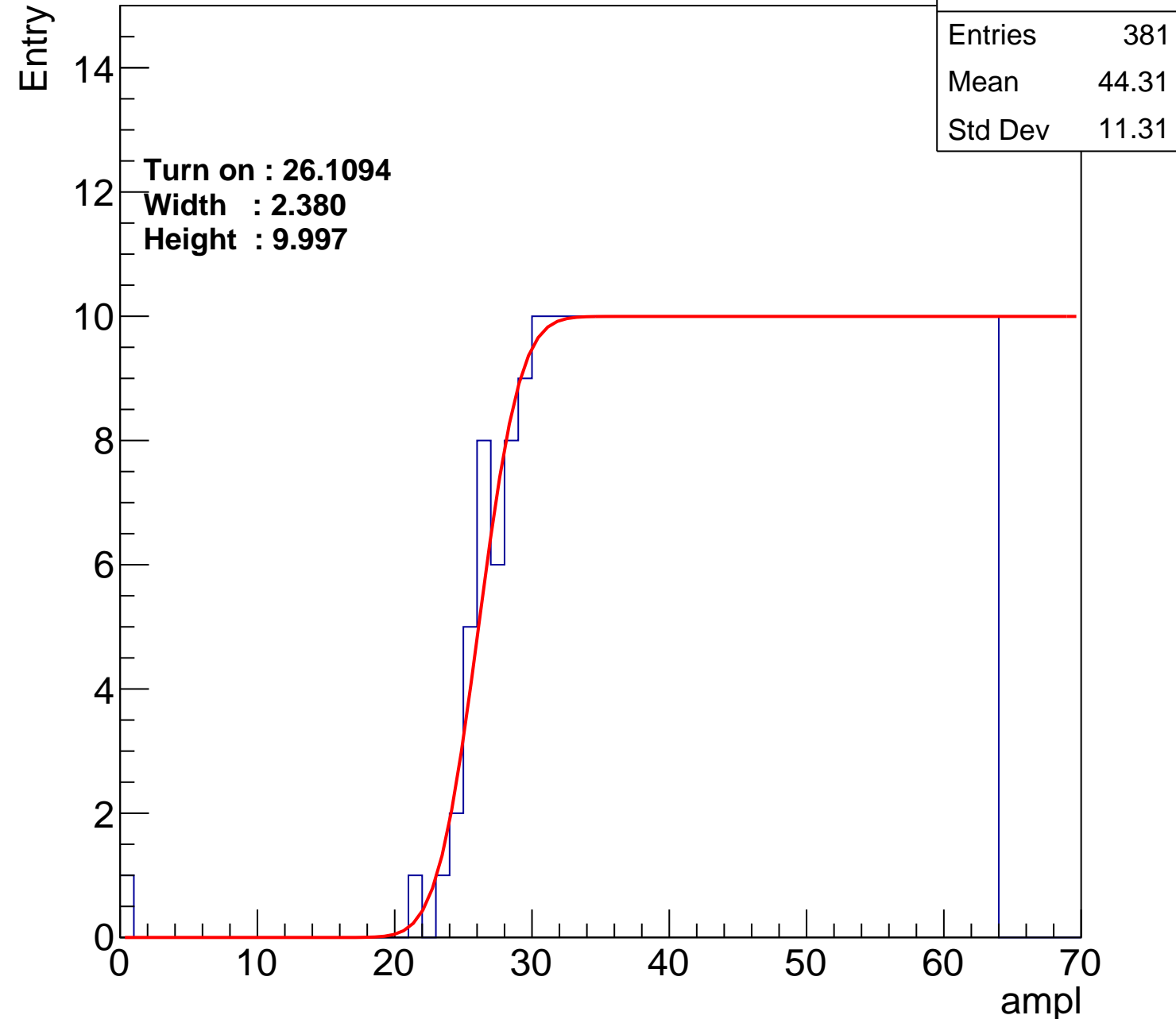
Width : 2.380

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch100

calib_packv5_042523_0143.root, FC#5, port B1

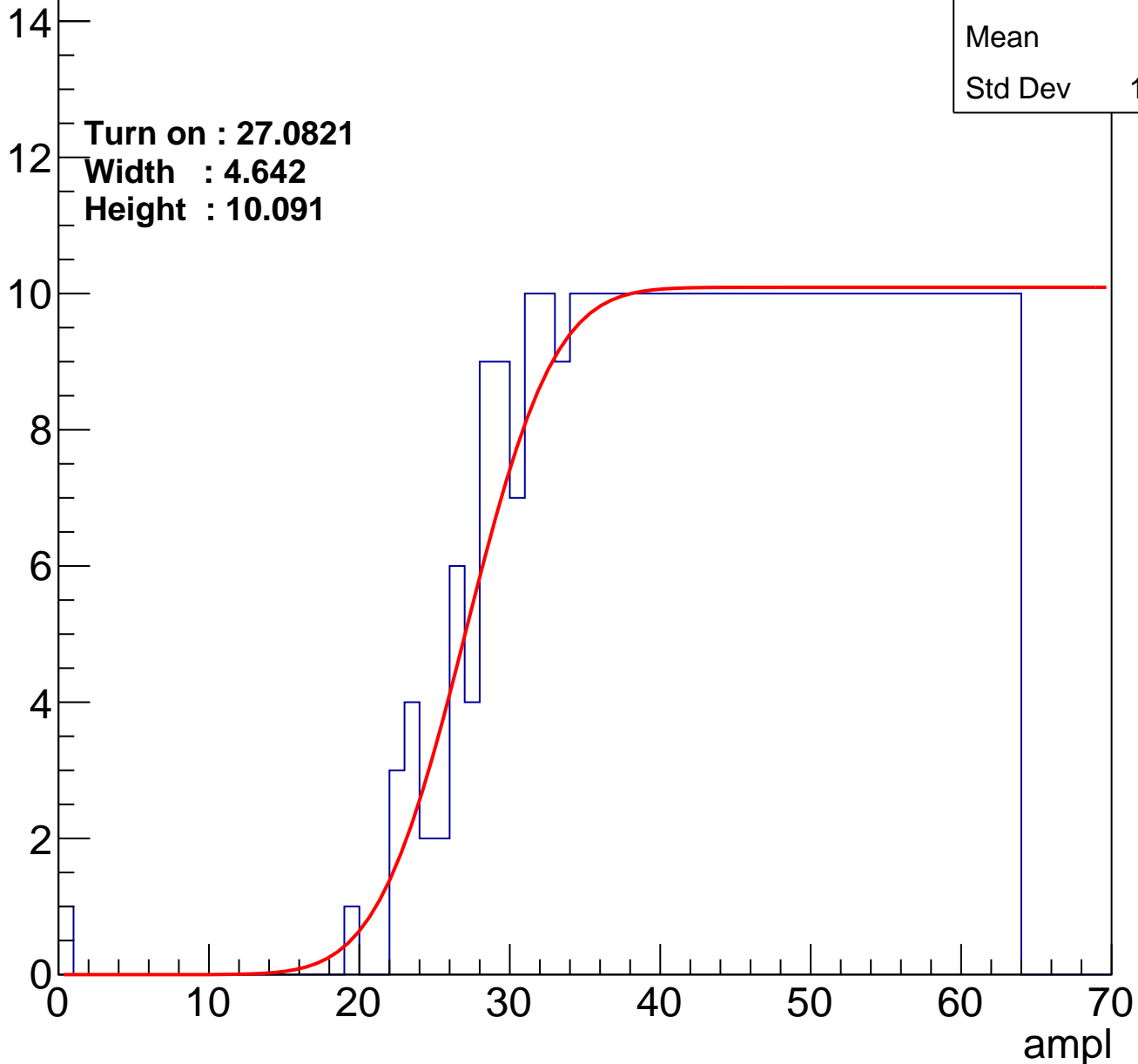
Entries	377
Mean	44.4
Std Dev	11.38

Turn on : 27.0821

Width : 4.642

Height : 10.091

Entry



B0L000S, U10-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.8
Std Dev	11.3

Turn on : 27.4965

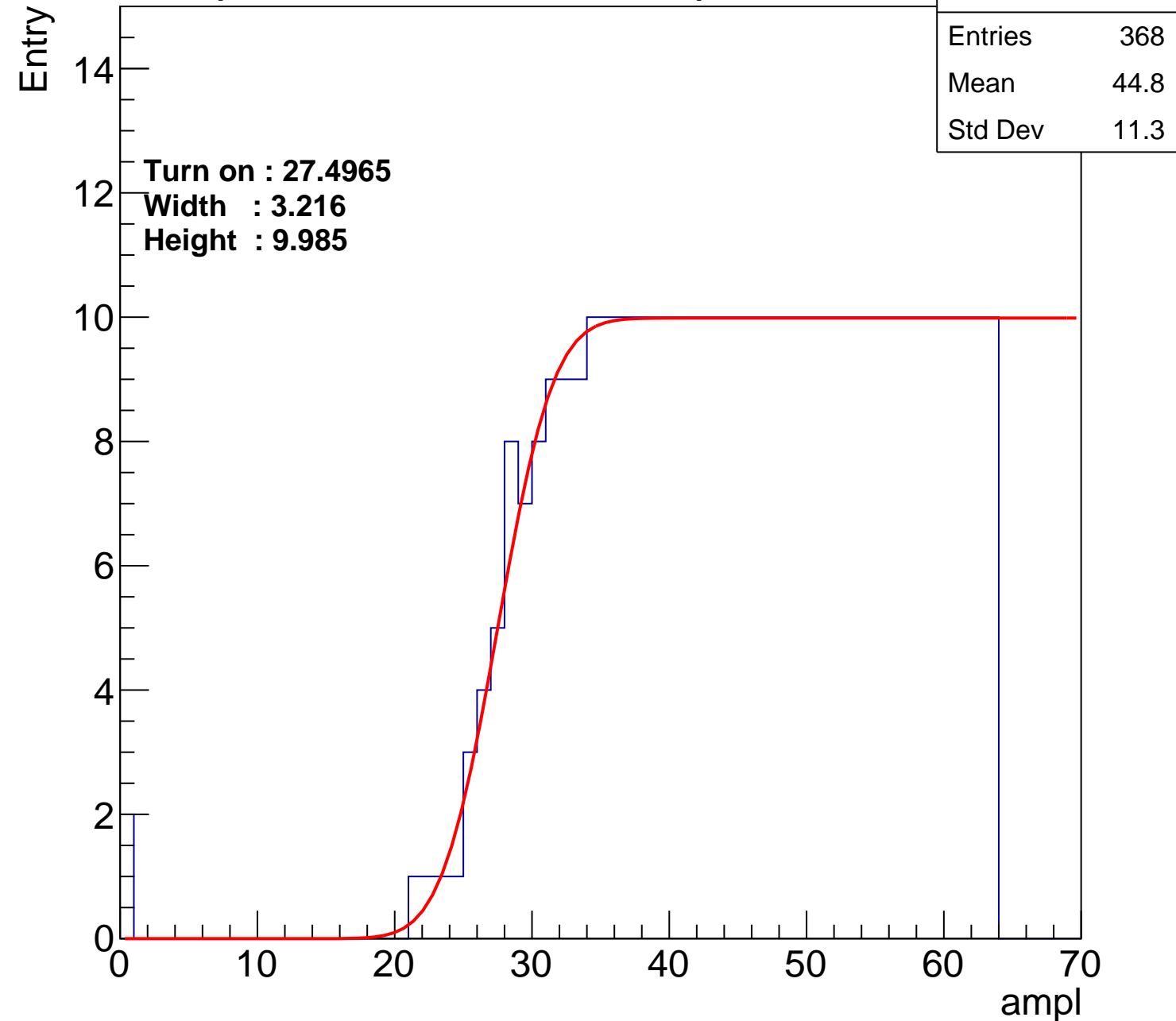
Width : 3.216

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.0849

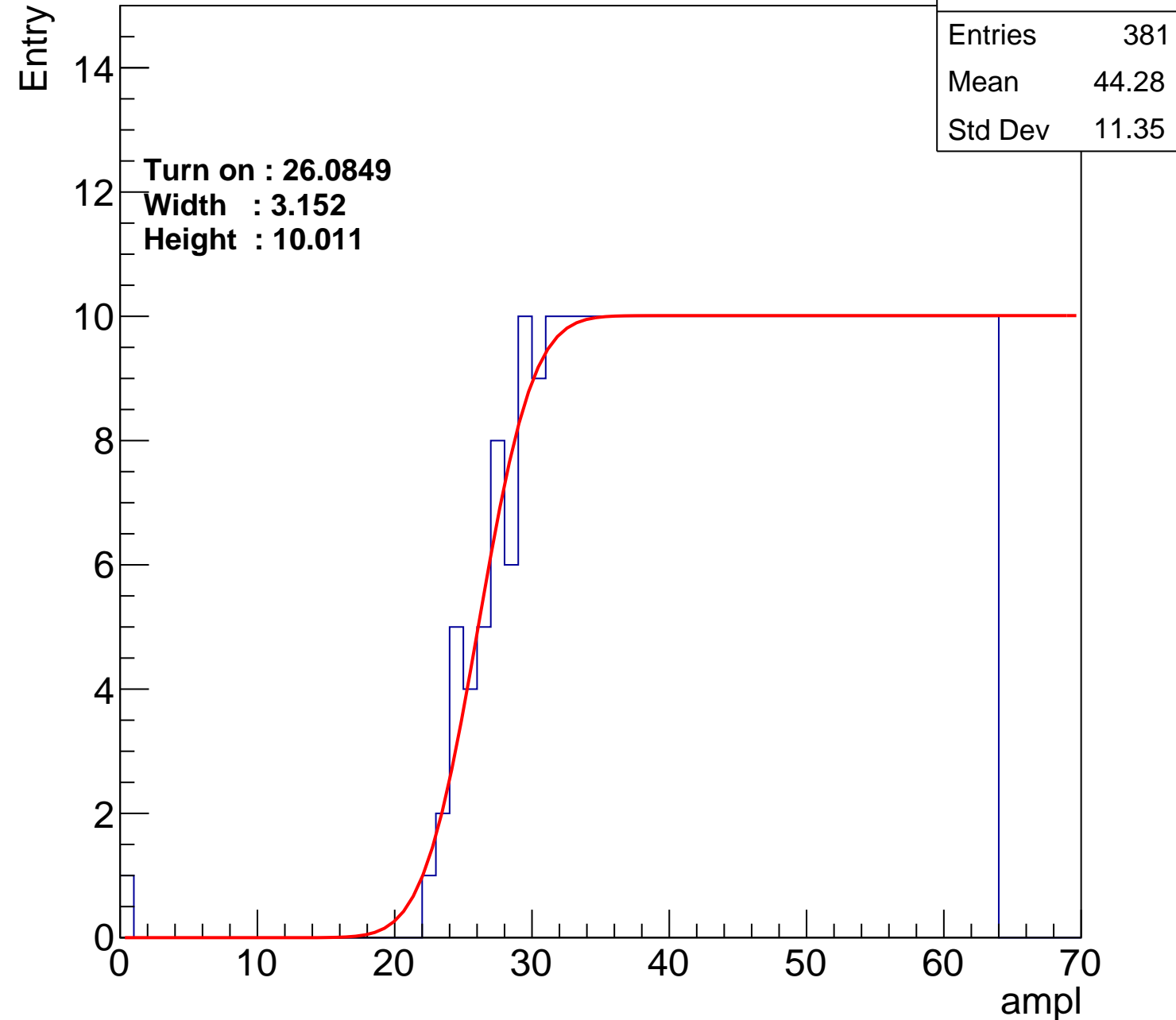
Width : 3.152

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch103

calib_packv5_042523_0143.root, FC#5, port B1

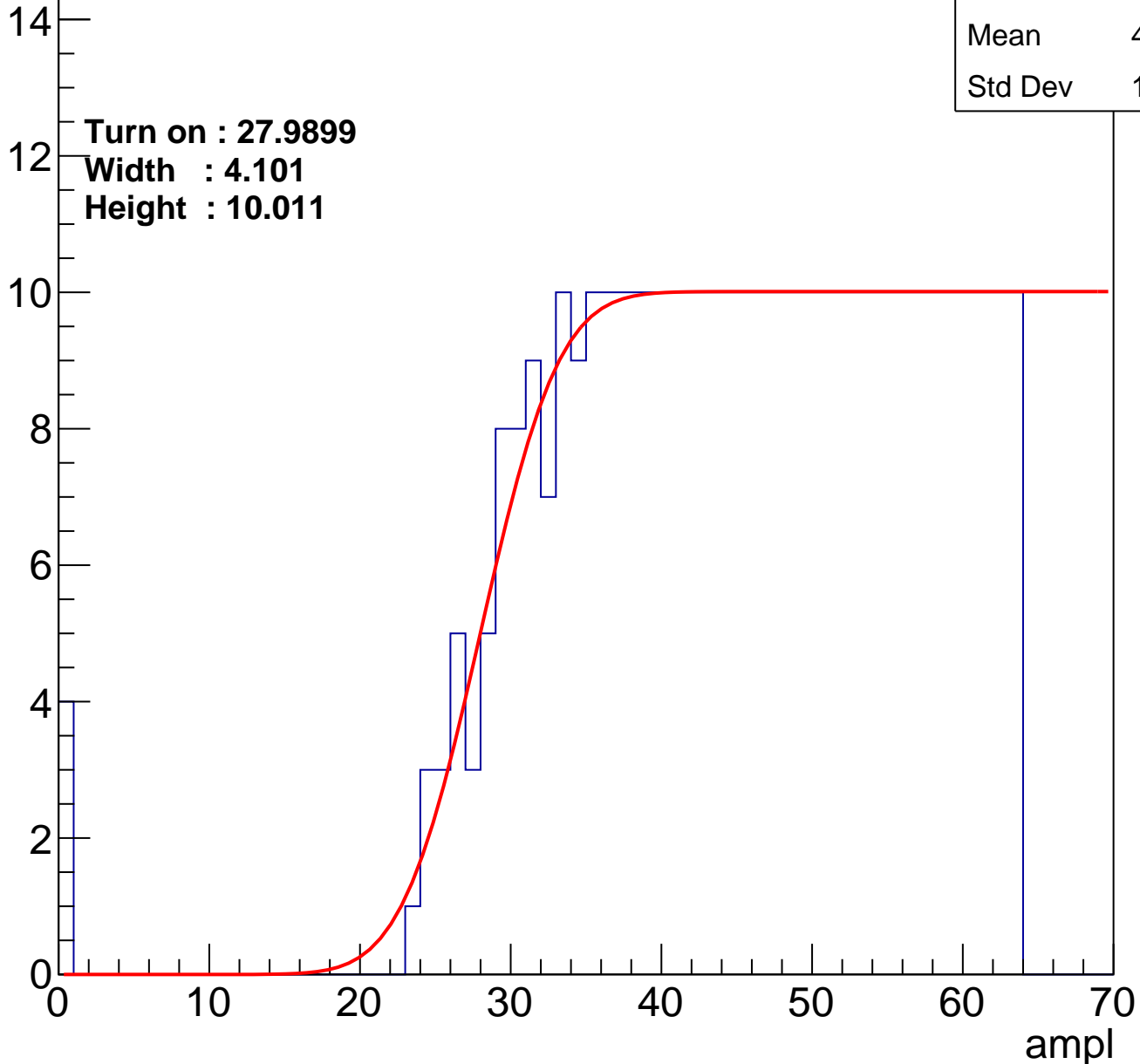
Entries	365
Mean	44.77
Std Dev	11.65

Turn on : 27.9899

Width : 4.101

Height : 10.011

Entry



B0L000S, U10-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.93
Std Dev	11.4

Turn on : 28.4658

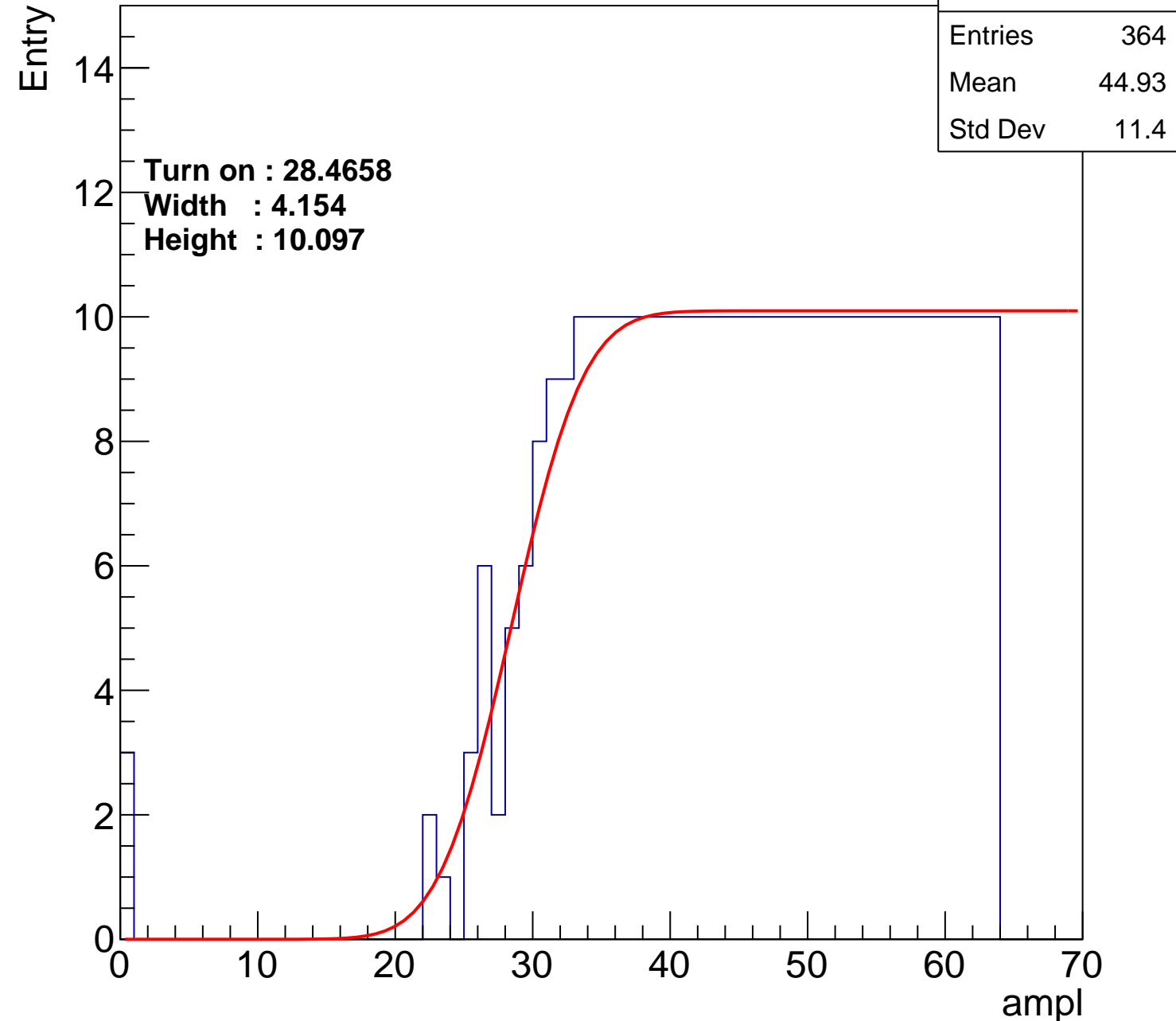
Width : 4.154

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.53
Std Dev	10.93

Turn on : 28.9909

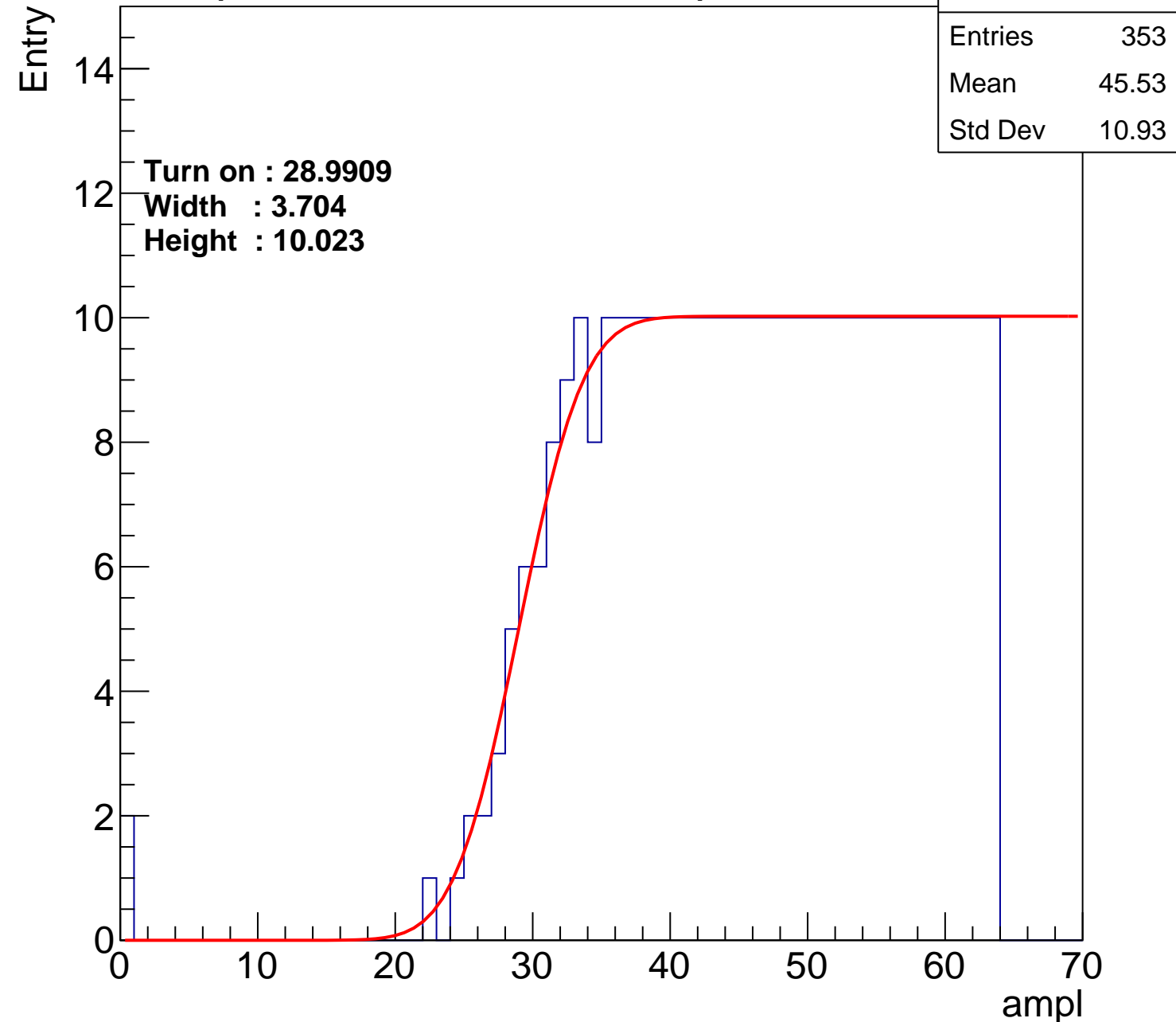
Width : 3.704

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.51
Std Dev	11.7

Turn on : 27.5434

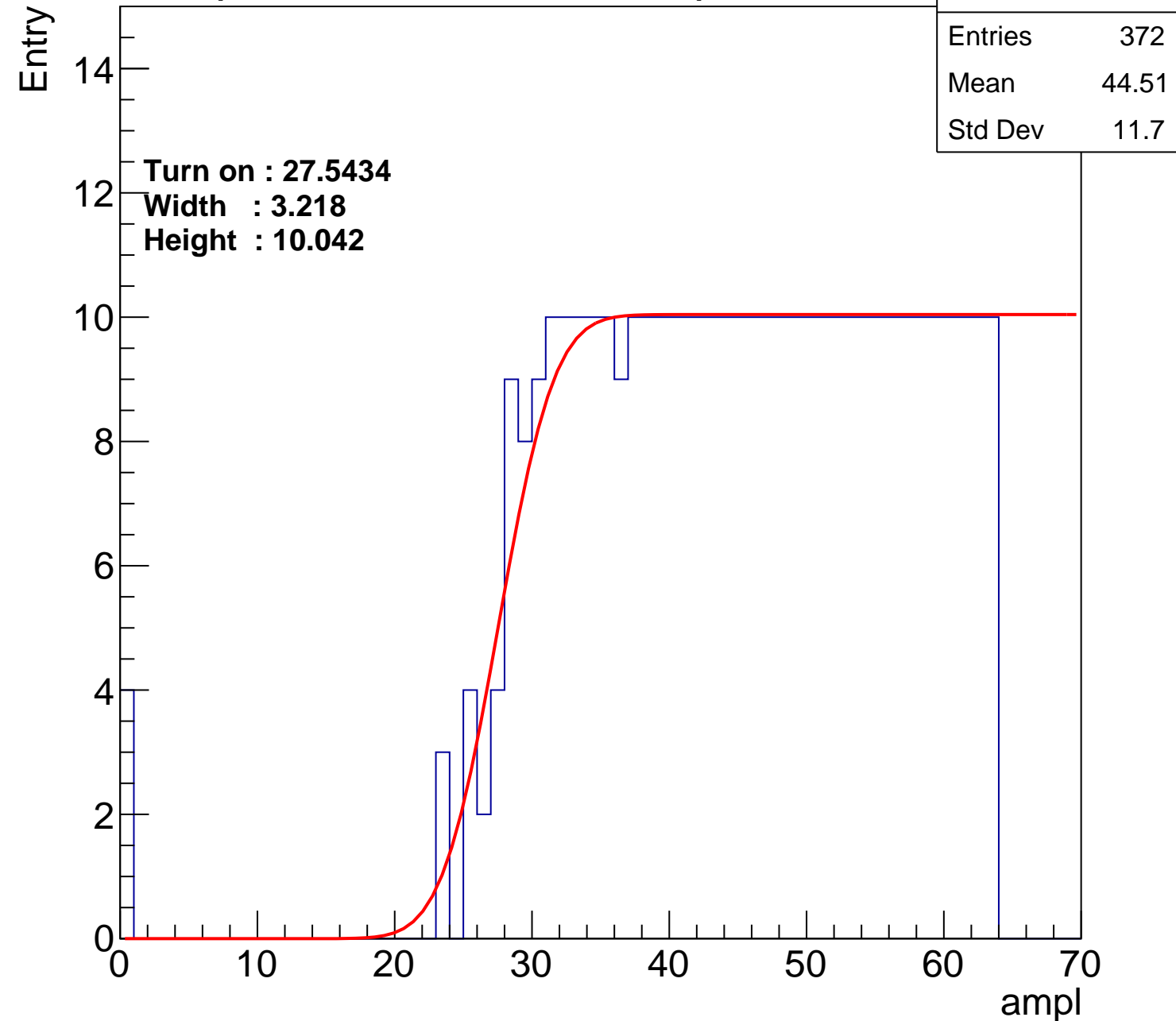
Width : 3.218

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 27.8227

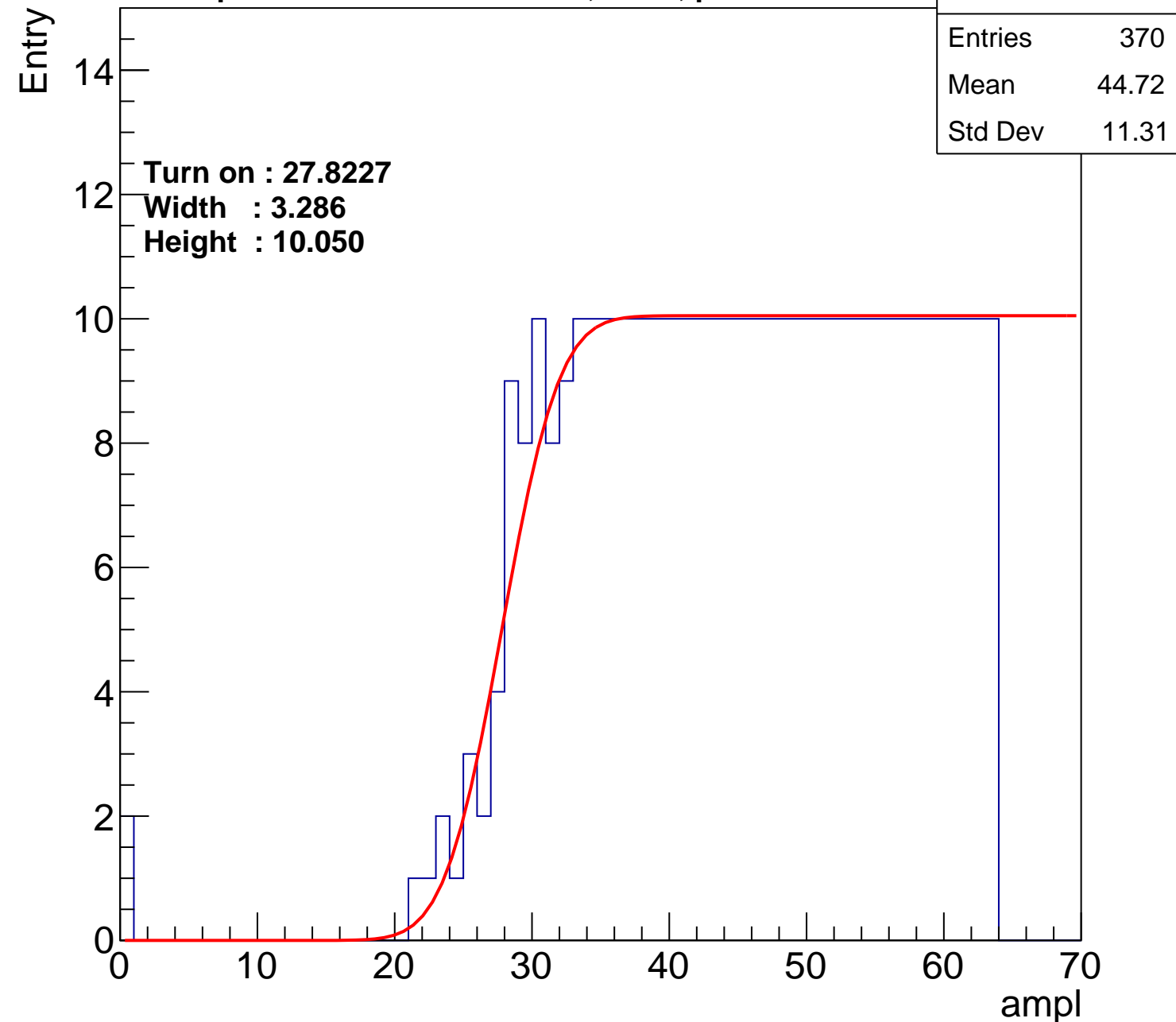
Width : 3.286

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.88
Std Dev	11.82

Turn on : 25.2991

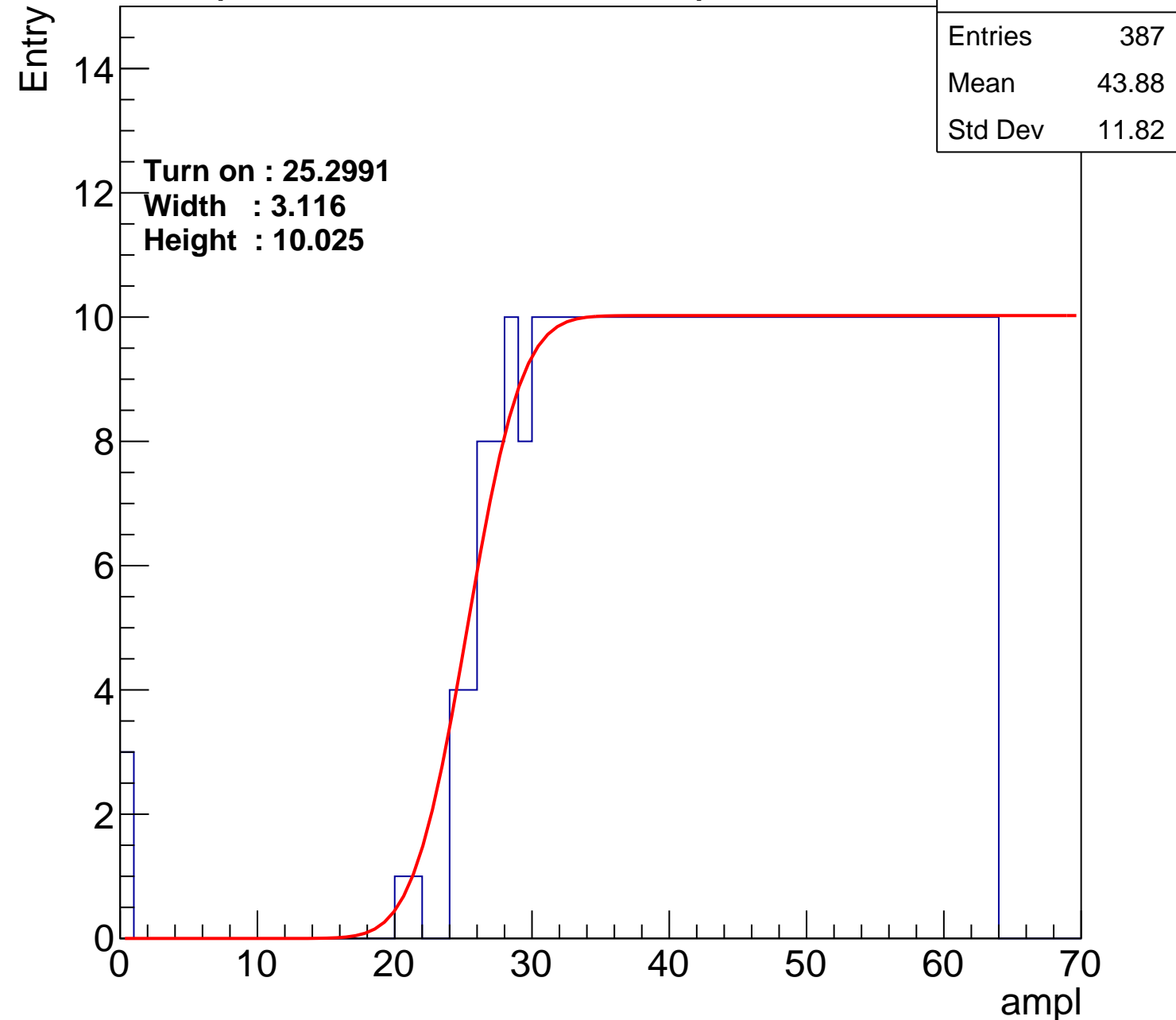
Width : 3.116

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.15
Std Dev	10.86

Turn on : 28.2318

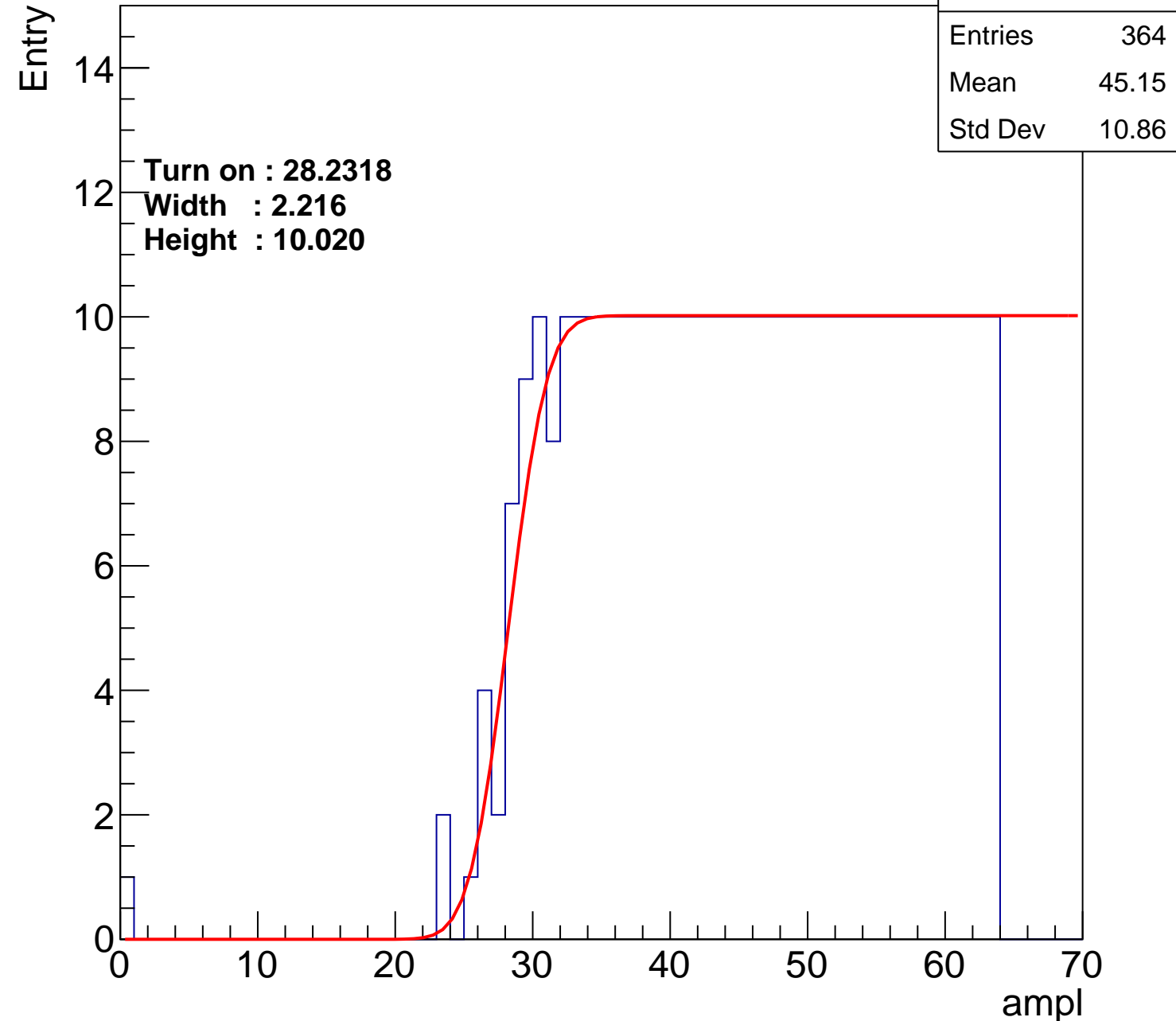
Width : 2.216

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch110

calib_packv5_042523_0143.root, FC#5, port B1

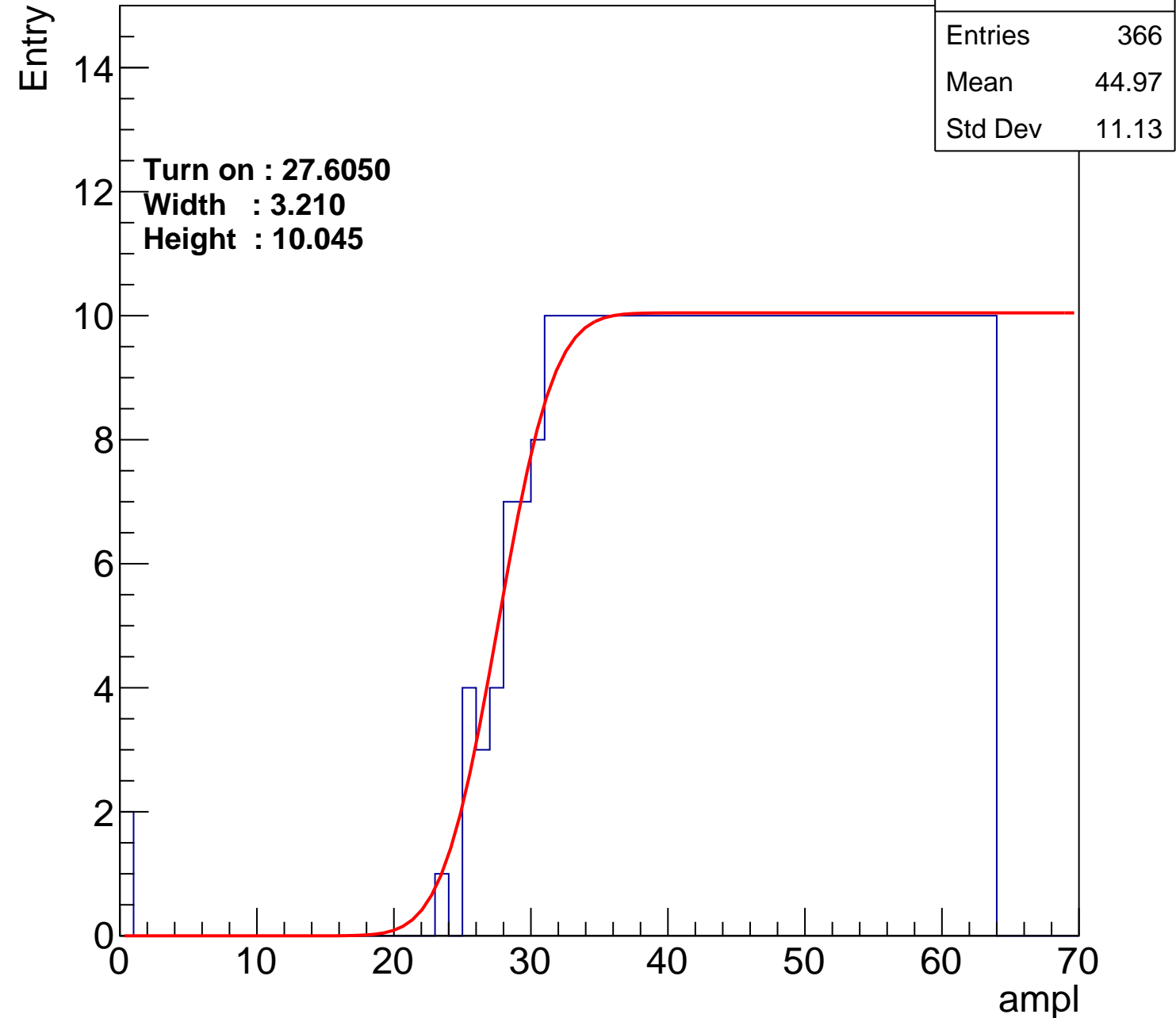
Entries	366
Mean	44.97
Std Dev	11.13

Entry

14
12
10
8
6
4
2
0

Turn on : 27.6050
Width : 3.210
Height : 10.045

ampl



B0L000S, U10-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.51
Std Dev	10.85

Turn on : 28.5120

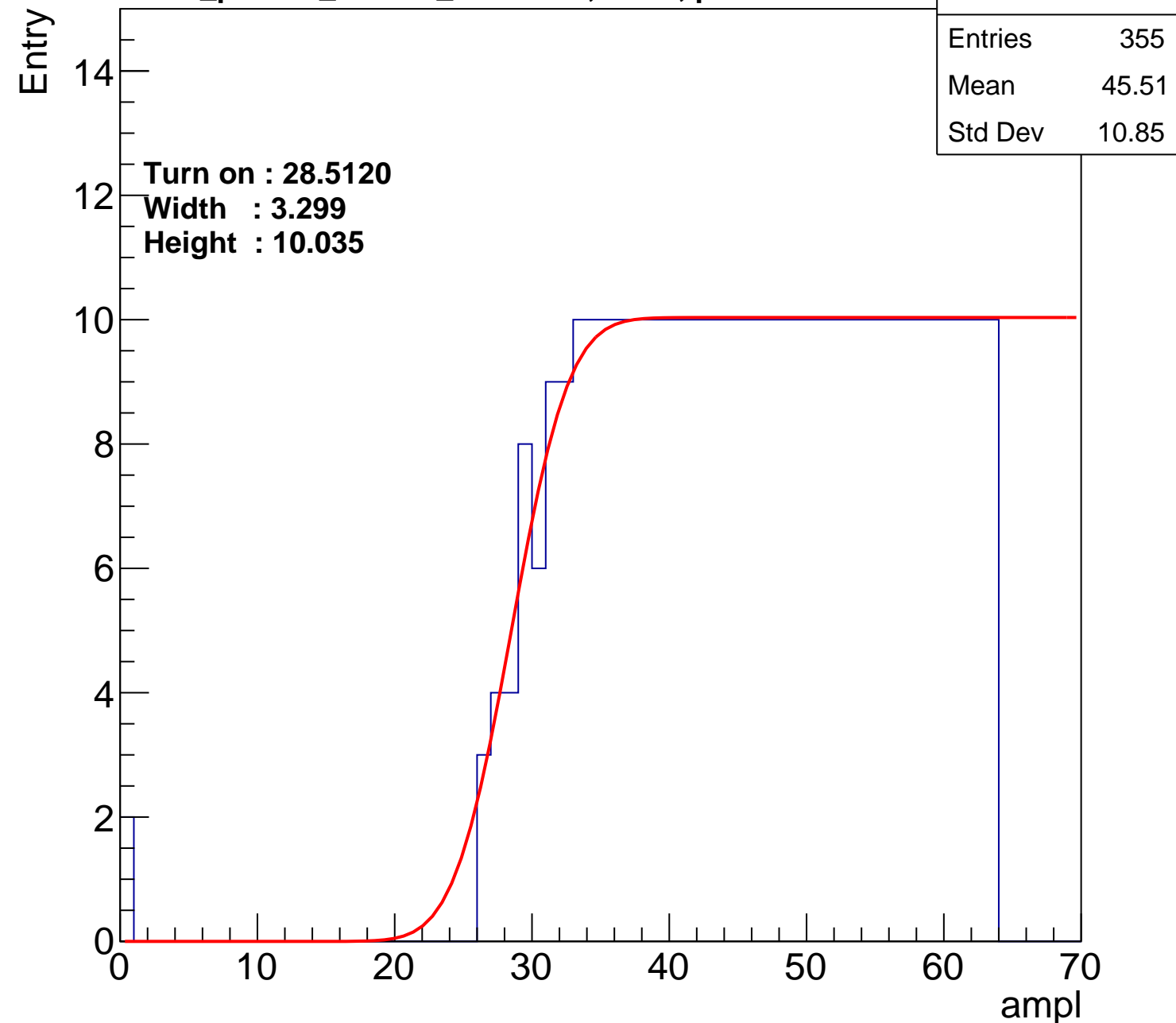
Width : 3.299

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.7588

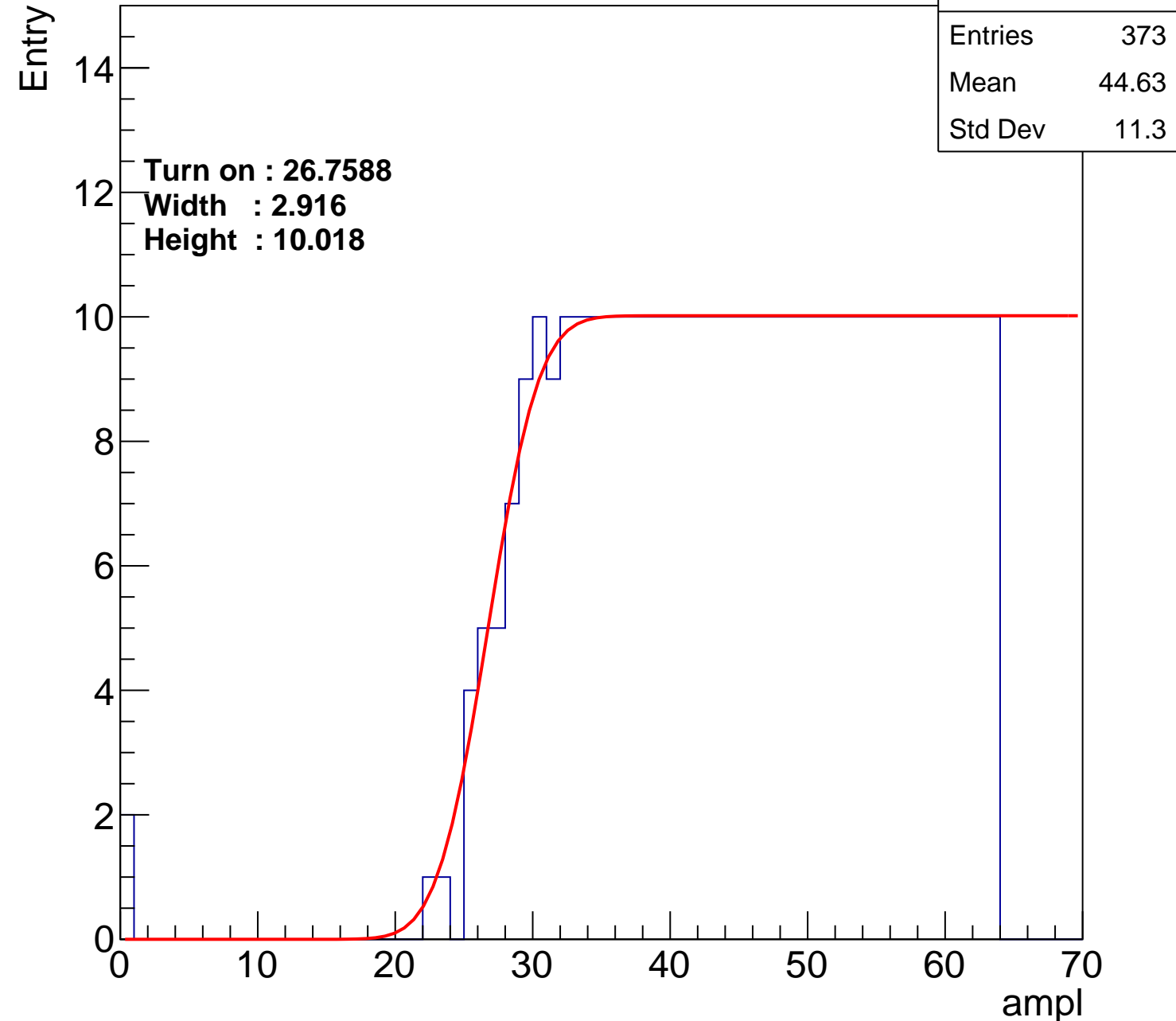
Width : 2.916

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.4833

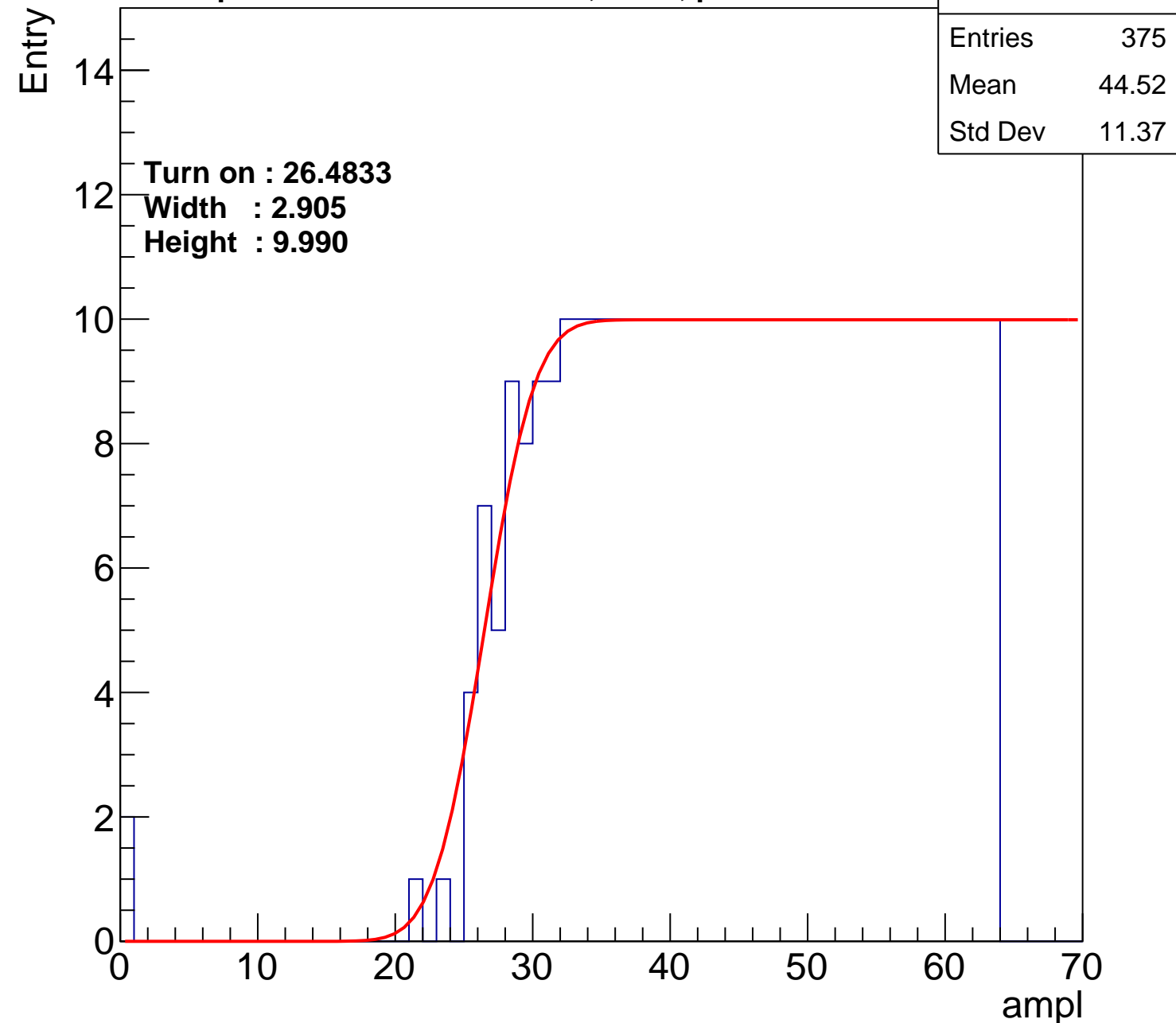
Width : 2.905

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 27.6506

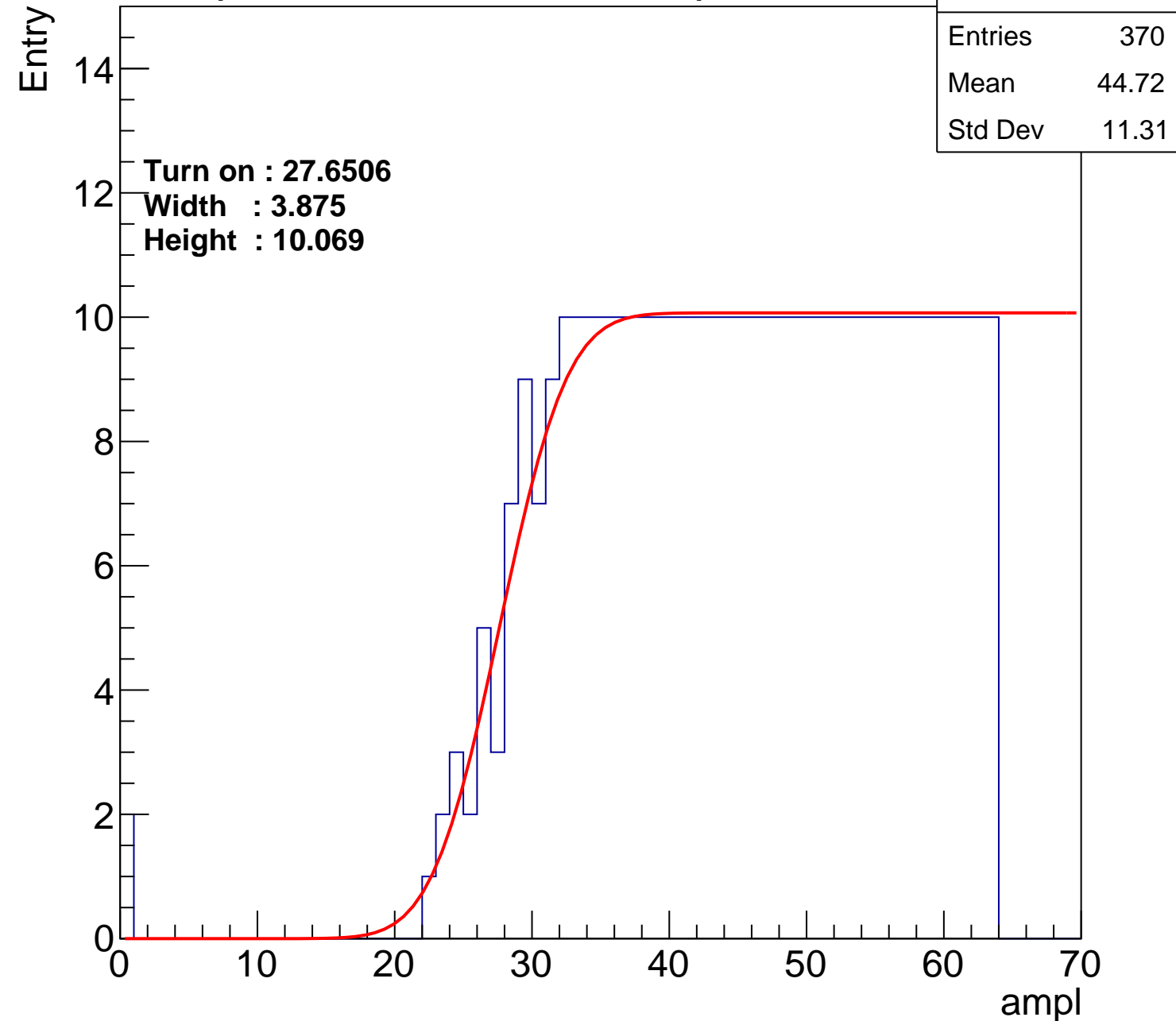
Width : 3.875

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.73
Std Dev	11.91

Turn on : 25.4809

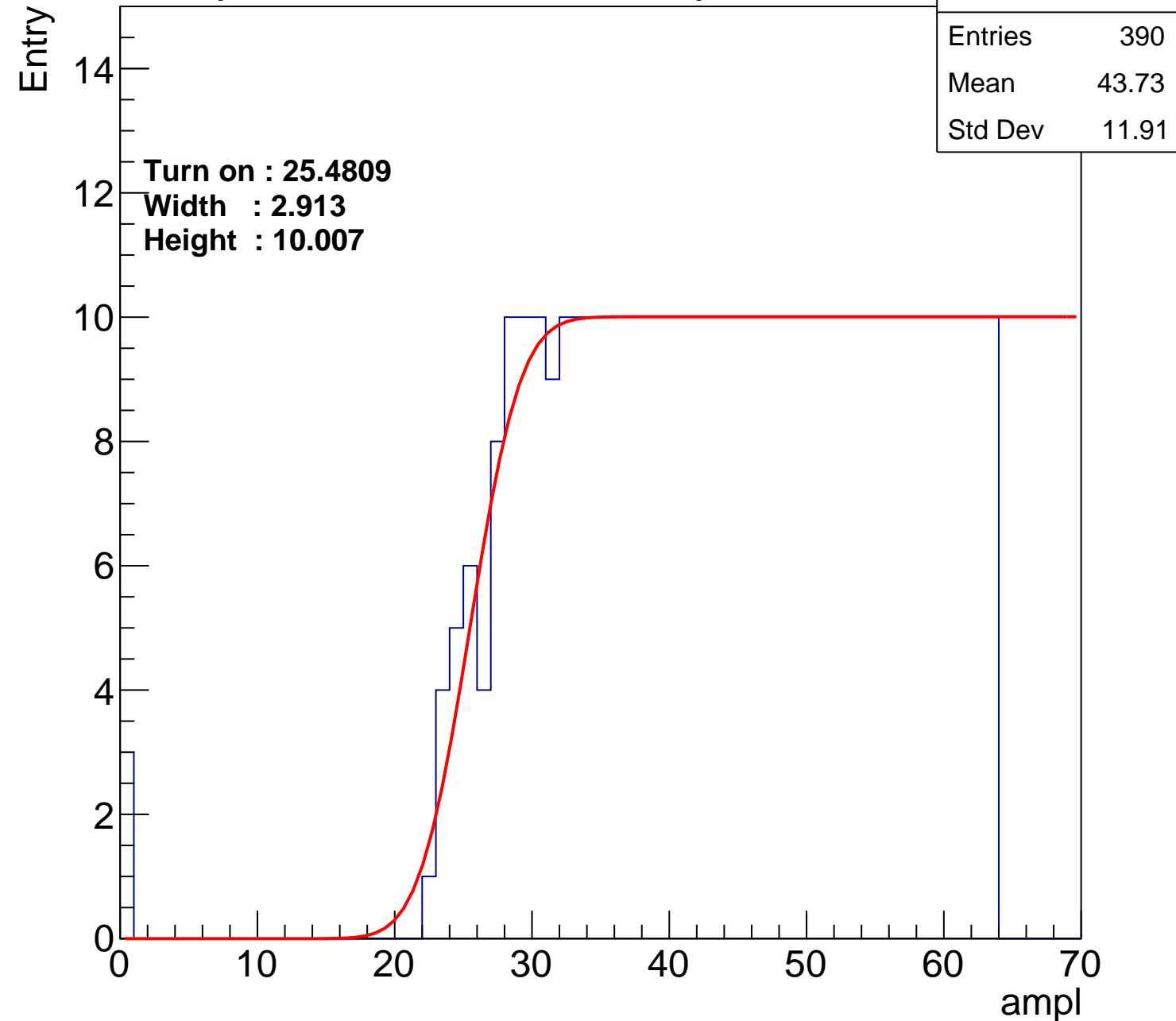
Width : 2.913

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	398
Mean	43.38
Std Dev	11.98

Turn on : 25.2639

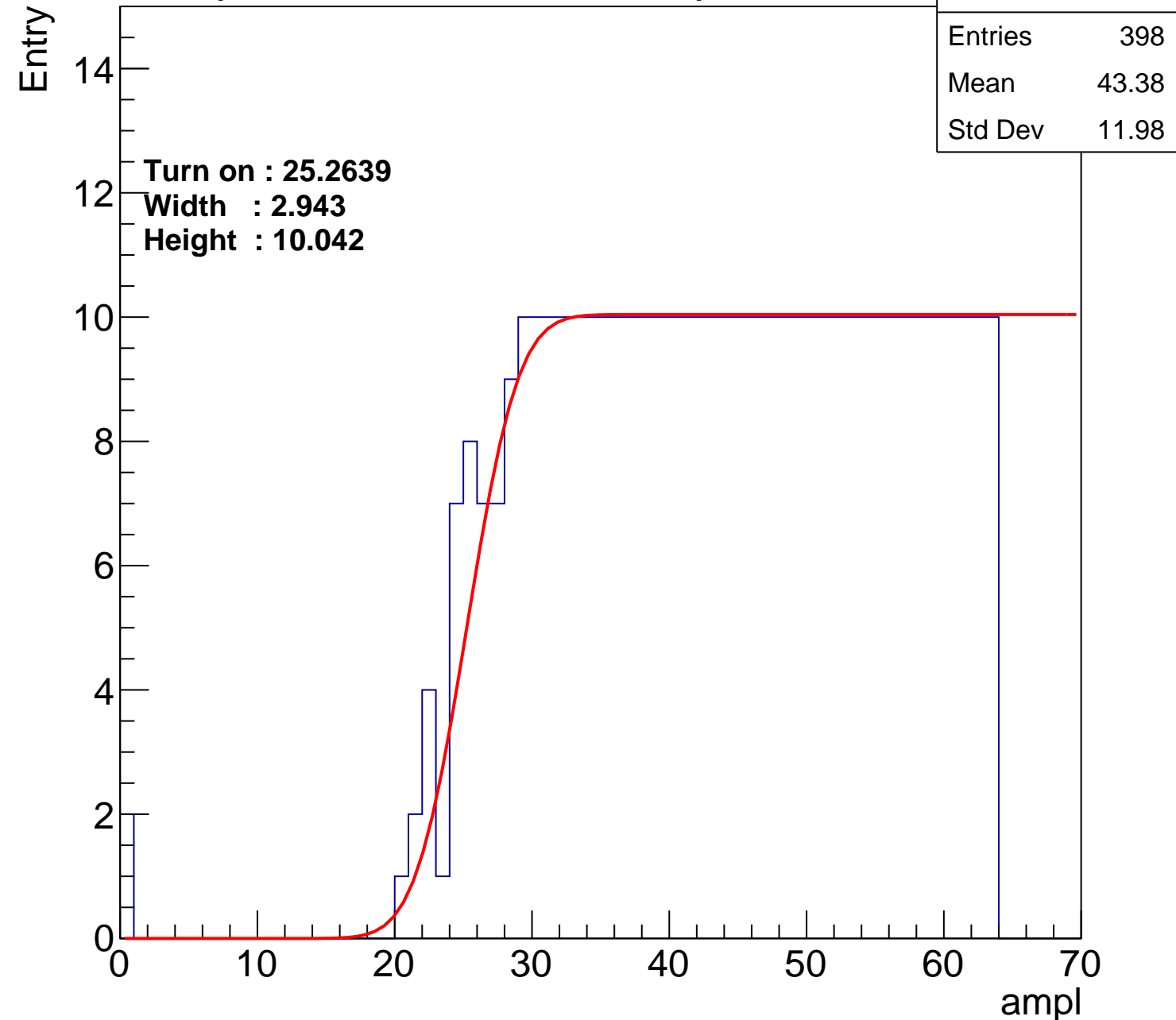
Width : 2.943

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch117

calib_packv5_042523_0143.root, FC#5, port B1

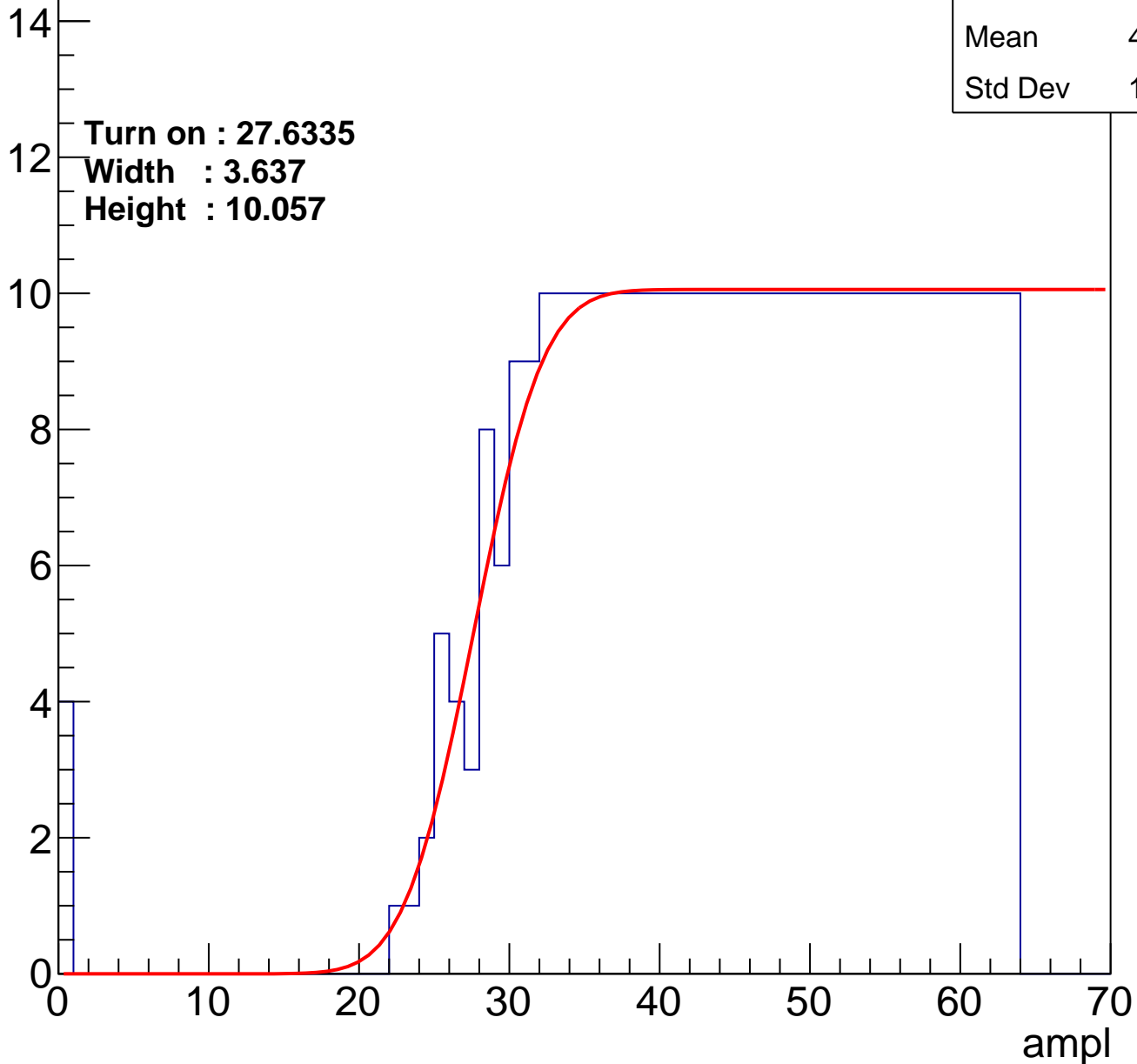
Entries	372
Mean	44.49
Std Dev	11.74

Turn on : 27.6335

Width : 3.637

Height : 10.057

Entry



B0L000S, U10-ch118

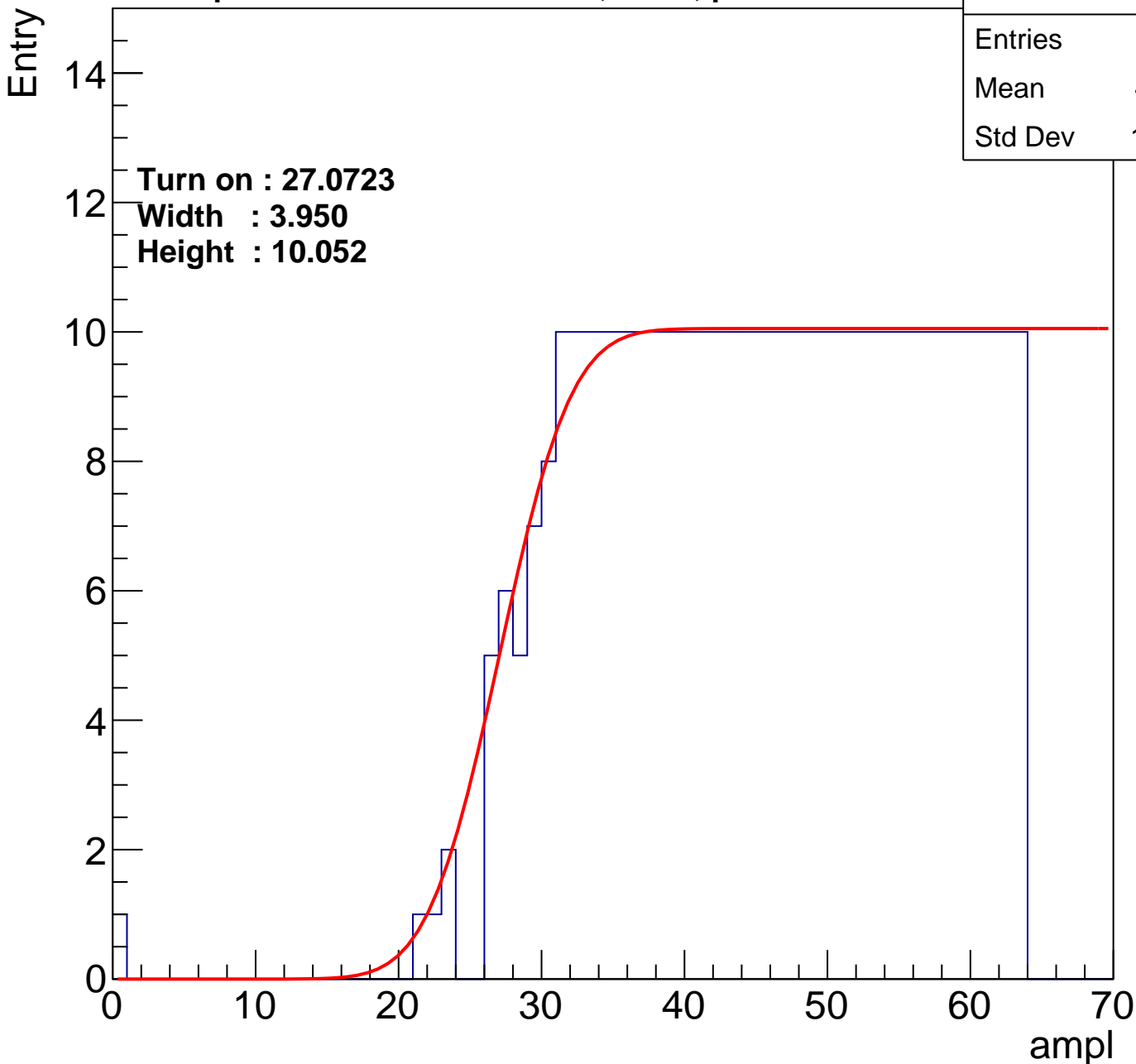
calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.0723

Width : 3.950

Height : 10.052



B0L000S, U10-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.36
Std Dev	11.65

Turn on : 26.9904

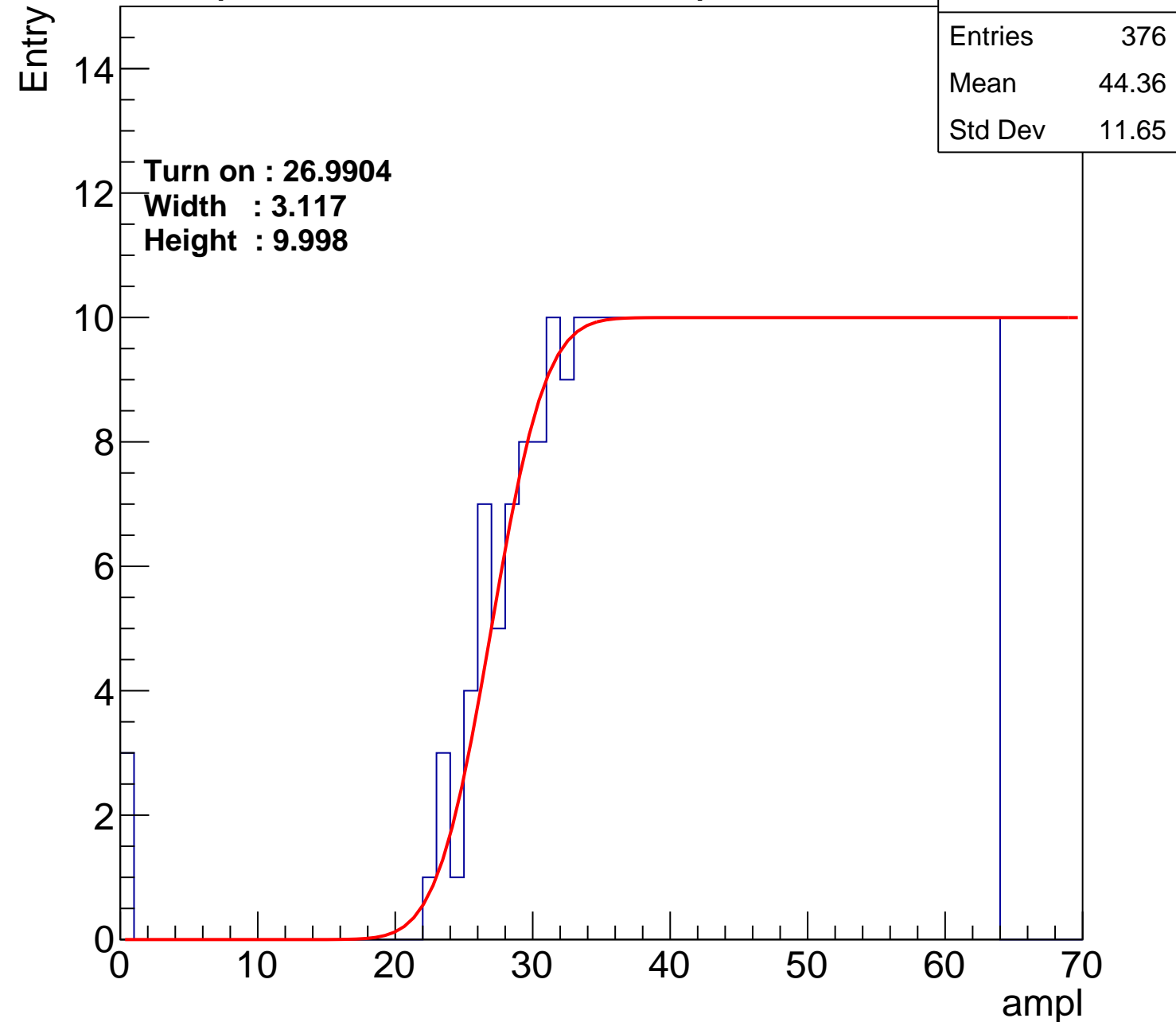
Width : 3.117

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	44.98
Std Dev	11.49

Turn on : 28.5295

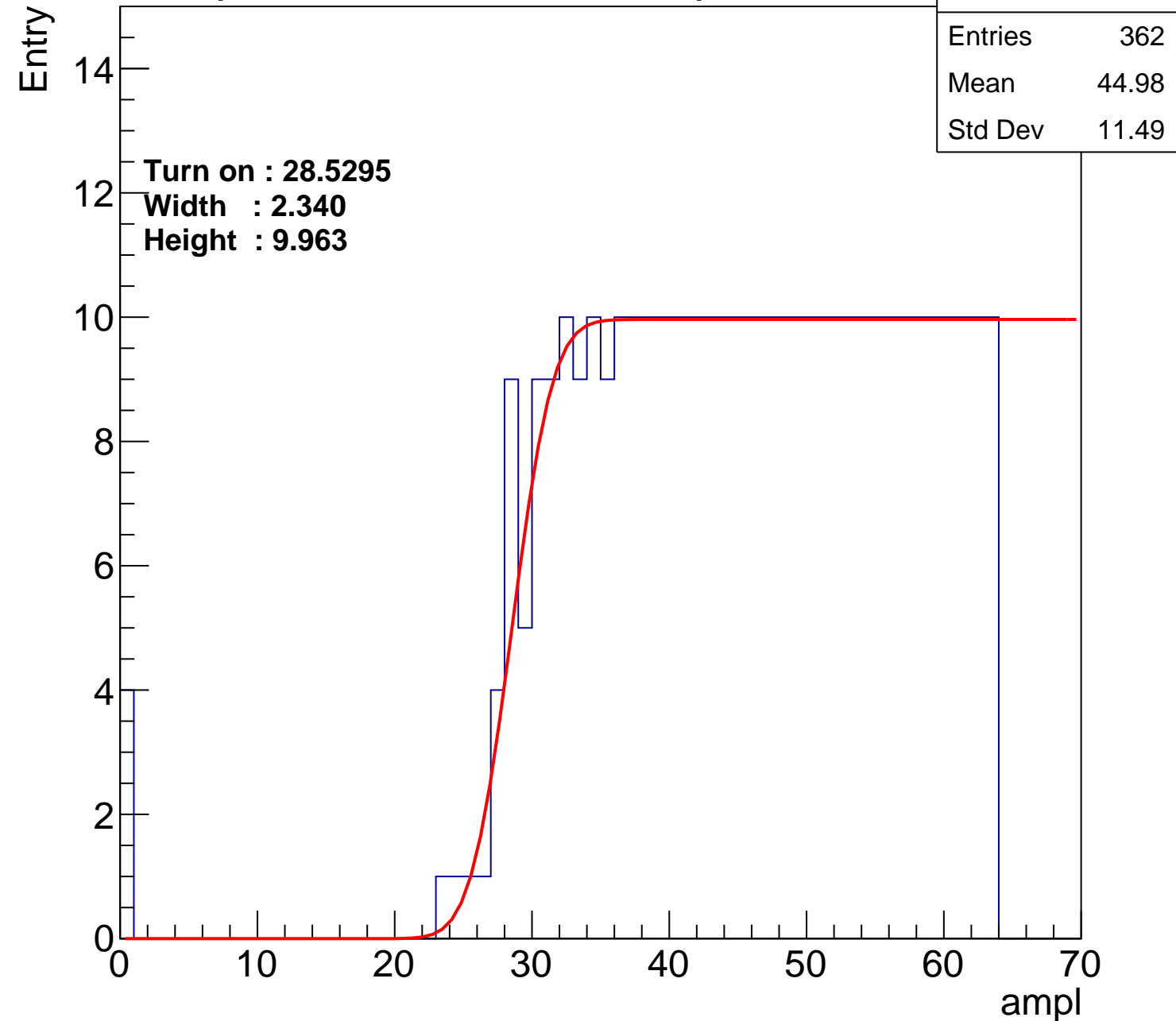
Width : 2.340

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.94
Std Dev	11.03

Turn on : 27.9330

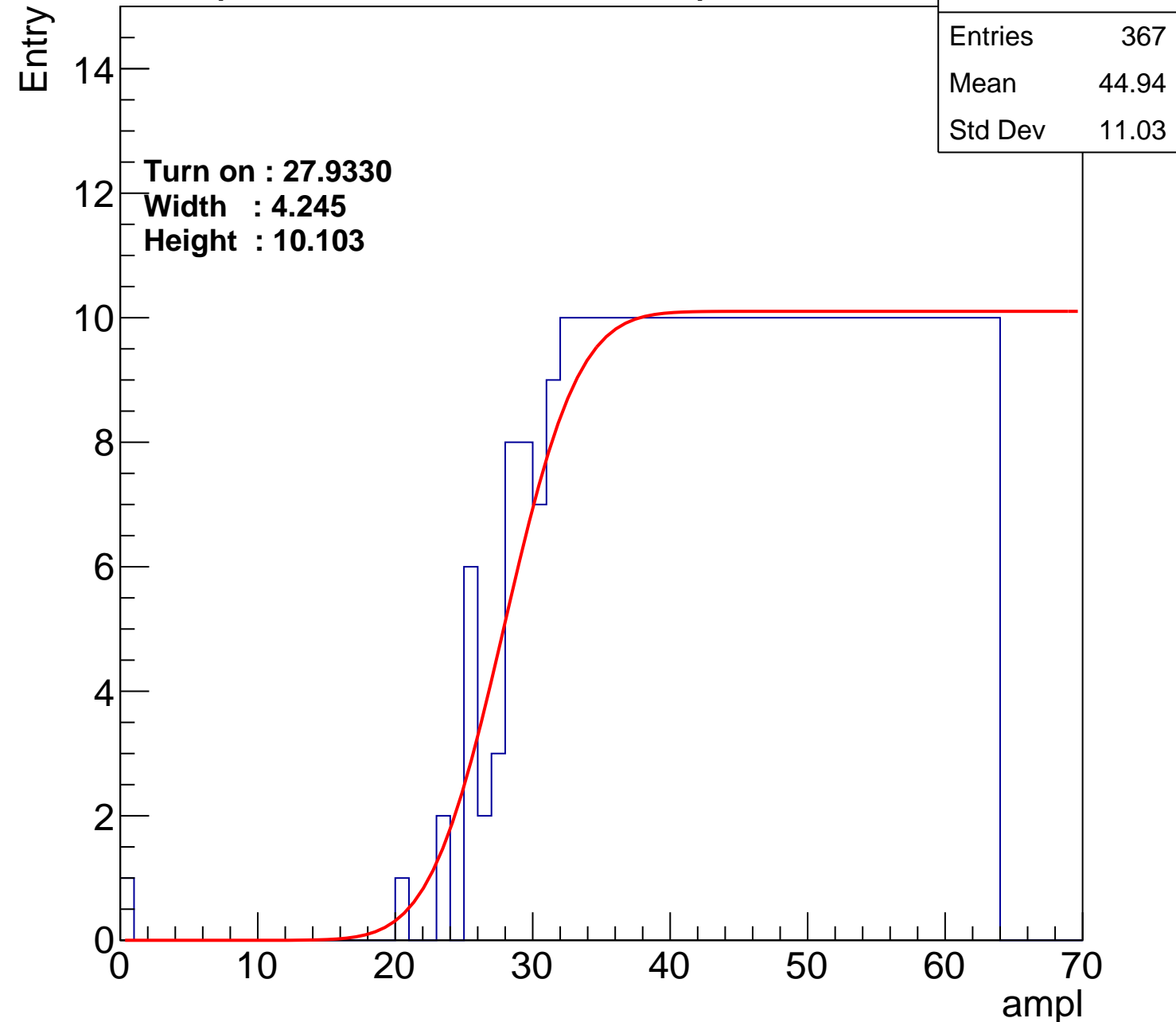
Width : 4.245

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.0941

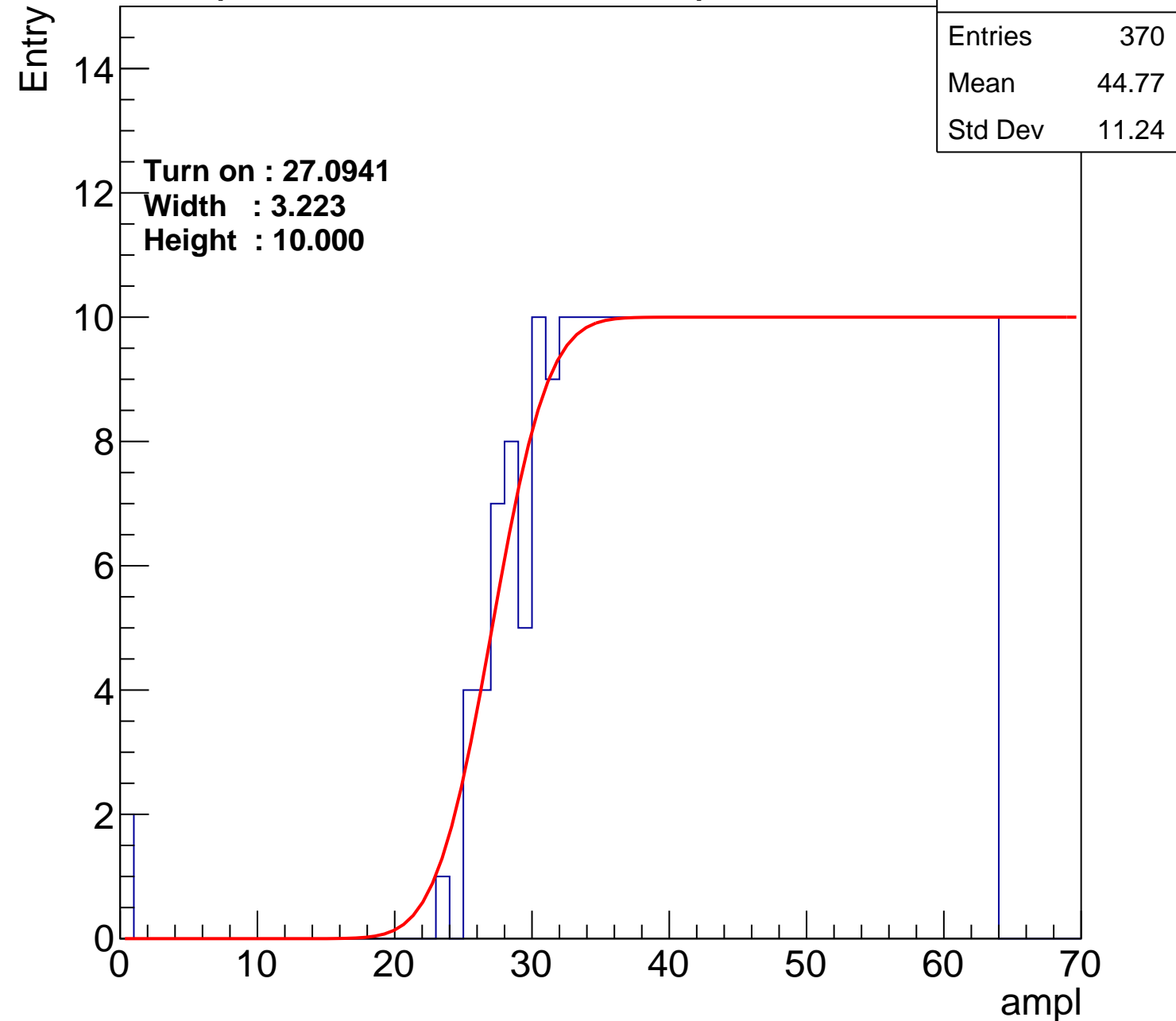
Width : 3.223

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.24
Std Dev	11.02

Turn on : 28.6270

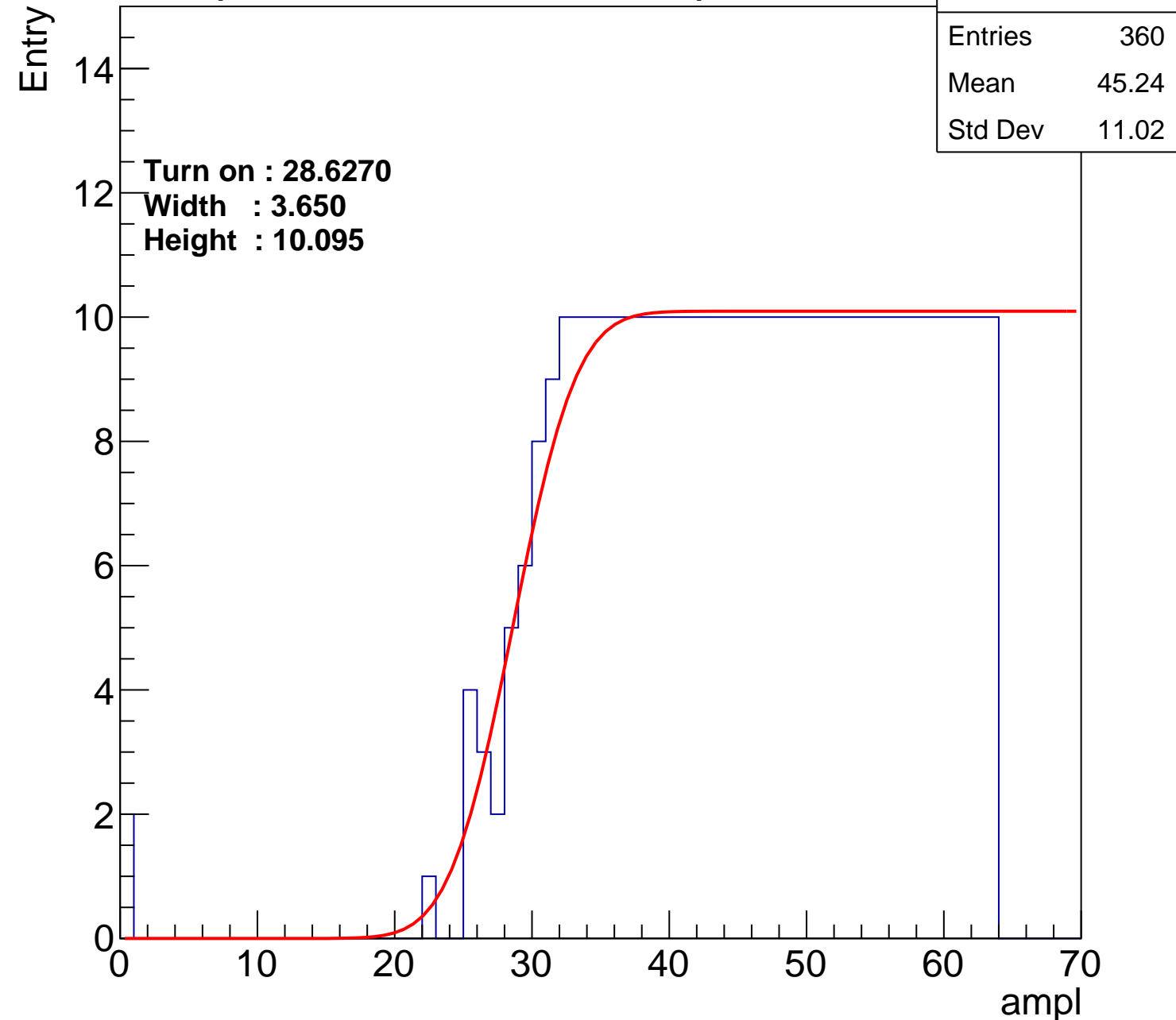
Width : 3.650

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 24.9692

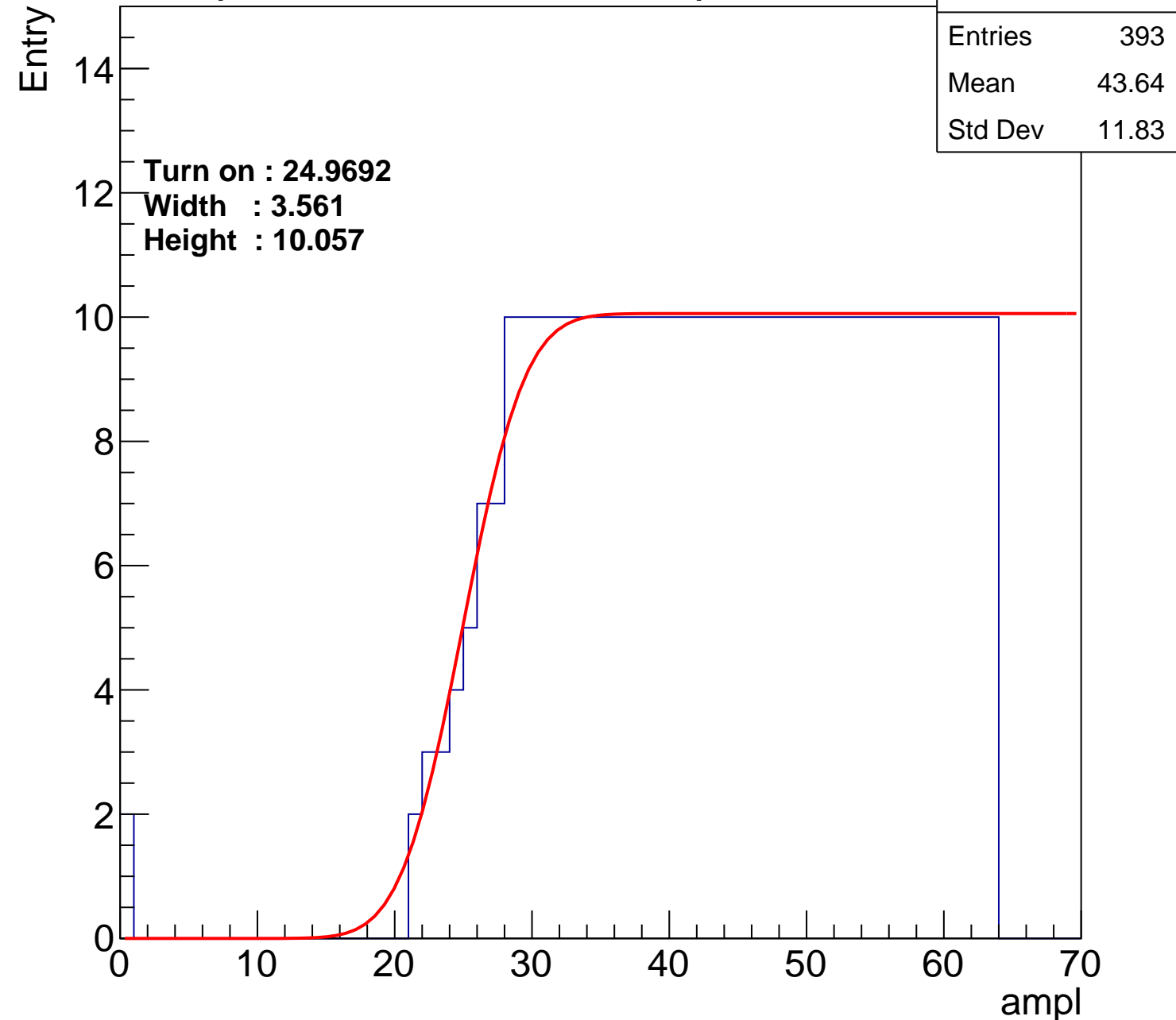
Width : 3.561

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.19
Std Dev	11.03

Turn on : 27.8270

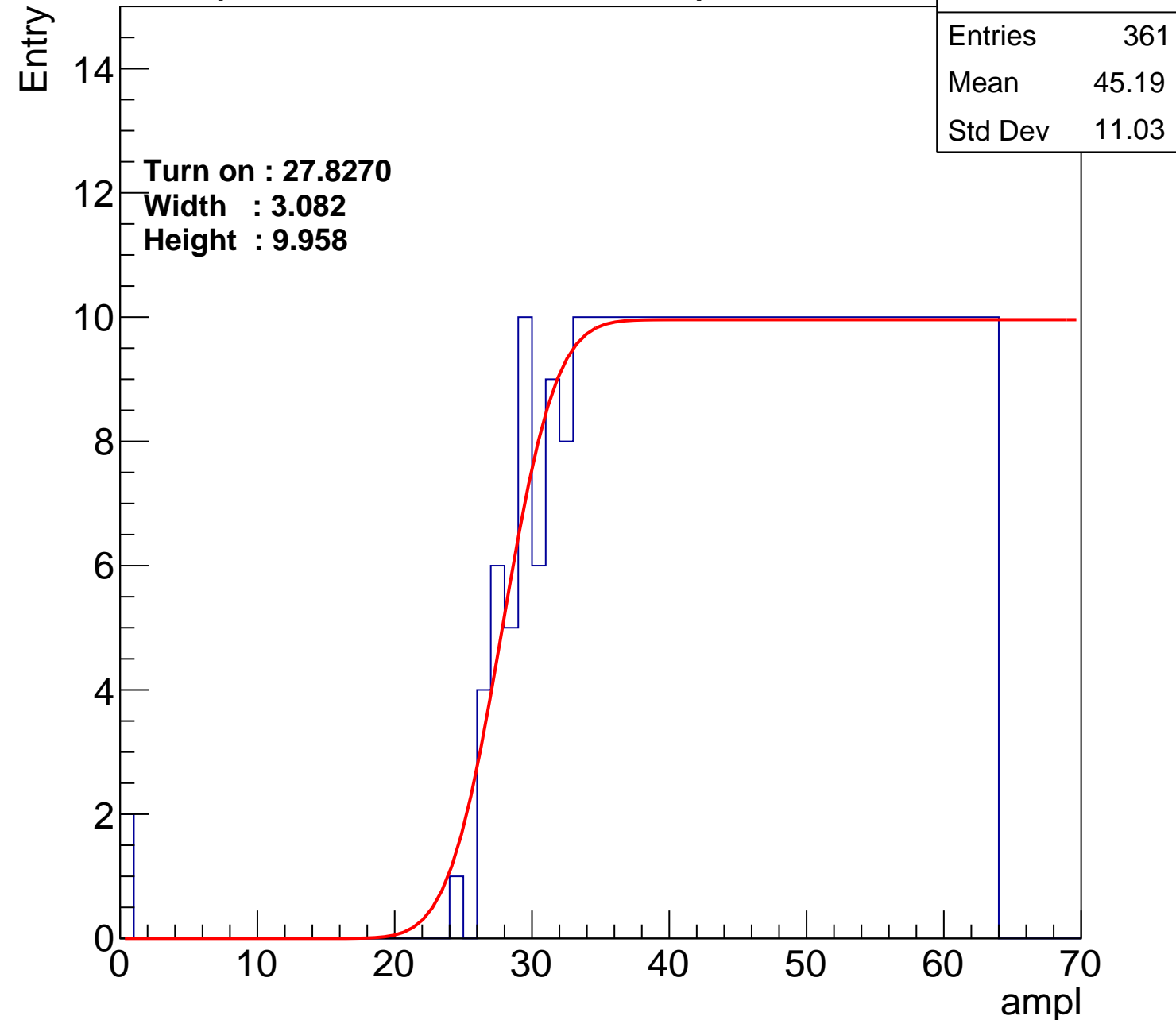
Width : 3.082

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.16
Std Dev	11.64

Turn on : 26.6612

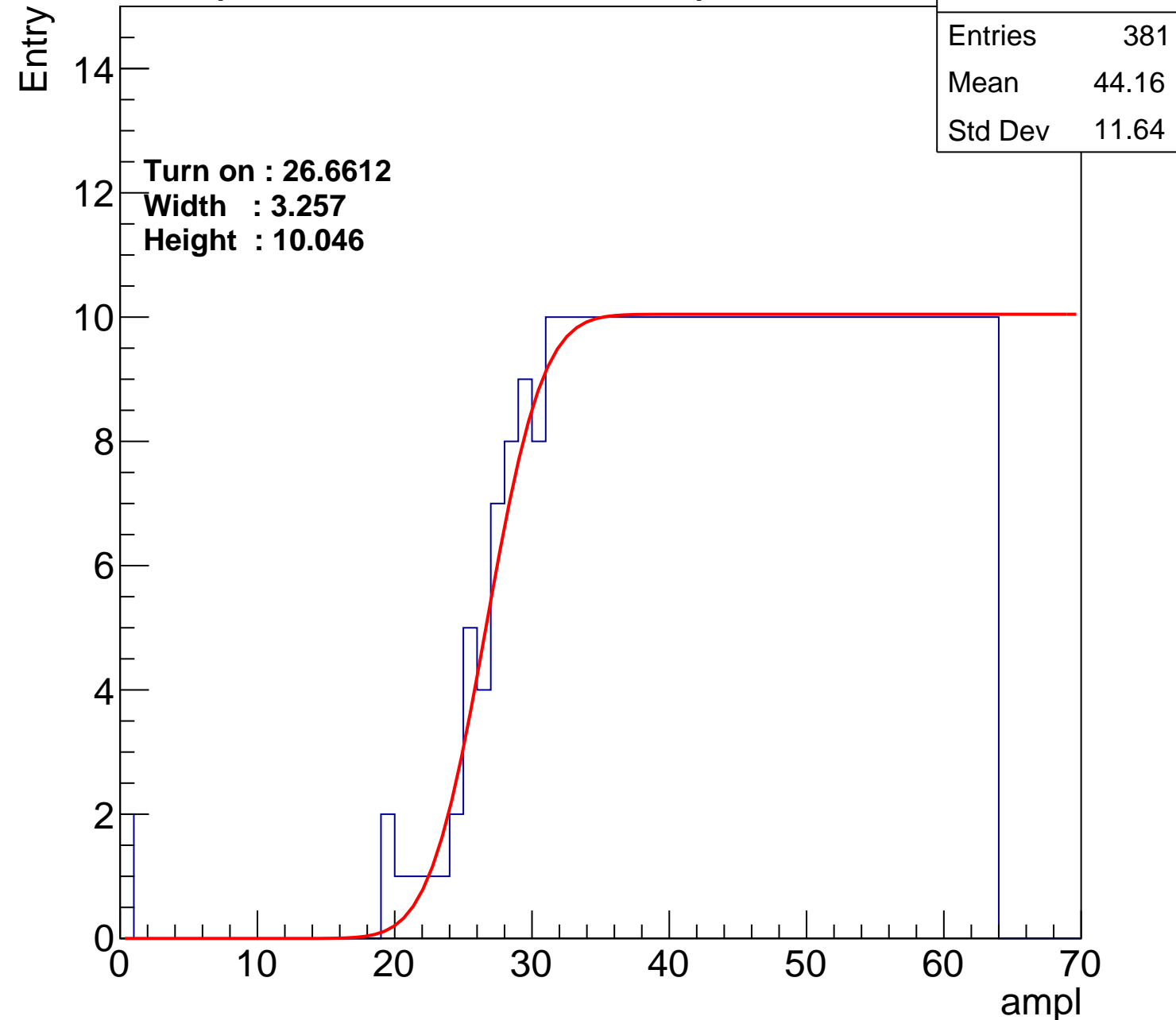
Width : 3.257

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.84
Std Dev	11.04

Turn on : 27.5314

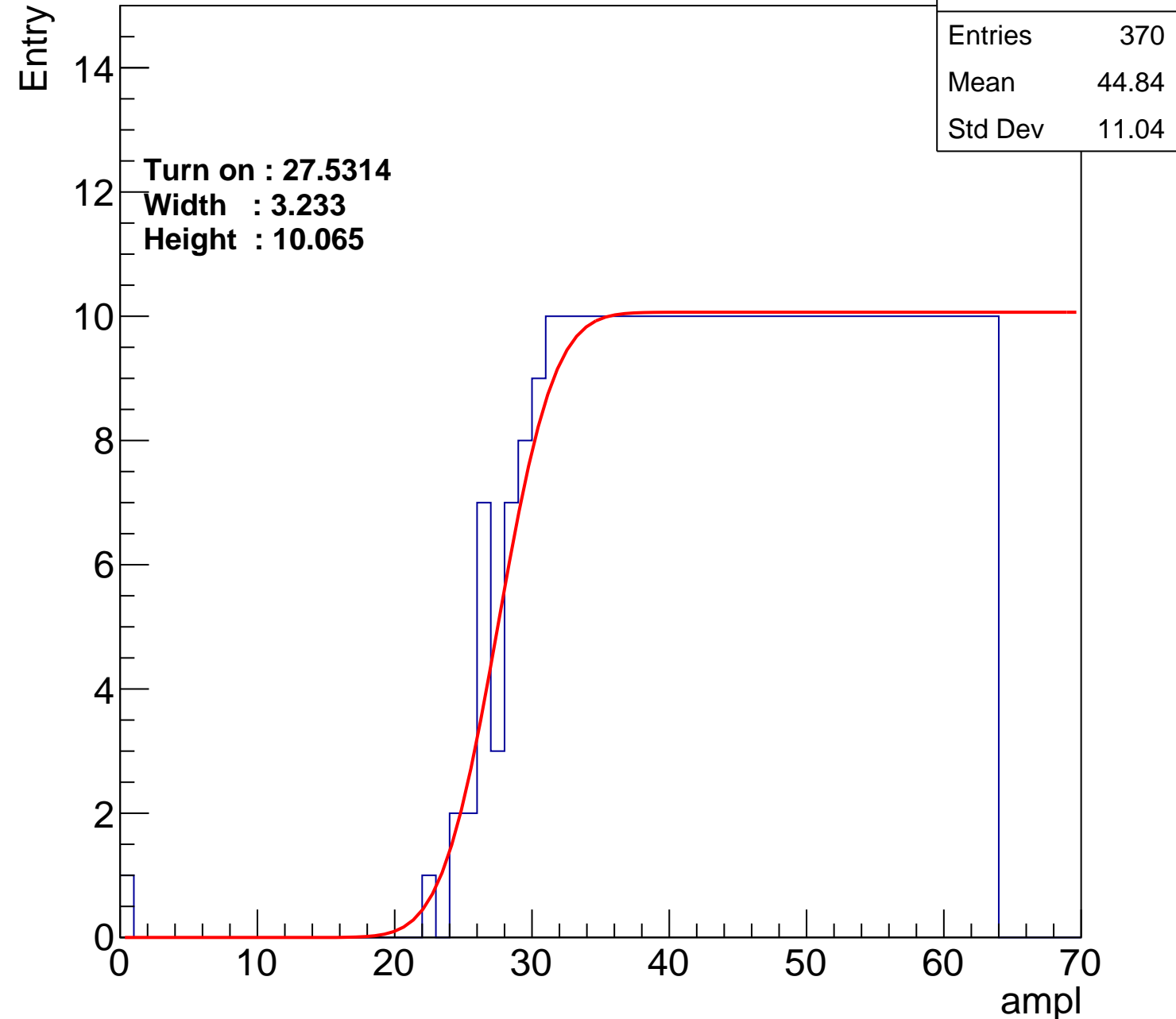
Width : 3.233

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U10-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.84
Std Dev	11.04

Turn on : 27.5314

Width : 3.233

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl

