

B0L103S, U11-ch0

calib_packv5_040323_1717.root, FC#2, port C3

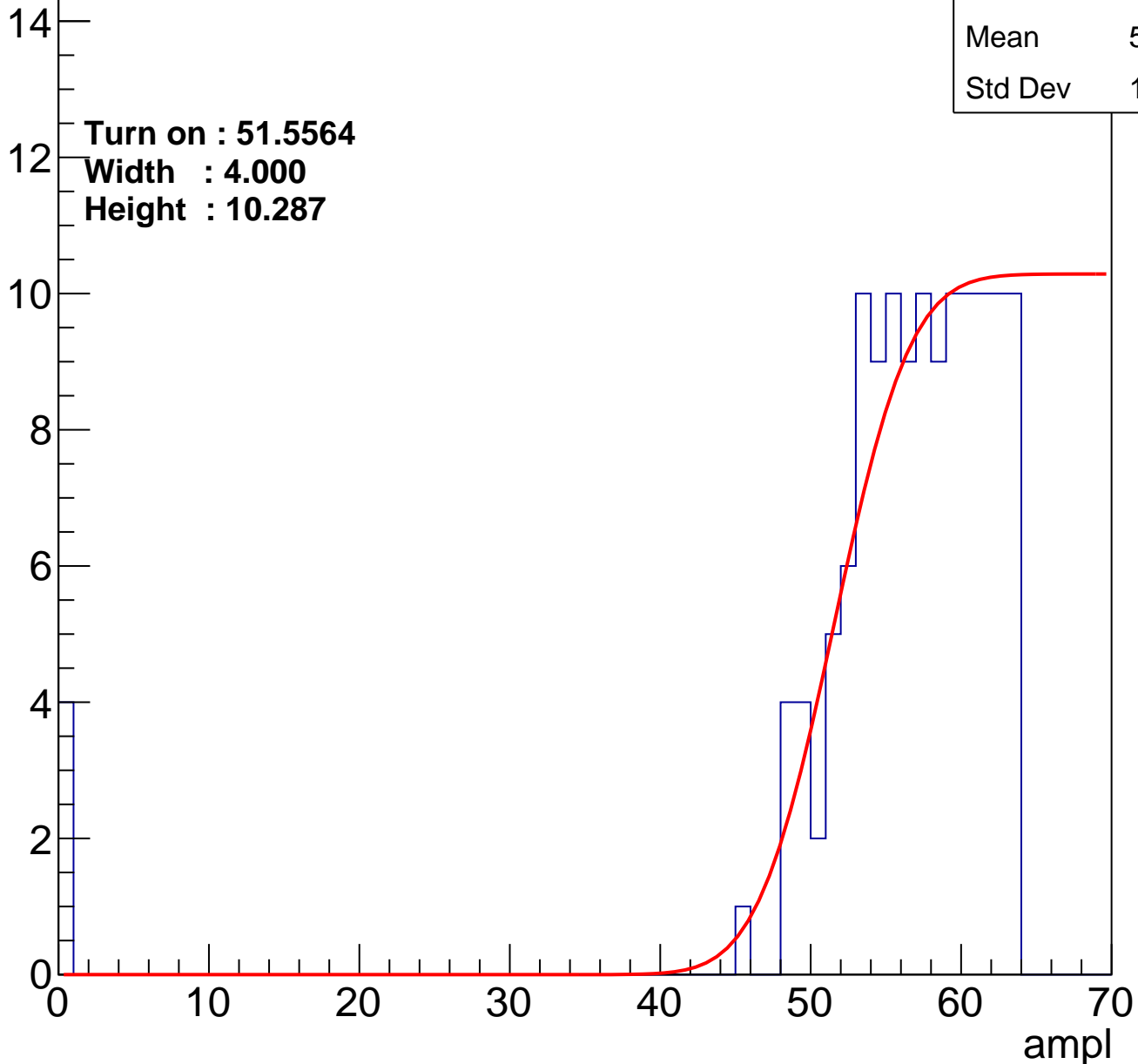
Entry

Entries	133
Mean	54.98
Std Dev	10.55

Turn on : 51.5564

Width : 4.000

Height : 10.287



B0L103S, U11-ch1

calib_packv5_040323_1717.root, FC#2, port C3

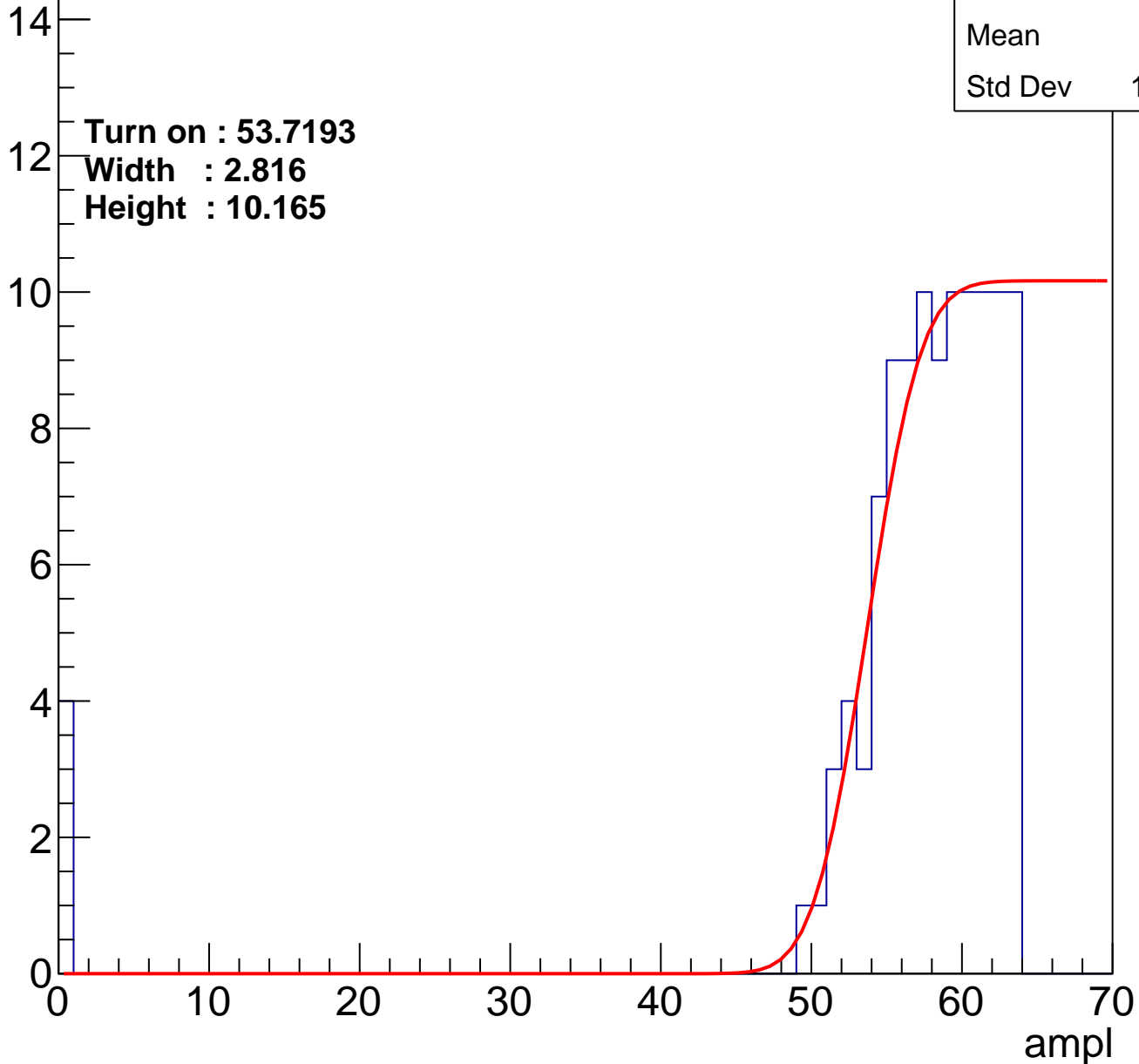
Entries	110
Mean	55.8
Std Dev	11.37

Turn on : 53.7193

Width : 2.816

Height : 10.165

Entry



B0L103S, U11-ch2

calib_packv5_040323_1717.root, FC#2, port C3

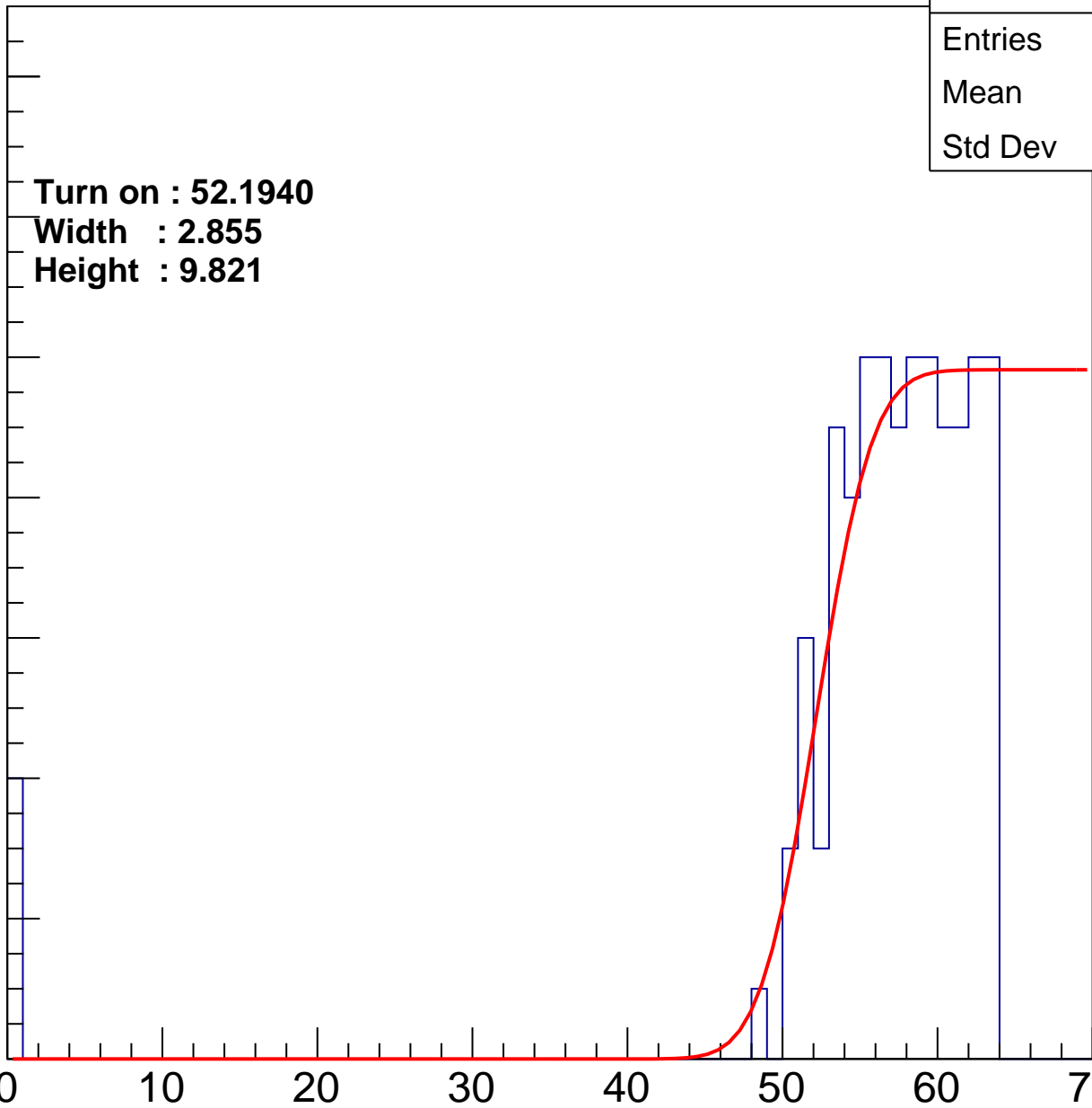
Entry

14
12
10
8
6
4
2
0

Turn on : 52.1940
Width : 2.855
Height : 9.821

Entries	121
Mean	55.38
Std Dev	10.89

ampl



B0L103S, U11-ch3

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	55.62
Std Dev	9.801

Turn on : 52.6226

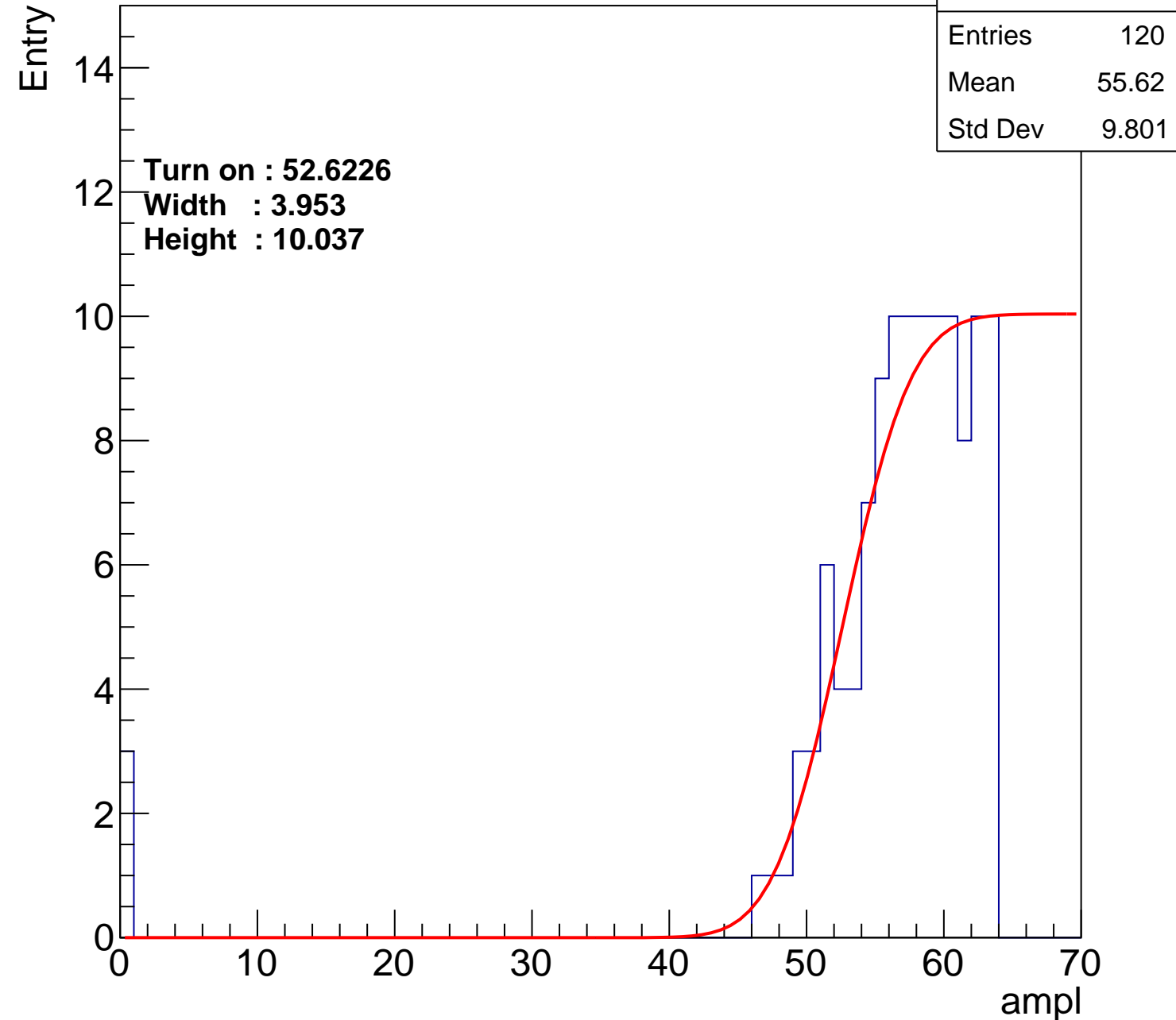
Width : 3.953

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch4

calib_packv5_040323_1717.root, FC#2, port C3

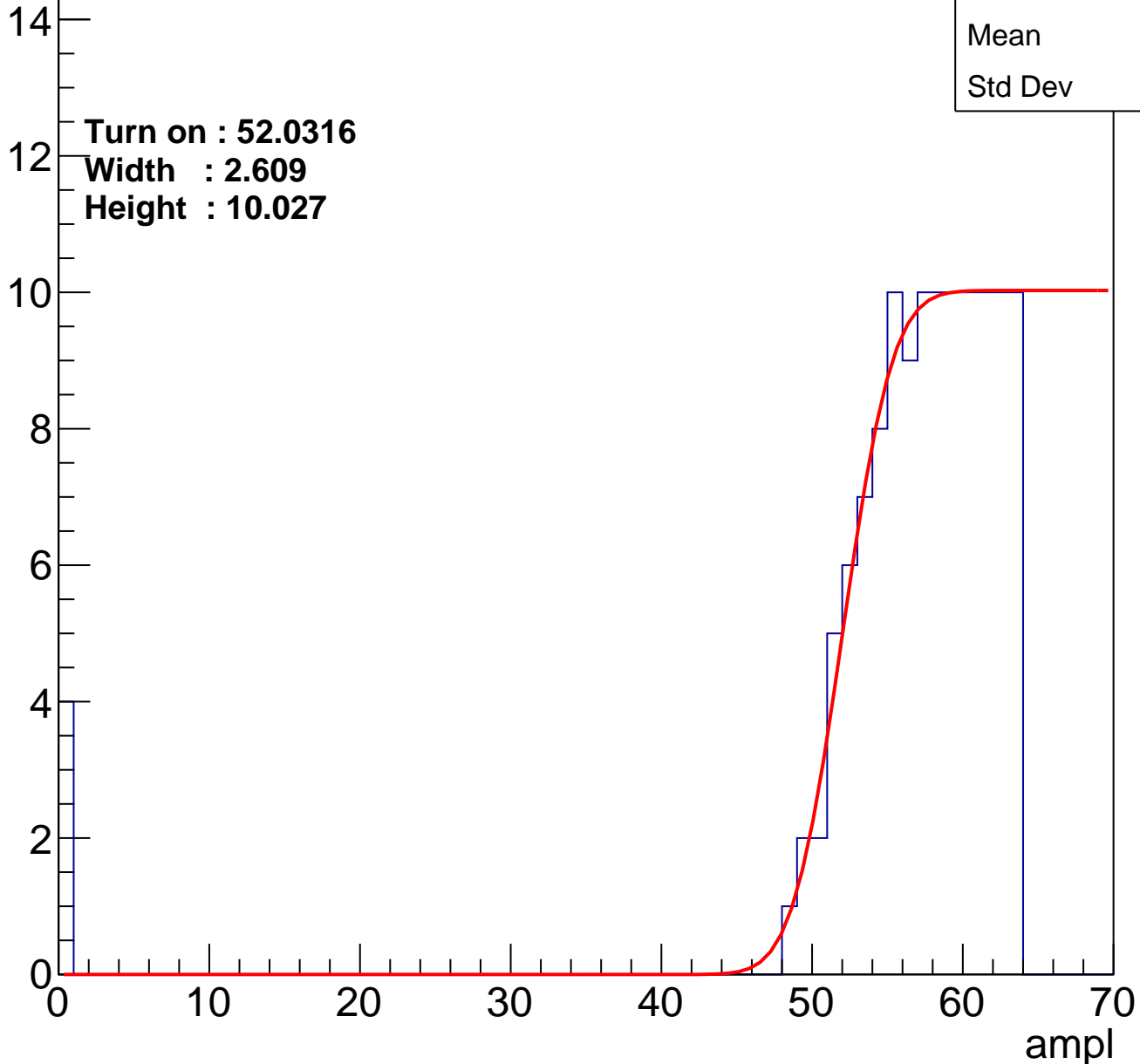
Entries	124
Mean	55.4
Std Dev	10.8

Turn on : 52.0316

Width : 2.609

Height : 10.027

Entry



B0L103S, U11-ch5

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.51
Std Dev	6.499

Turn on : 52.1696

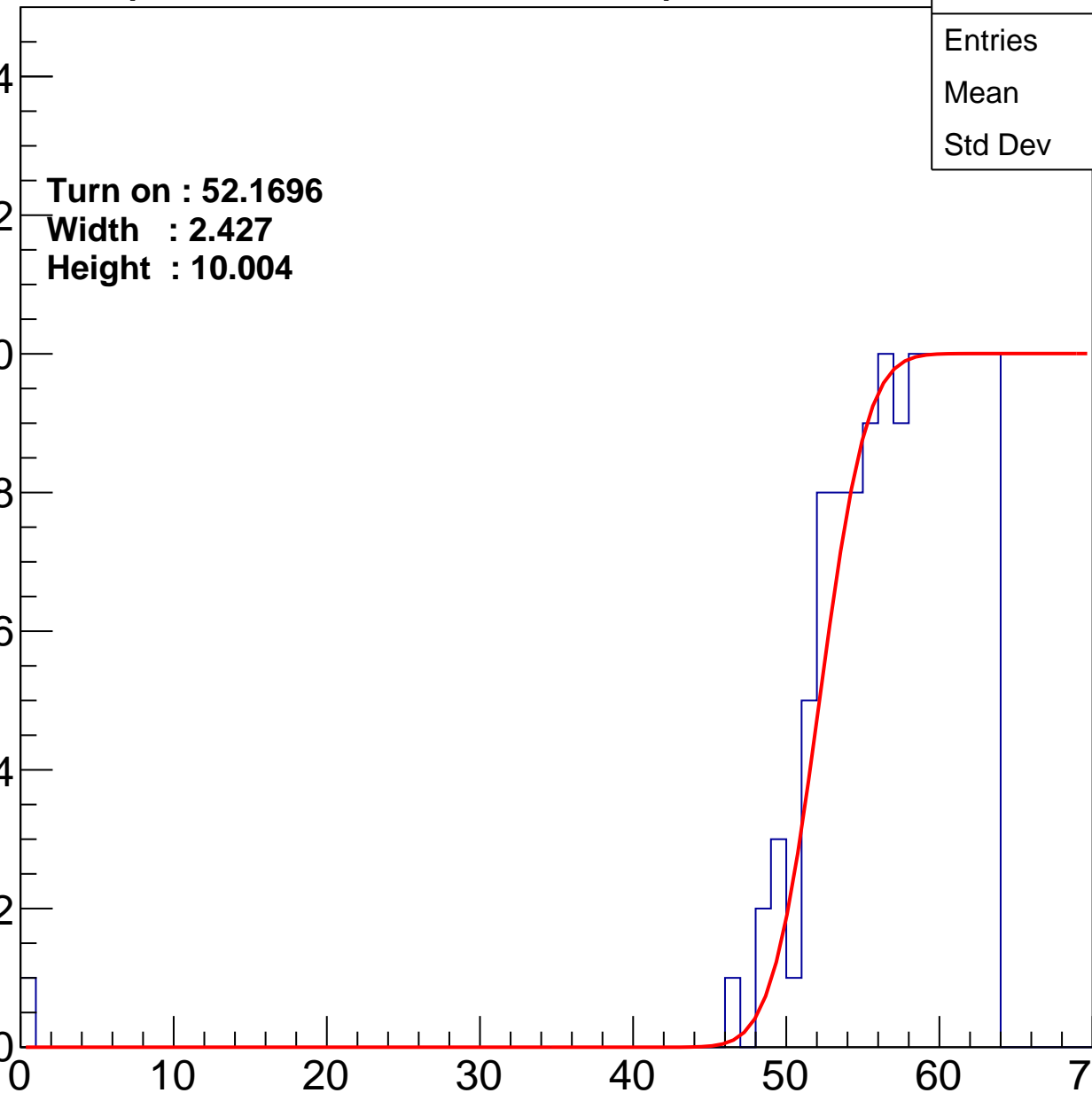
Width : 2.427

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch6

calib_packv5_040323_1717.root, FC#2, port C3

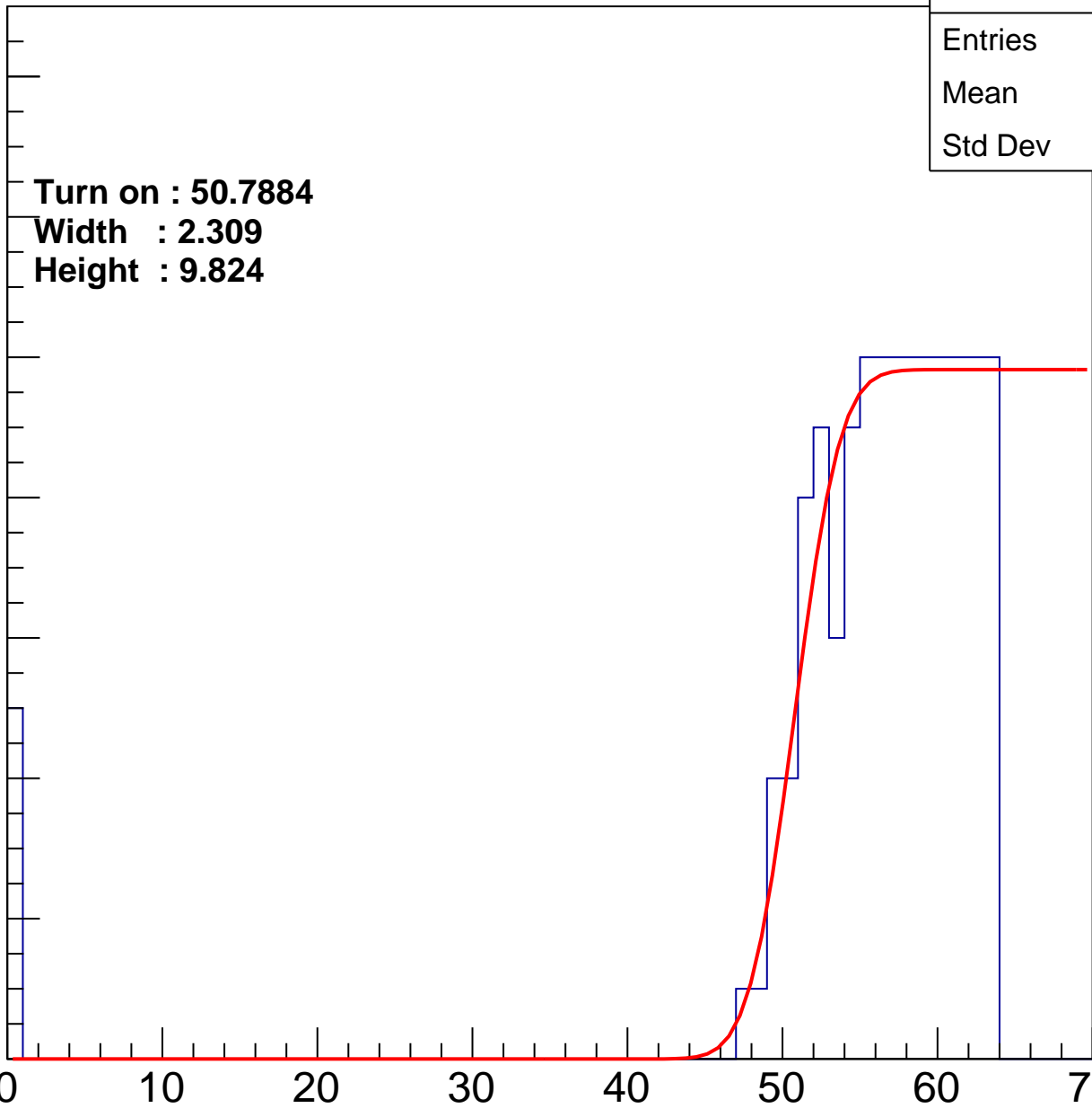
Entry

14
12
10
8
6
4
2
0

Turn on : 50.7884
Width : 2.309
Height : 9.824

Entries	137
Mean	54.61
Std Dev	11.38

ampl



B0L103S, U11-ch7

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.19
Std Dev	8.179

Turn on : 51.5229

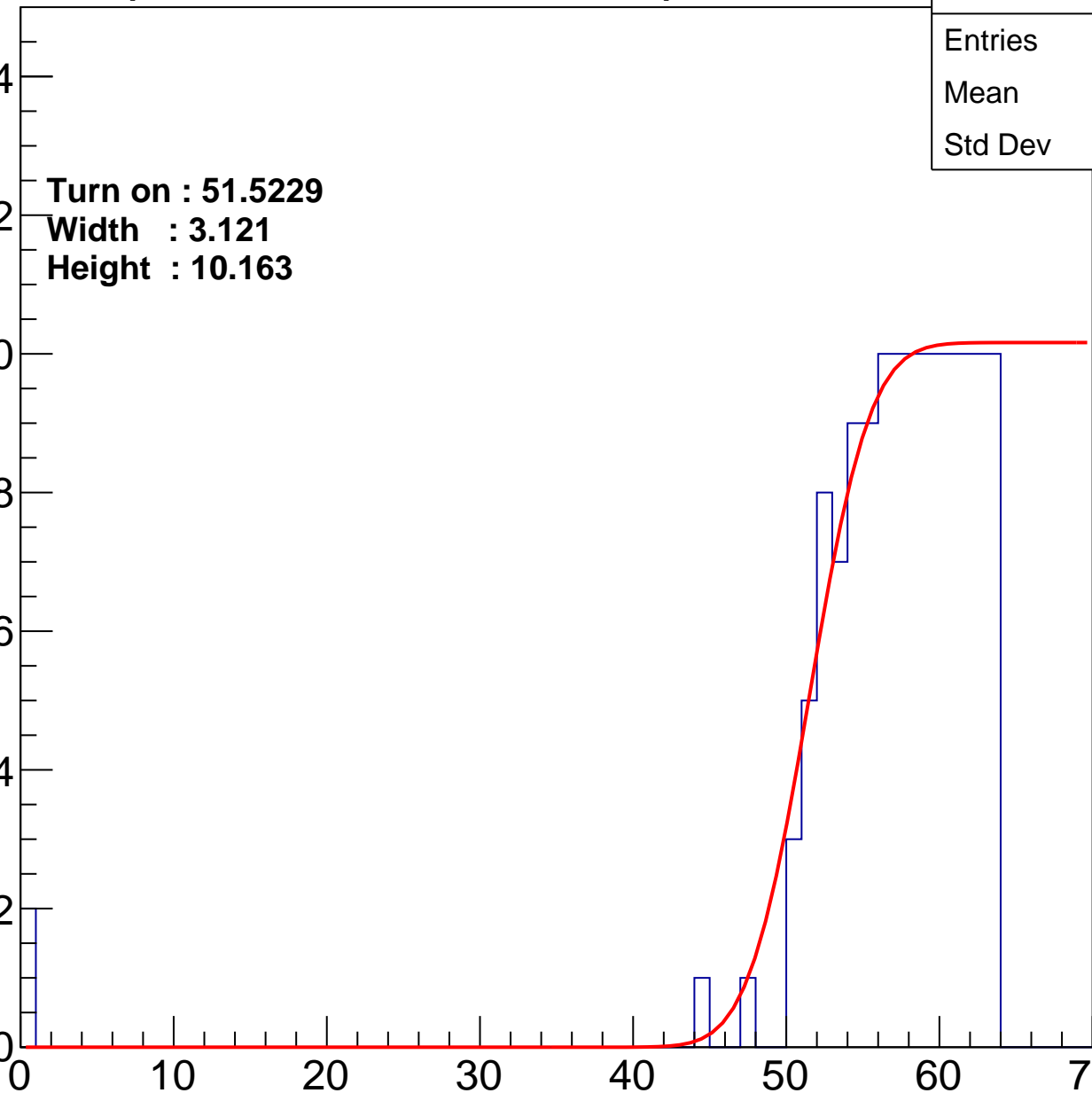
Width : 3.121

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch8

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.76
Std Dev	8.167

Turn on : 51.7319

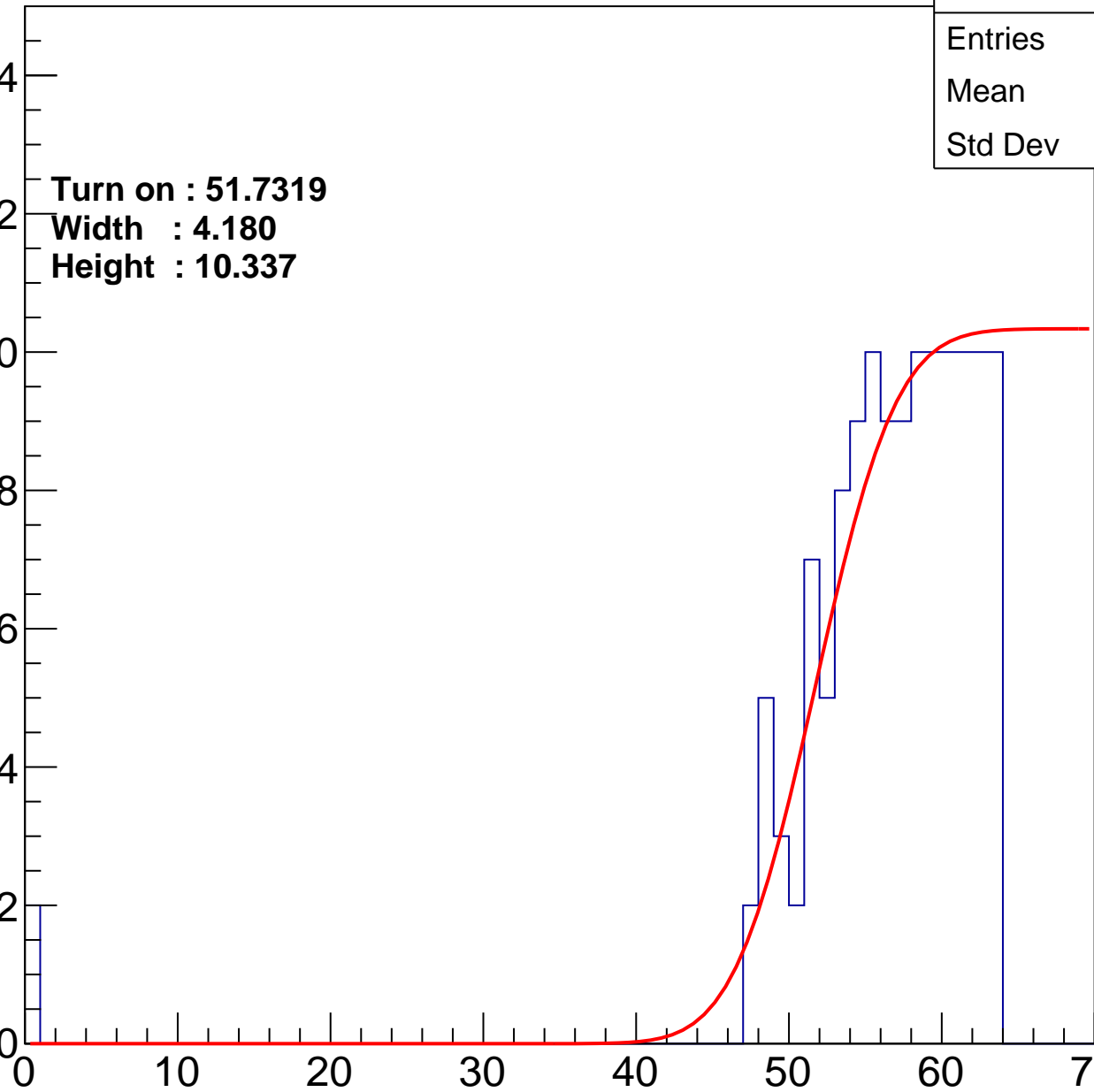
Width : 4.180

Height : 10.337

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch9

calib_packv5_040323_1717.root, FC#2, port C3

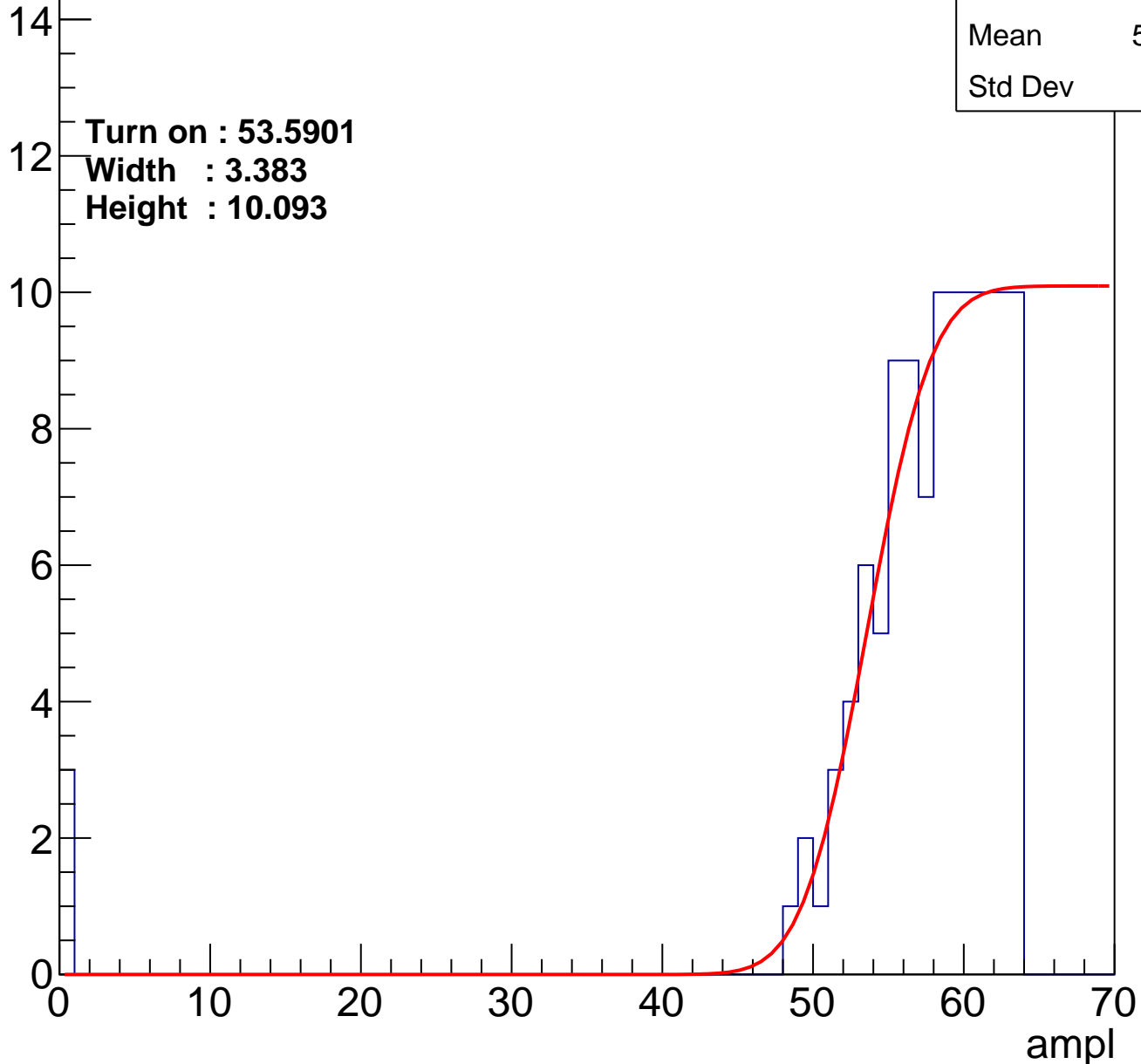
Entries	110
Mean	56.12
Std Dev	10.1

Turn on : 53.5901

Width : 3.383

Height : 10.093

Entry



B0L103S, U11-ch10

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.78
Std Dev	6.504

Turn on : 52.3398

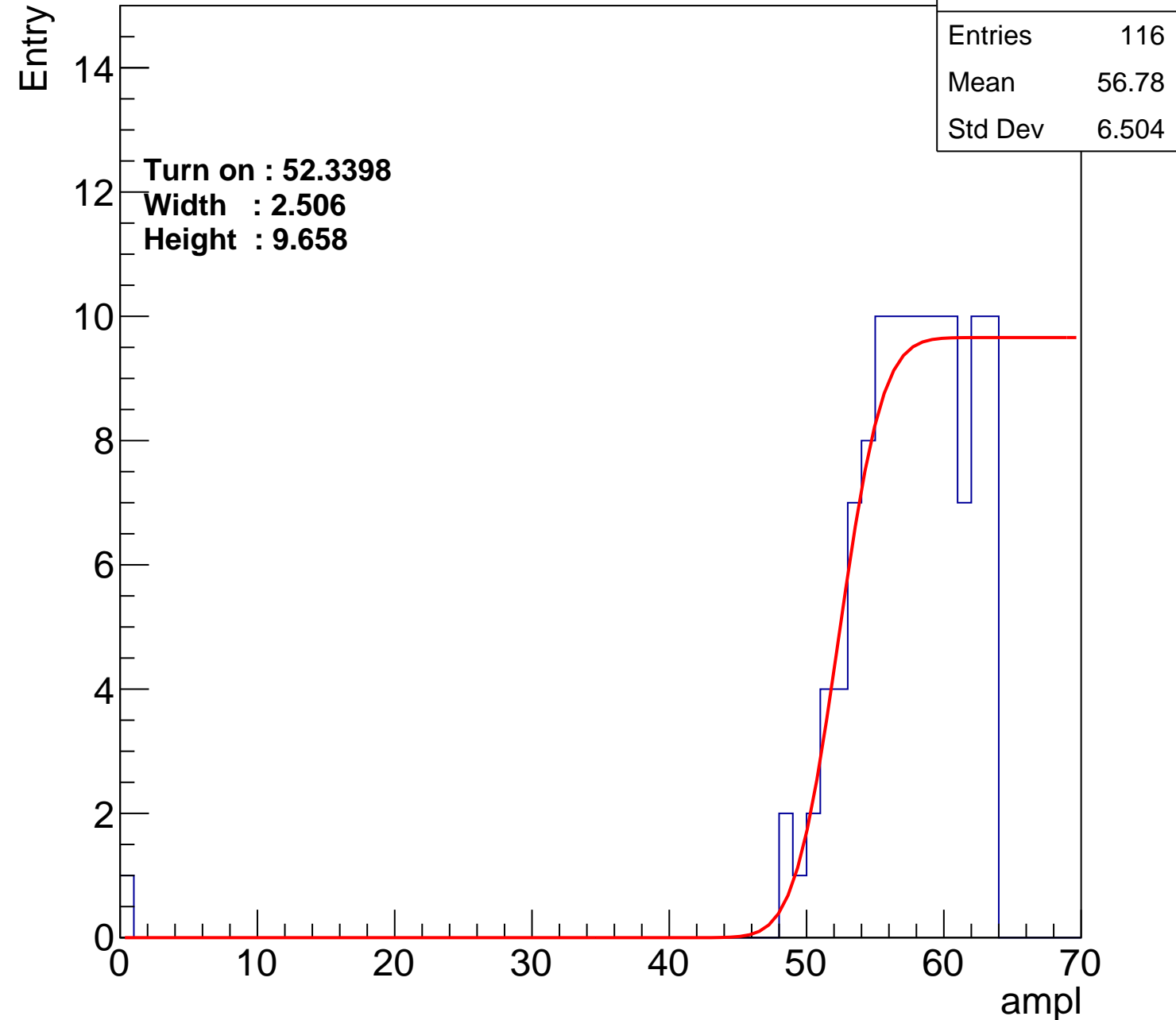
Width : 2.506

Height : 9.658

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch11

calib_packv5_040323_1717.root, FC#2, port C3

Entries	105
Mean	56.57
Std Dev	8.727

Turn on : 53.9459

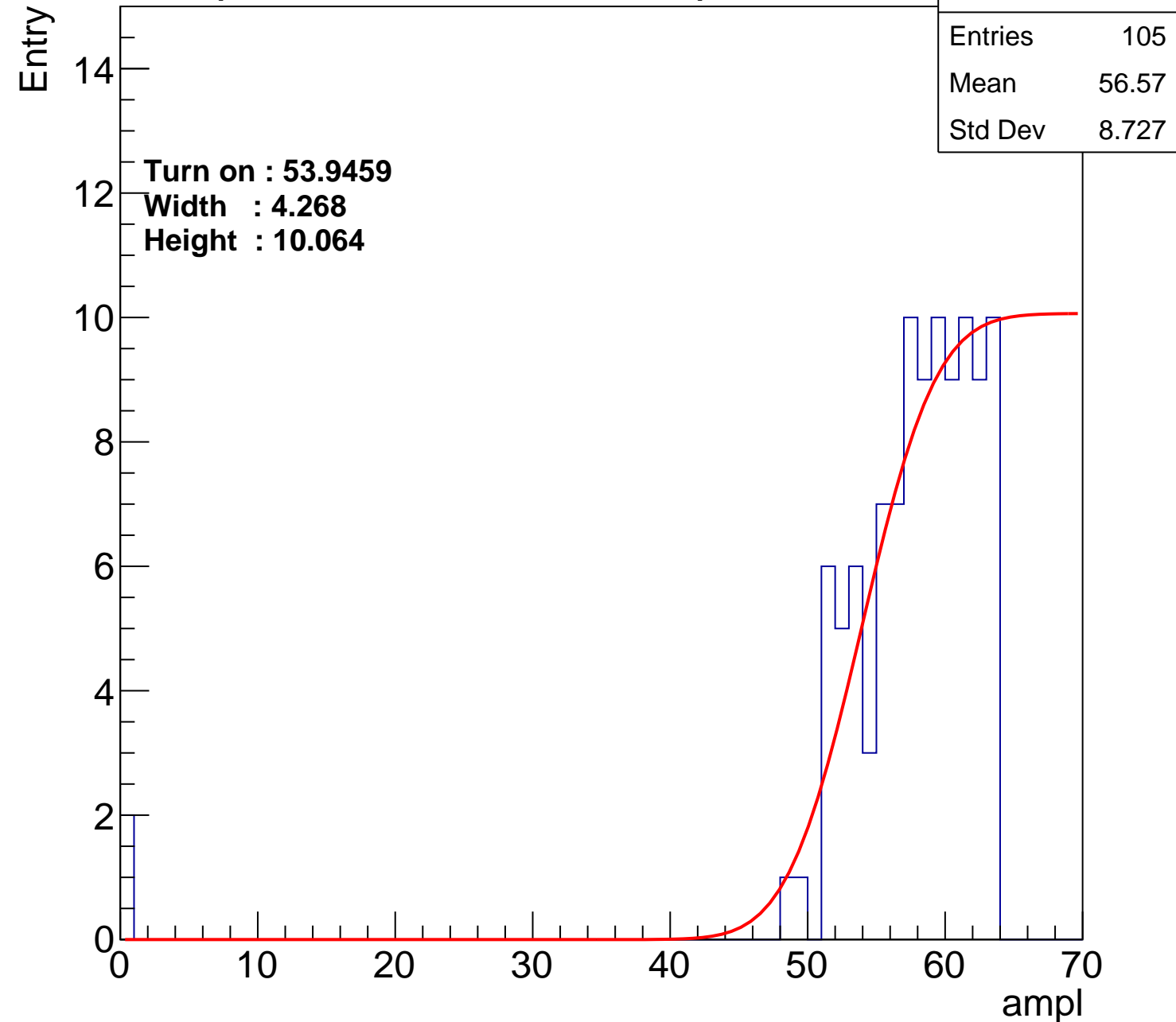
Width : 4.268

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch12

calib_packv5_040323_1717.root, FC#2, port C3

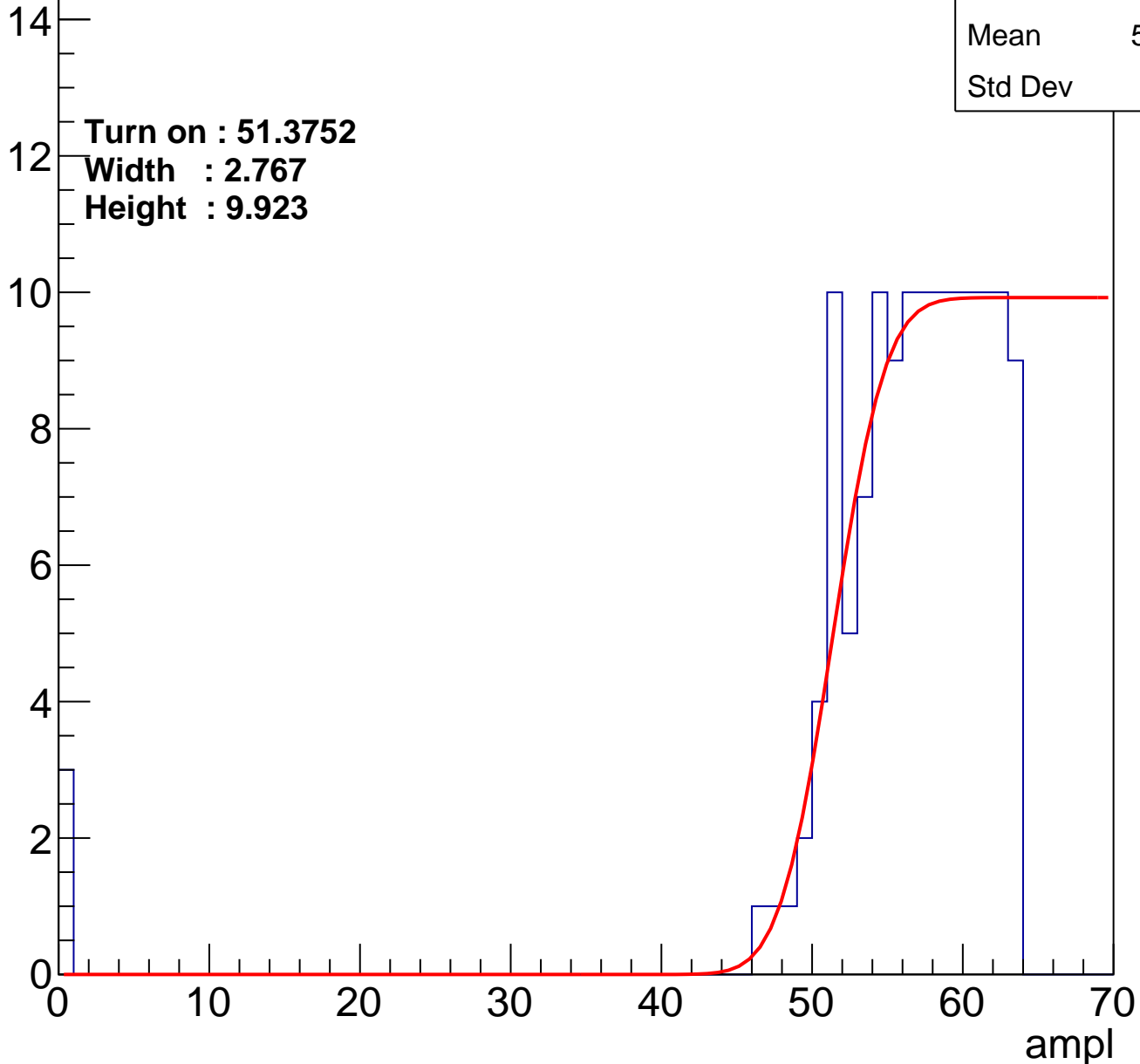
Entries	132
Mean	55.39
Std Dev	9.39

Turn on : 51.3752

Width : 2.767

Height : 9.923

Entry



B0L103S, U11-ch13

calib_packv5_040323_1717.root, FC#2, port C3

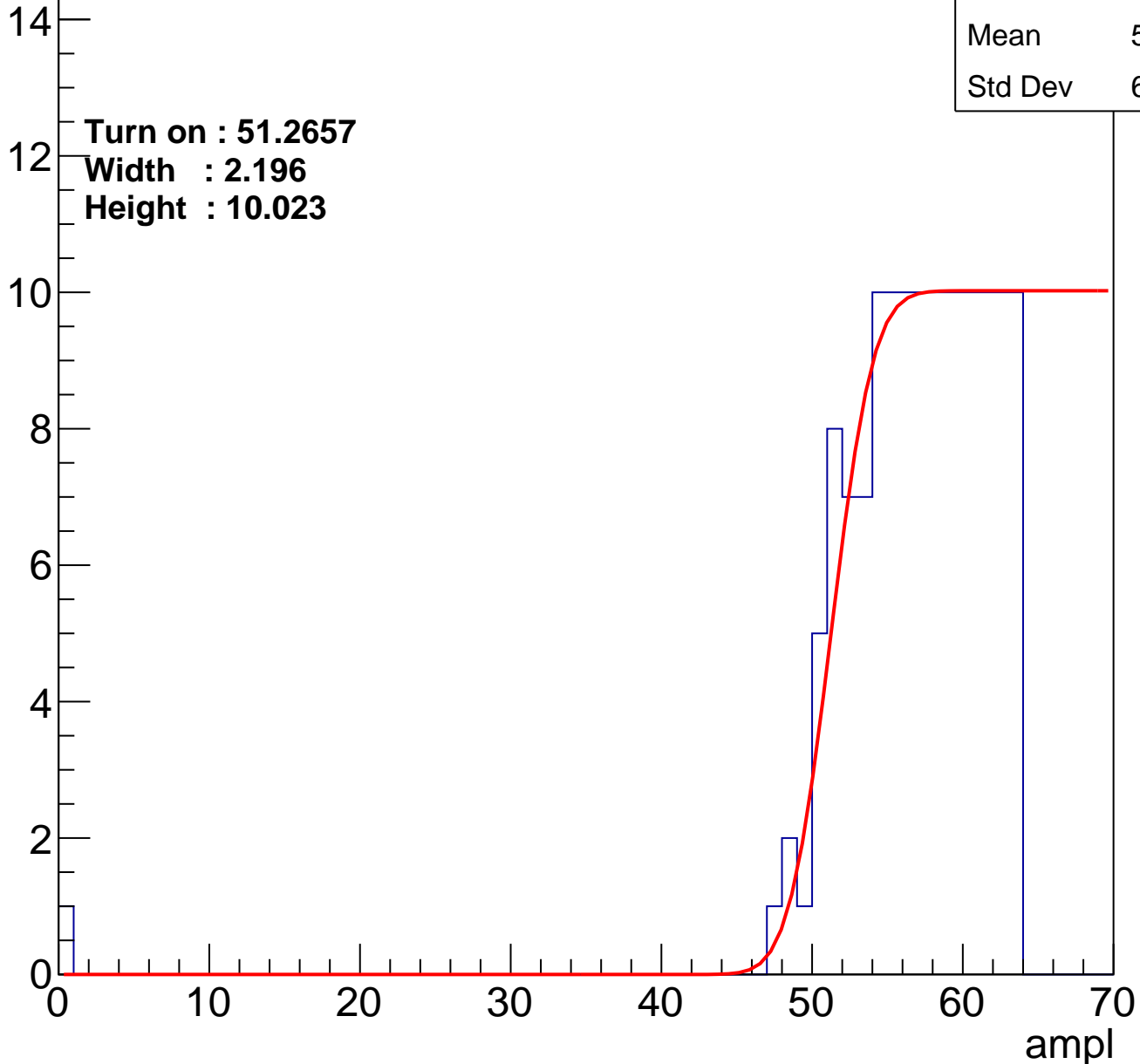
Entries	132
Mean	56.33
Std Dev	6.387

Turn on : 51.2657

Width : 2.196

Height : 10.023

Entry



B0L103S, U11-ch14

calib_packv5_040323_1717.root, FC#2, port C3

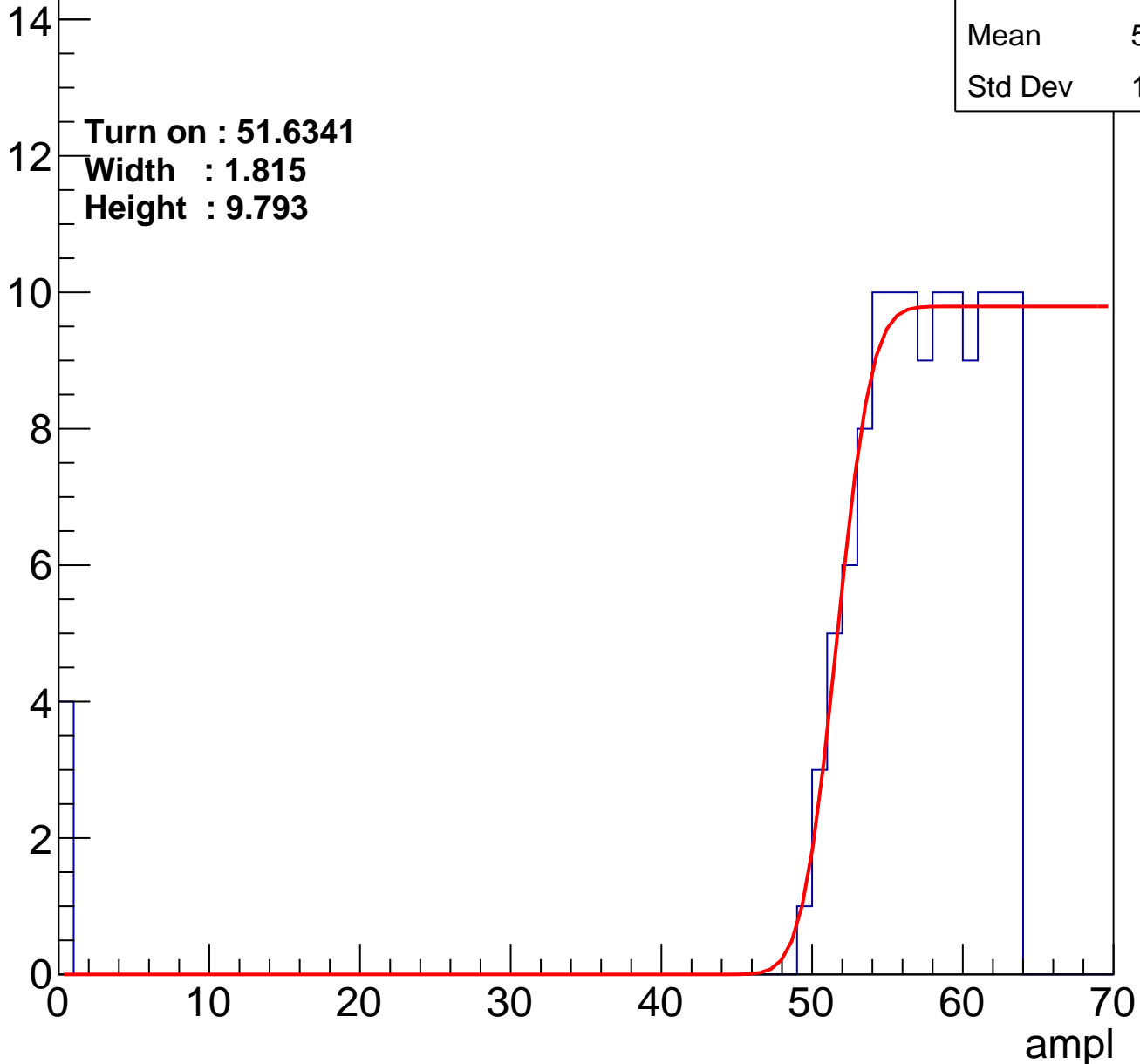
Entries	125
Mean	55.38
Std Dev	10.73

Turn on : 51.6341

Width : 1.815

Height : 9.793

Entry



B0L103S, U11-ch15

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.27
Std Dev	9.457

Turn on : 50.8487

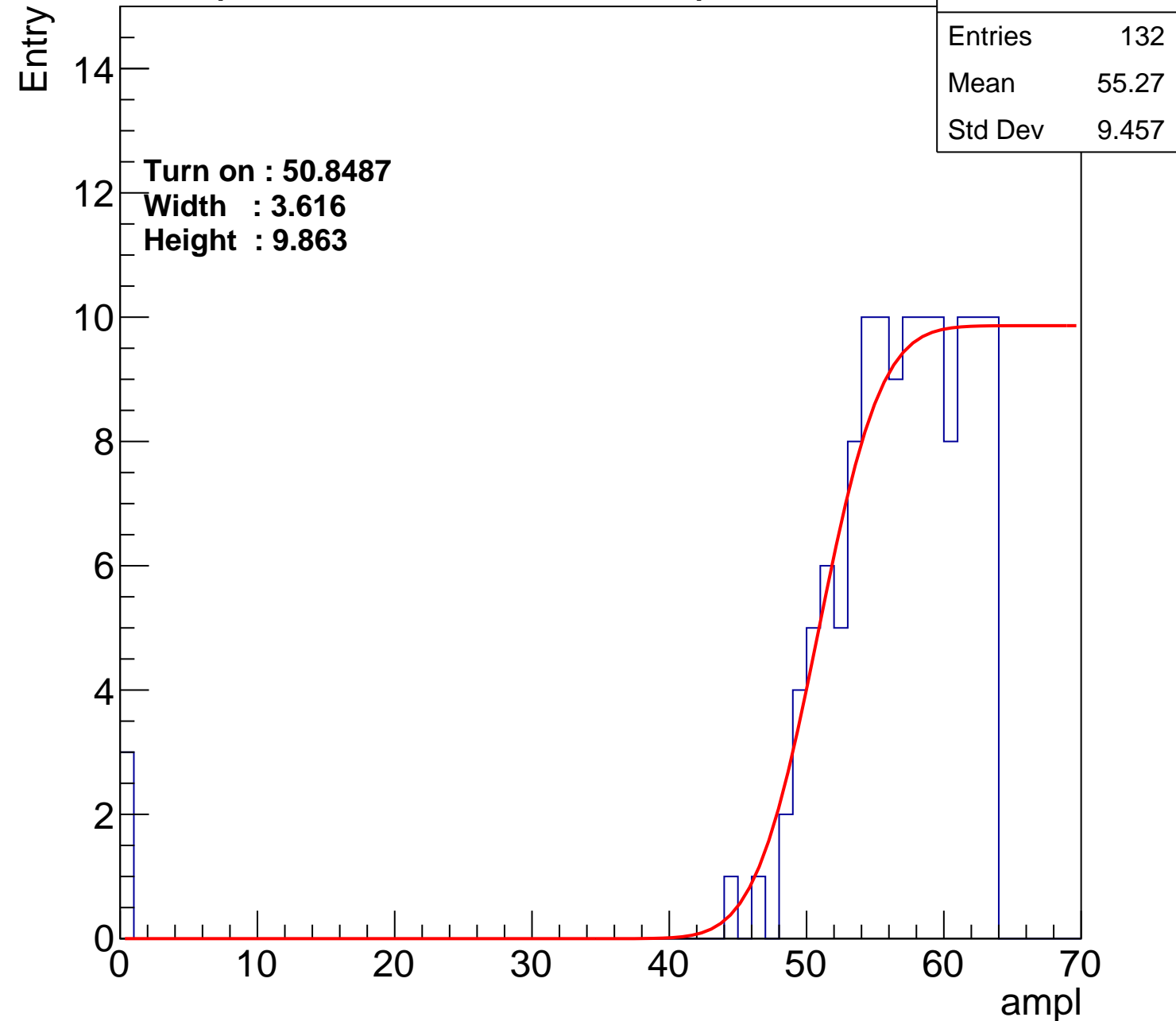
Width : 3.616

Height : 9.863

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_040323_1717.root, FC#2, port C3

calib_packv5_040323_1717.root, FC#2, port C3

Turn on : 48.8668
Width : 1.498
Height : 9.717



B0L103S, U11-ch17

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.33
Std Dev	8.296

Turn on : 52.4745

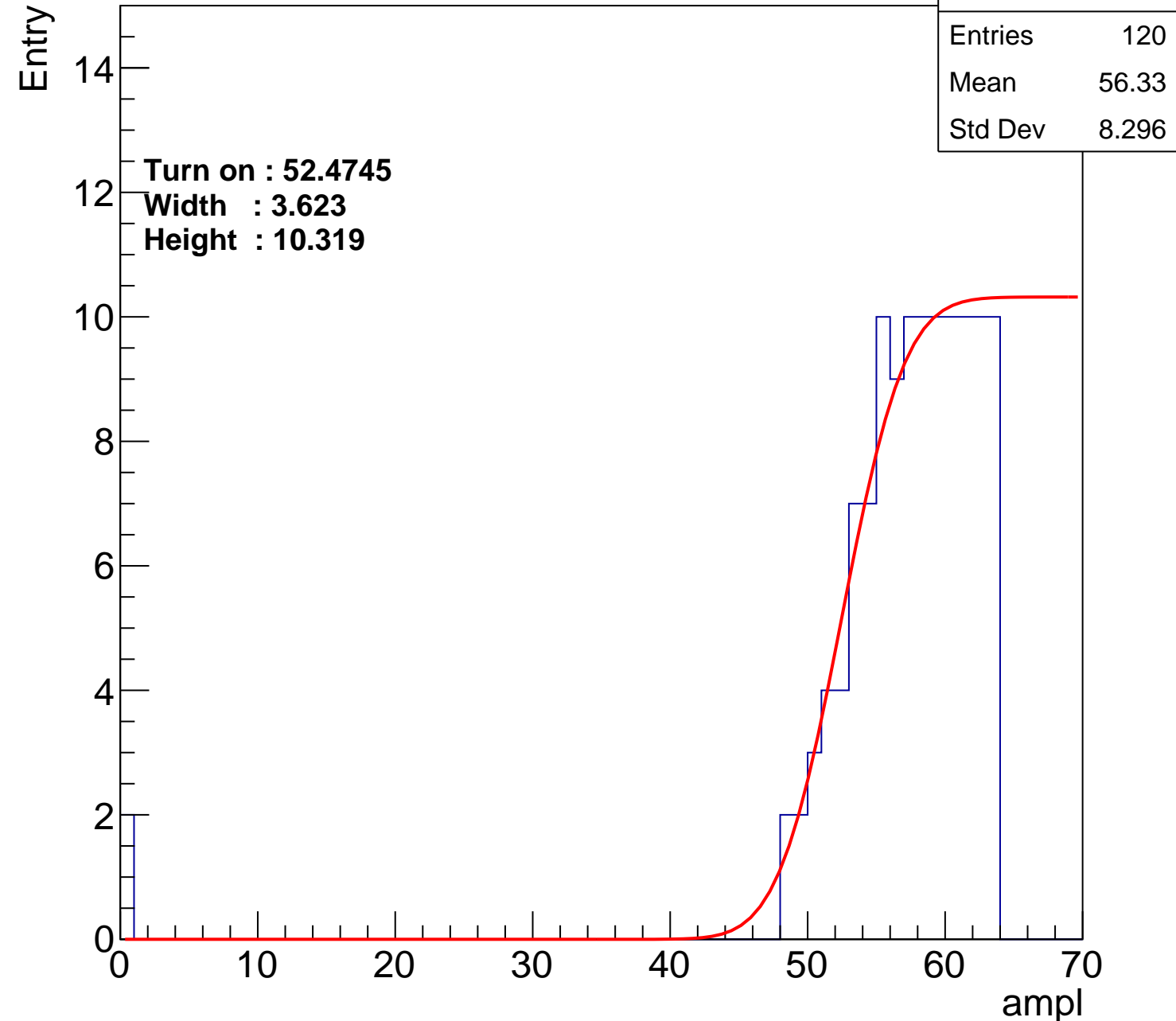
Width : 3.623

Height : 10.319

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch18

calib_packv5_040323_1717.root, FC#2, port C3

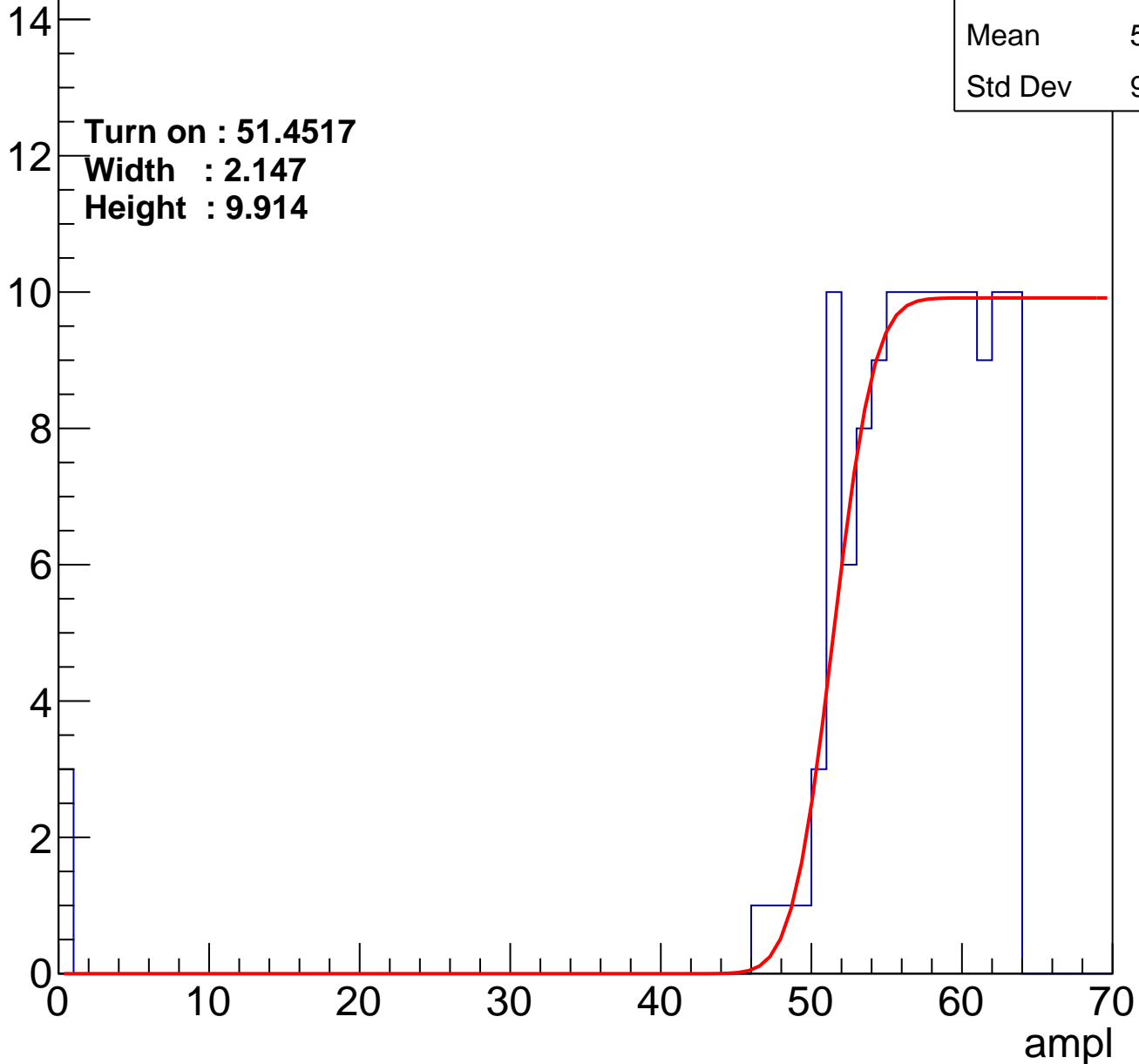
Entries	132
Mean	55.46
Std Dev	9.379

Turn on : 51.4517

Width : 2.147

Height : 9.914

Entry



B0L103S, U11-ch19

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.96
Std Dev	10.52

Turn on : 51.3257

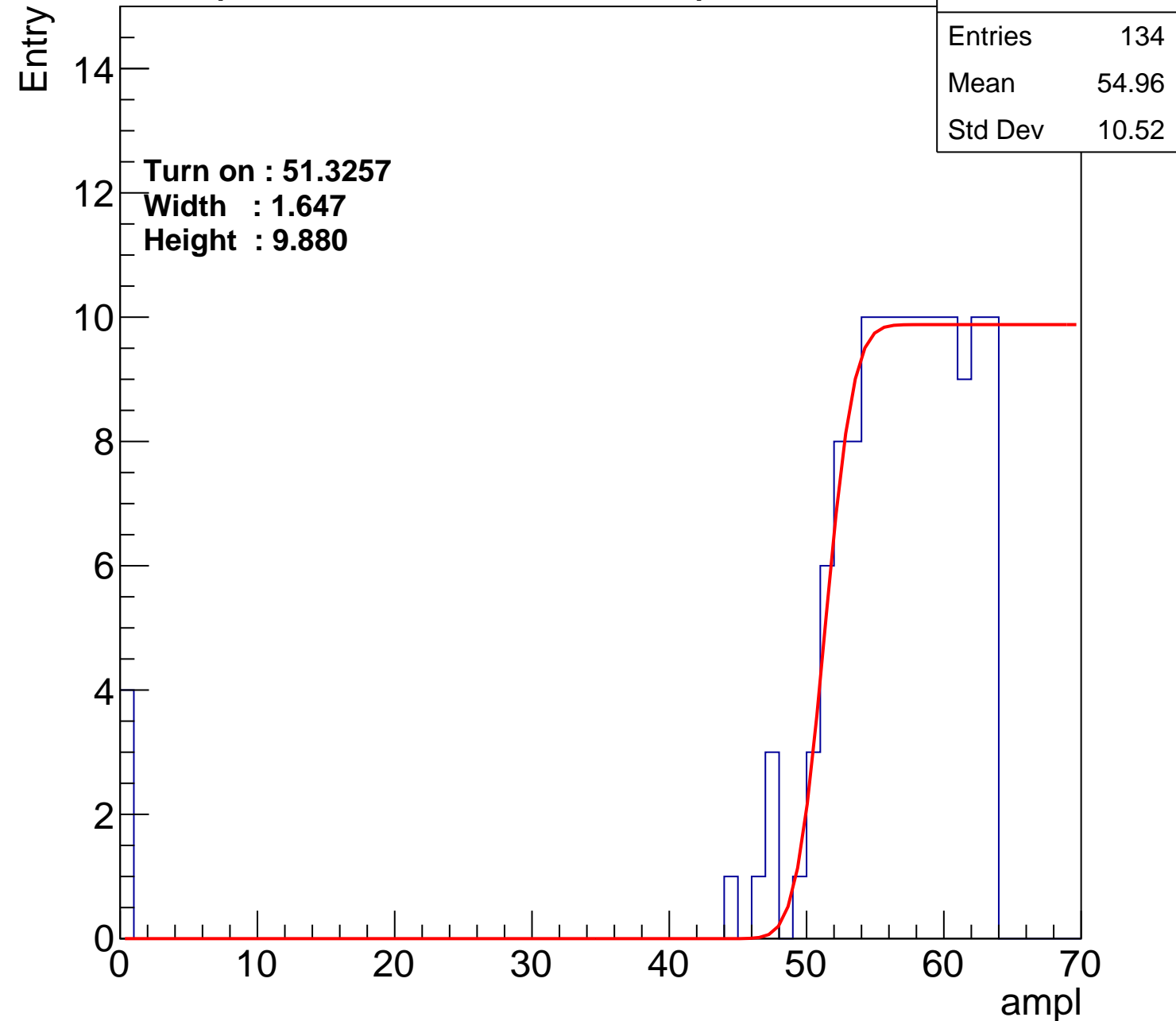
Width : 1.647

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch20

calib_packv5_040323_1717.root, FC#2, port C3

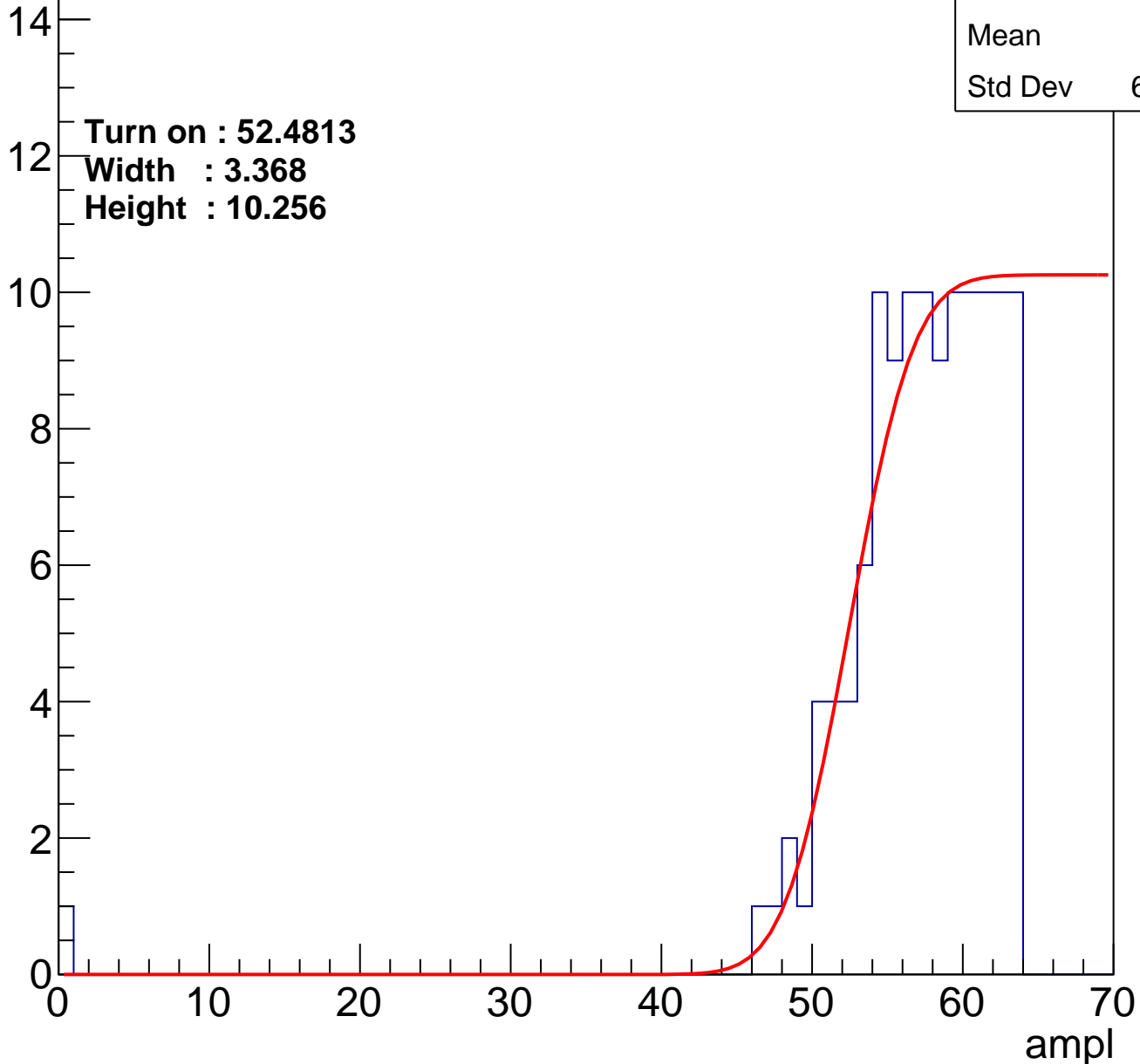
Entries	122
Mean	56.6
Std Dev	6.565

Turn on : 52.4813

Width : 3.368

Height : 10.256

Entry



B0L103S, U11-ch21

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	55.67
Std Dev	11.26

Turn on : 52.9598

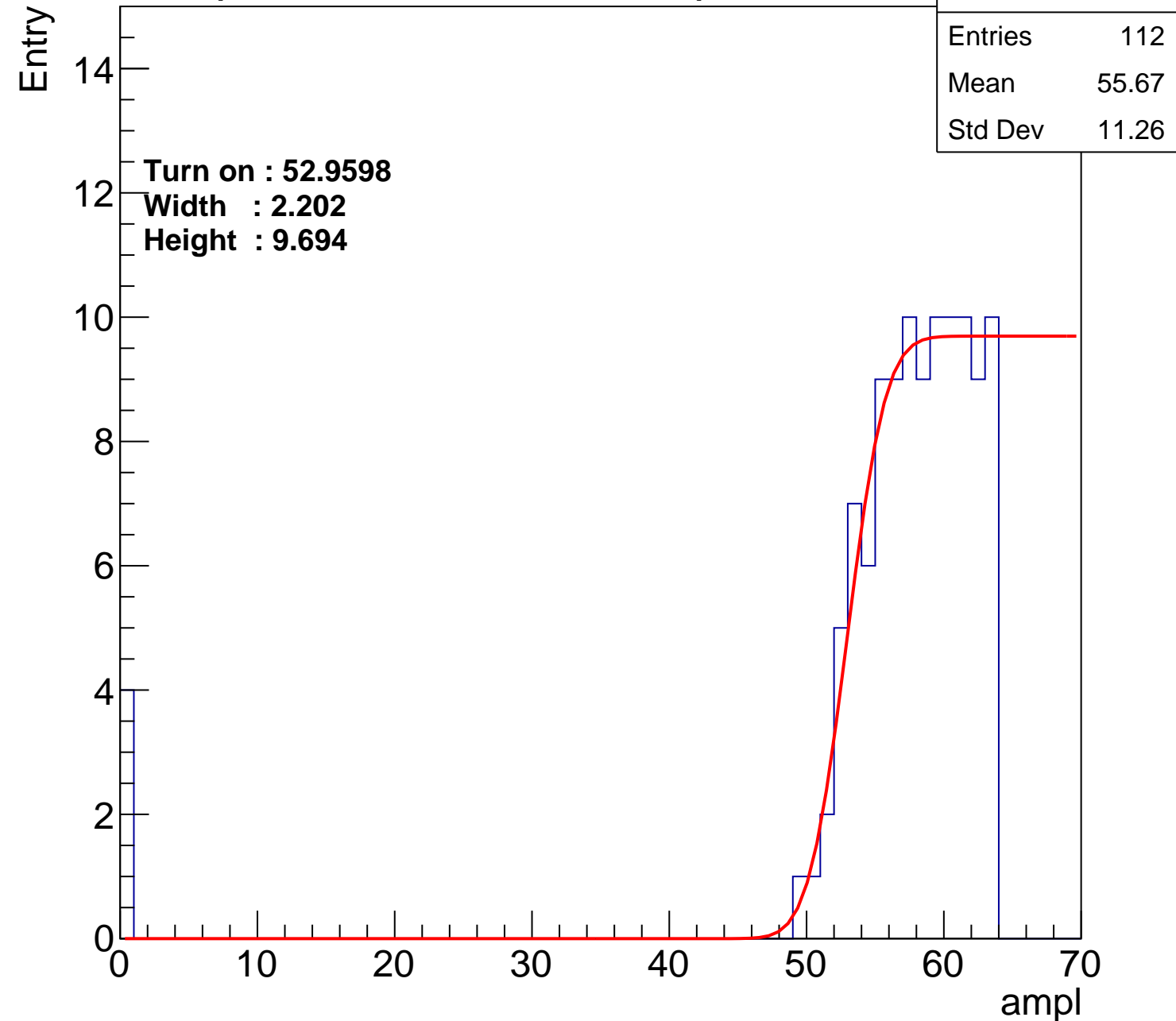
Width : 2.202

Height : 9.694

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch22

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.47
Std Dev	11.44

Turn on : 51.0580

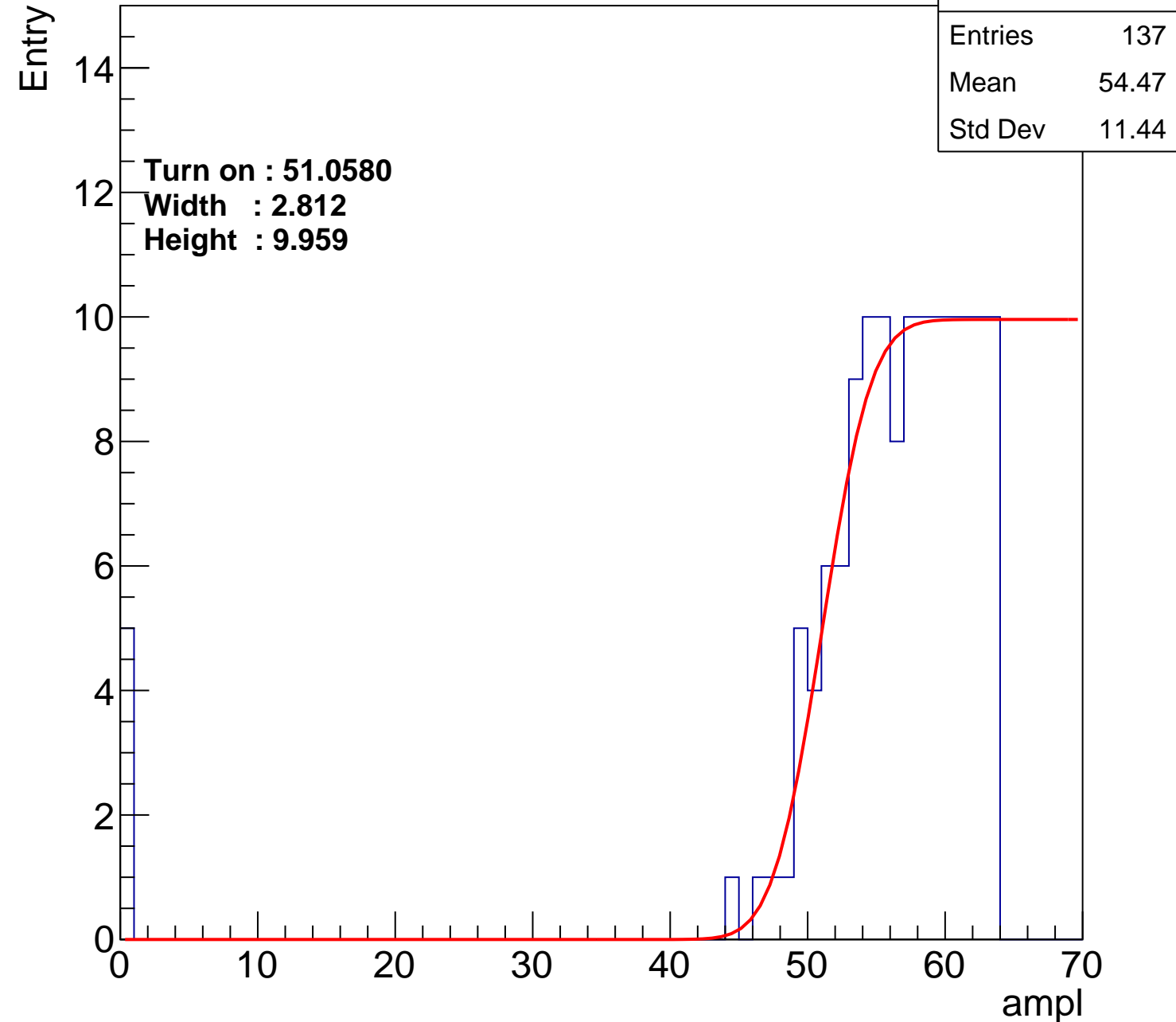
Width : 2.812

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch23

calib_packv5_040323_1717.root, FC#2, port C3

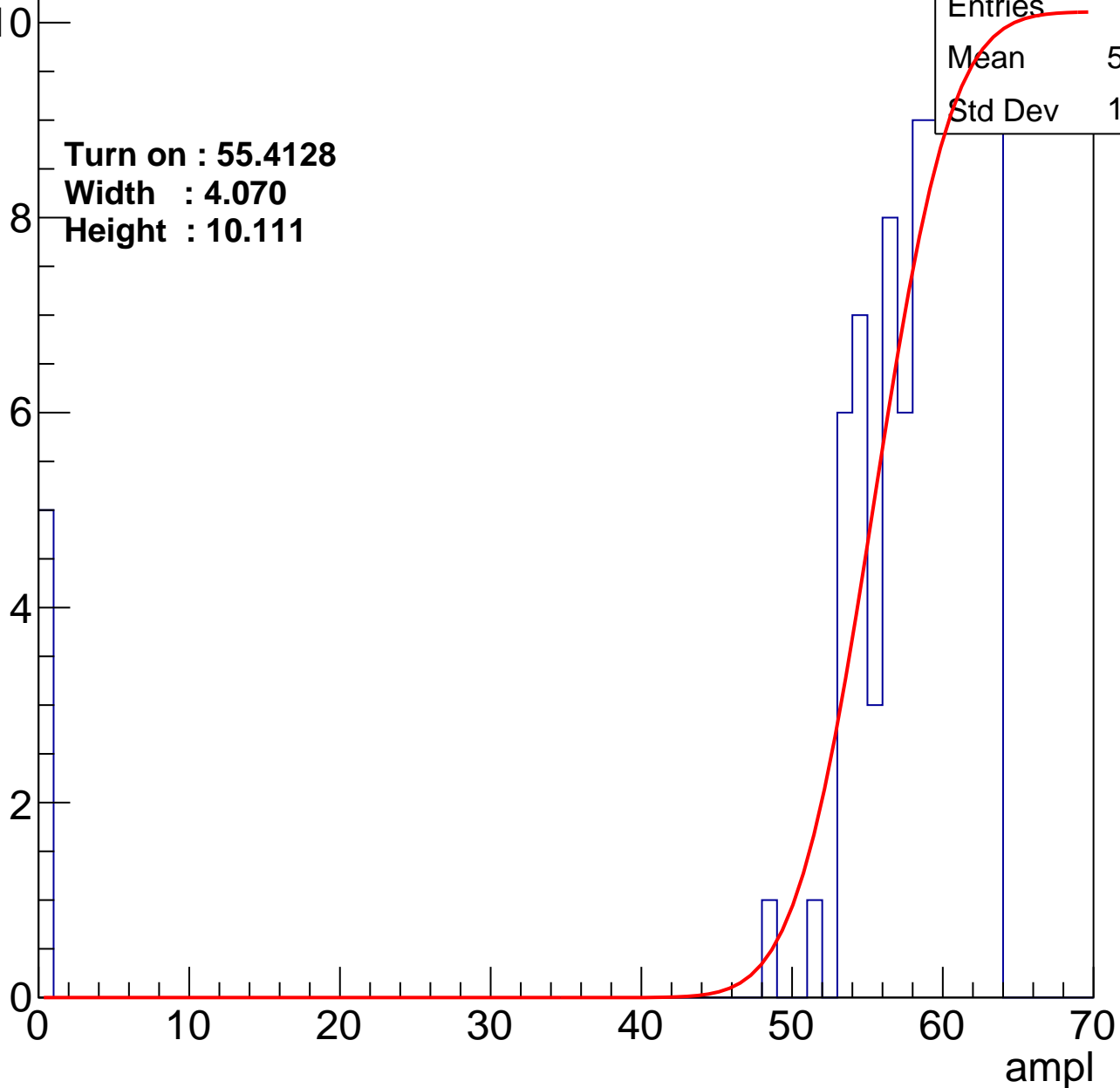
Entry

Entries	93
Mean	55.27
Std Dev	13.57

Turn on : 55.4128

Width : 4.070

Height : 10.111



B0L103S, U11-ch24

calib_packv5_040323_1717.root, FC#2, port C3

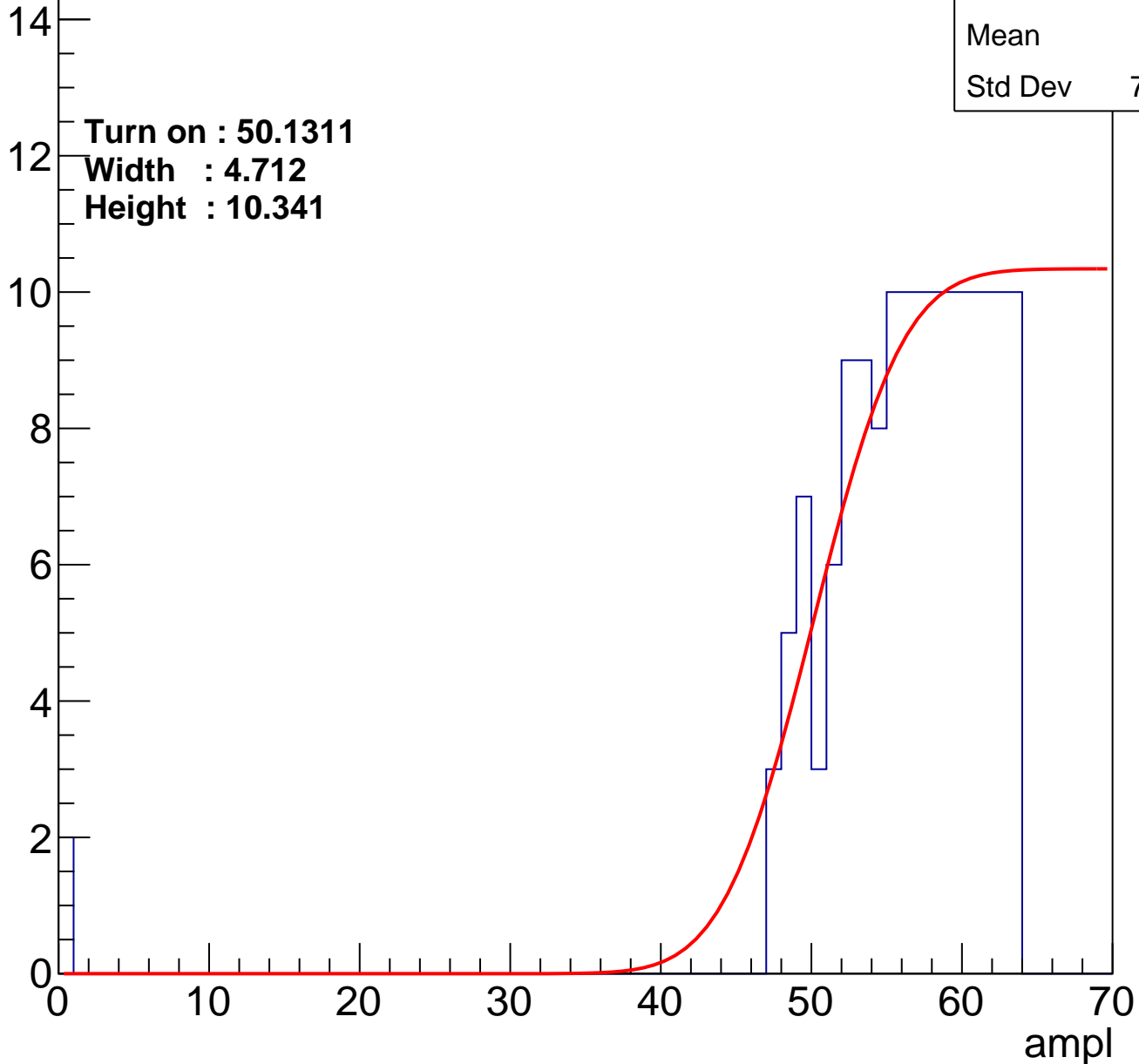
Entries	142
Mean	55.4
Std Dev	7.984

Turn on : 50.1311

Width : 4.712

Height : 10.341

Entry



B0L103S, U11-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.92
Std Dev	8.133

Turn on : 51.8115

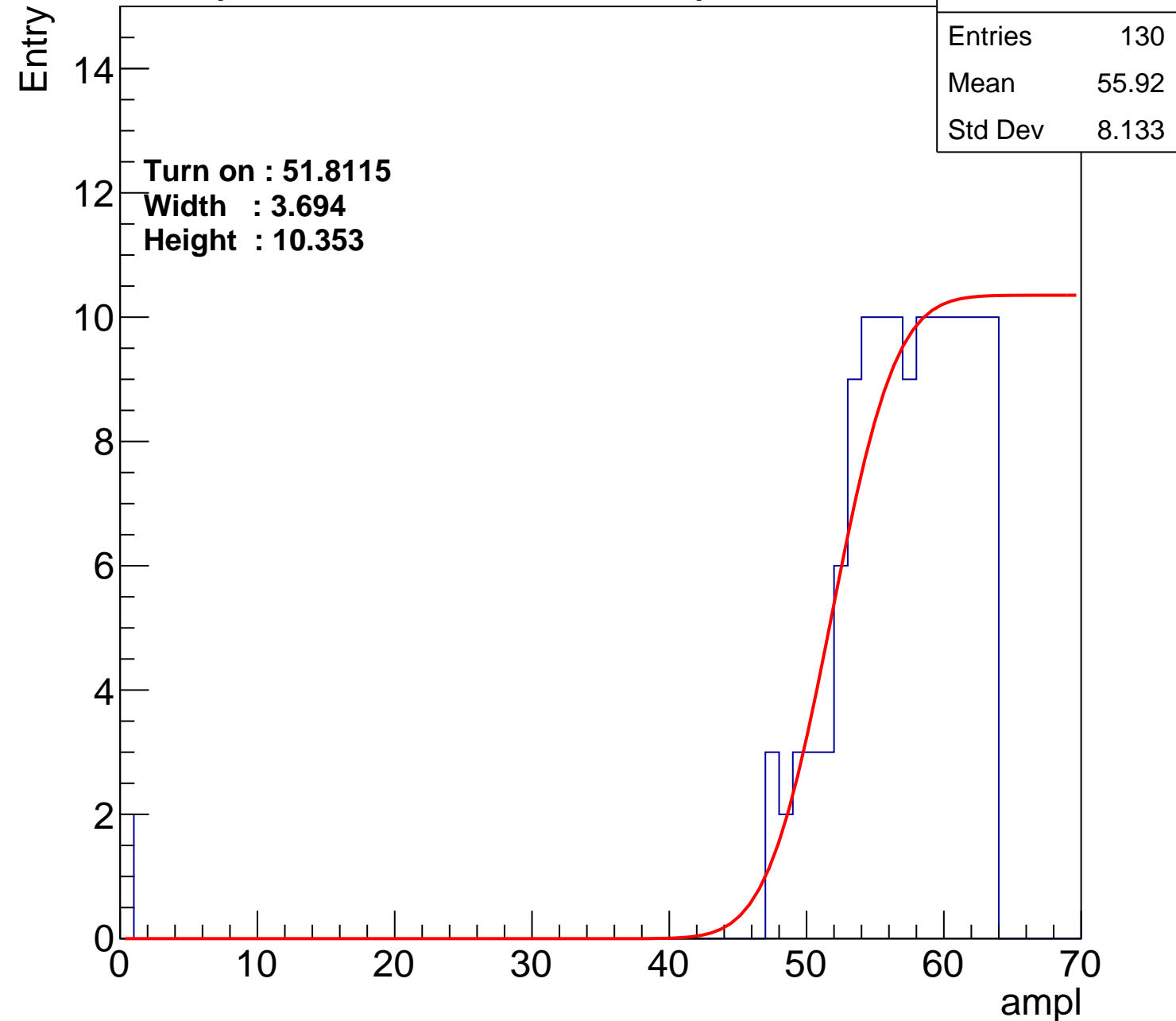
Width : 3.694

Height : 10.353

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.14
Std Dev	10.14

Turn on : 53.6795

Width : 3.869

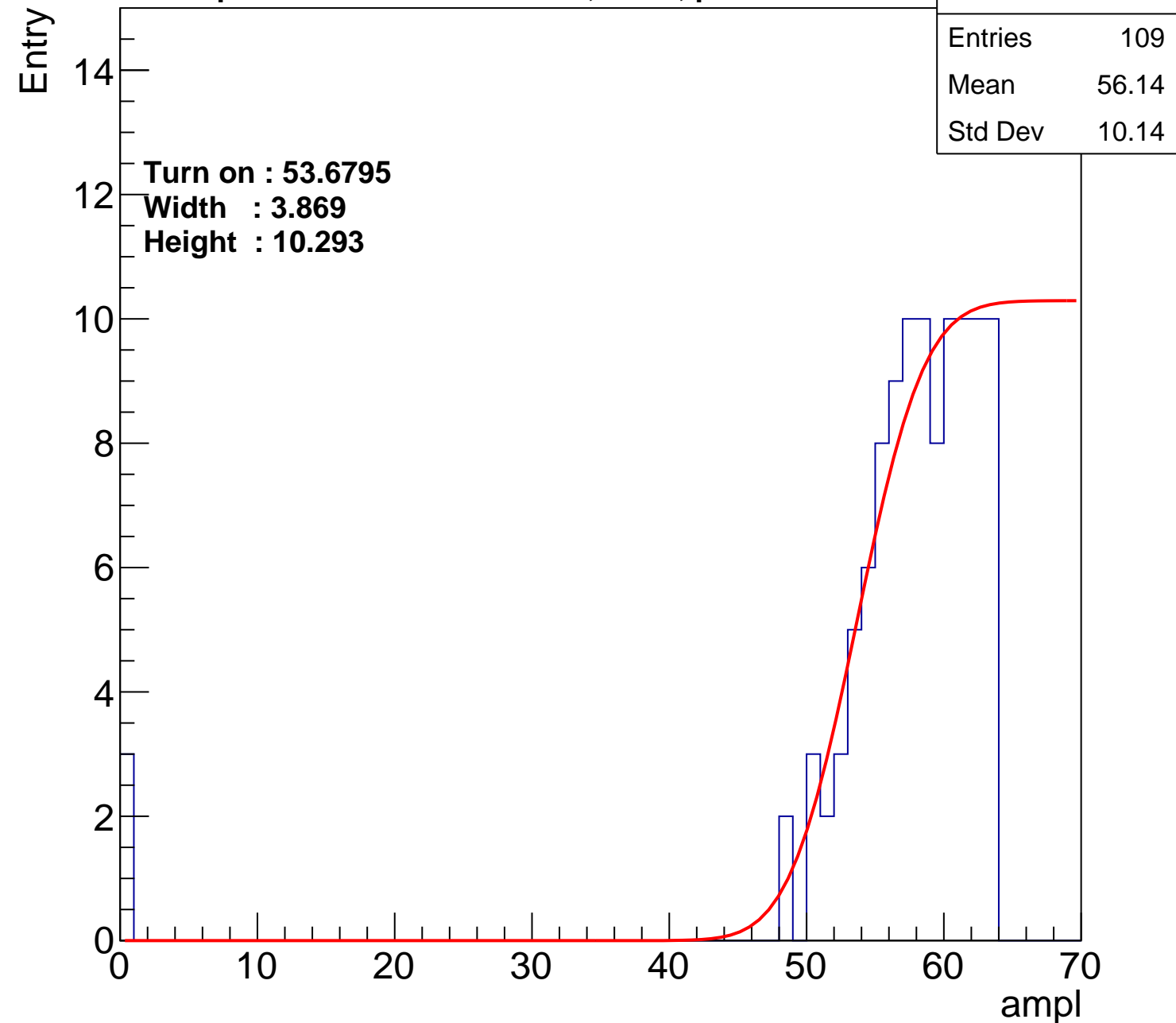
Height : 10.293

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	104
Mean	55.95
Std Dev	11.68

Turn on : 54.2252

Width : 4.401

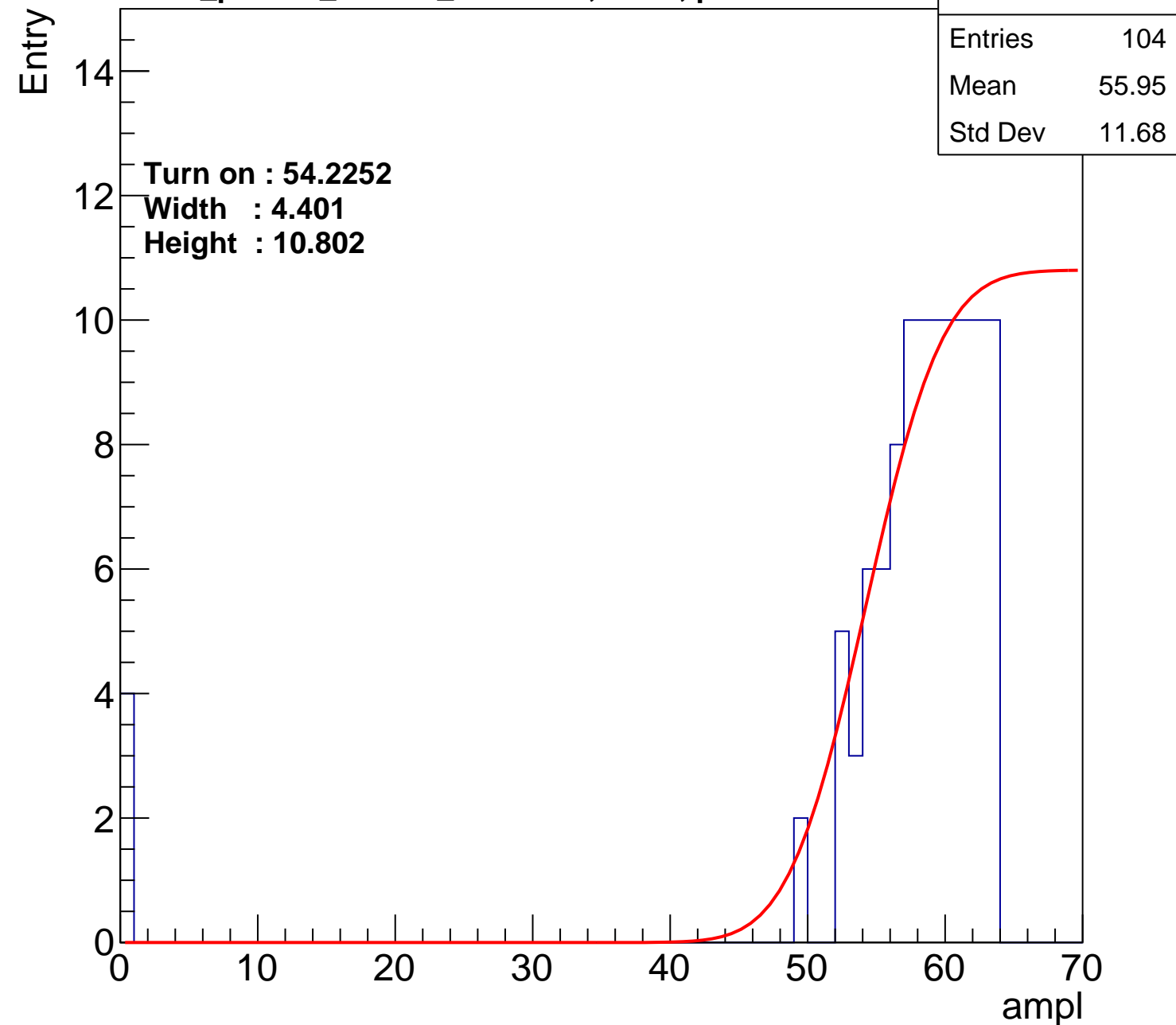
Height : 10.802

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch28

calib_packv5_040323_1717.root, FC#2, port C3

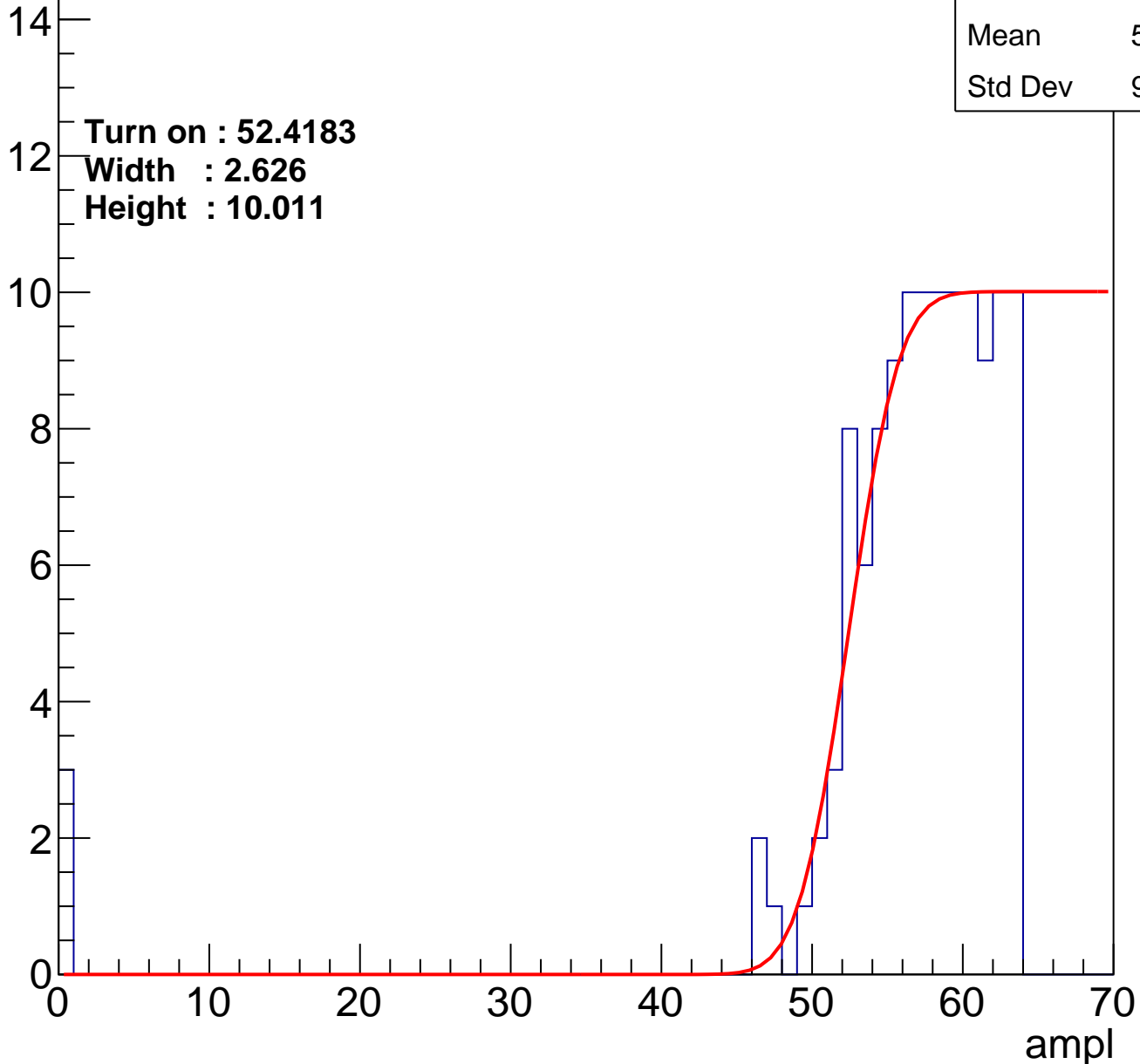
Entries	122
Mean	55.75
Std Dev	9.698

Turn on : 52.4183

Width : 2.626

Height : 10.011

Entry



B0L103S, U11-ch29

calib_packv5_040323_1717.root, FC#2, port C3

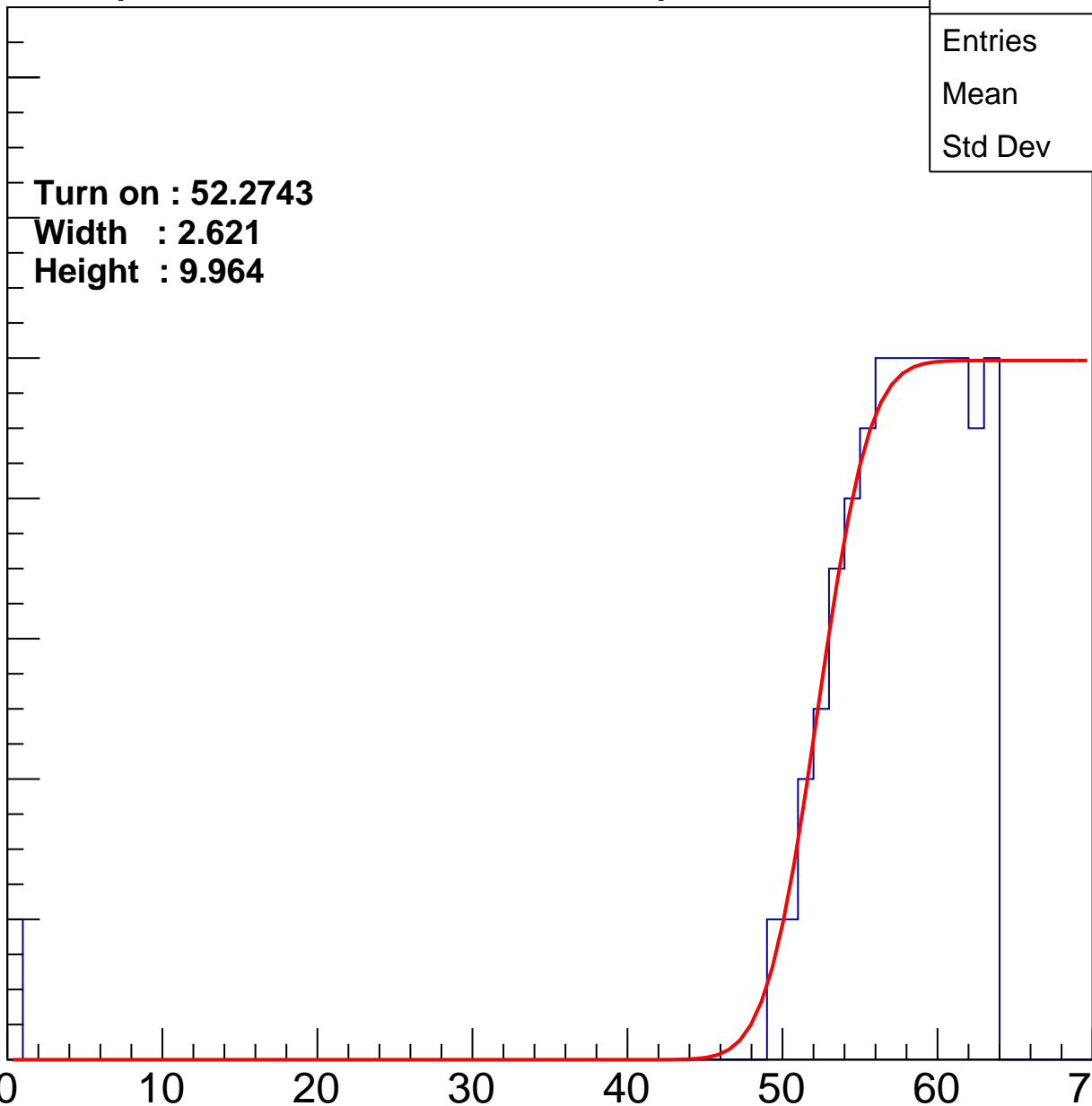
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2743
Width : 2.621
Height : 9.964

Entries	118
Mean	56.42
Std Dev	8.269

ampl



B0L103S, U11-ch30

calib_packv5_040323_1717.root, FC#2, port C3

Entries	107
Mean	57.03
Std Dev	8.502

Turn on : 53.0431

Width : 0.437

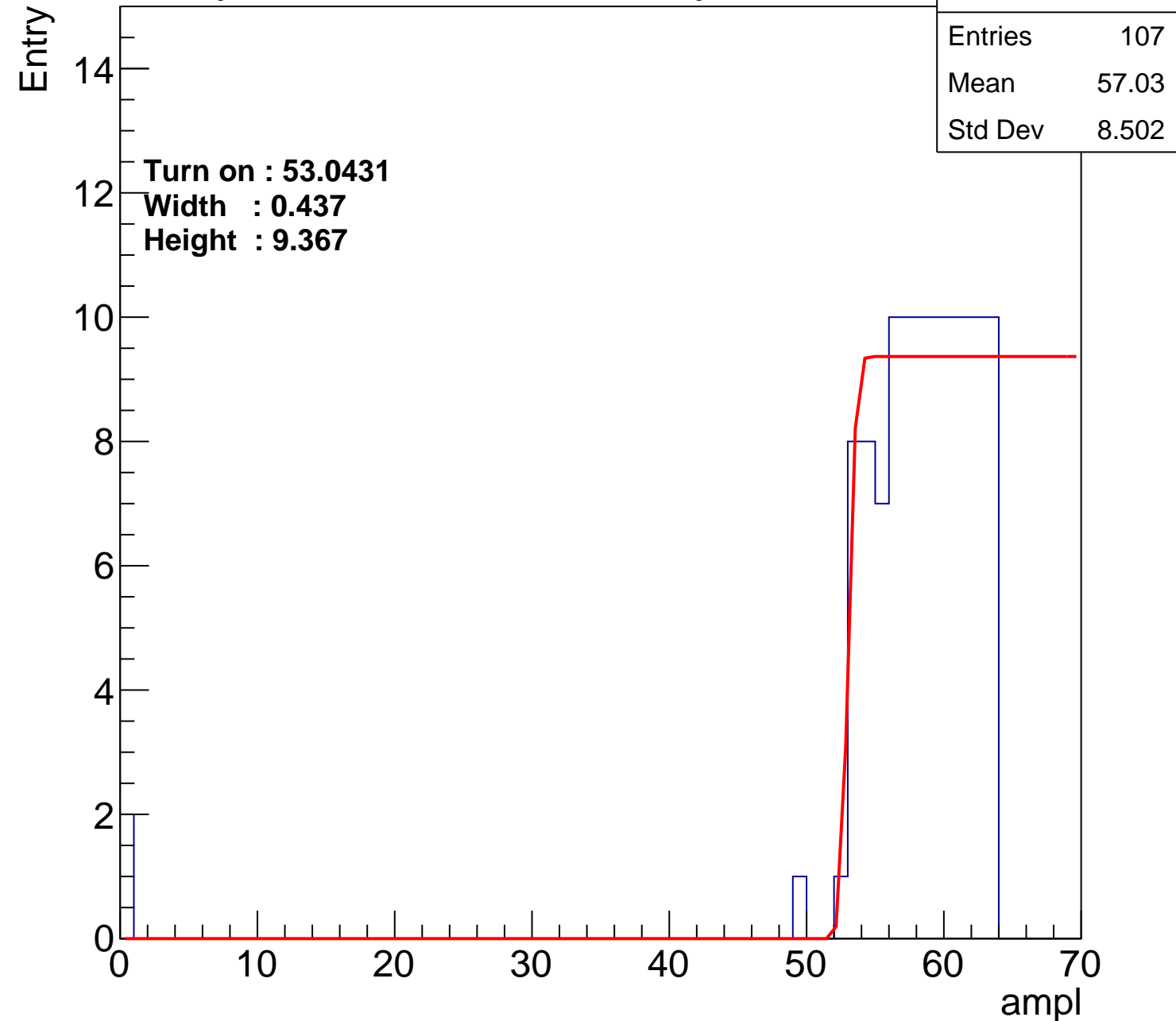
Height : 9.367

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch31

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.7
Std Dev	11.67

Turn on : 51.3437

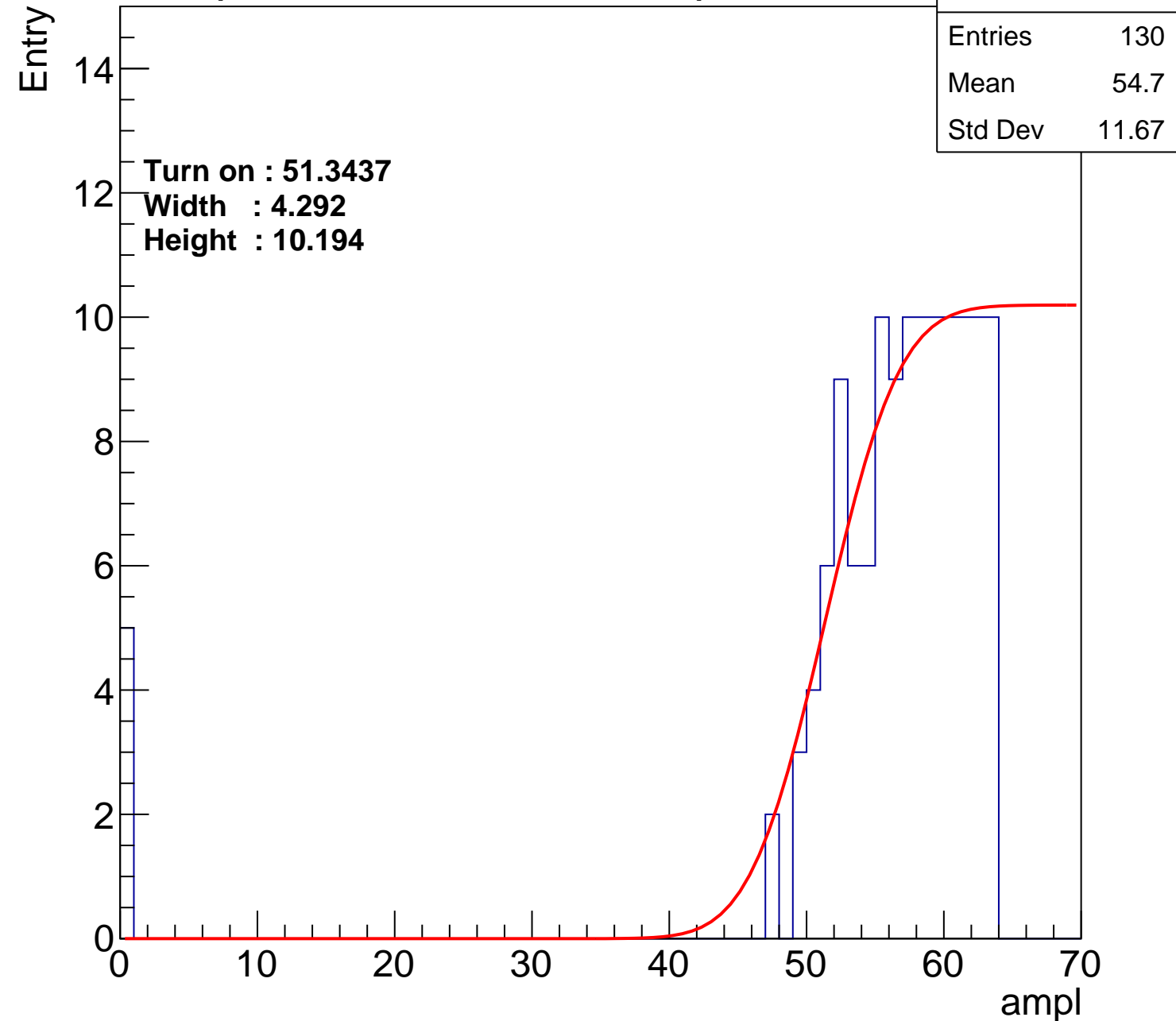
Width : 4.292

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.48
Std Dev	9.665

Turn on : 50.8720

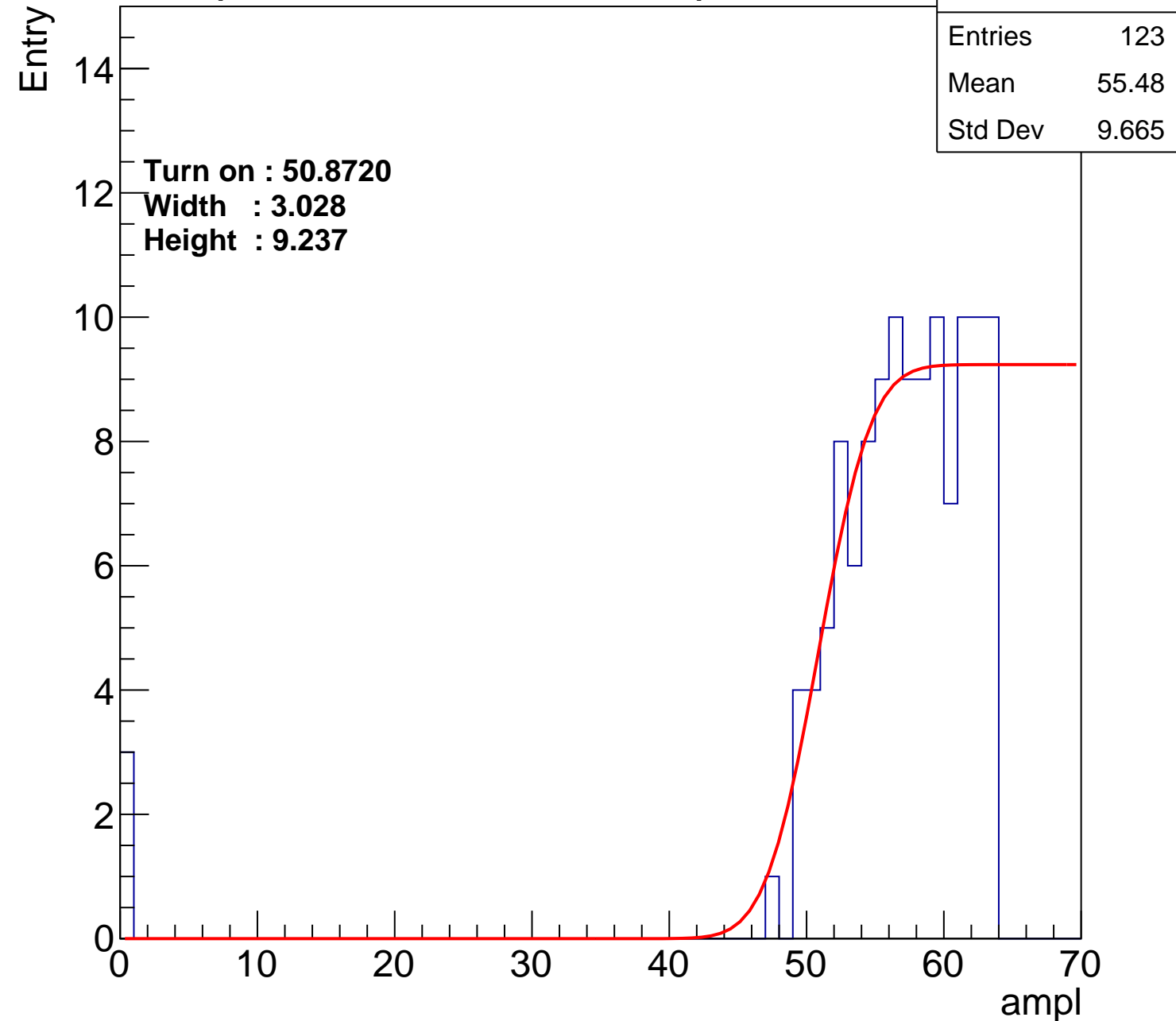
Width : 3.028

Height : 9.237

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch33

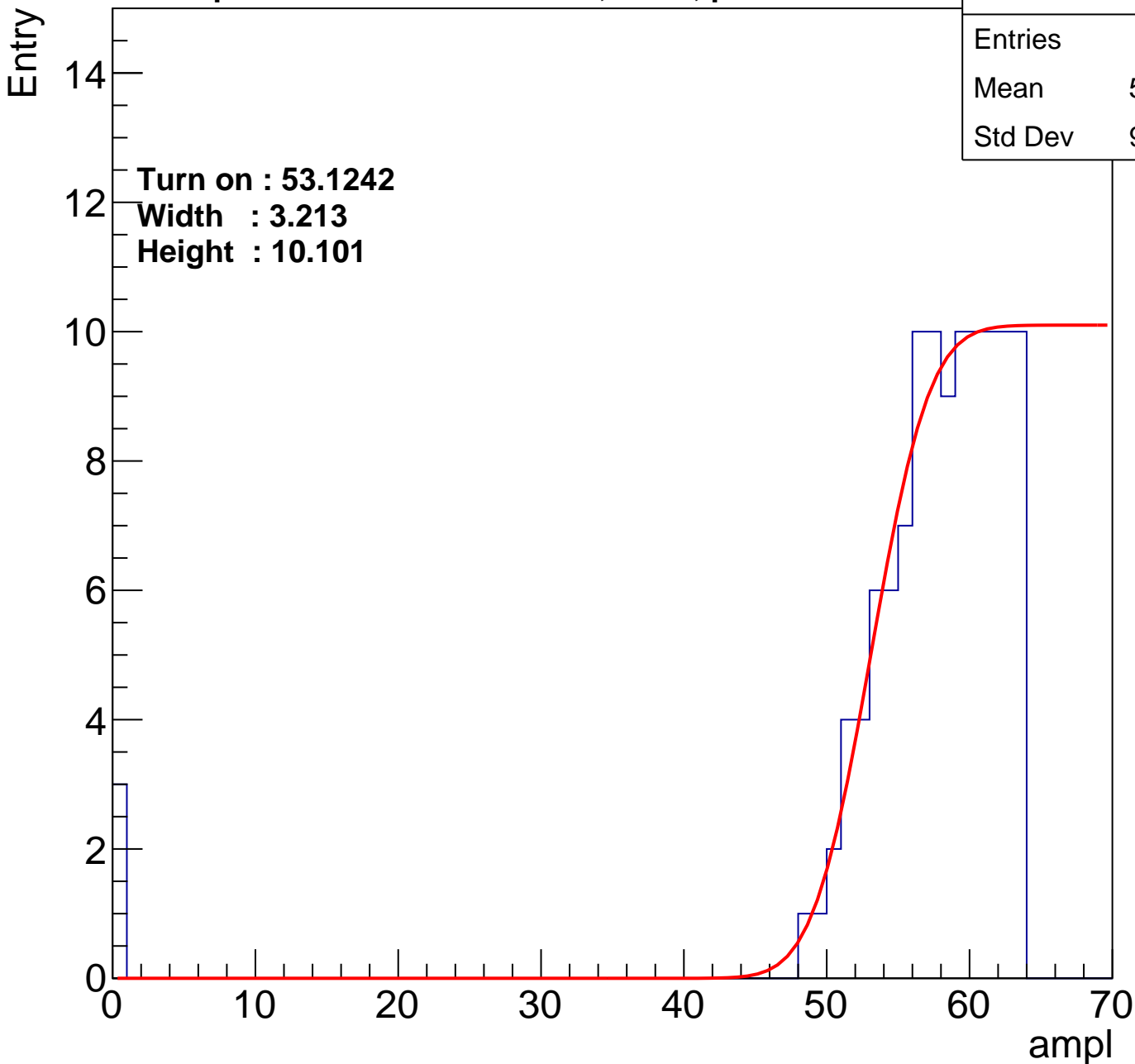
calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	56.09
Std Dev	9.972

Turn on : 53.1242

Width : 3.213

Height : 10.101



B0L103S, U11-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	53.93
Std Dev	13.8

Turn on : 52.1787

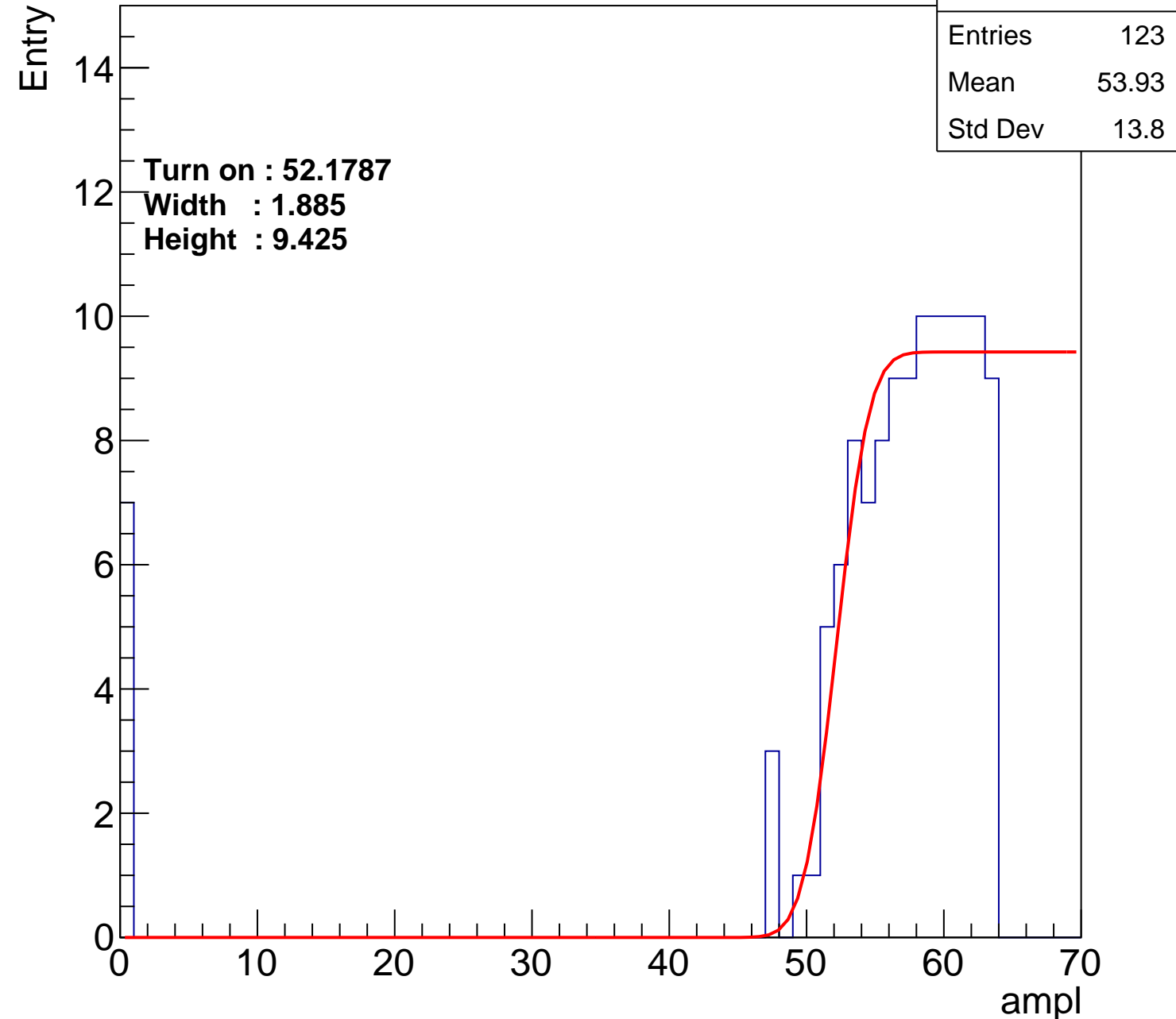
Width : 1.885

Height : 9.425

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch35

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	55.54
Std Dev	10.96

Turn on : 52.7728

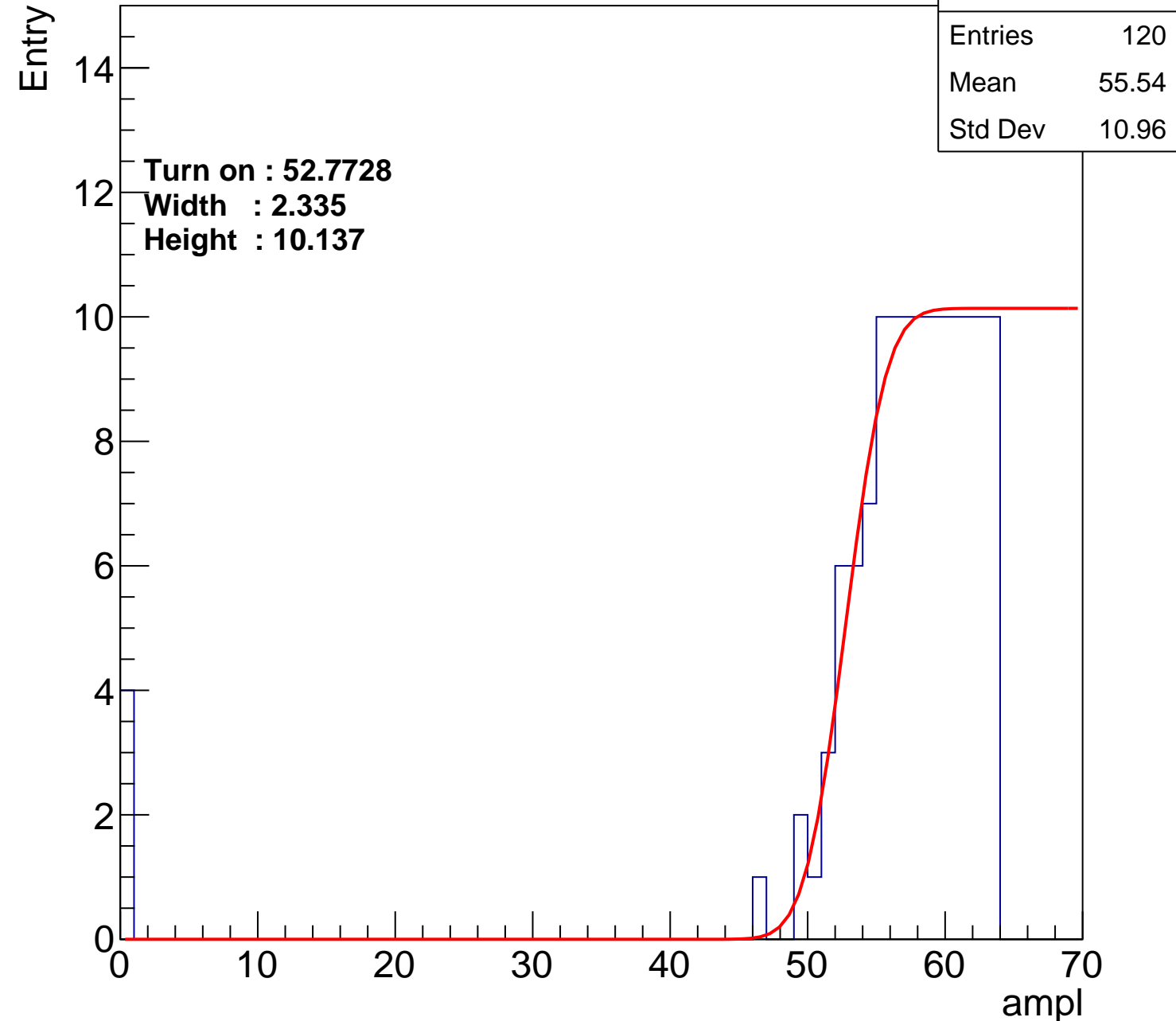
Width : 2.335

Height : 10.137

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch36

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.55
Std Dev	7.979

Turn on : 50.4708

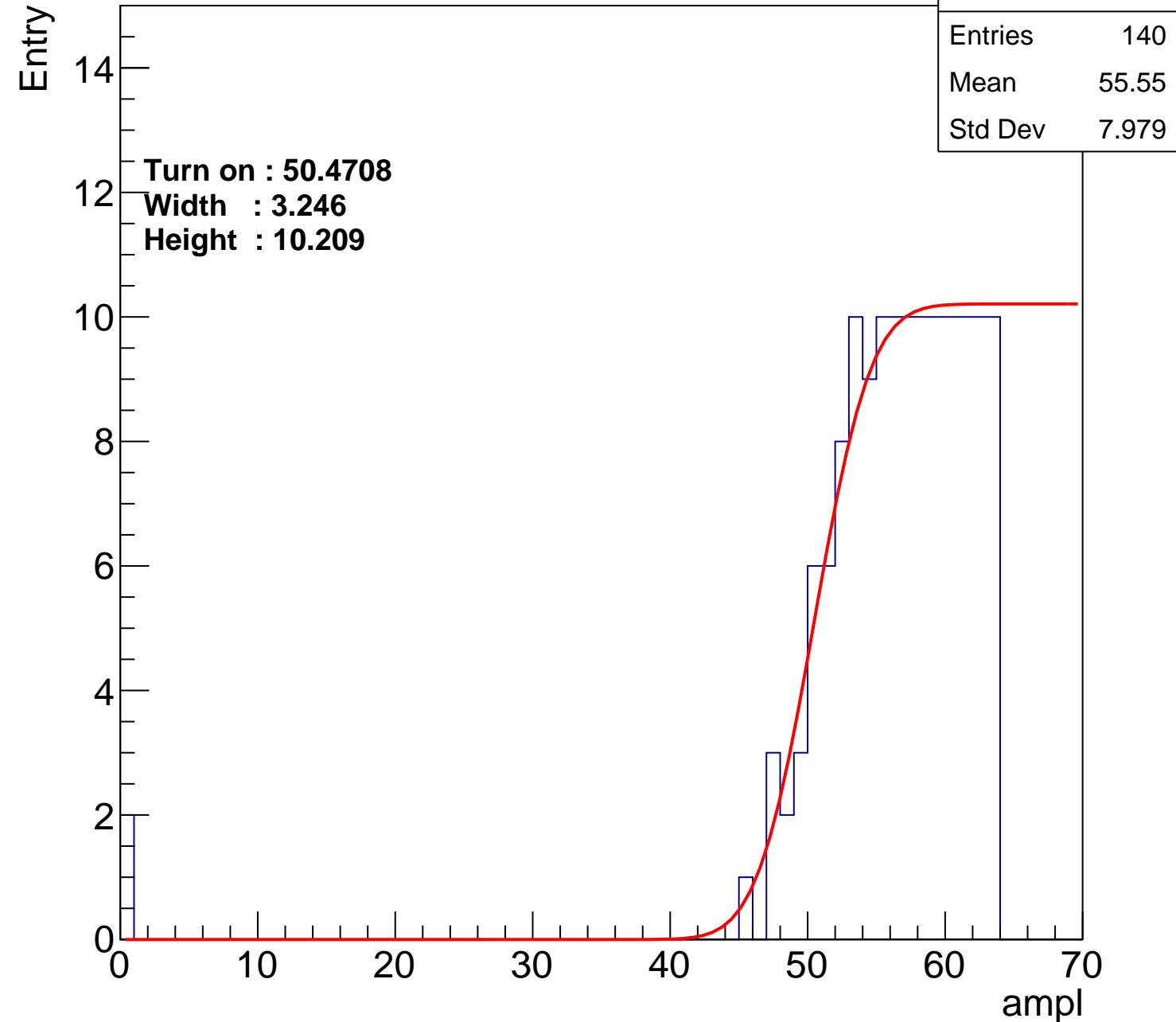
Width : 3.246

Height : 10.209

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch37

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	54.88
Std Dev	9.092

Turn on : 49.3819

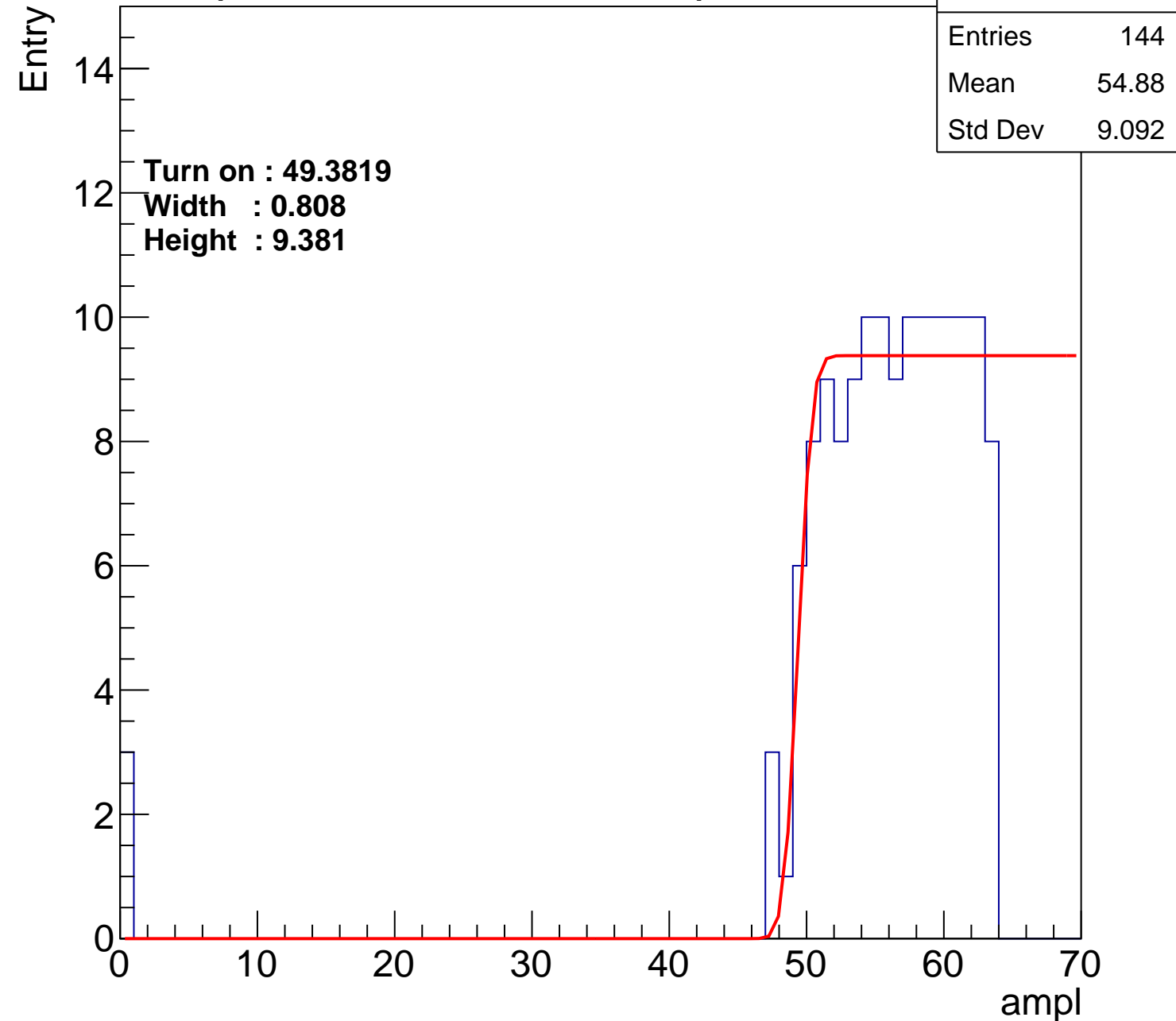
Width : 0.808

Height : 9.381

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch38

calib_packv5_040323_1717.root, FC#2, port C3

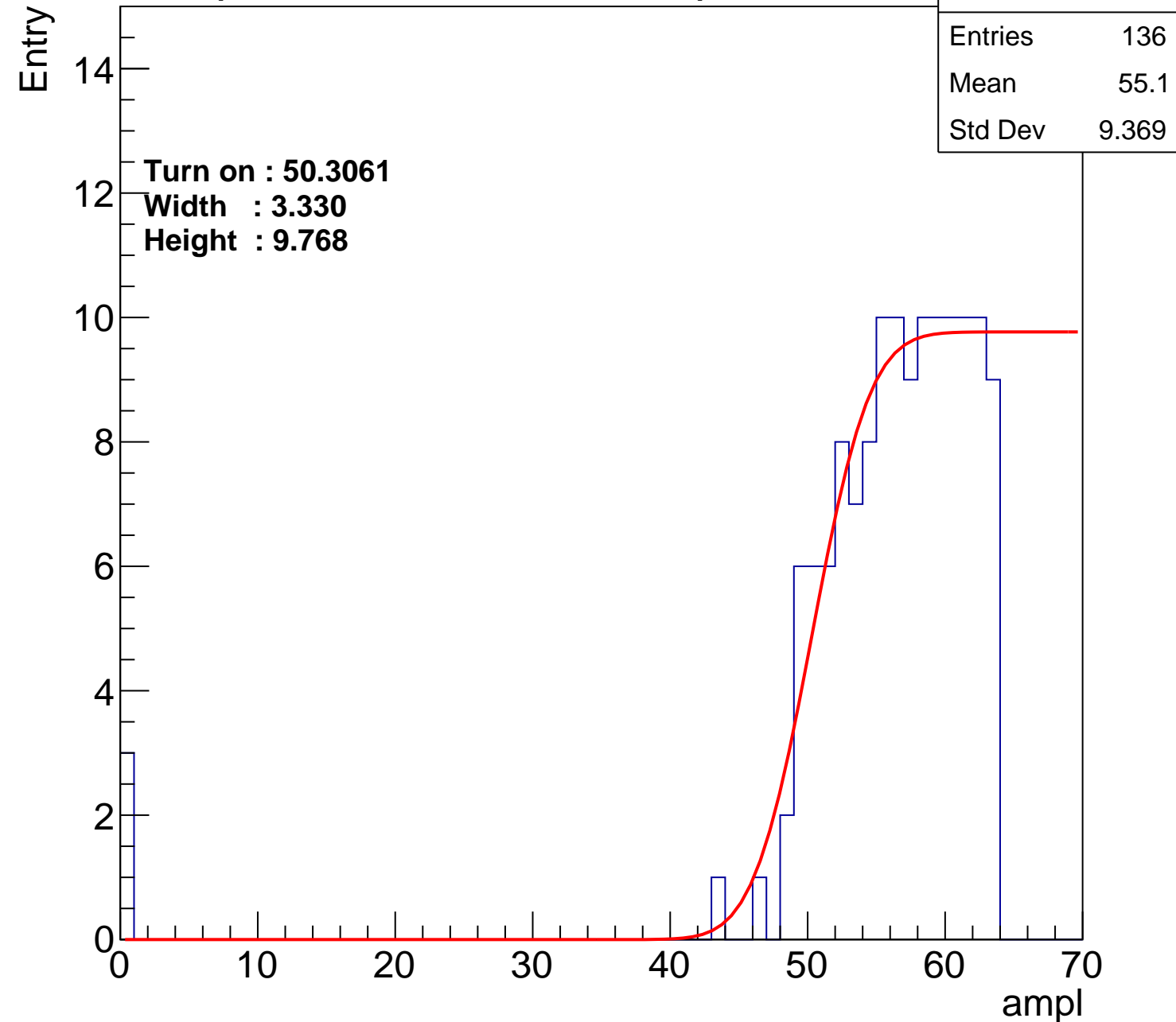
Entry

14
12
10
8
6
4
2
0

Turn on : 50.3061
Width : 3.330
Height : 9.768

Entries	136
Mean	55.1
Std Dev	9.369

ampl



B0L103S, U11-ch39

calib_packv5_040323_1717.root, FC#2, port C3

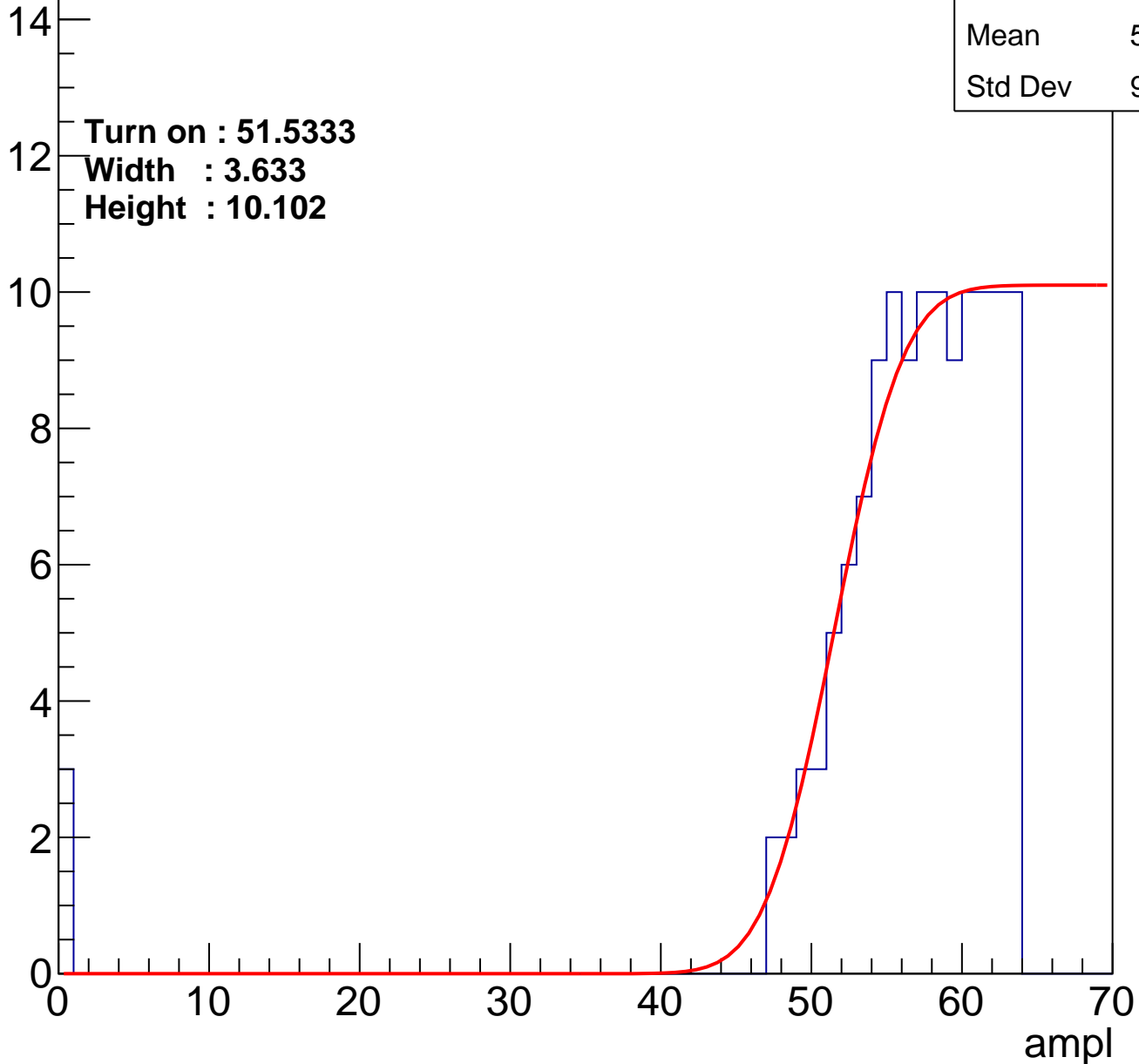
Entries	128
Mean	55.52
Std Dev	9.538

Turn on : 51.5333

Width : 3.633

Height : 10.102

Entry



B0L103S, U11-ch40

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	53.9
Std Dev	13.38

Turn on : 51.8667

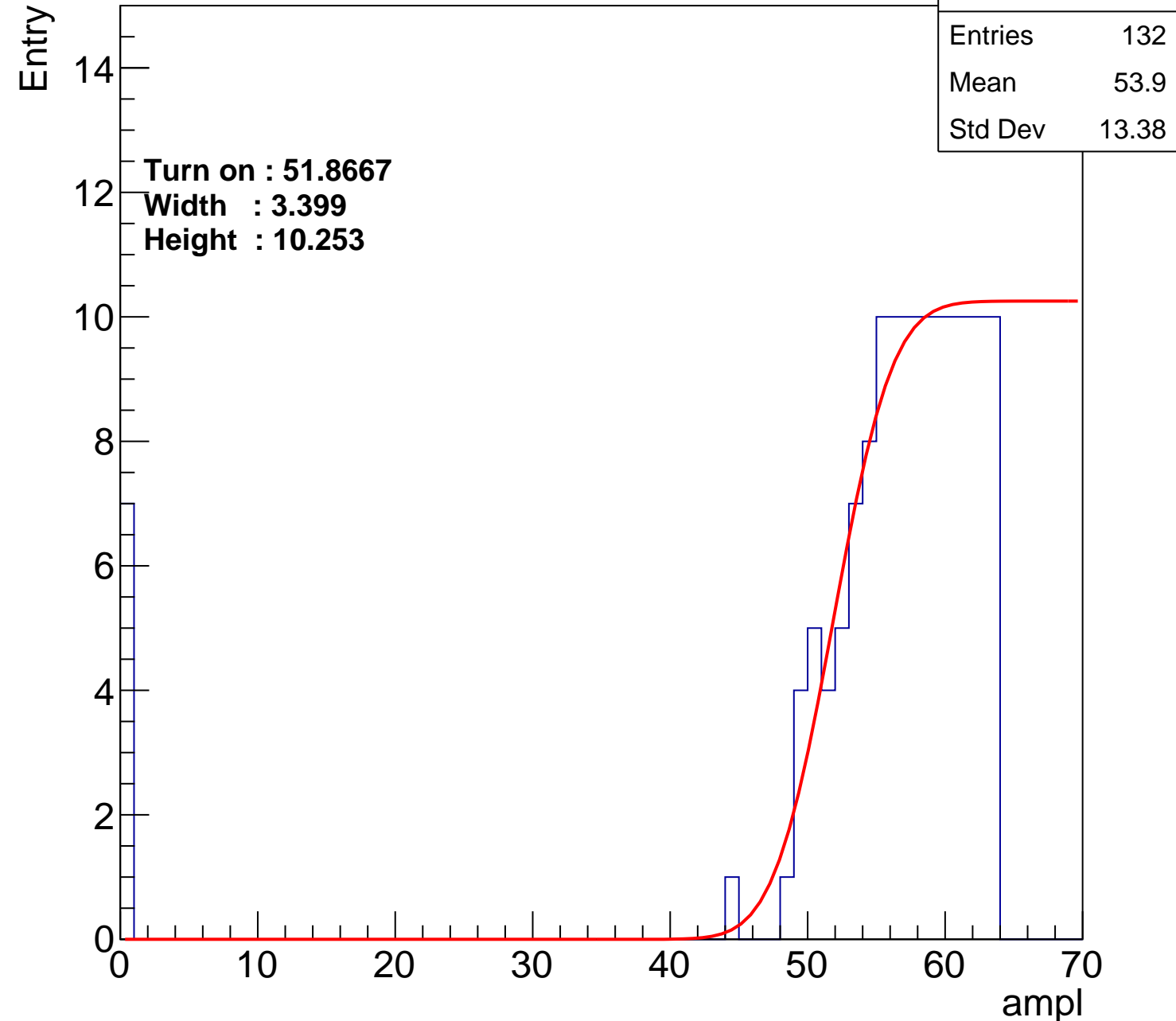
Width : 3.399

Height : 10.253

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch41

calib_packv5_040323_1717.root, FC#2, port C3

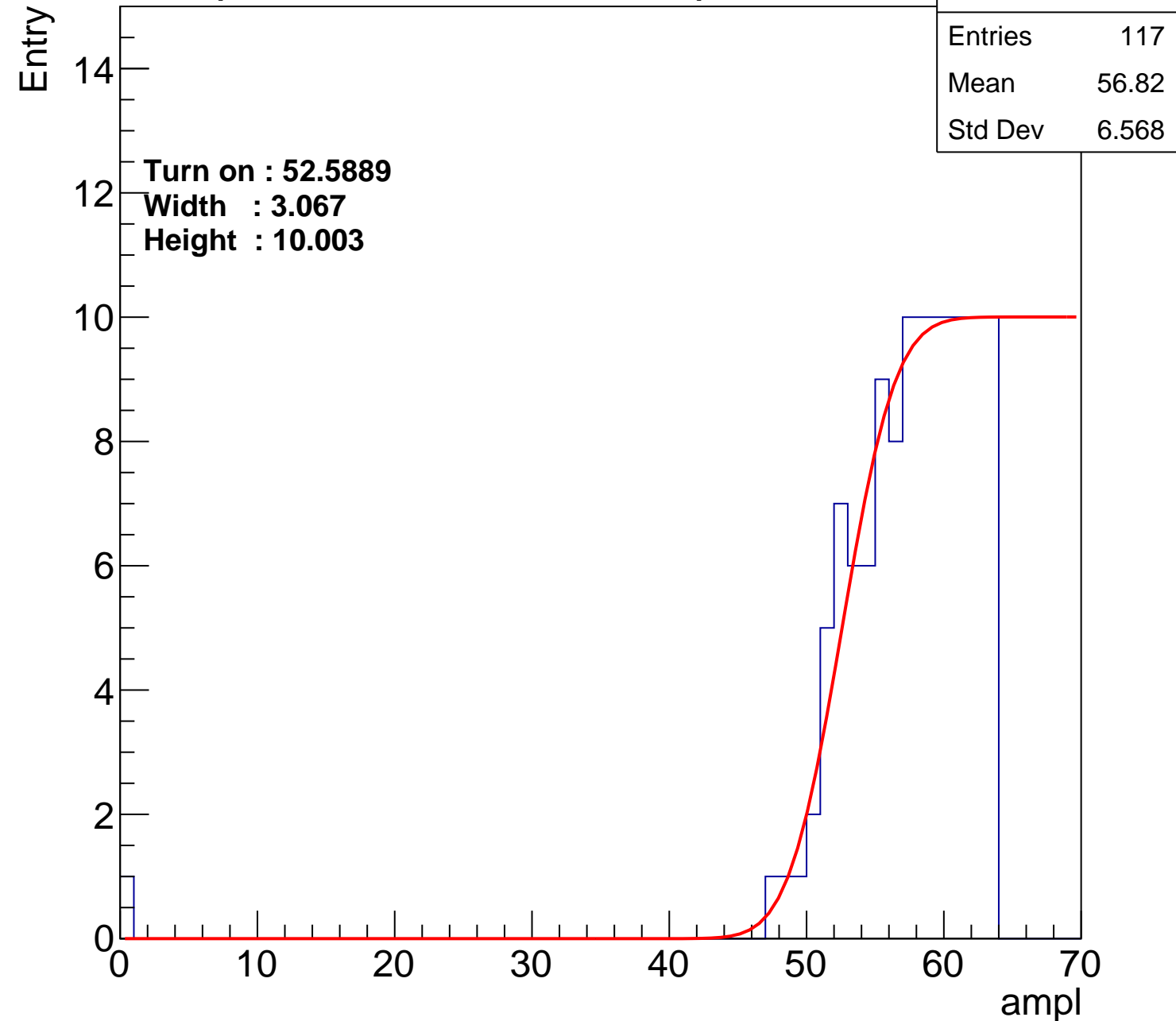
Entry

14
12
10
8
6
4
2
0

Turn on : 52.5889
Width : 3.067
Height : 10.003

Entries	117
Mean	56.82
Std Dev	6.568

ampl



B0L103S, U11-ch42

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.09
Std Dev	9.83

Turn on : 52.9826

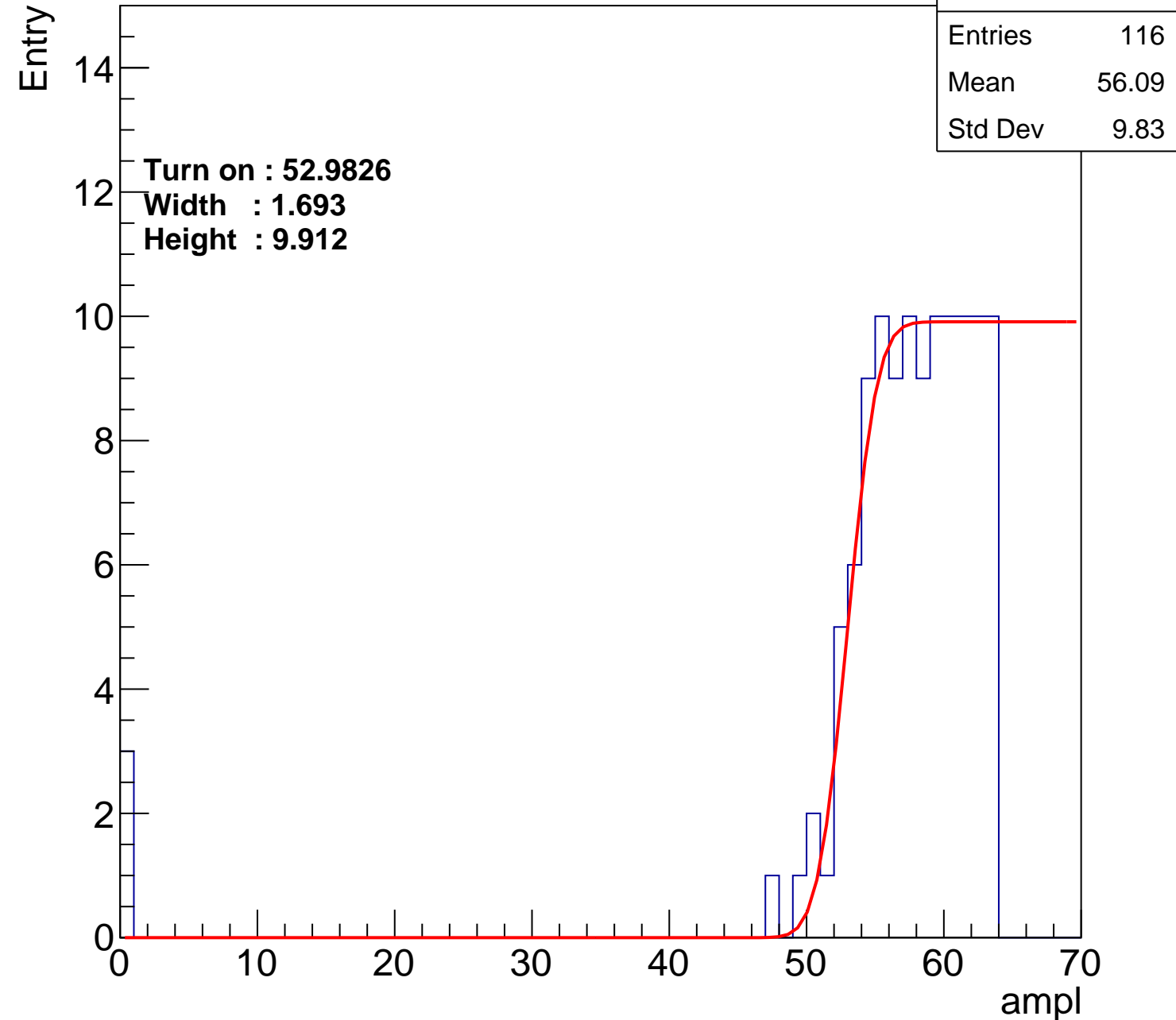
Width : 1.693

Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch43

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.01
Std Dev	9.753

Turn on : 52.2127

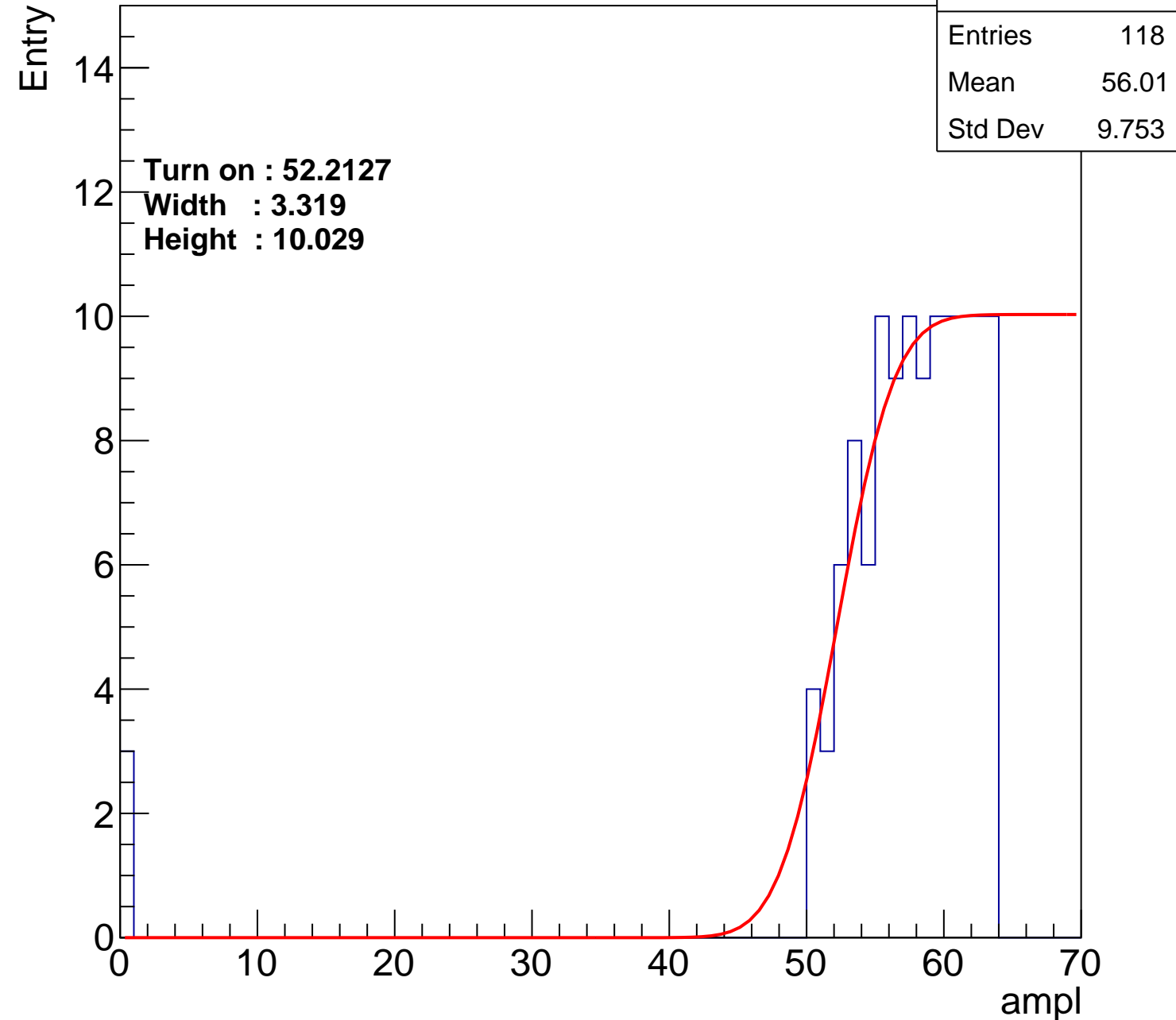
Width : 3.319

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch44

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.67
Std Dev	8.215

Turn on : 51.1355

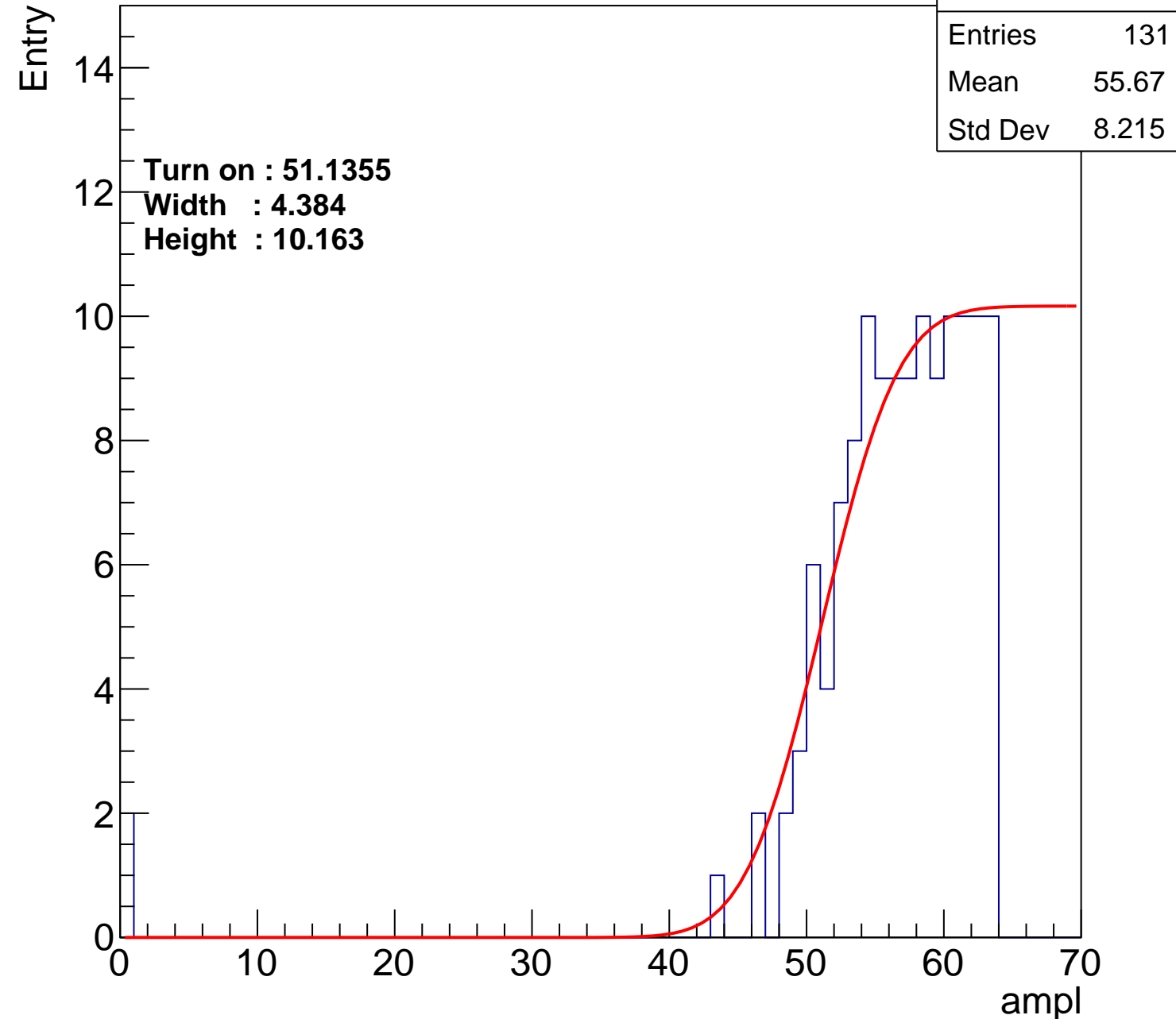
Width : 4.384

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch45

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.26
Std Dev	9.34

Turn on : 51.1050

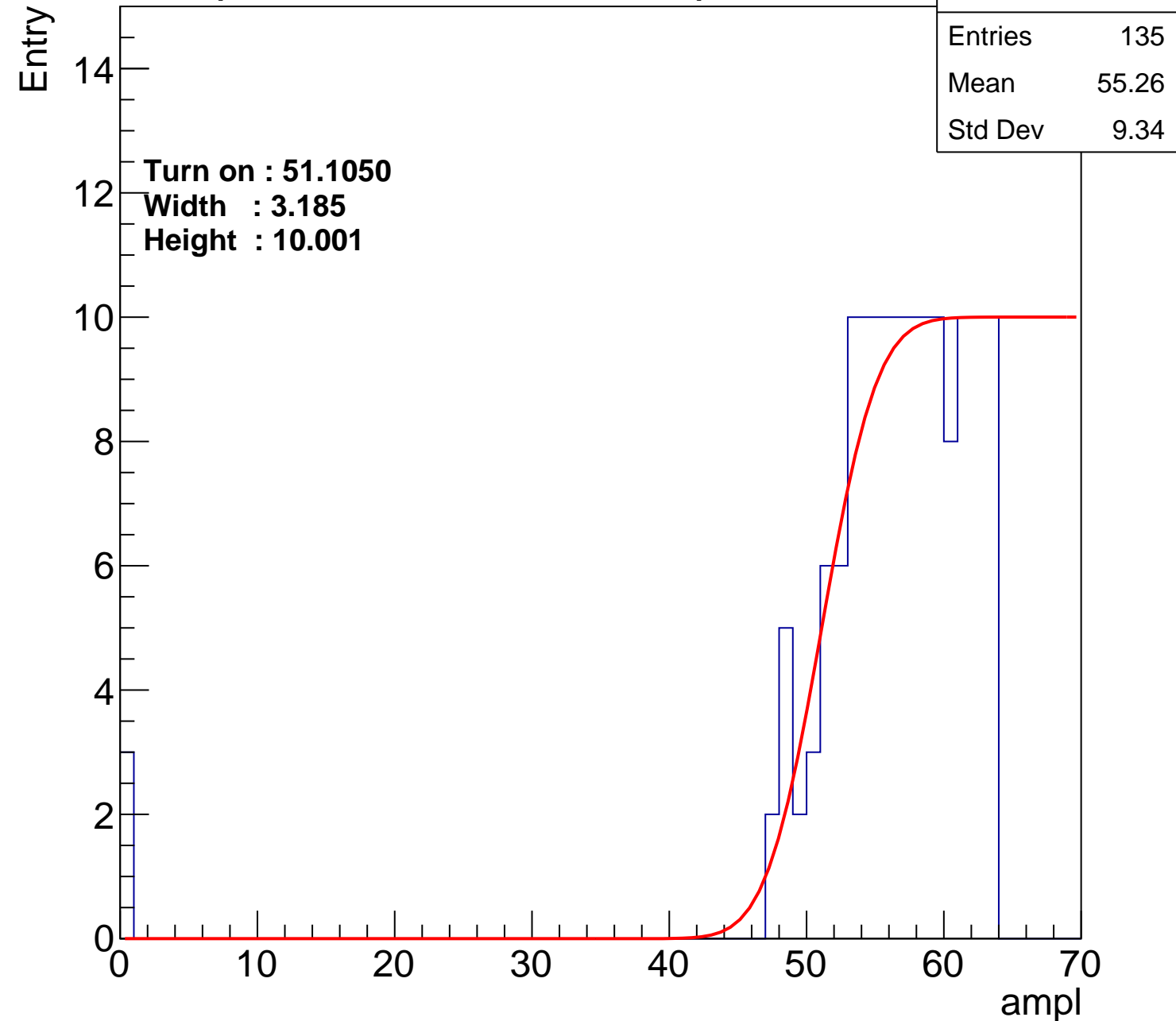
Width : 3.185

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch46

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.38
Std Dev	8.61

Turn on : 53.3452

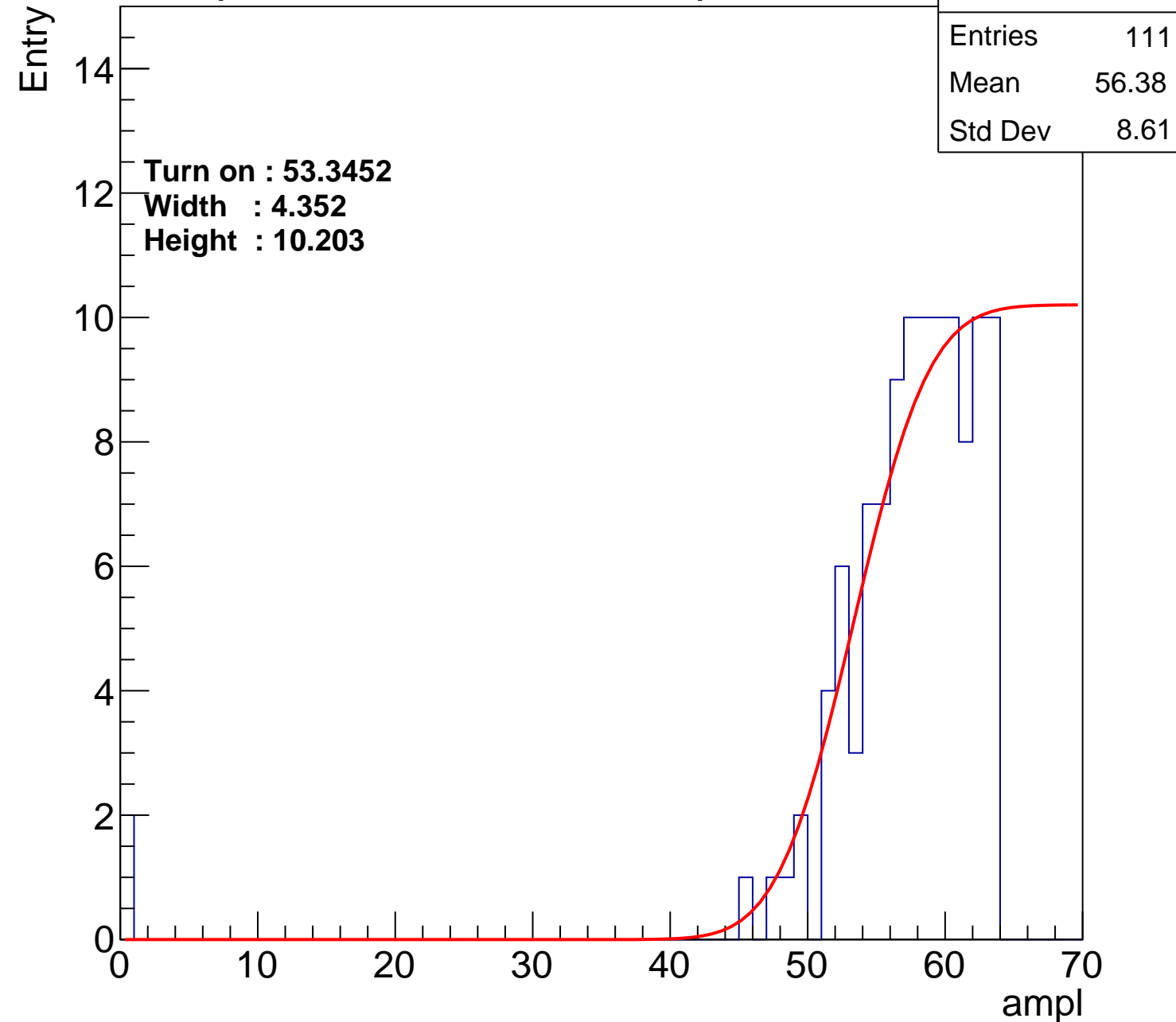
Width : 4.352

Height : 10.203

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch47

calib_packv5_040323_1717.root, FC#2, port C3

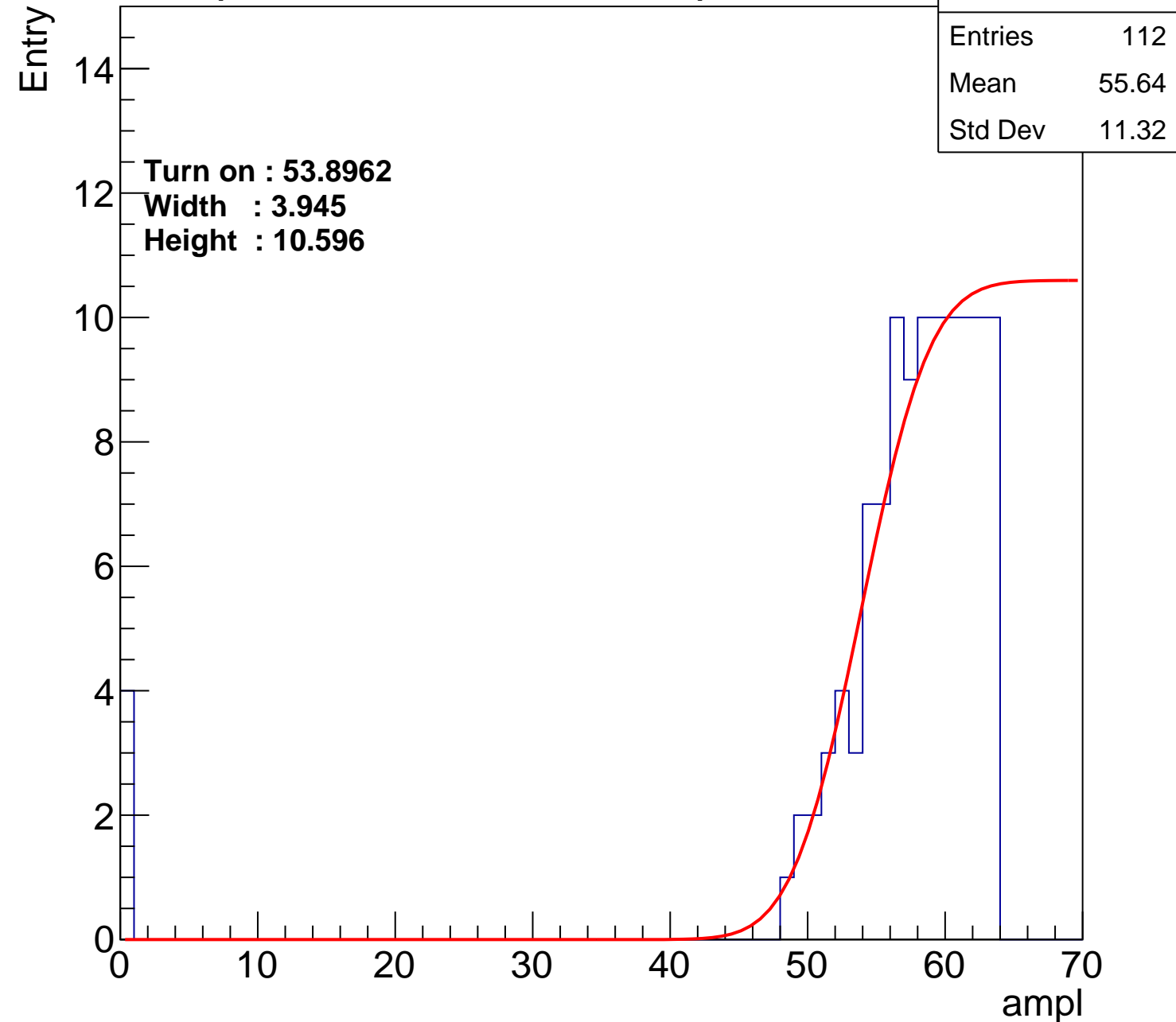
Entry

14
12
10
8
6
4
2
0

Turn on : 53.8962
Width : 3.945
Height : 10.596

Entries	112
Mean	55.64
Std Dev	11.32

ampl



B0L103S, U11-ch48

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.92
Std Dev	9.802

Turn on : 52.9214

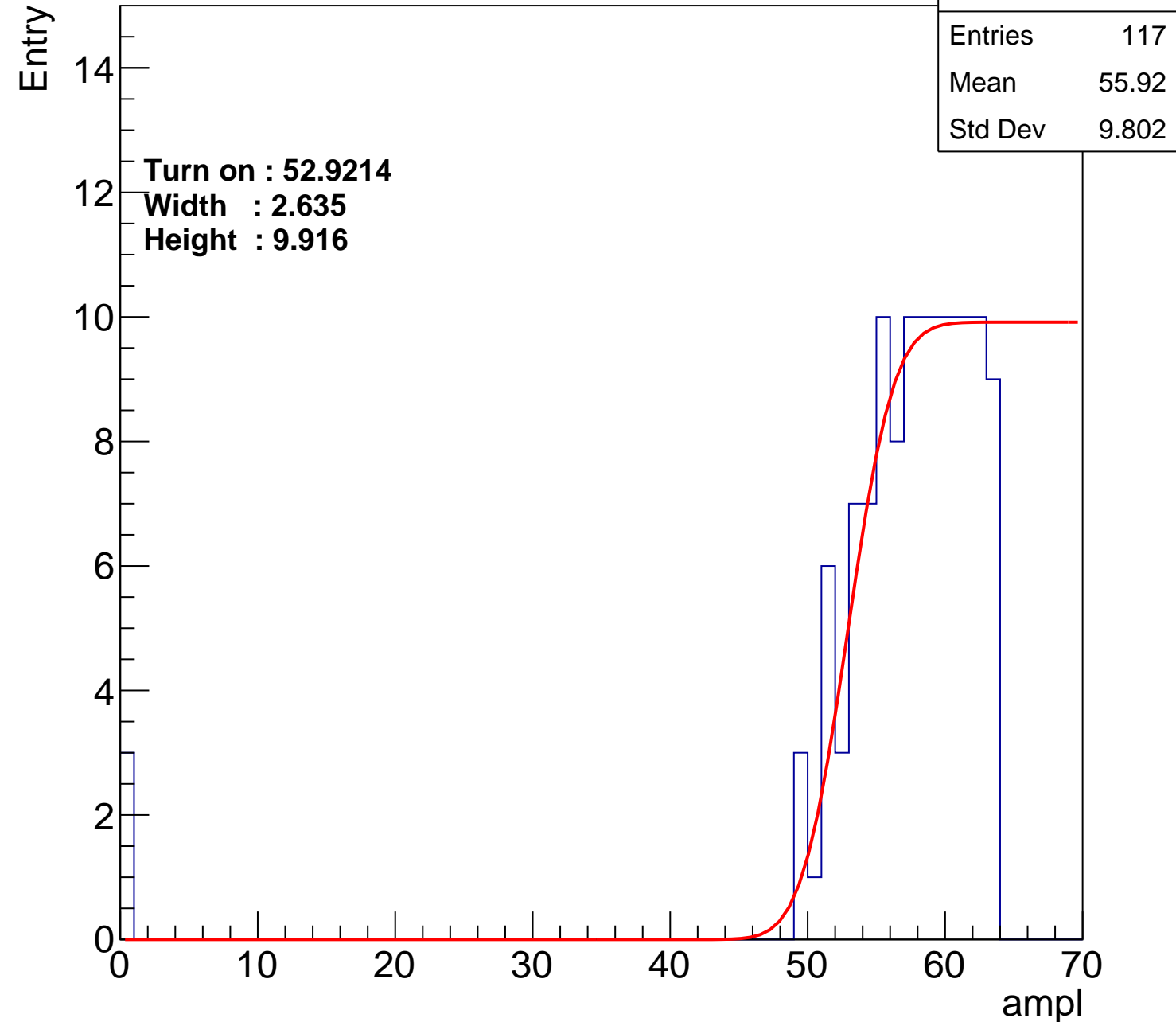
Width : 2.635

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch49

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.19
Std Dev	8.212

Turn on : 52.1599

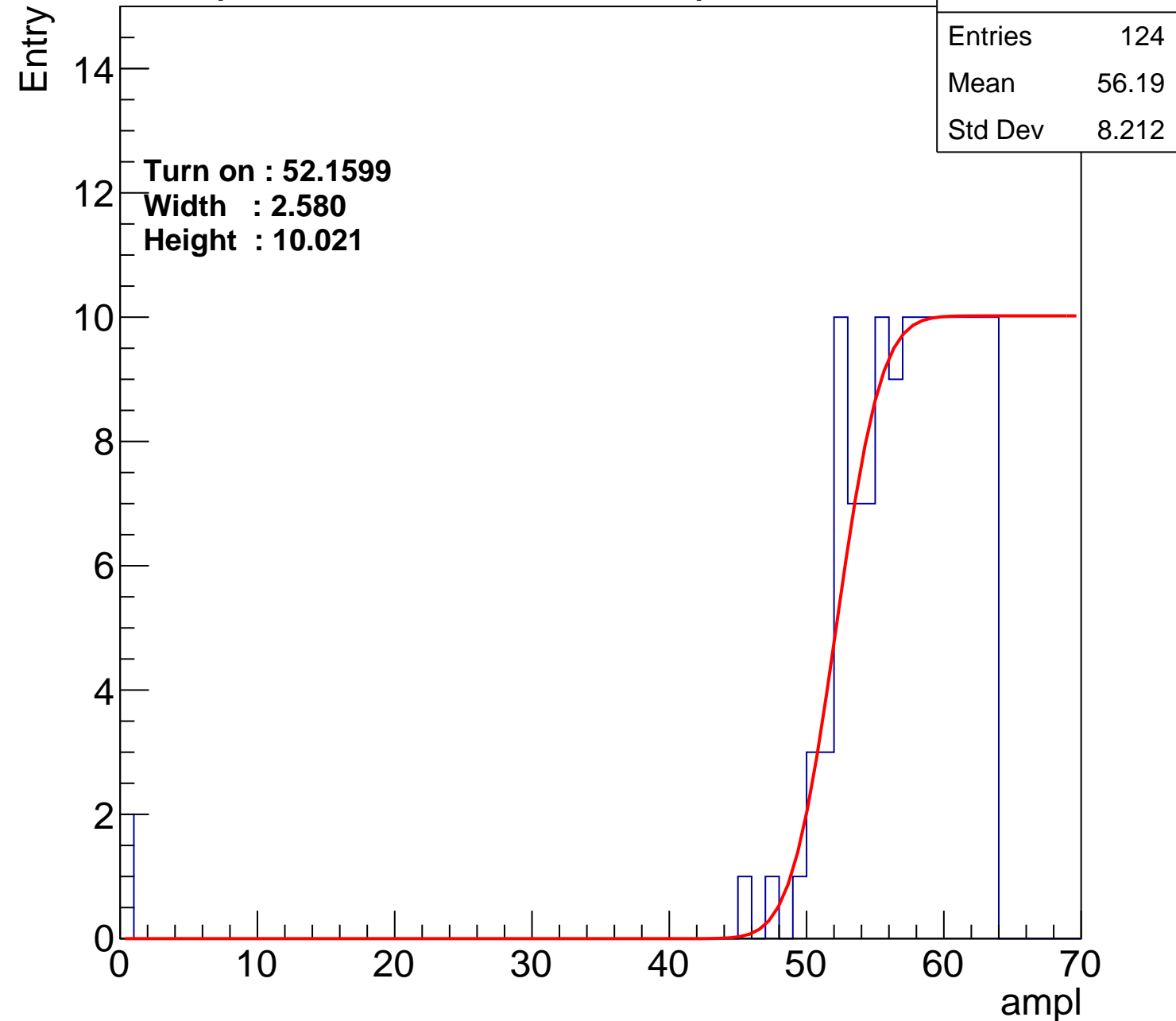
Width : 2.580

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch50

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.94
Std Dev	8.087

Turn on : 51.6417

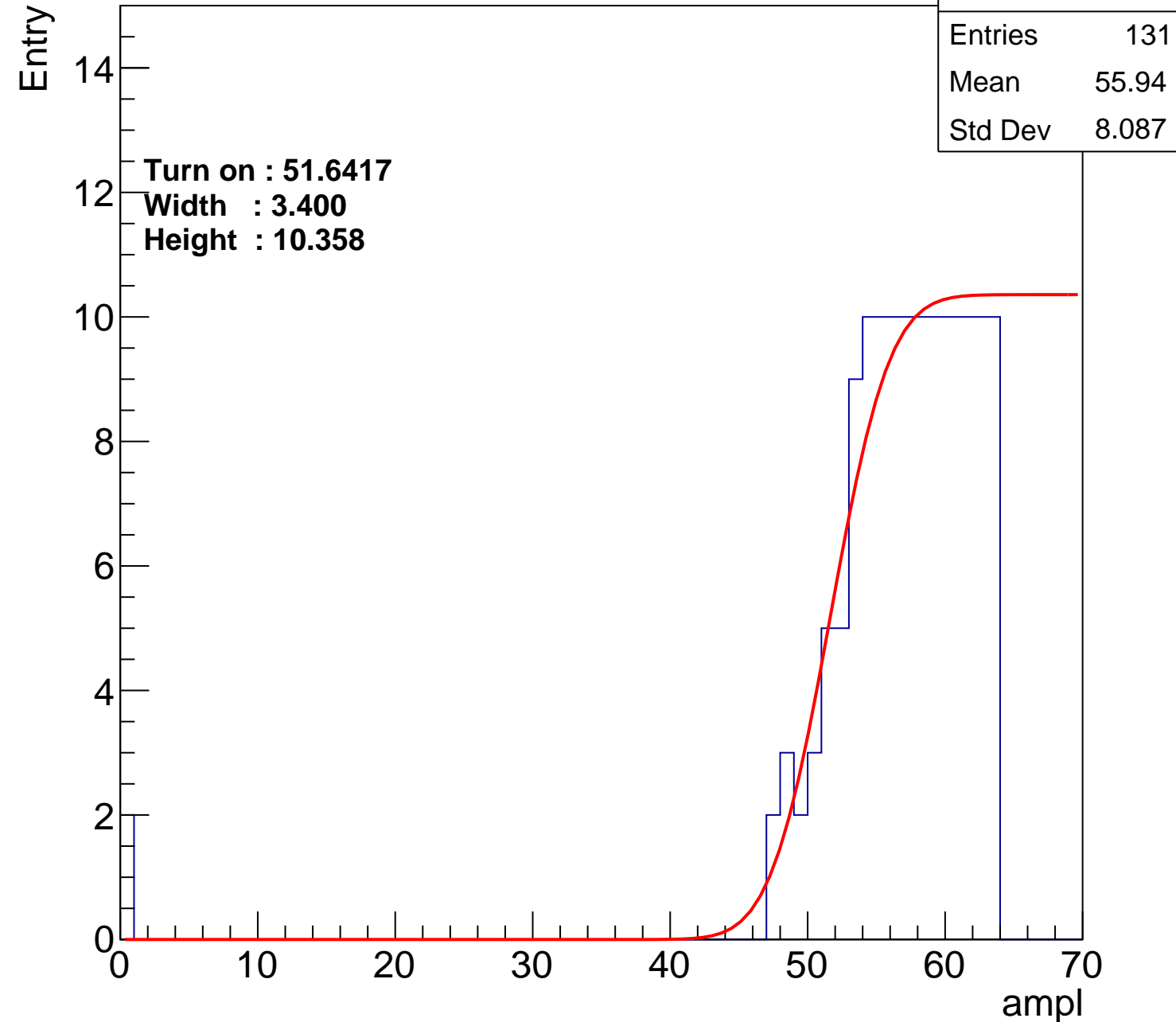
Width : 3.400

Height : 10.358

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch51

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.36
Std Dev	11.19

Turn on : 50.3901

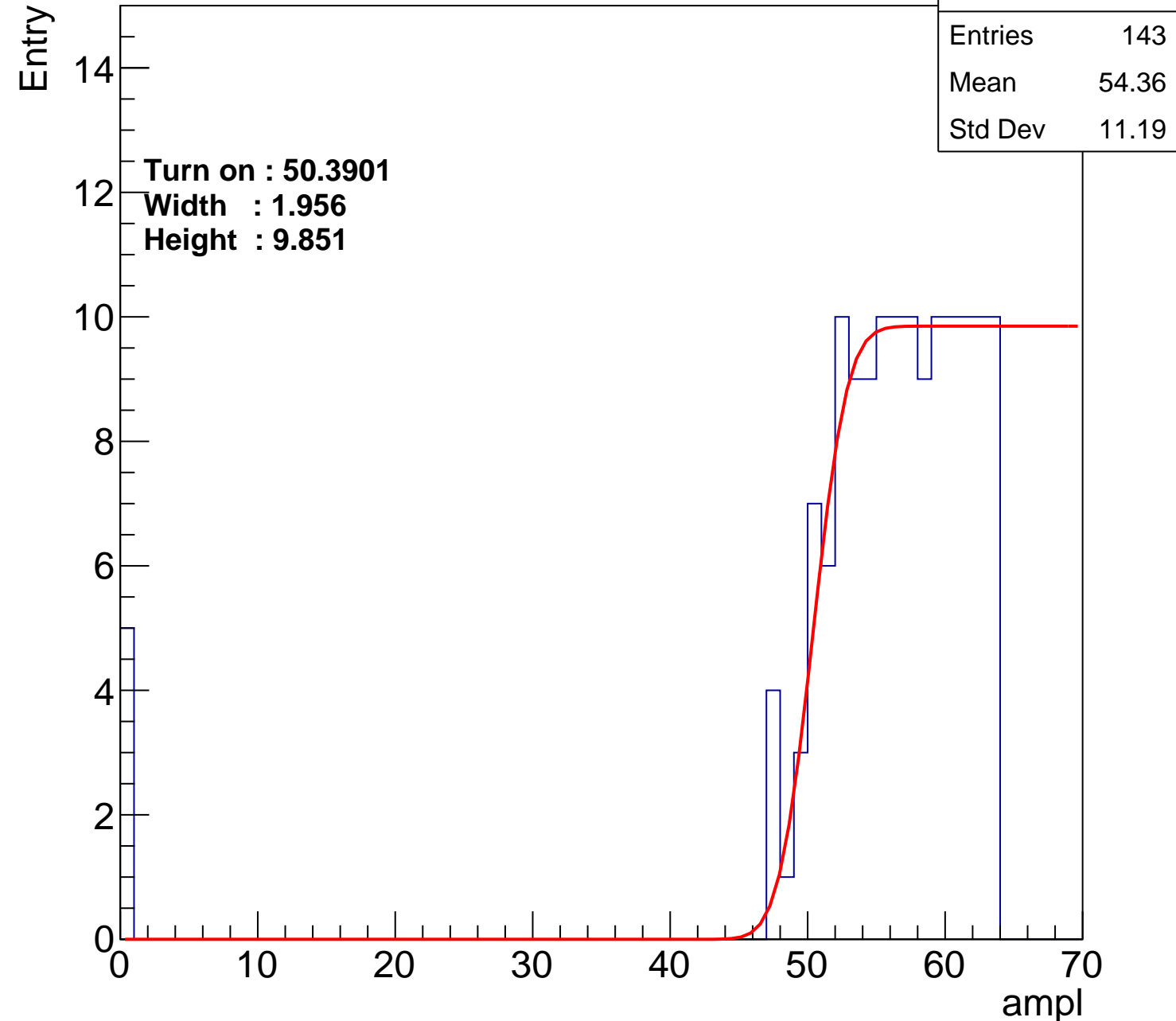
Width : 1.956

Height : 9.851

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch52

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	55.98
Std Dev	9.941

Turn on : 53.2901

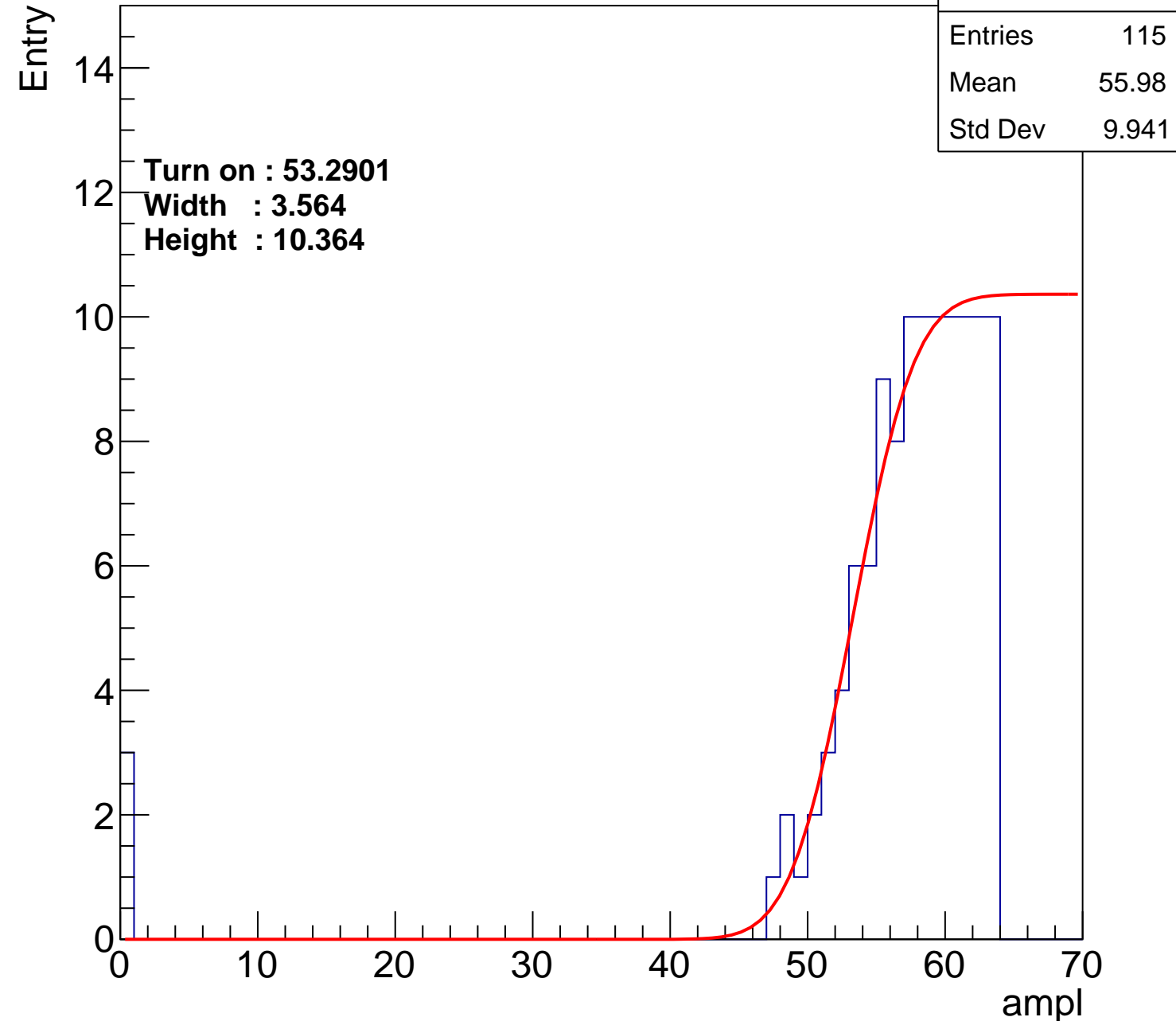
Width : 3.564

Height : 10.364

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch53

calib_packv5_040323_1717.root, FC#2, port C3

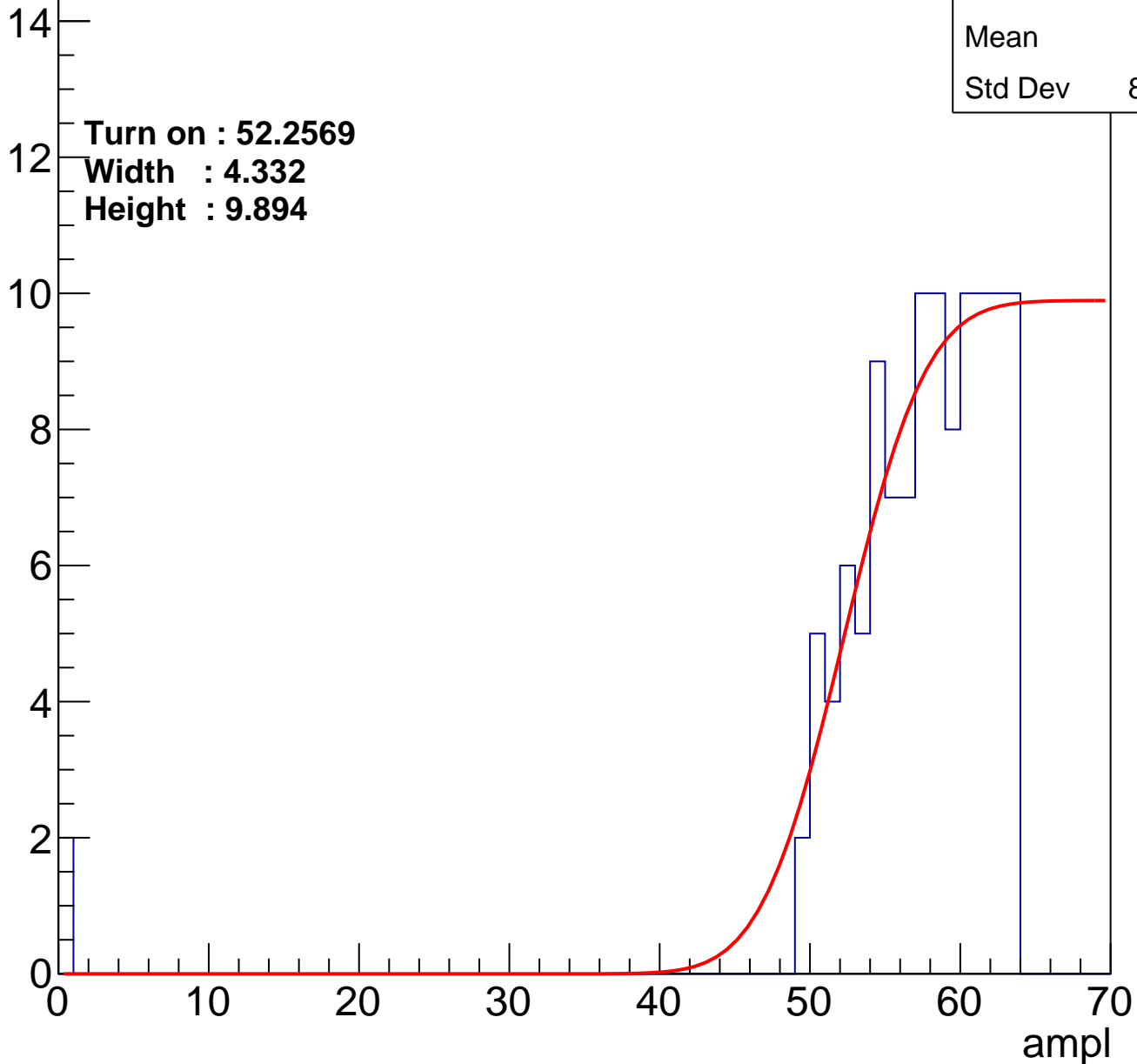
Entries	115
Mean	56.3
Std Dev	8.447

Turn on : 52.2569

Width : 4.332

Height : 9.894

Entry



B0L103S, U11-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	156
Mean	54.75
Std Dev	7.905

Turn on : 47.6867

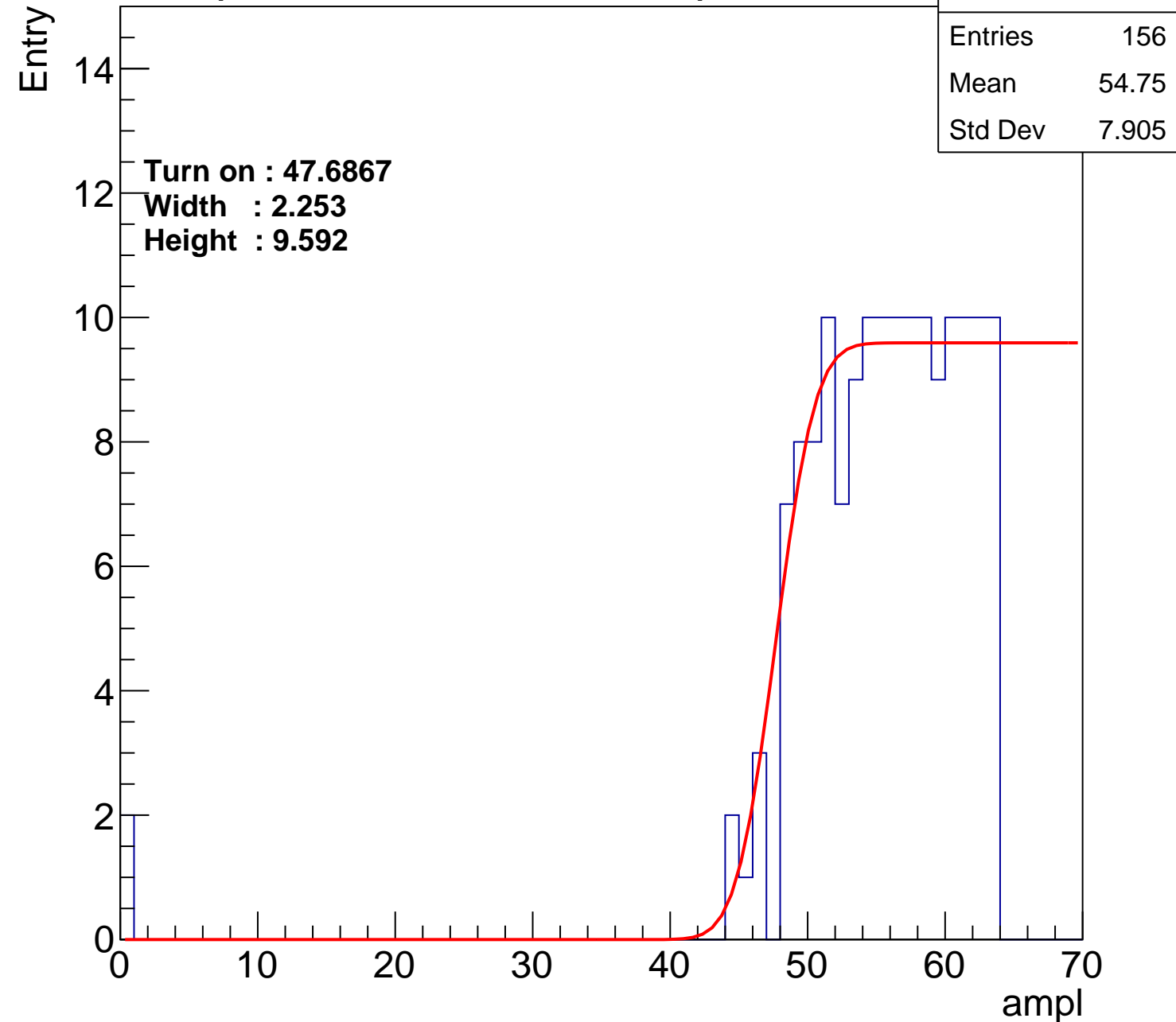
Width : 2.253

Height : 9.592

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch55

calib_packv5_040323_1717.root, FC#2, port C3

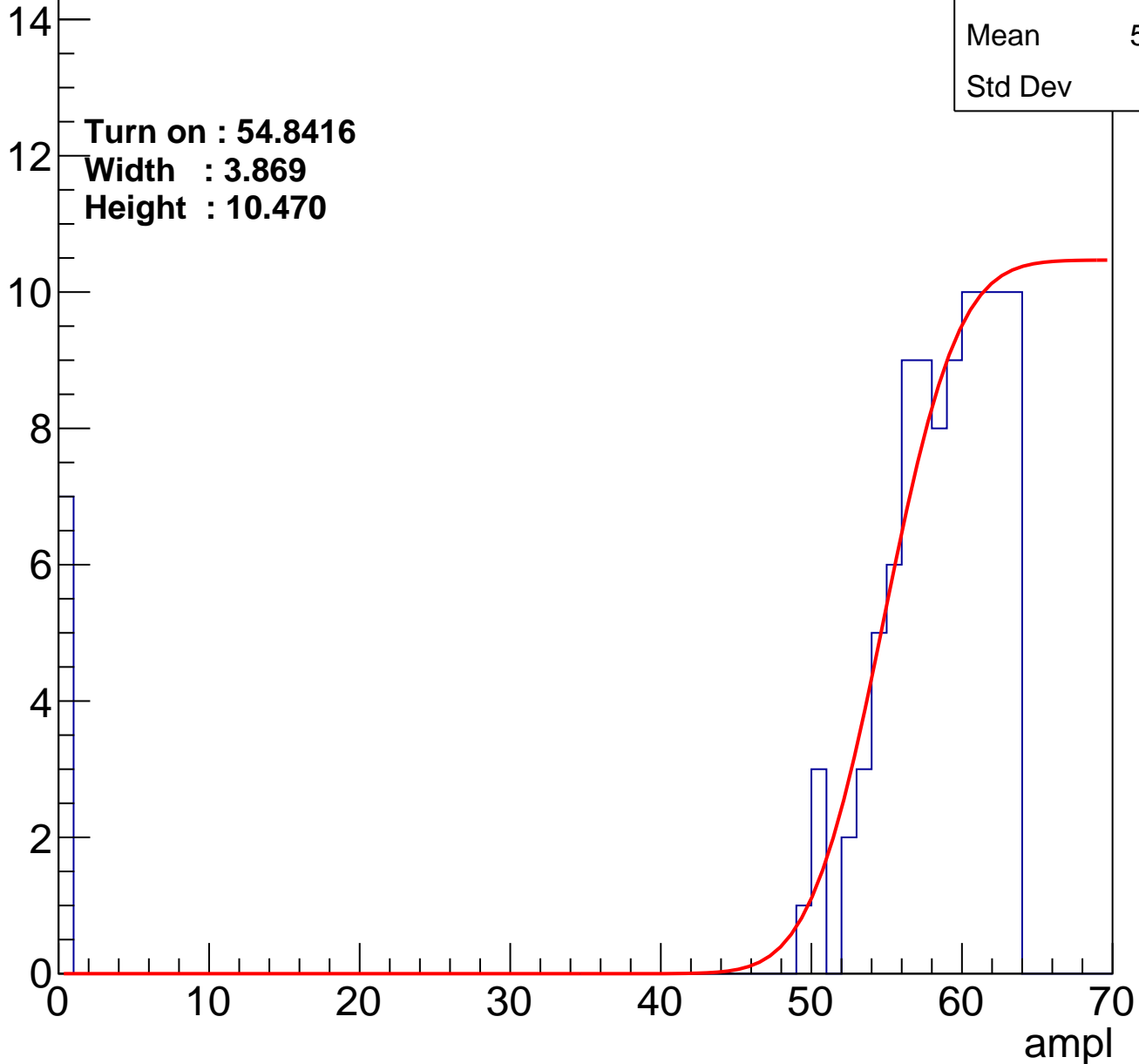
Entries	102
Mean	54.25
Std Dev	15.1

Turn on : 54.8416

Width : 3.869

Height : 10.470

Entry



B0L103S, U11-ch56

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	53.69
Std Dev	15.02

Turn on : 53.8106

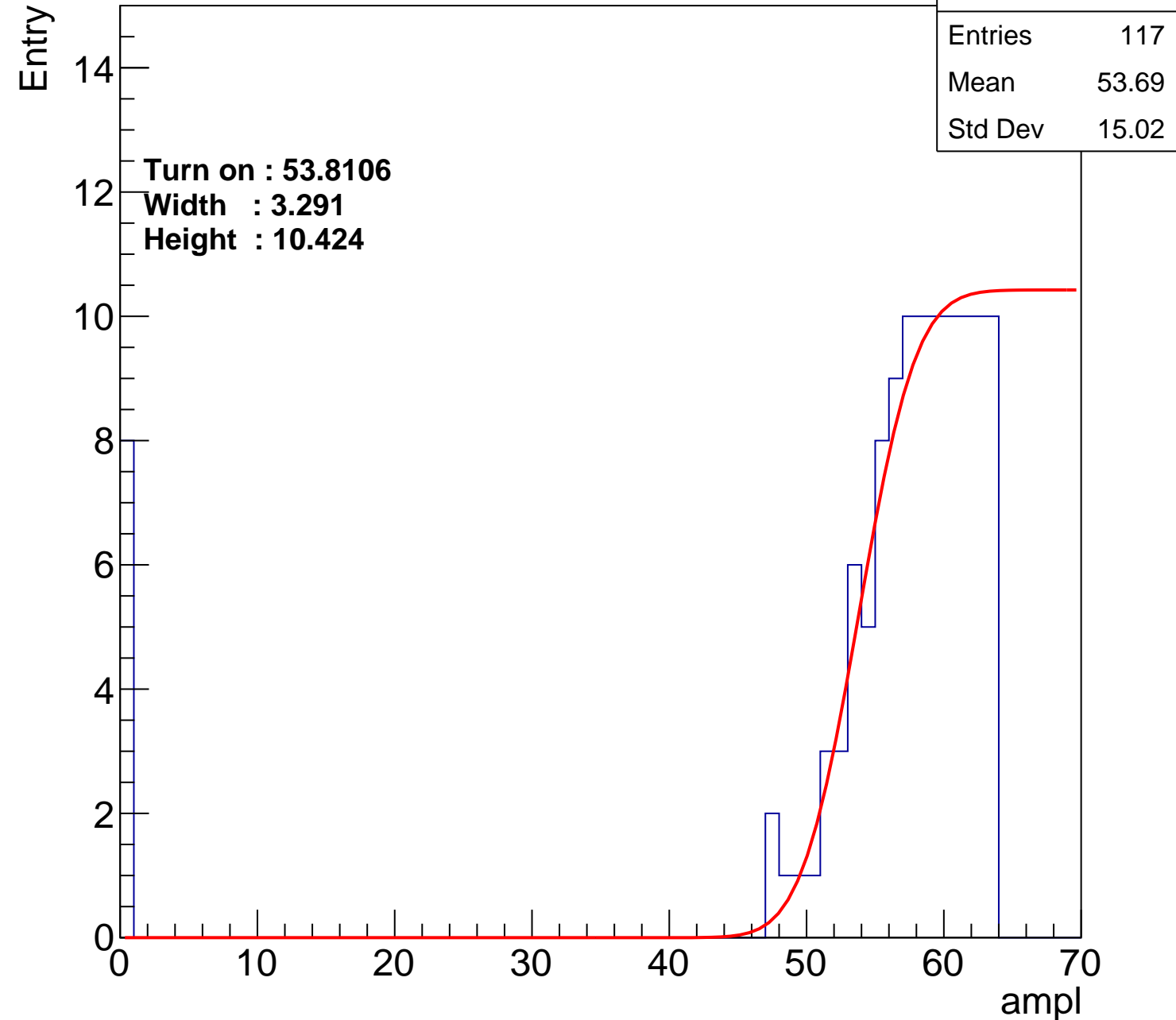
Width : 3.291

Height : 10.424

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch57

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.77
Std Dev	9.566

Turn on : 51.9873

Width : 2.958

Height : 10.049

Entry

14

12

10

8

6

4

2

0

ampl

0

10

20

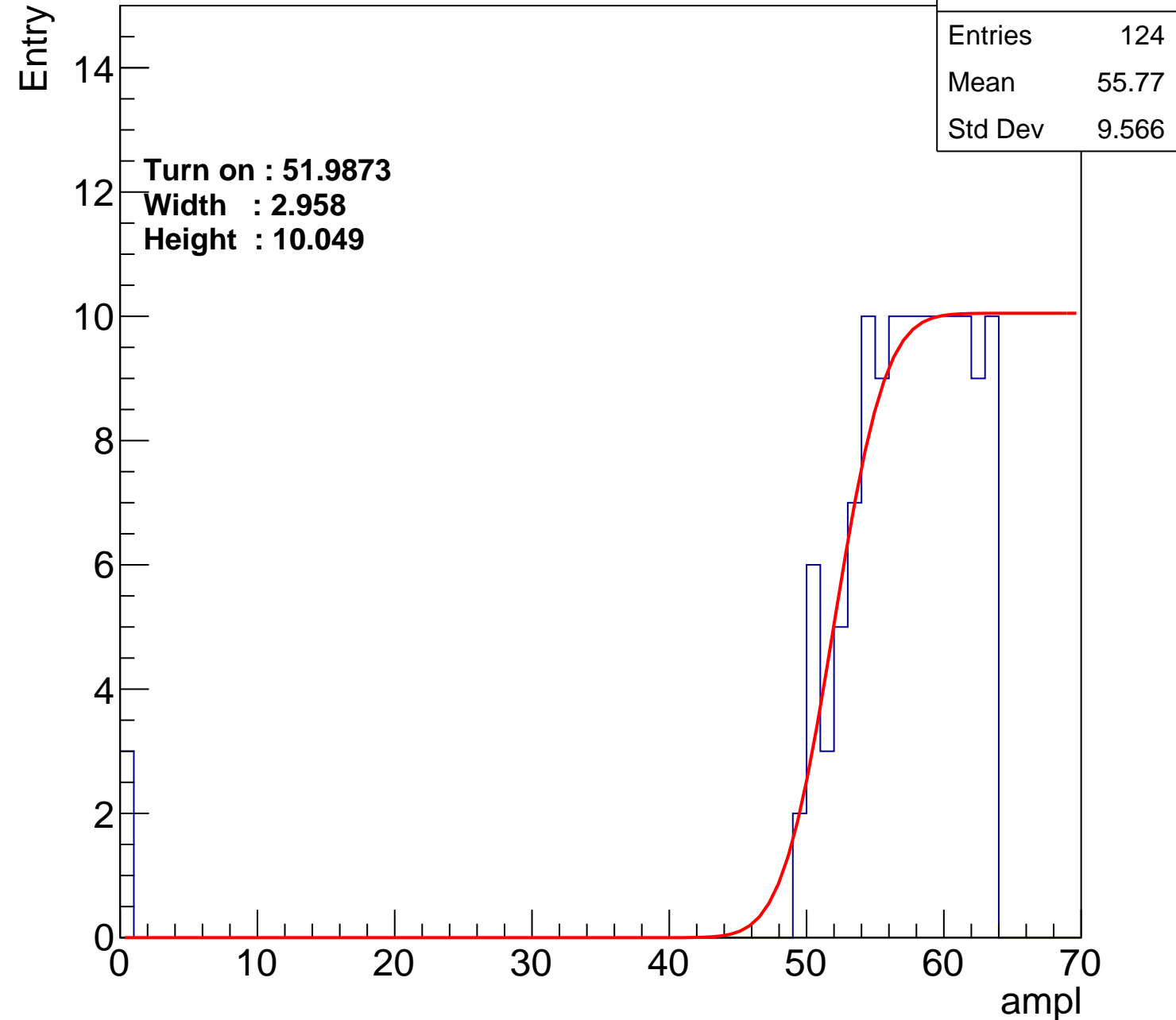
30

40

50

60

70



calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.52
Std Dev	10.85

Mean	55.52
------	-------

Std Dev	10.85
---------	-------

Width : 3.259

Height : 10.126



14

12

10

1

8

1

6

1

4

1

2

2

0

0

10

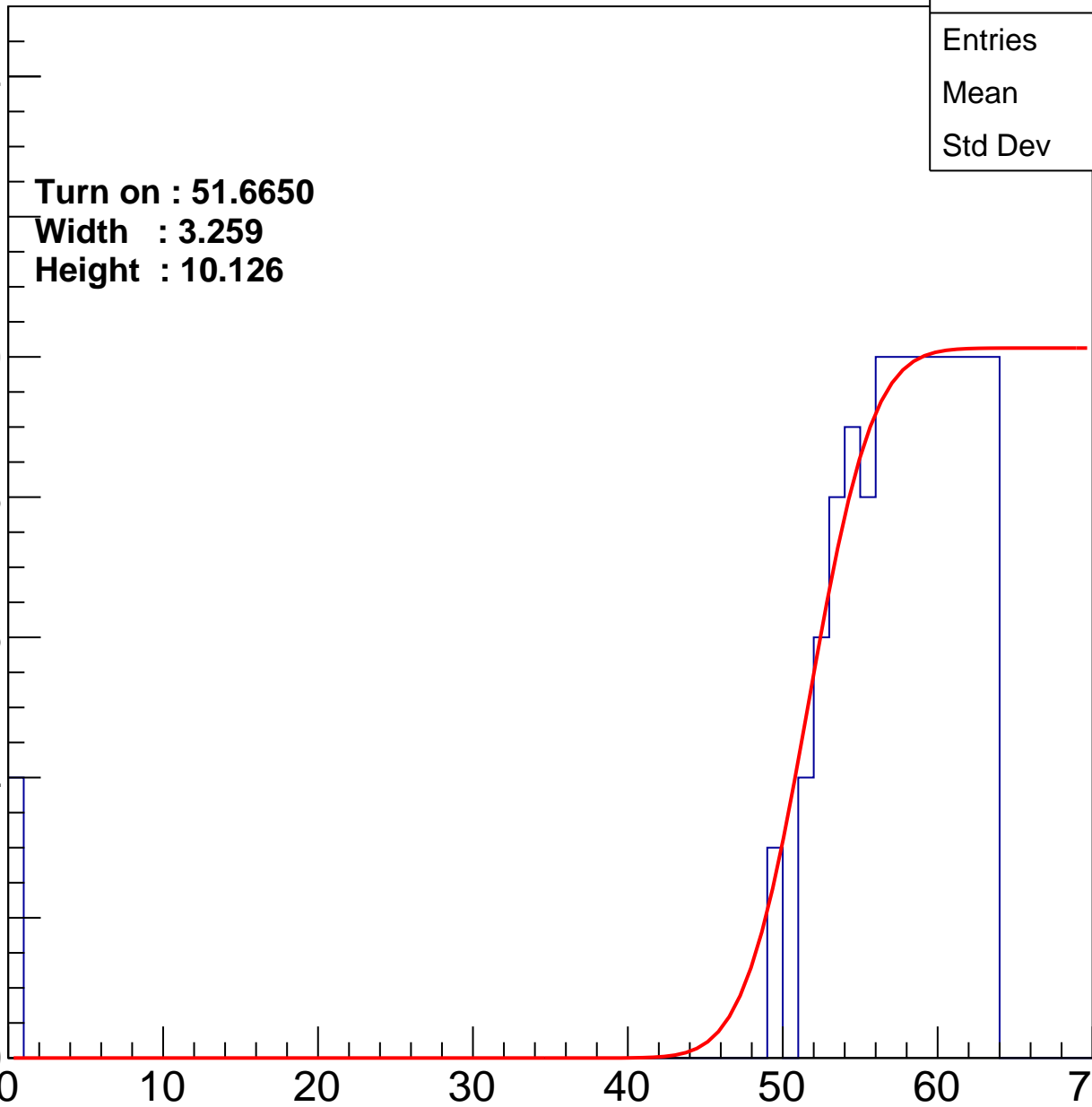
20

30

0

50

ampl



B0L103S, U11-ch59

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.77
Std Dev	11.56

Turn on : 51.5109

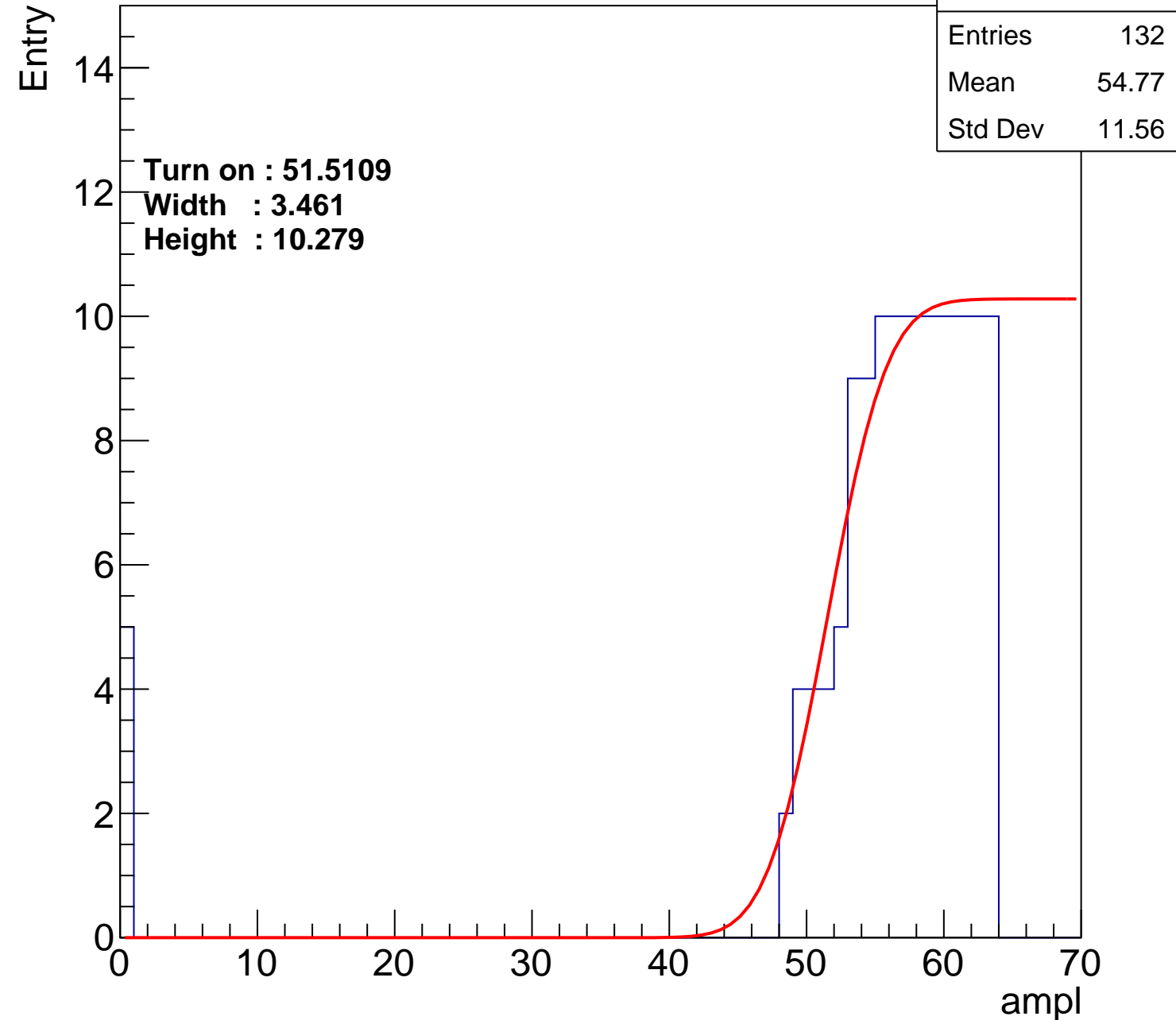
Width : 3.461

Height : 10.279

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch60

calib_packv5_040323_1717.root, FC#2, port C3

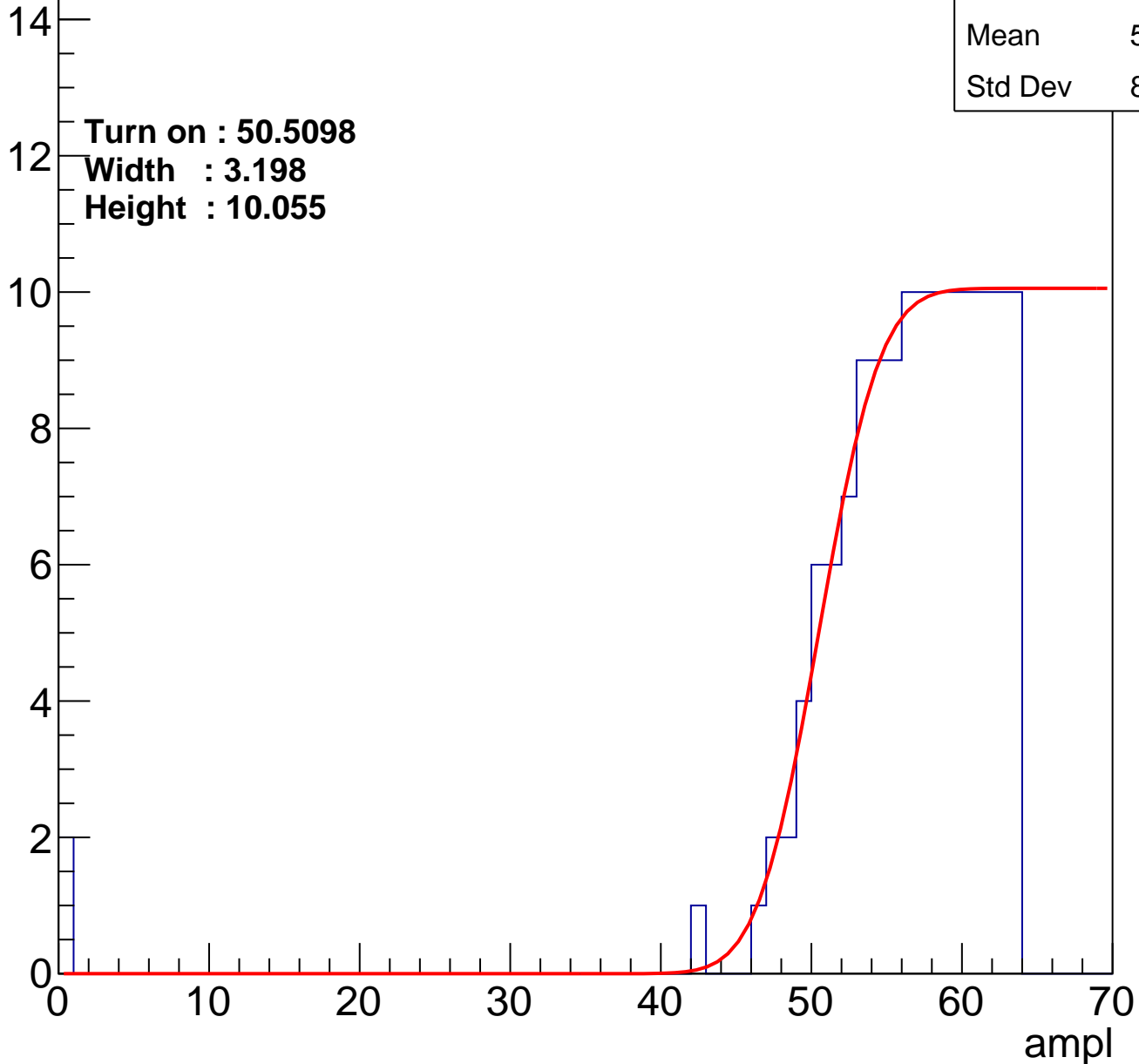
Entries	138
Mean	55.52
Std Dev	8.088

Turn on : 50.5098

Width : 3.198

Height : 10.055

Entry



B0L103S, U11-ch61

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.42
Std Dev	10.91

Turn on : 52.0817

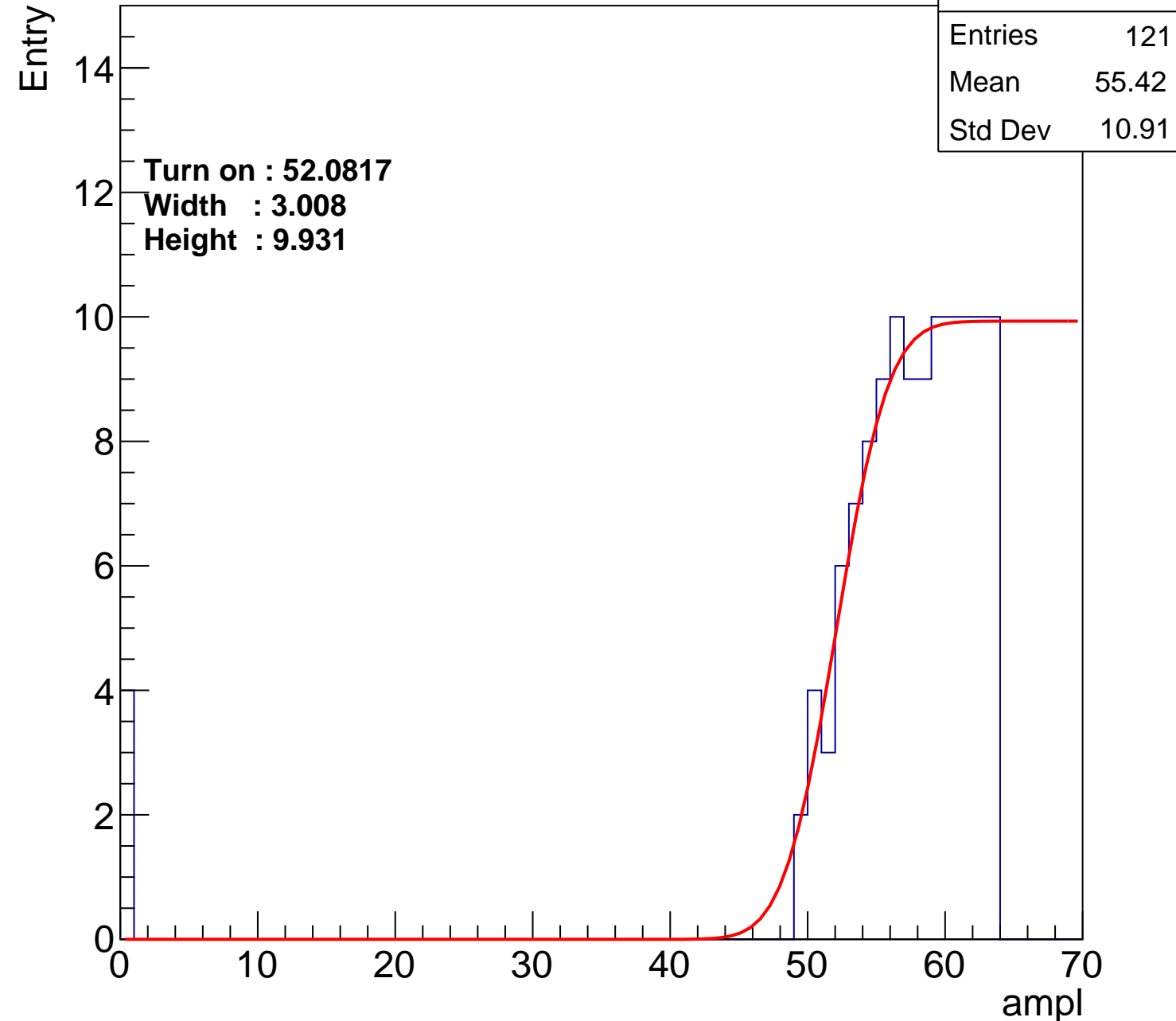
Width : 3.008

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch62

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	55.72
Std Dev	9.531

Turn on : 51.8229

Width : 2.791

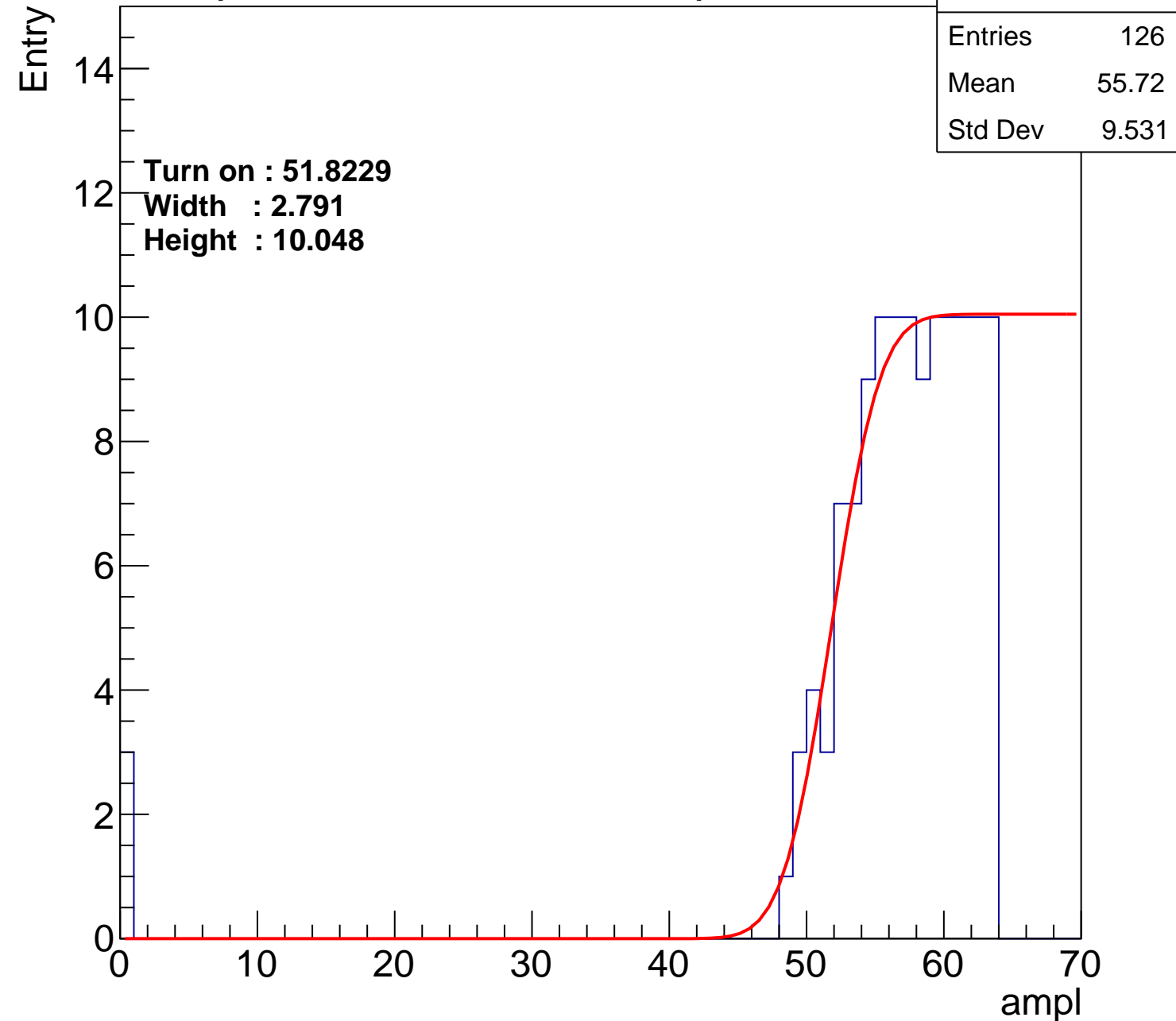
Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch63

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.44
Std Dev	10.21

Turn on : 50.1622

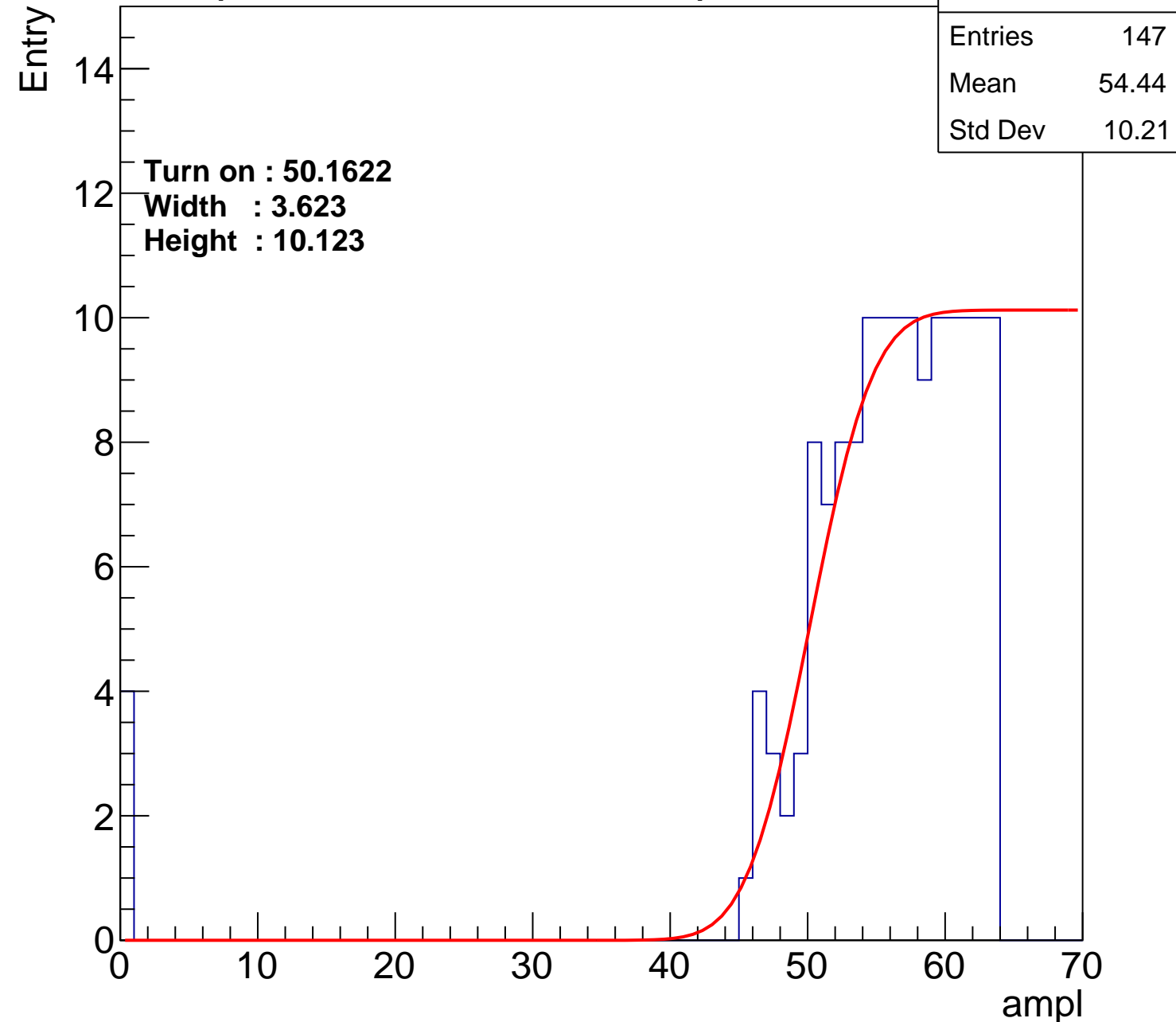
Width : 3.623

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch64

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	56.03
Std Dev	6.364

Turn on : 50.1427

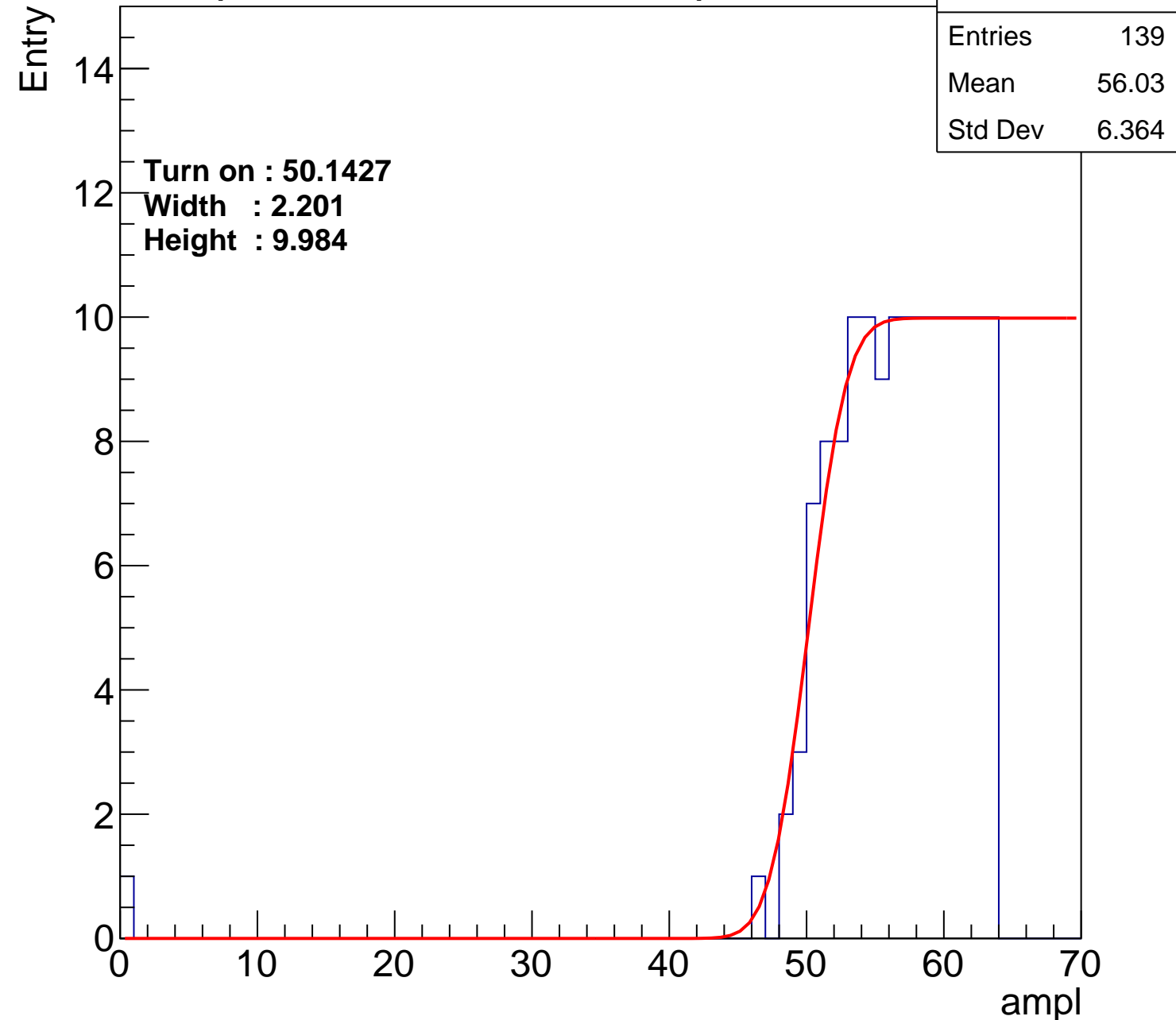
Width : 2.201

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	56.11
Std Dev	8.31

Turn on : 52.6216

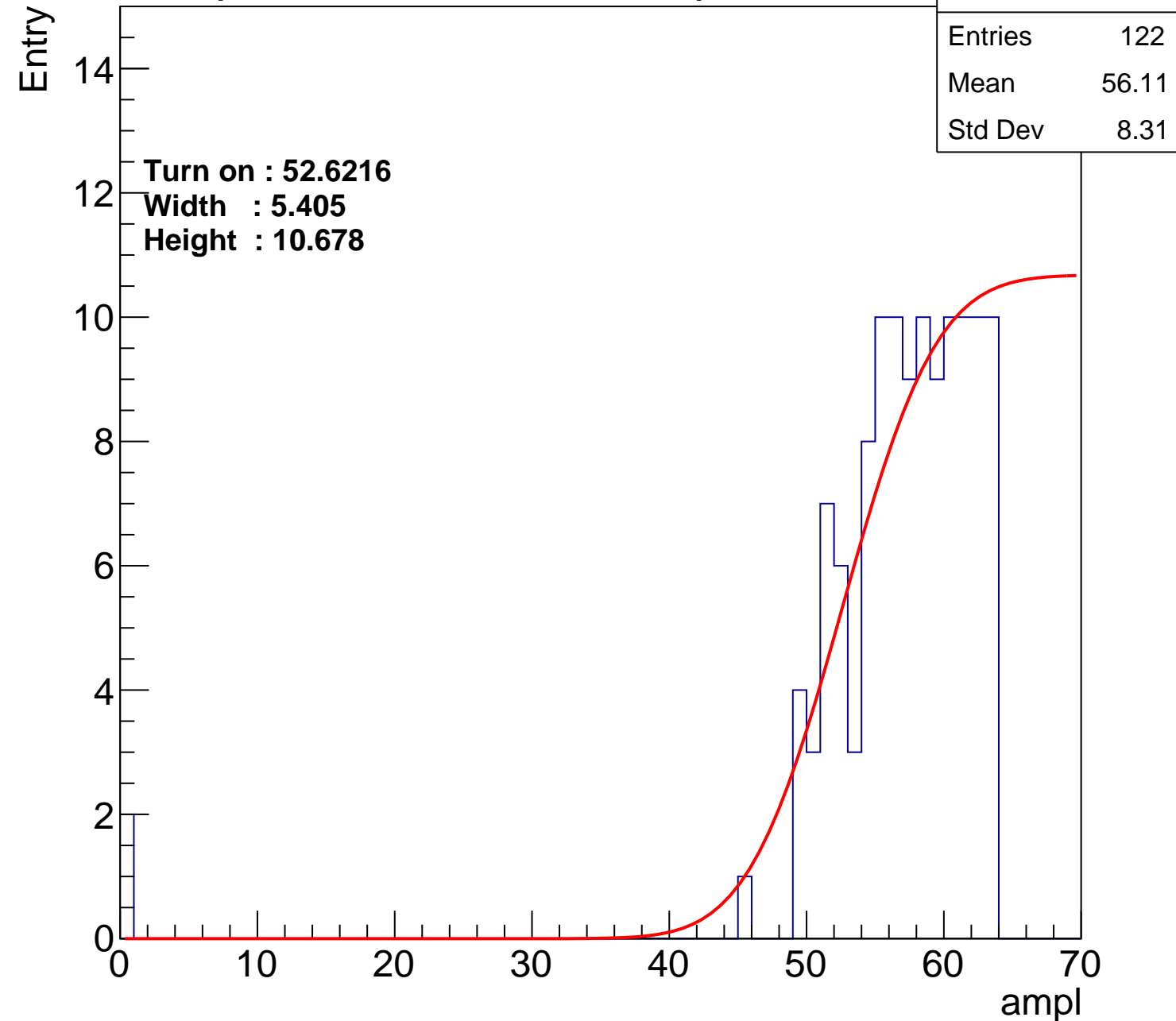
Width : 5.405

Height : 10.678

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.43
Std Dev	6.413

Turn on : 51.5850

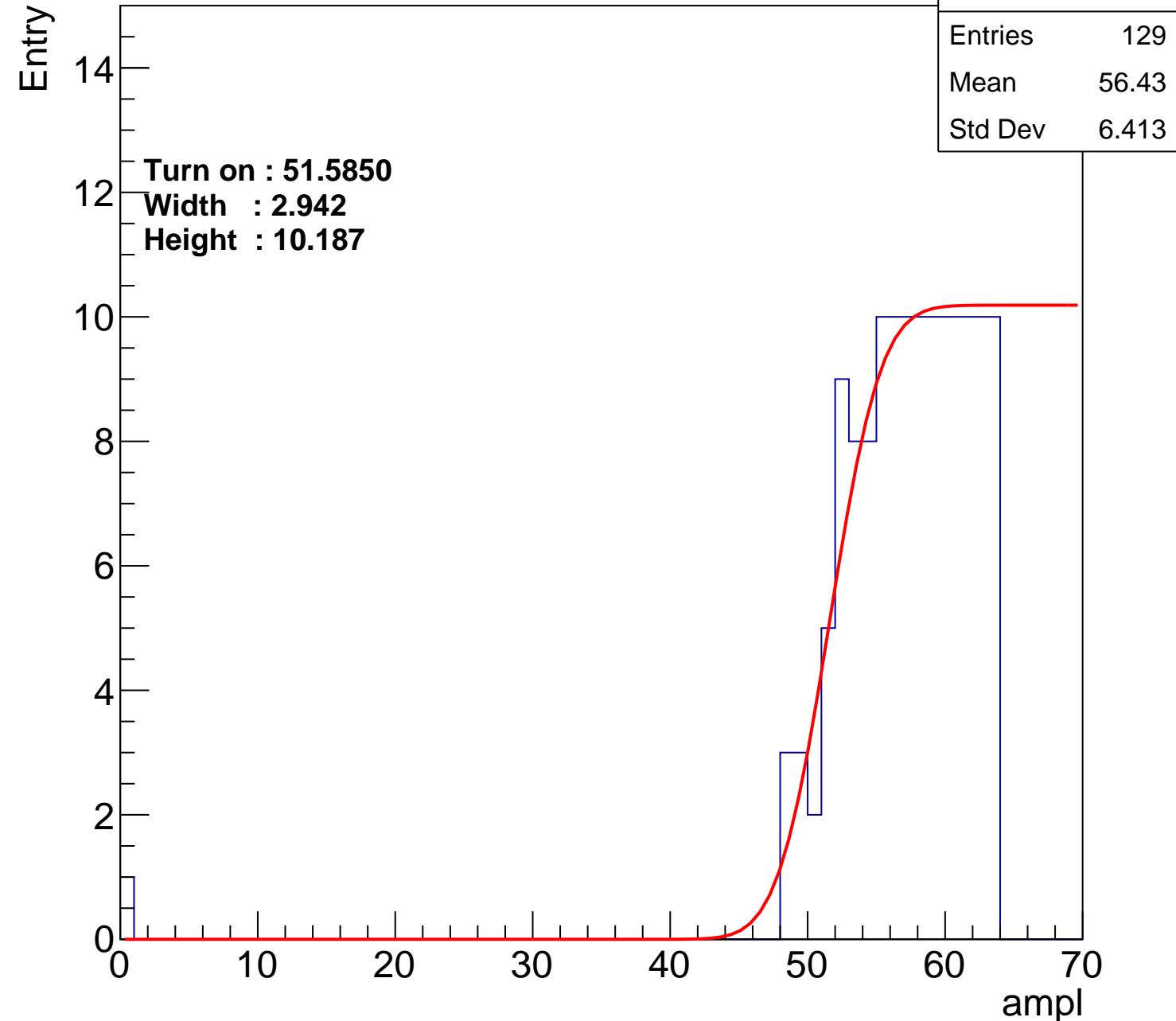
Width : 2.942

Height : 10.187

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	55.56
Std Dev	9.632

Turn on : 52.2037

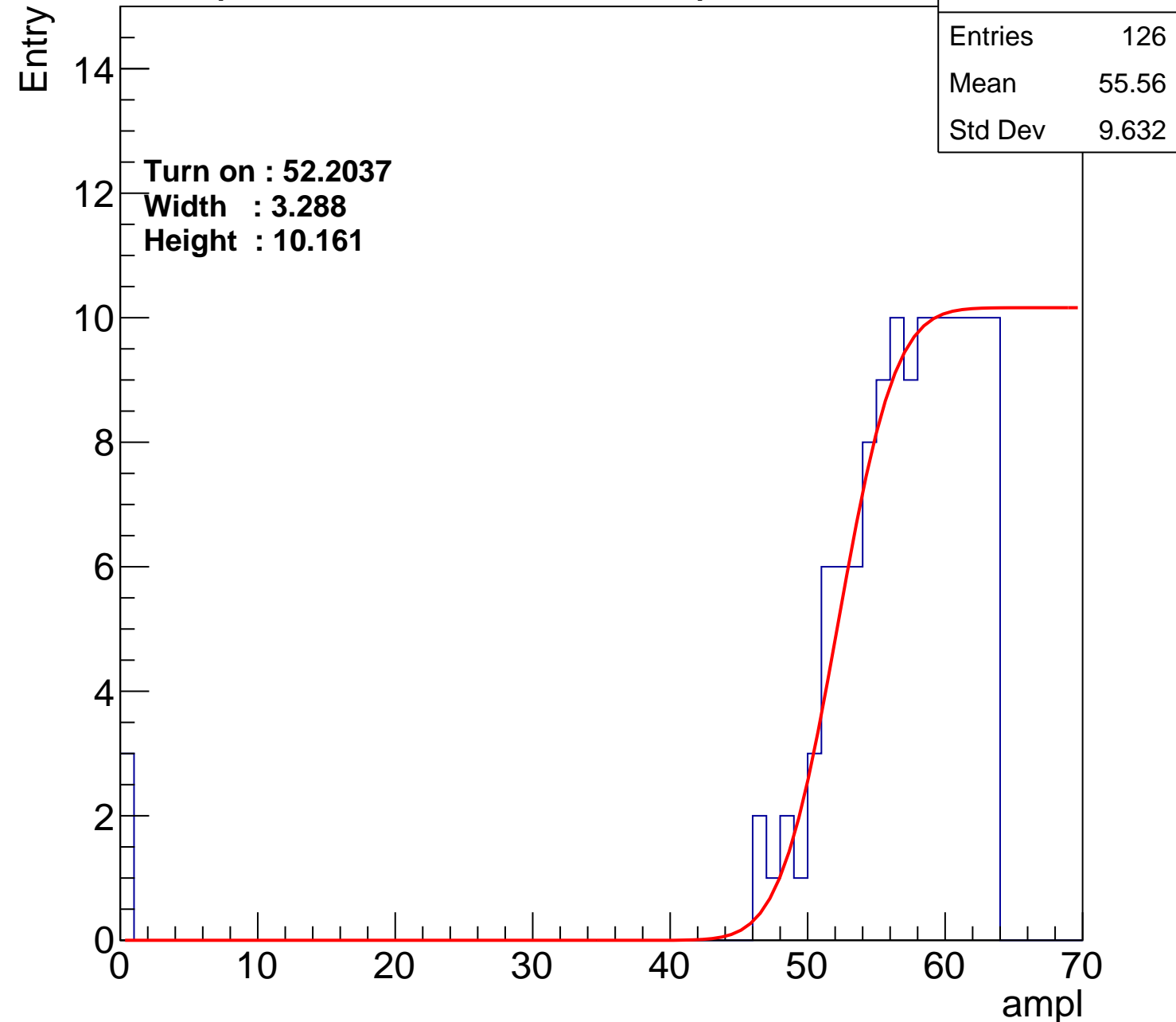
Width : 3.288

Height : 10.161

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	54.79
Std Dev	10.42

Turn on : 50.6093

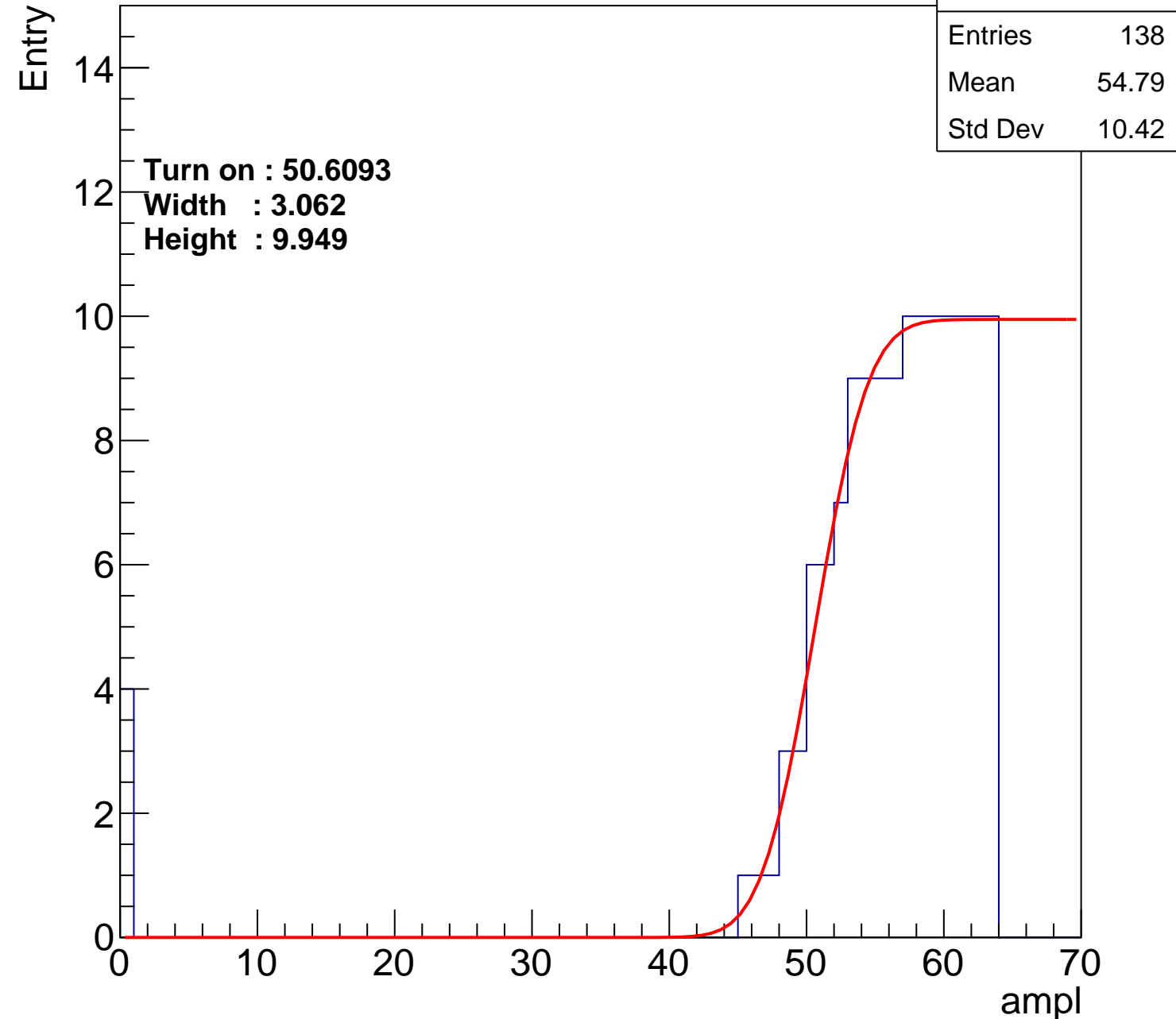
Width : 3.062

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch69

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	54.88
Std Dev	12.02

Turn on : 51.6683

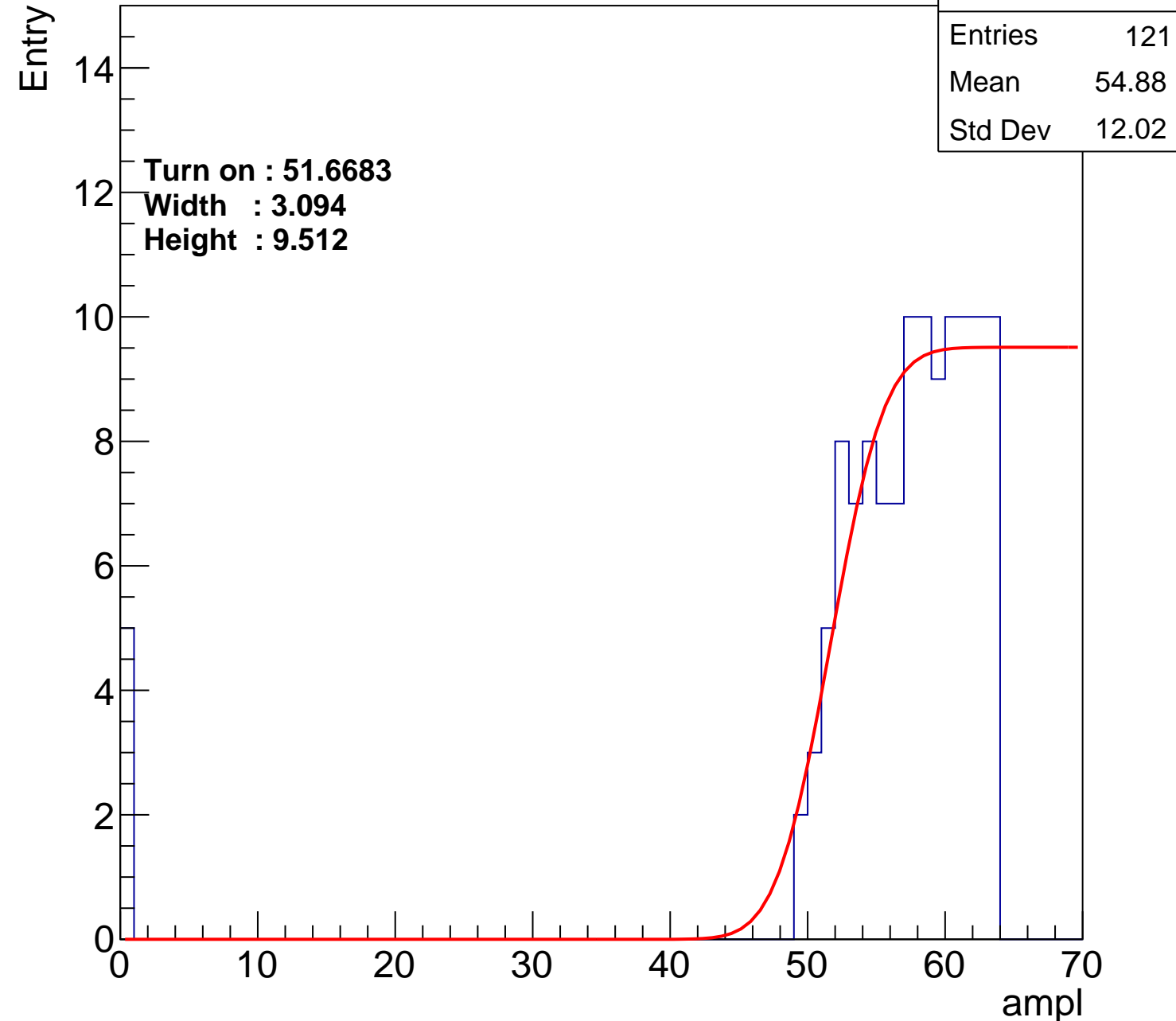
Width : 3.094

Height : 9.512

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	56.71
Std Dev	6.43

Turn on : 50.8725

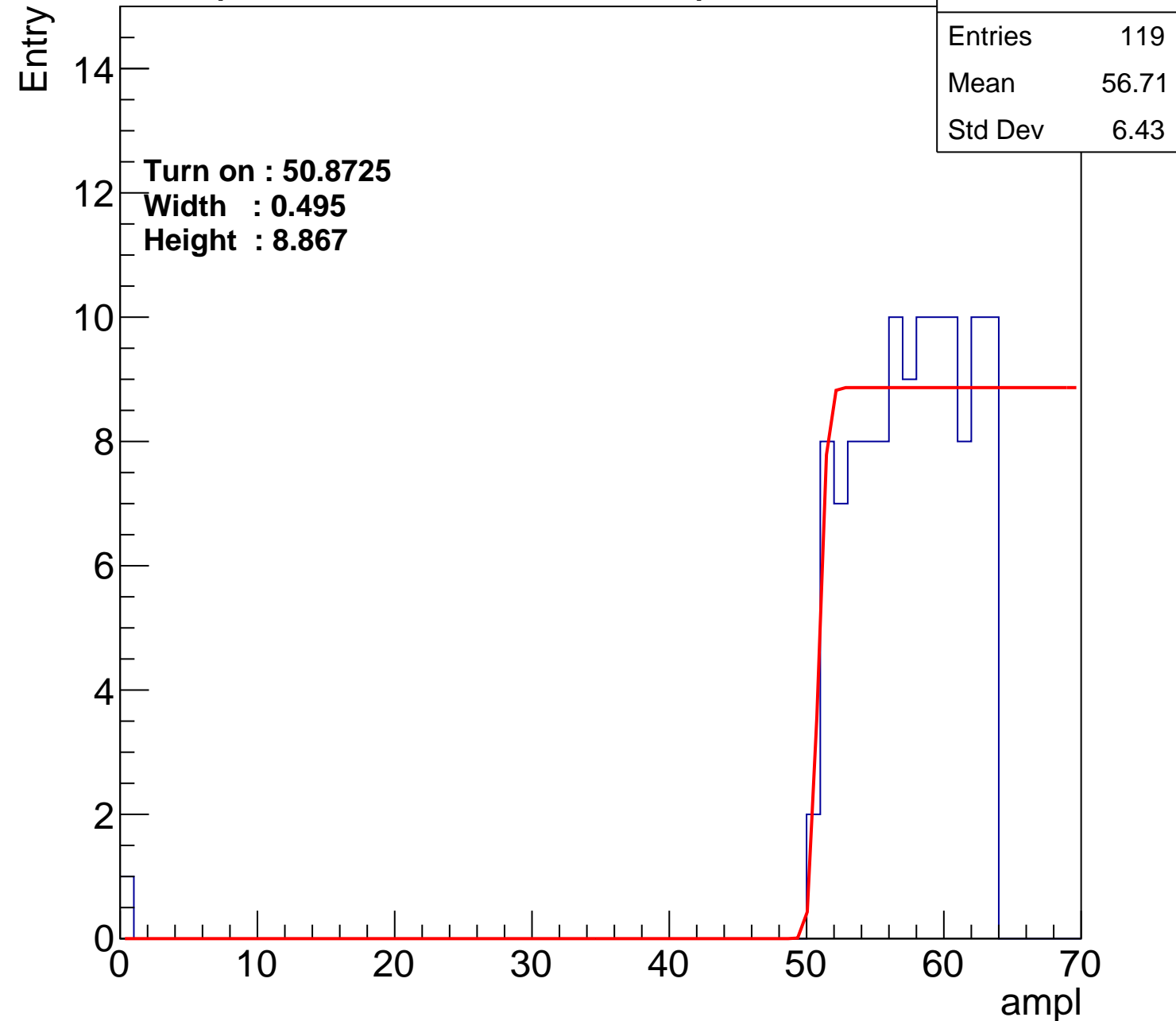
Width : 0.495

Height : 8.867

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch71

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.36
Std Dev	9.643

Turn on : 51.8432

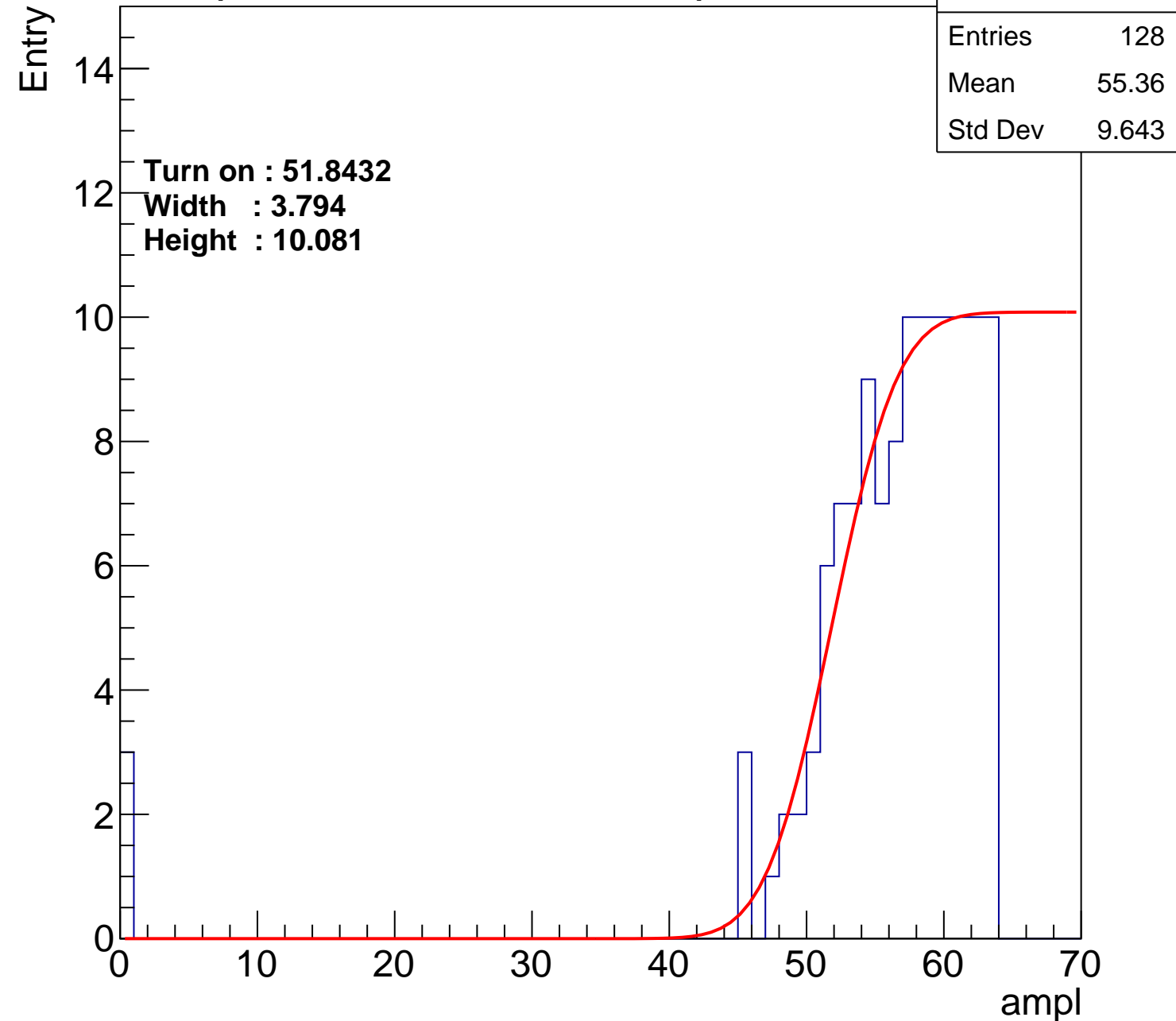
Width : 3.794

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch72

calib_packv5_040323_1717.root, FC#2, port C3

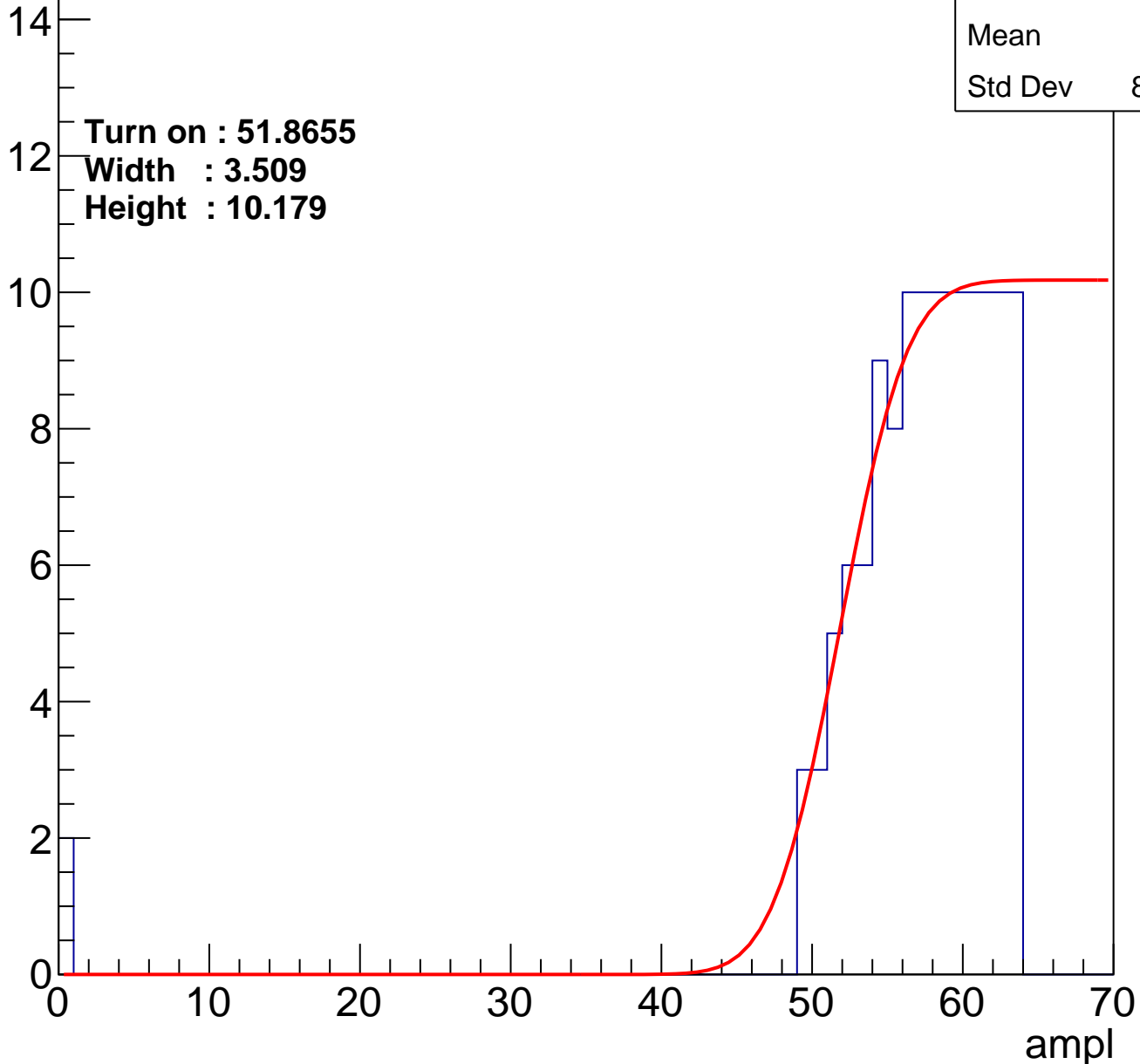
Entries	122
Mean	56.3
Std Dev	8.216

Turn on : 51.8655

Width : 3.509

Height : 10.179

Entry



B0L103S, U11-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	102
Mean	57.62
Std Dev	6.62

Turn on : 54.1757

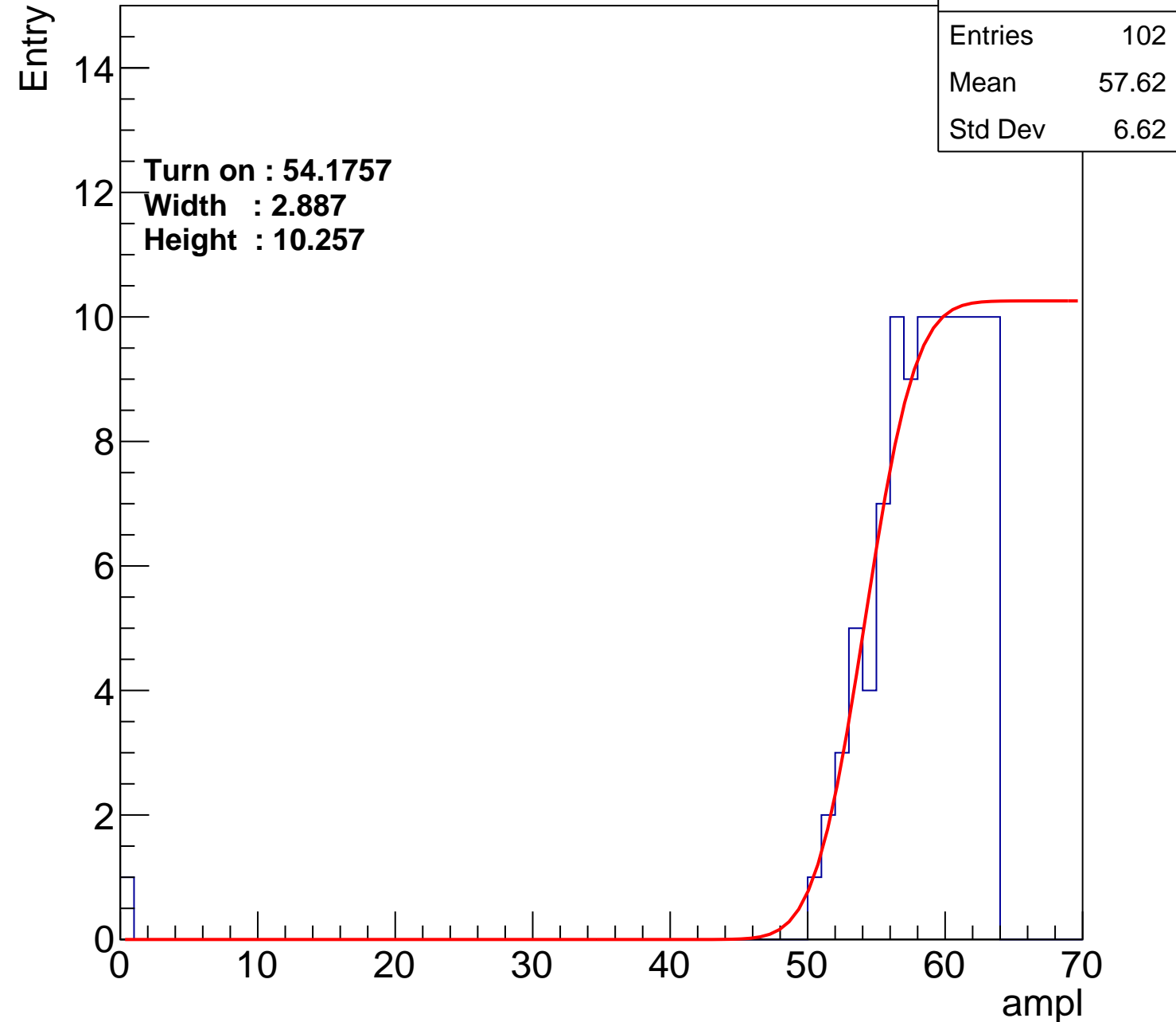
Width : 2.887

Height : 10.257

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch74

calib_packv5_040323_1717.root, FC#2, port C3

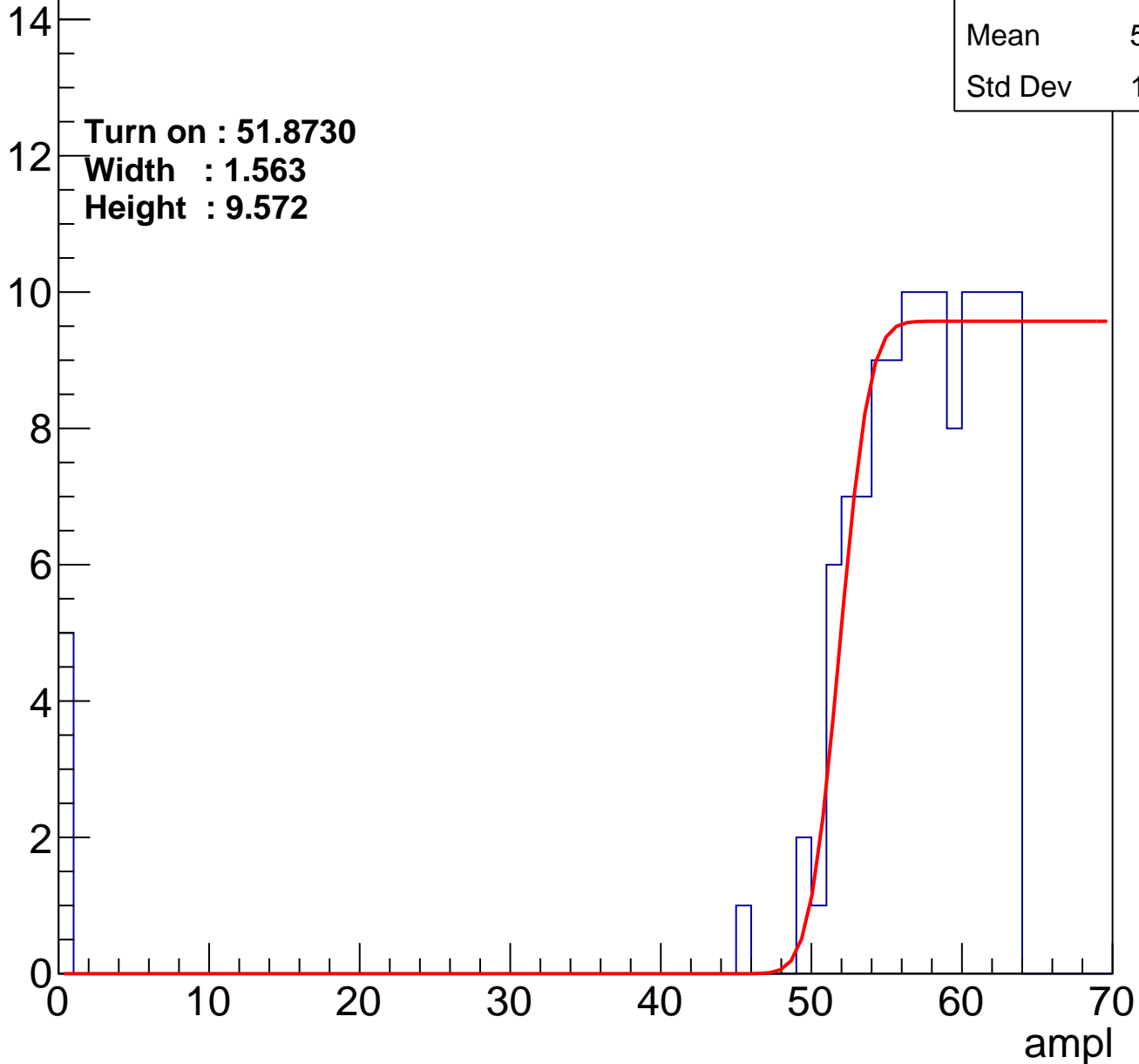
Entries	125
Mean	54.86
Std Dev	11.84

Turn on : 51.8730

Width : 1.563

Height : 9.572

Entry



B0L103S, U11-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	54.82
Std Dev	11.88

Turn on : 52.2382

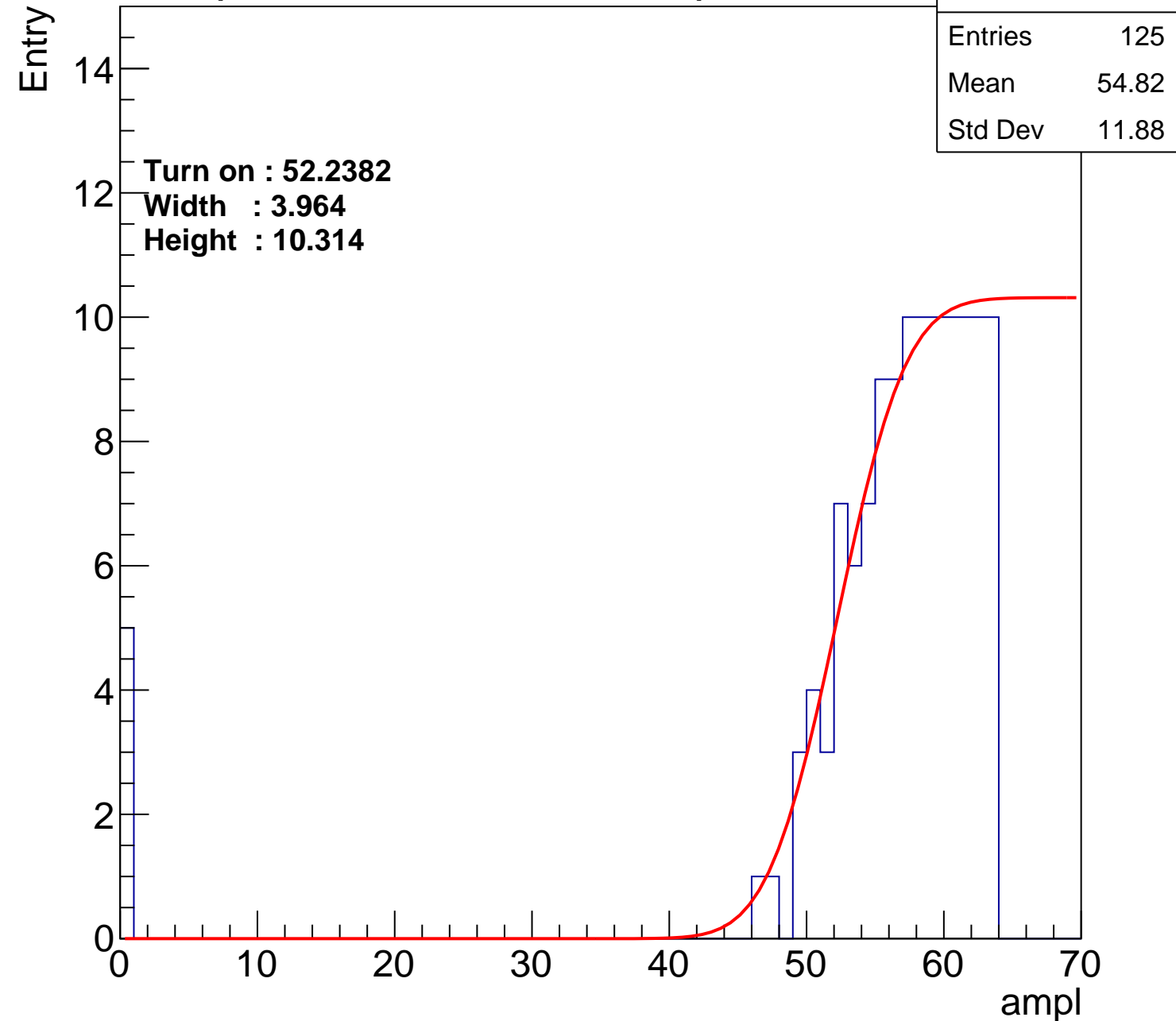
Width : 3.964

Height : 10.314

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch76

calib_packv5_040323_1717.root, FC#2, port C3

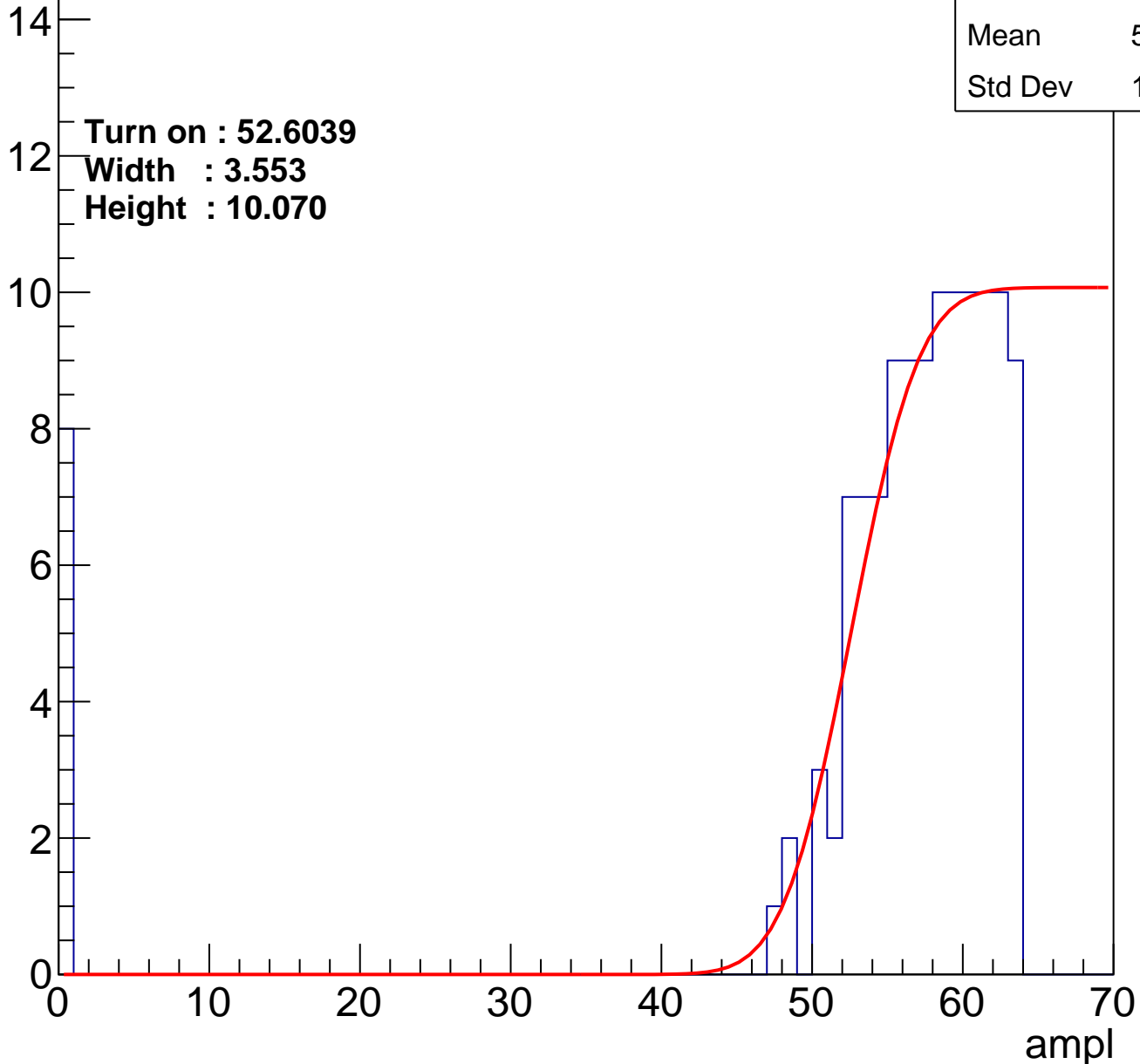
Entries	123
Mean	53.55
Std Dev	14.62

Turn on : 52.6039

Width : 3.553

Height : 10.070

Entry



B0L103S, U11-ch77

calib_packv5_040323_1717.root, FC#2, port C3

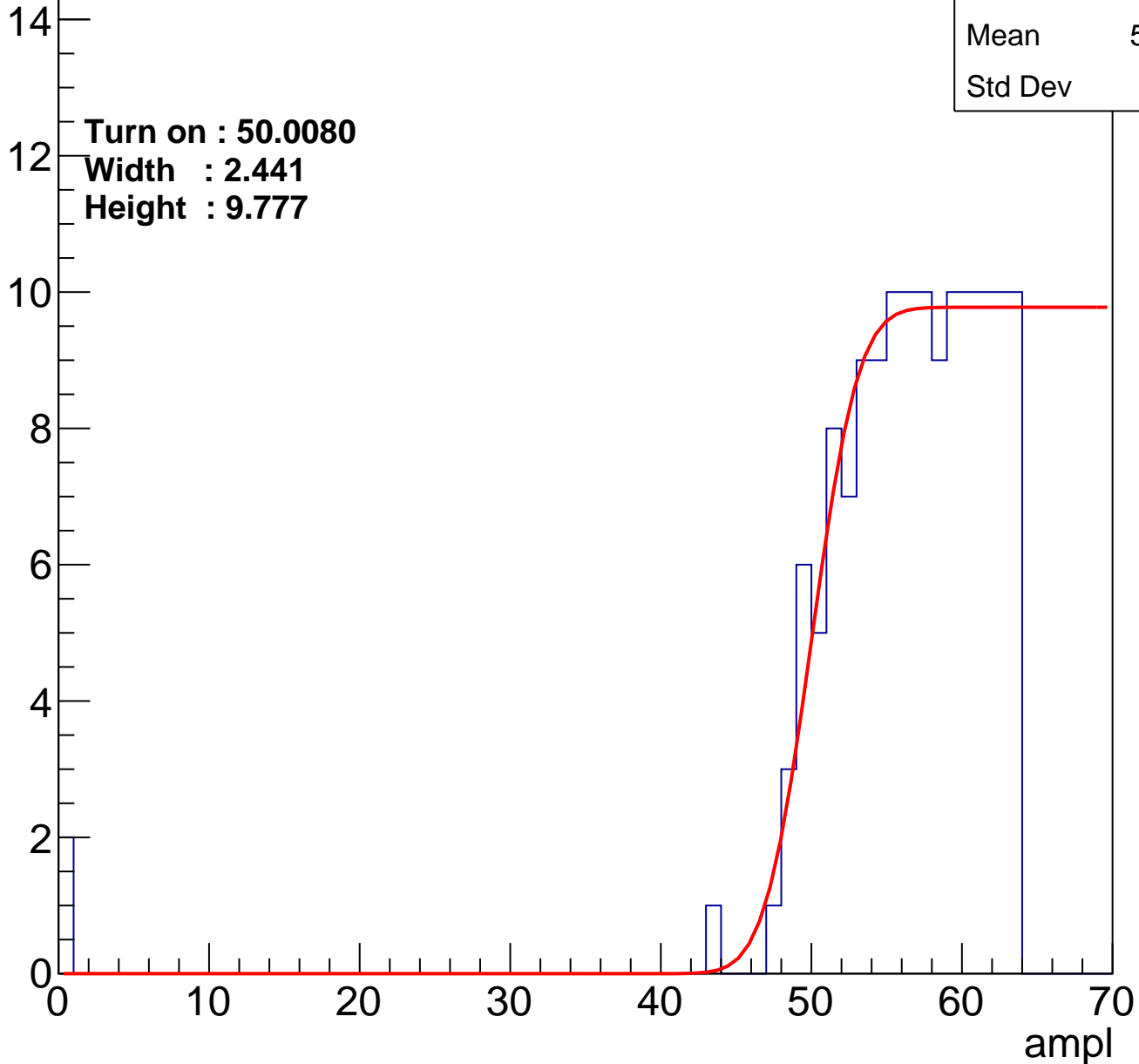
Entries	140
Mean	55.46
Std Dev	8.01

Turn on : 50.0080

Width : 2.441

Height : 9.777

Entry



B0L103S, U11-ch78

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.64
Std Dev	9.499

Turn on : 52.3409

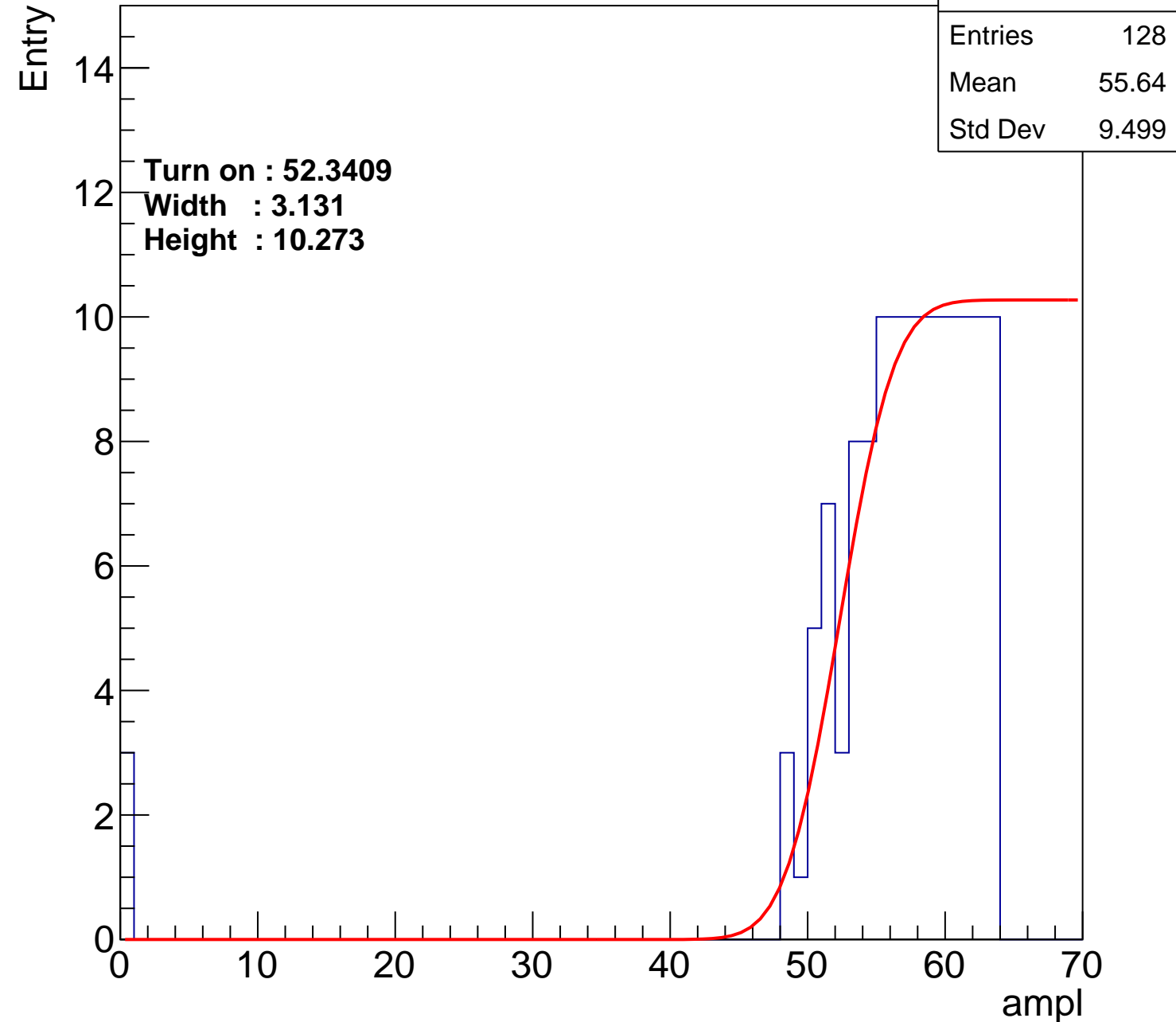
Width : 3.131

Height : 10.273

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	56.06
Std Dev	8.142

Turn on : 51.5927

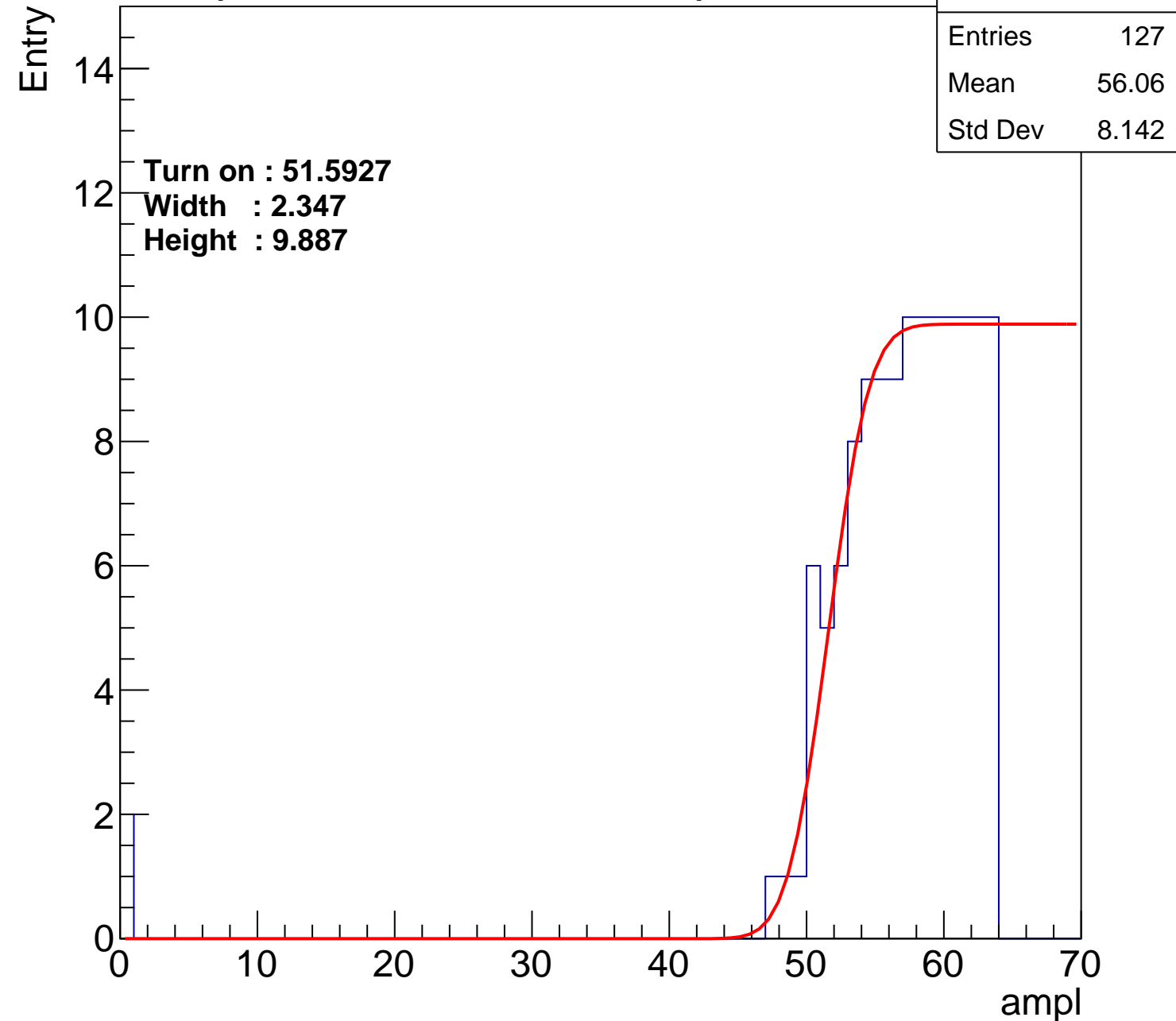
Width : 2.347

Height : 9.887

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch80

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.73
Std Dev	8.2

Turn on : 51.2433

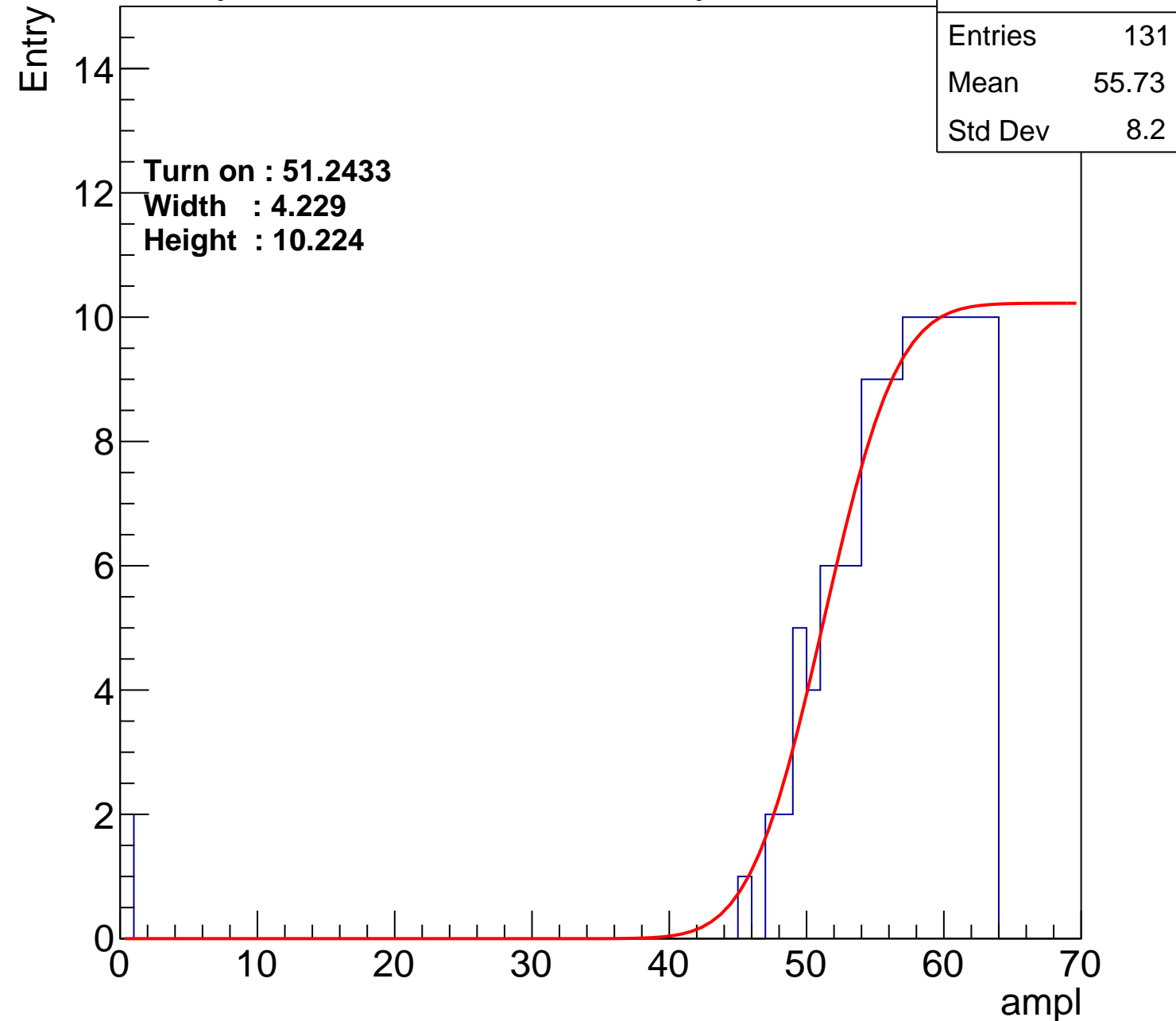
Width : 4.229

Height : 10.224

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch81

calib_packv5_040323_1717.root, FC#2, port C3

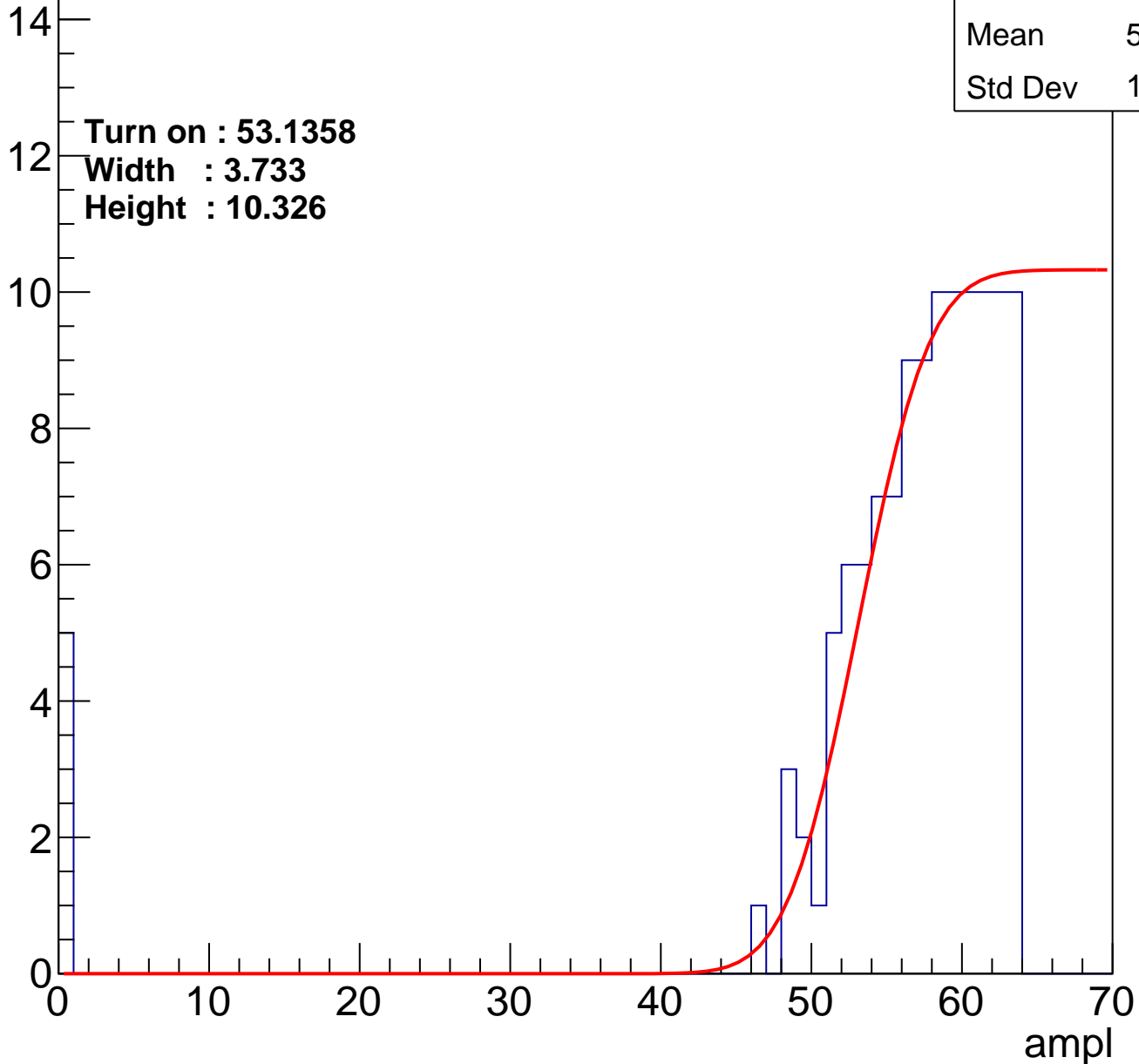
Entries	121
Mean	54.82
Std Dev	12.08

Turn on : 53.1358

Width : 3.733

Height : 10.326

Entry



B0L103S, U11-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	53.44
Std Dev	14.12

Turn on : 51.7182

Width : 4.239

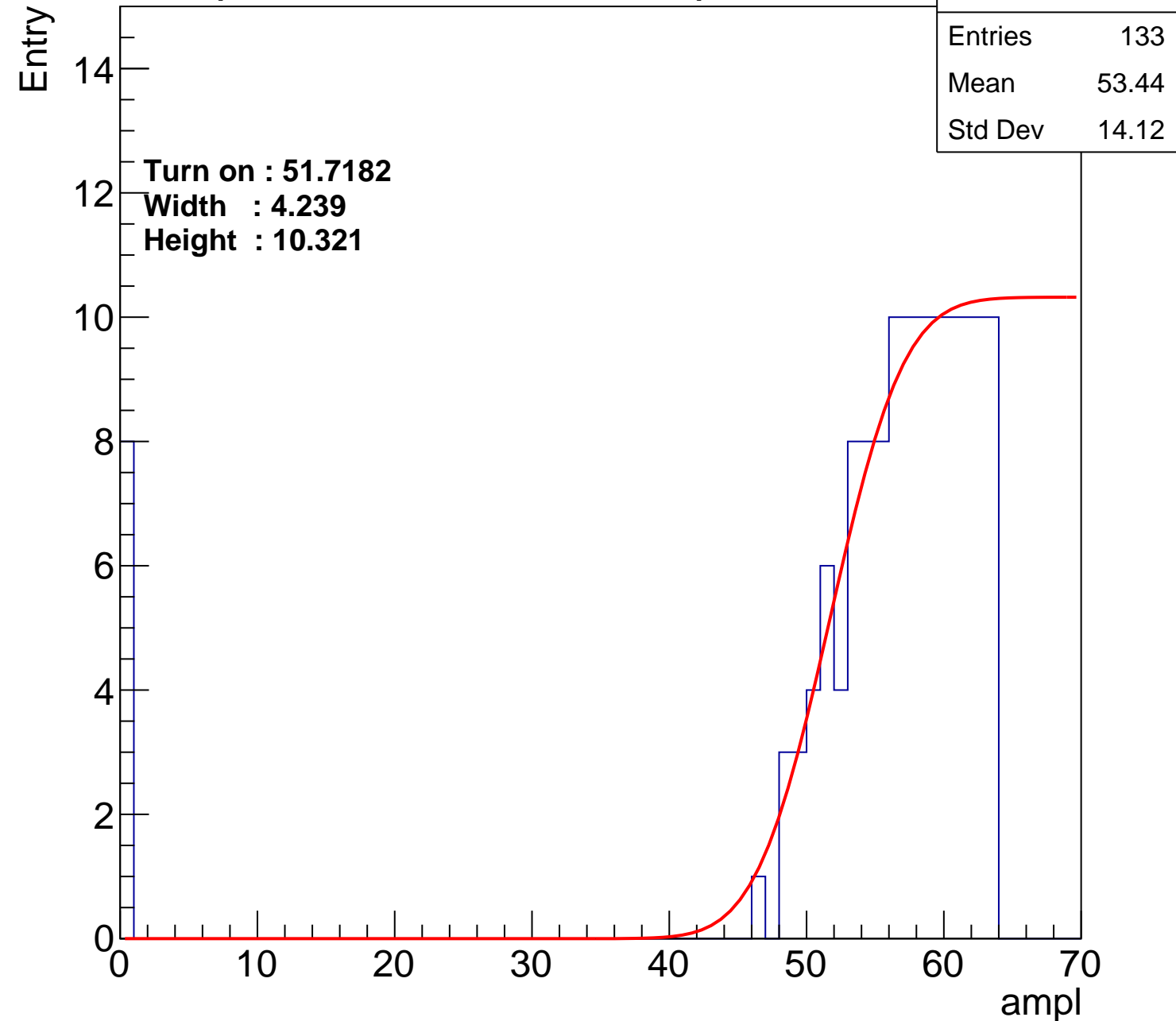
Height : 10.321

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch83

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.5
Std Dev	6.478

Turn on : 51.7843

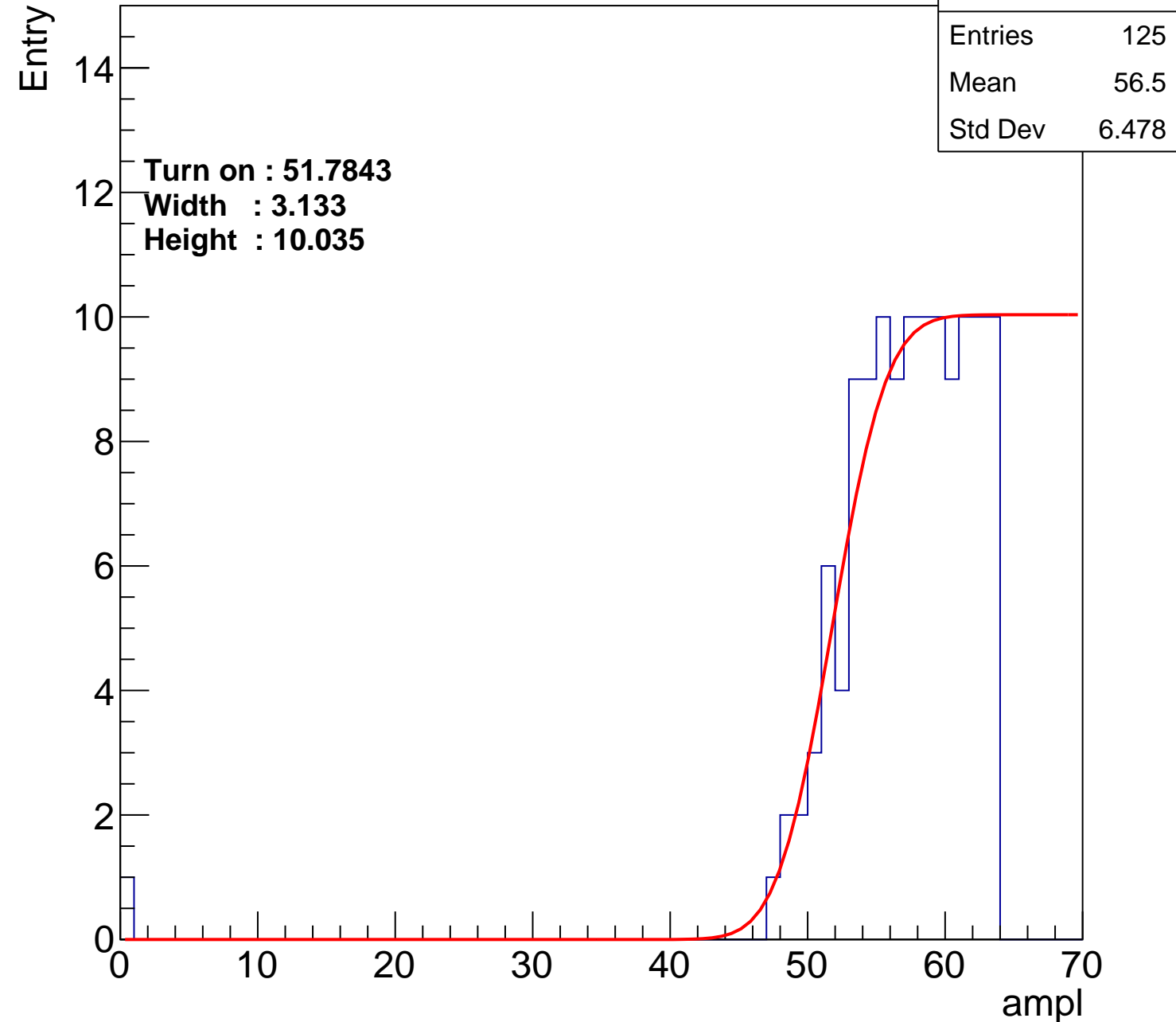
Width : 3.133

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch84

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	55.14
Std Dev	12.49

Turn on : 53.6201

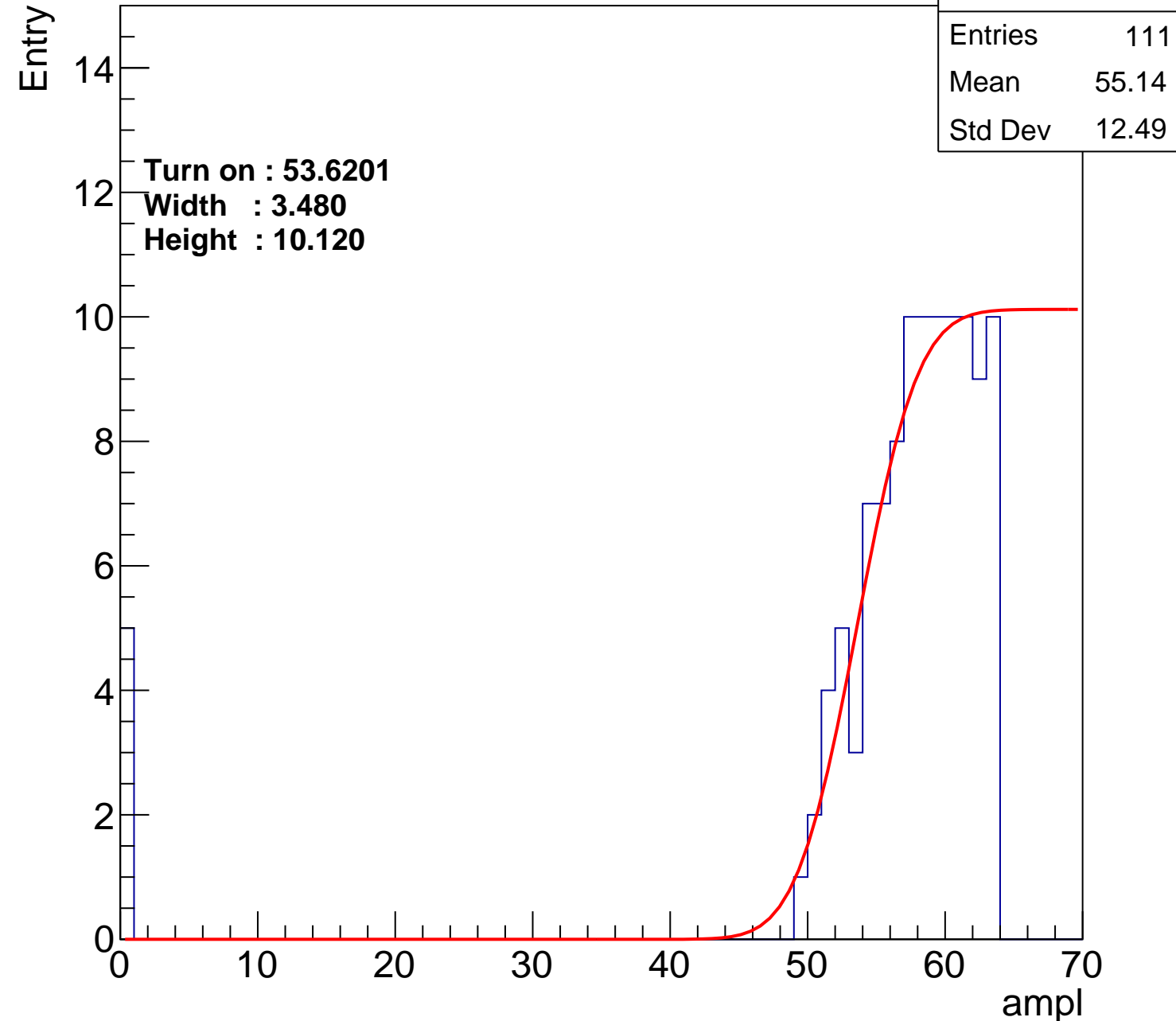
Width : 3.480

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	103
Mean	57.26
Std Dev	6.668

Turn on : 53.5070

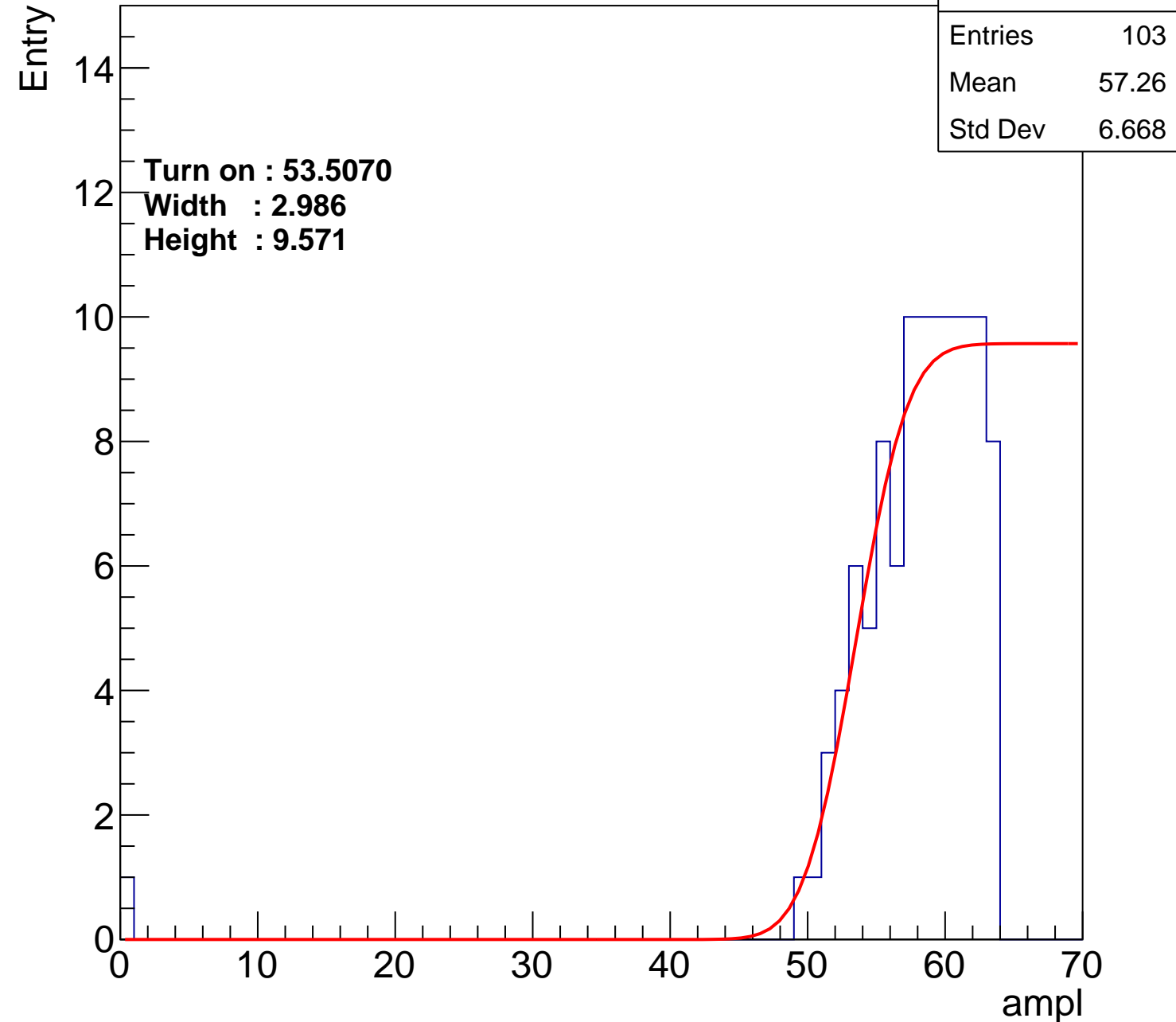
Width : 2.986

Height : 9.571

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch86

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.95
Std Dev	8.181

Turn on : 51.0175

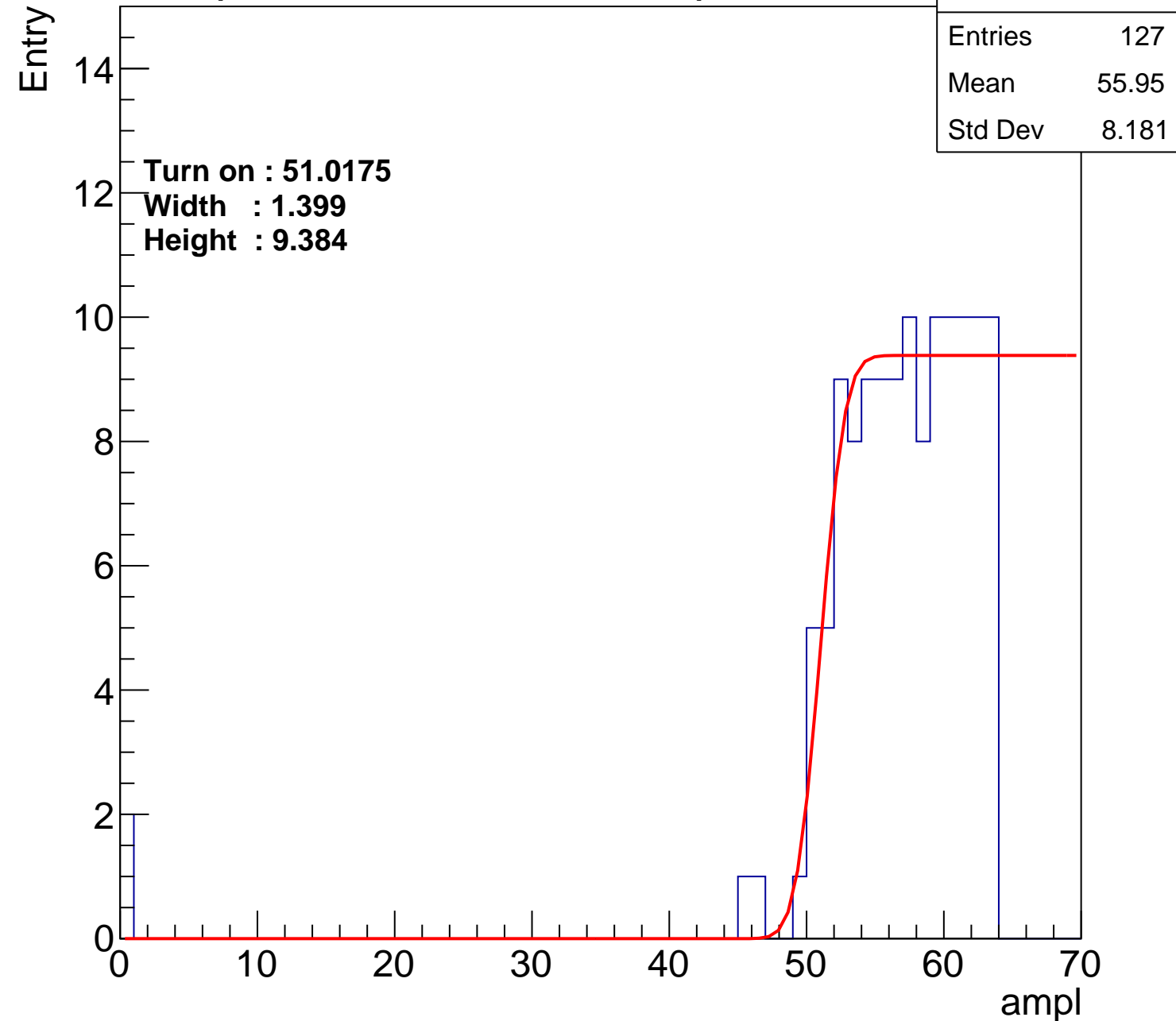
Width : 1.399

Height : 9.384

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	104
Mean	56.43
Std Dev	10.29

Turn on : 54.4974

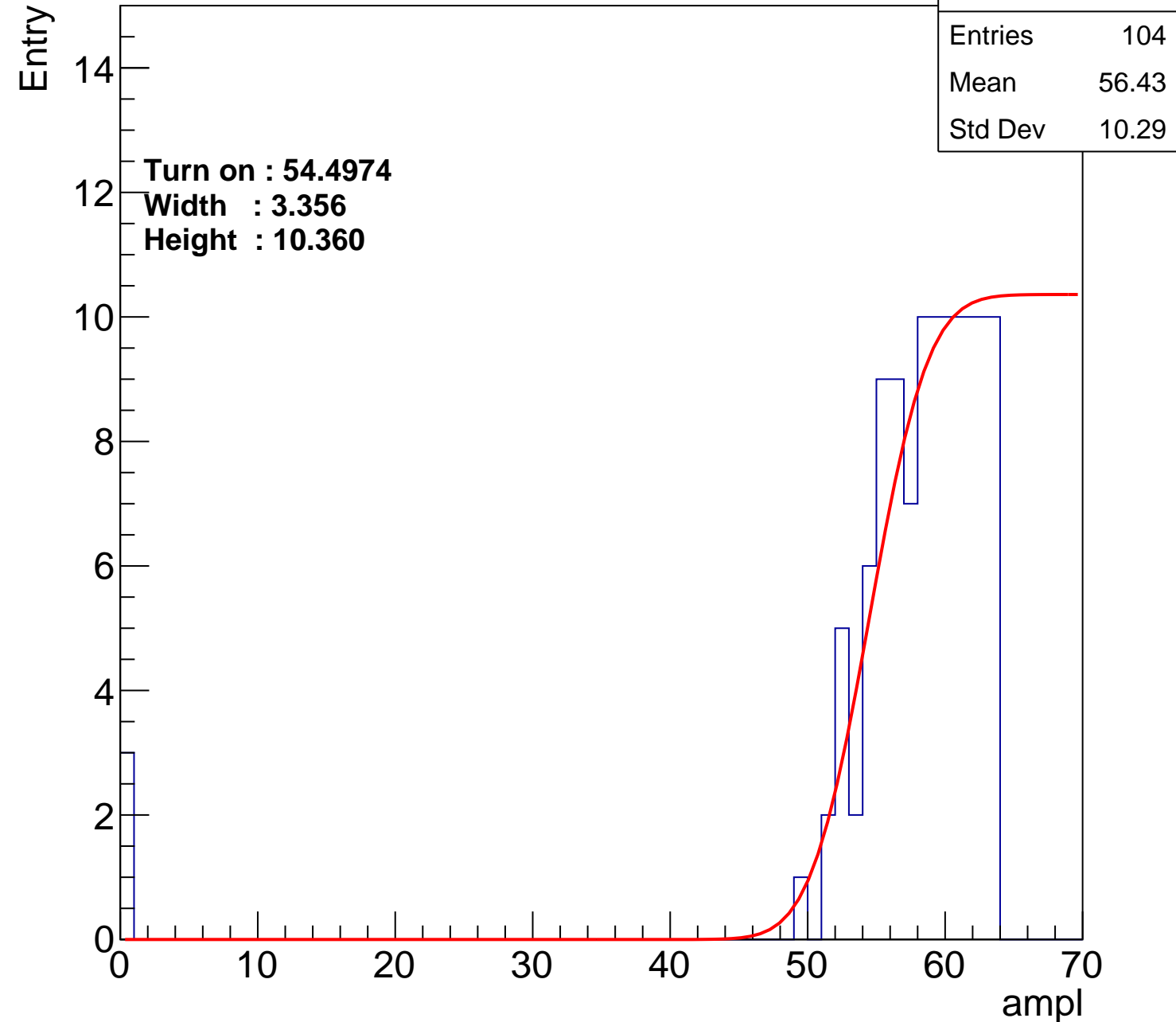
Width : 3.356

Height : 10.360

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.89
Std Dev	9.773

Turn on : 52.4289

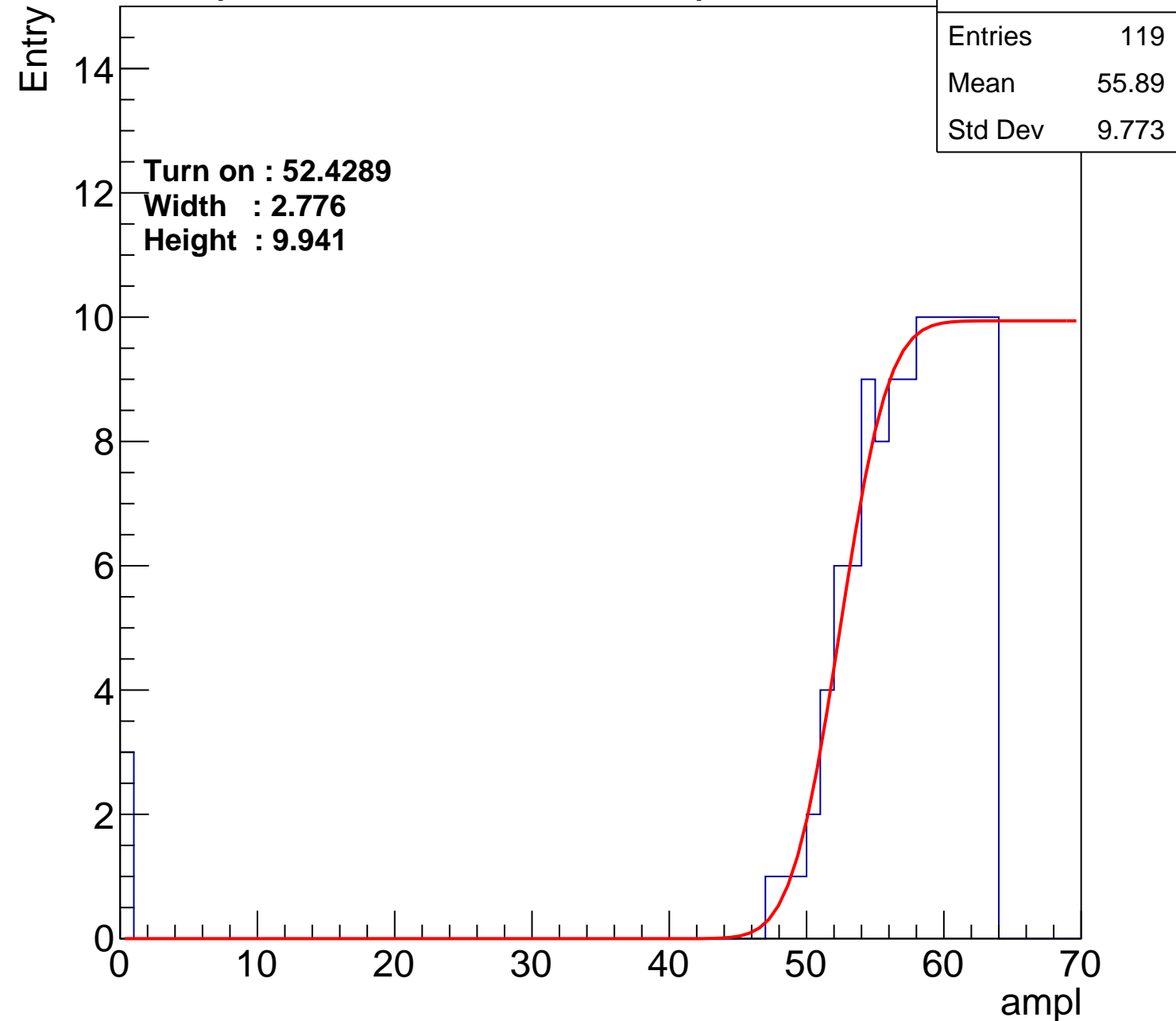
Width : 2.776

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch89

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	56.61
Std Dev	6.546

Turn on : 51.8793

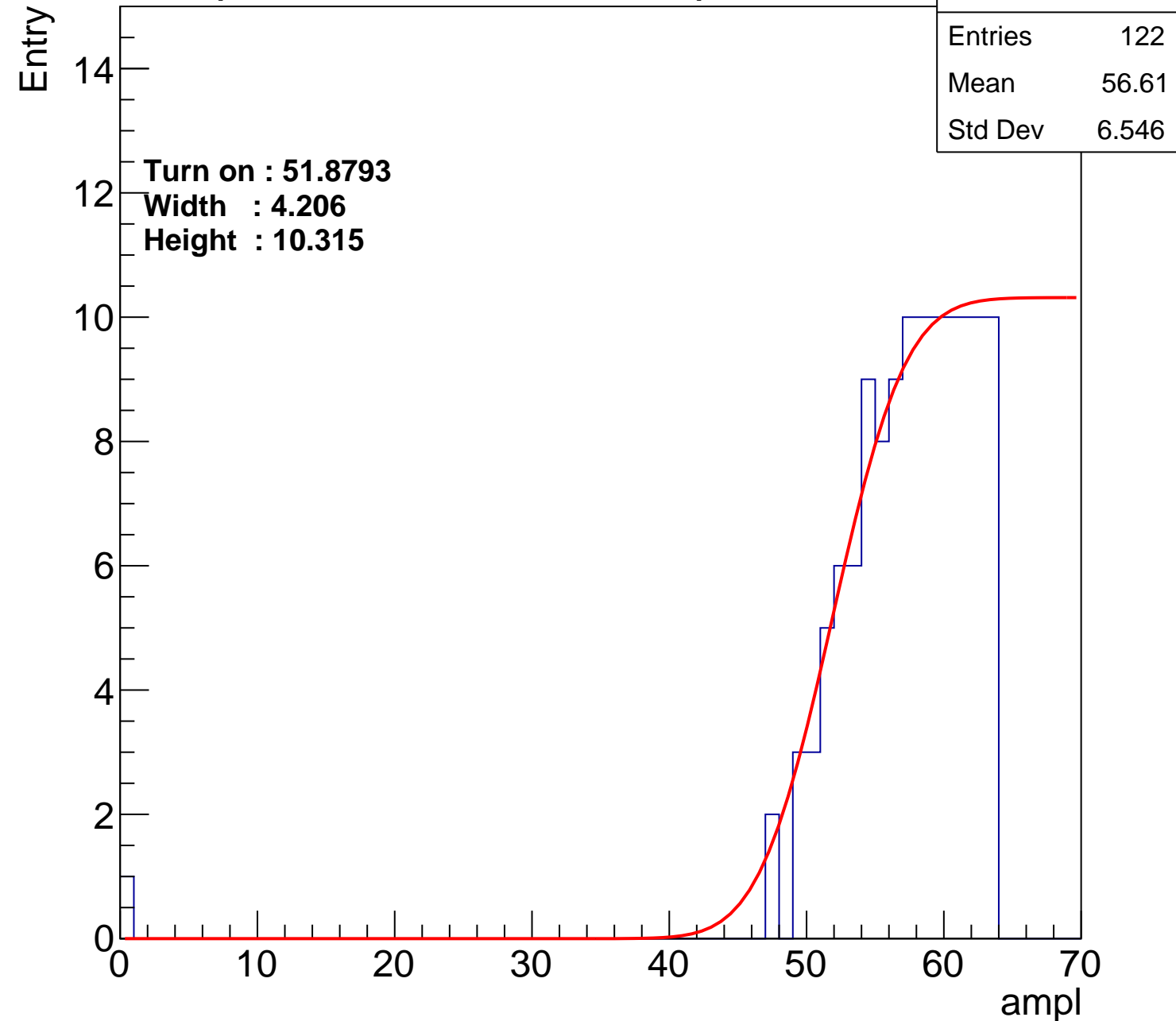
Width : 4.206

Height : 10.315

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.39
Std Dev	9.374

Turn on : 51.1365

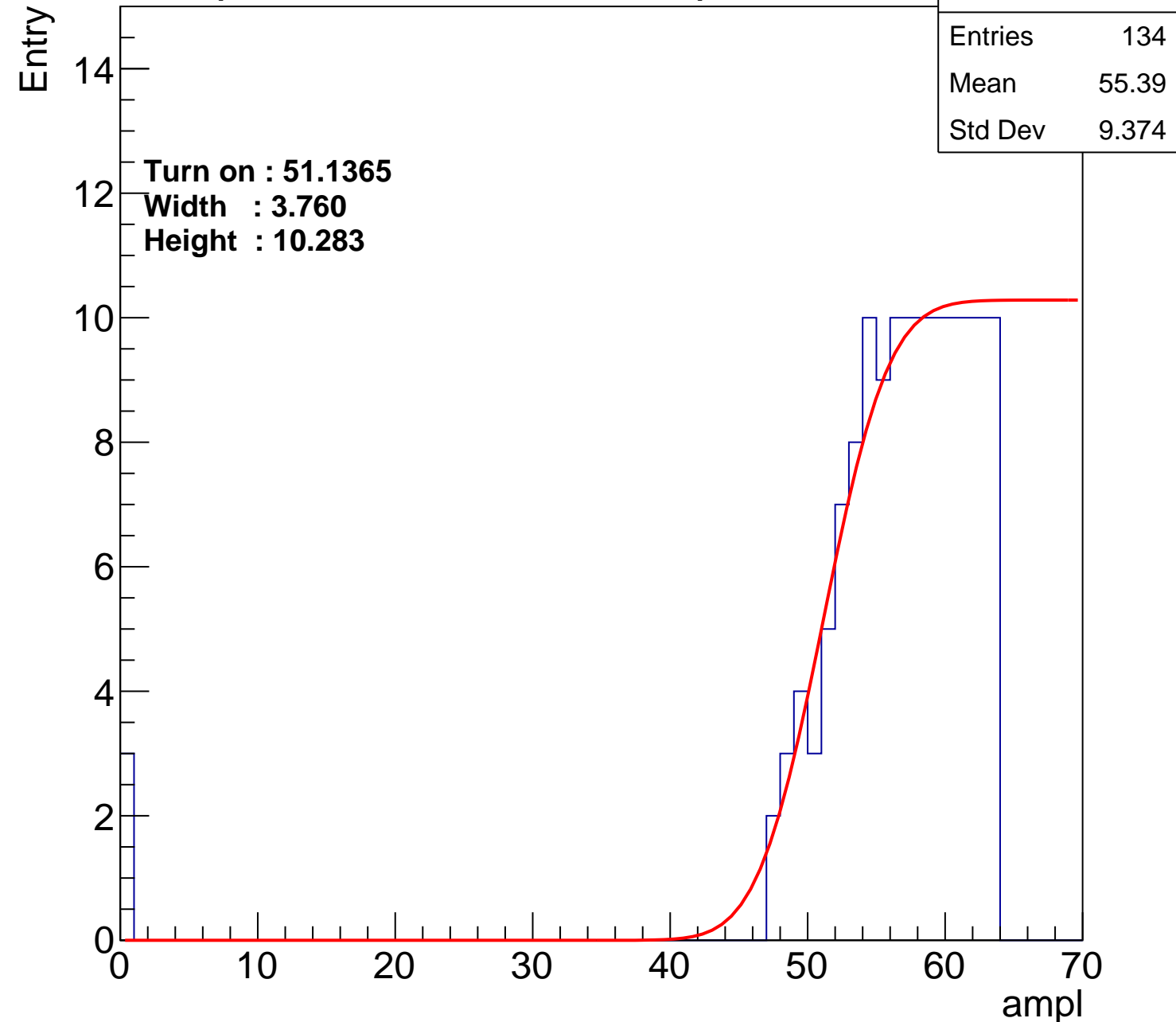
Width : 3.760

Height : 10.283

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch91

calib_packv5_040323_1717.root, FC#2, port C3

Entries	100
Mean	56.97
Std Dev	8.797

Turn on : 53.4315

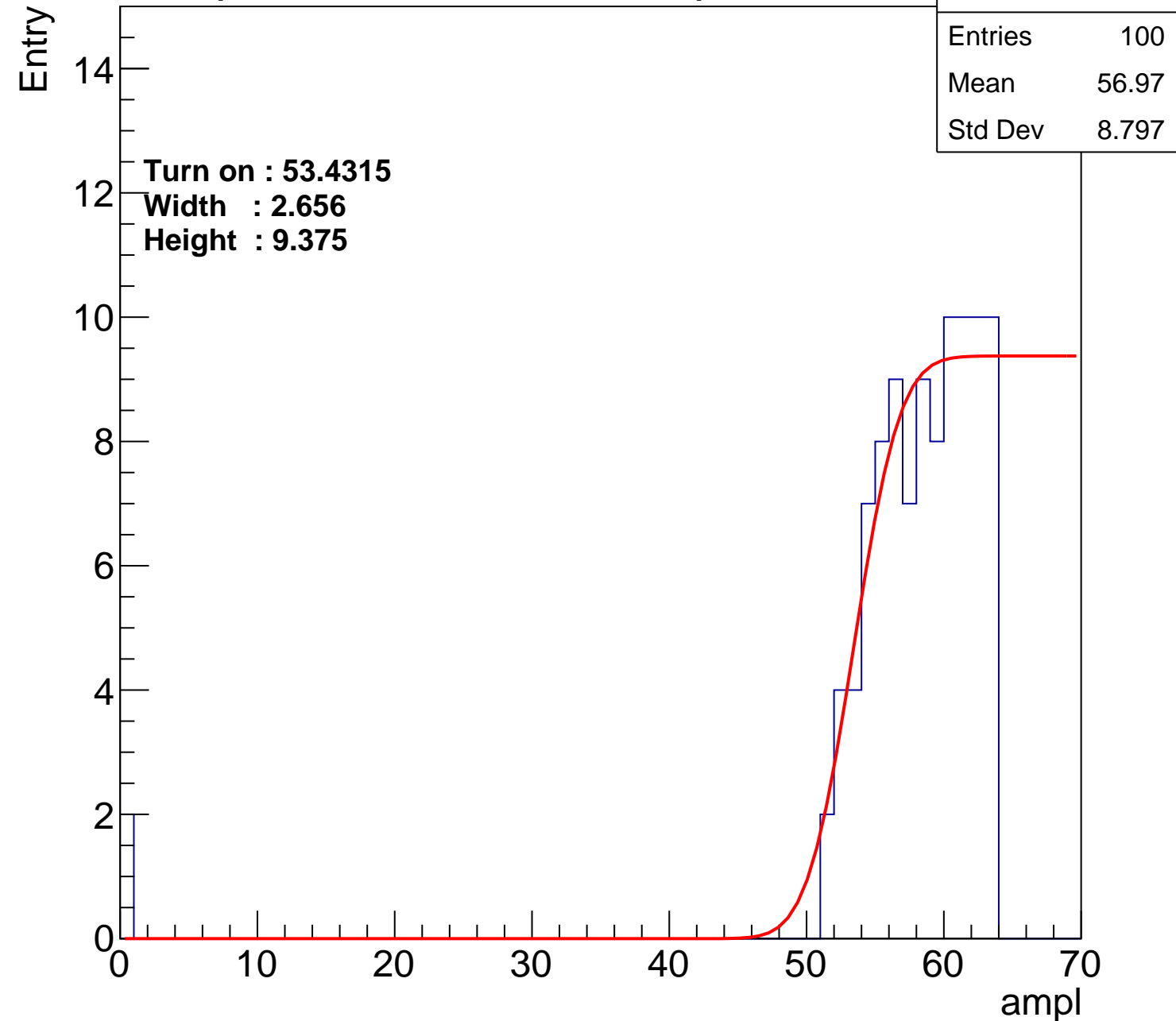
Width : 2.656

Height : 9.375

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch92

calib_packv5_040323_1717.root, FC#2, port C3

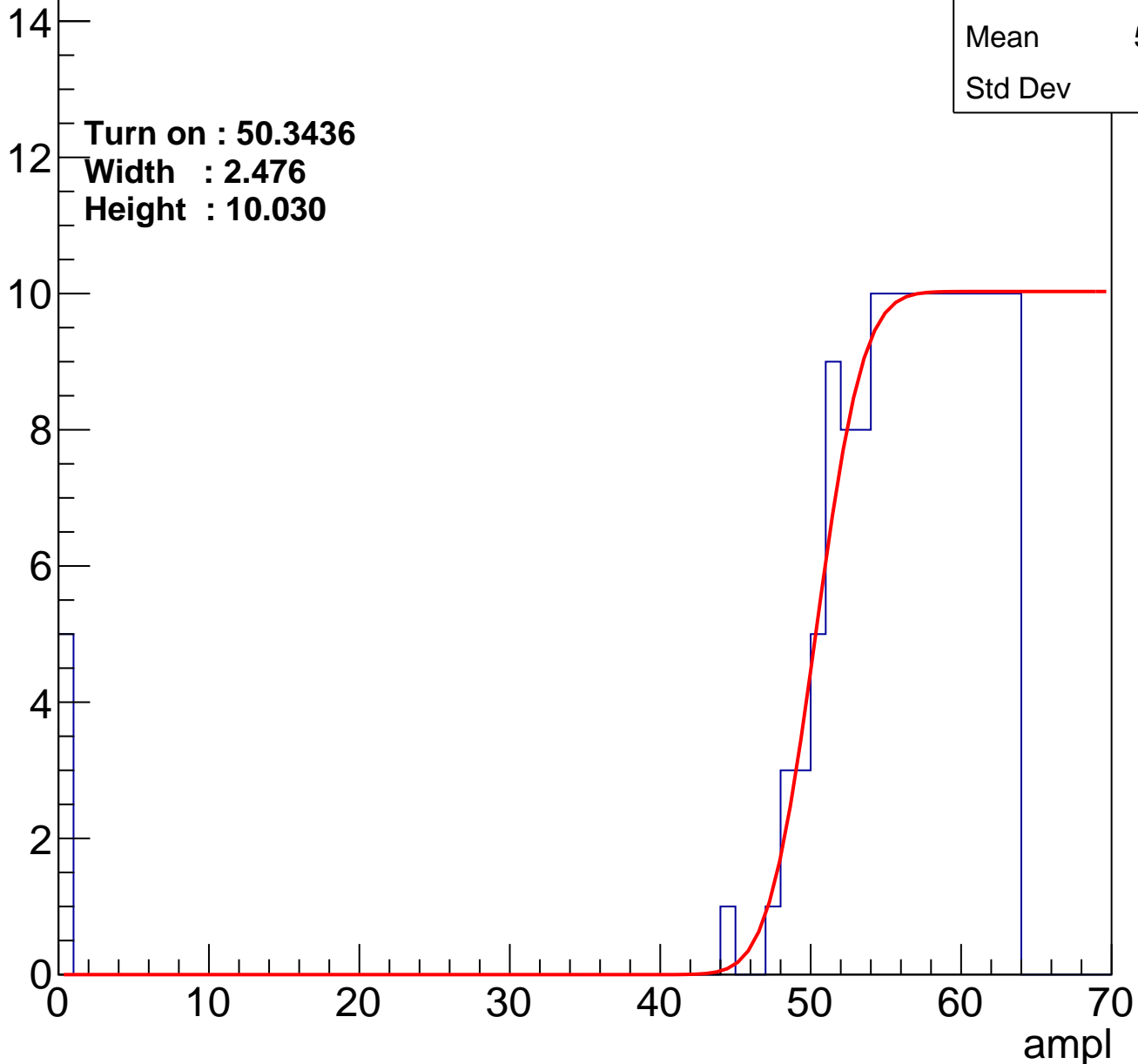
Entries	143
Mean	54.41
Std Dev	11.2

Turn on : 50.3436

Width : 2.476

Height : 10.030

Entry



B0L103S, U11-ch93

calib_packv5_040323_1717.root, FC#2, port C3

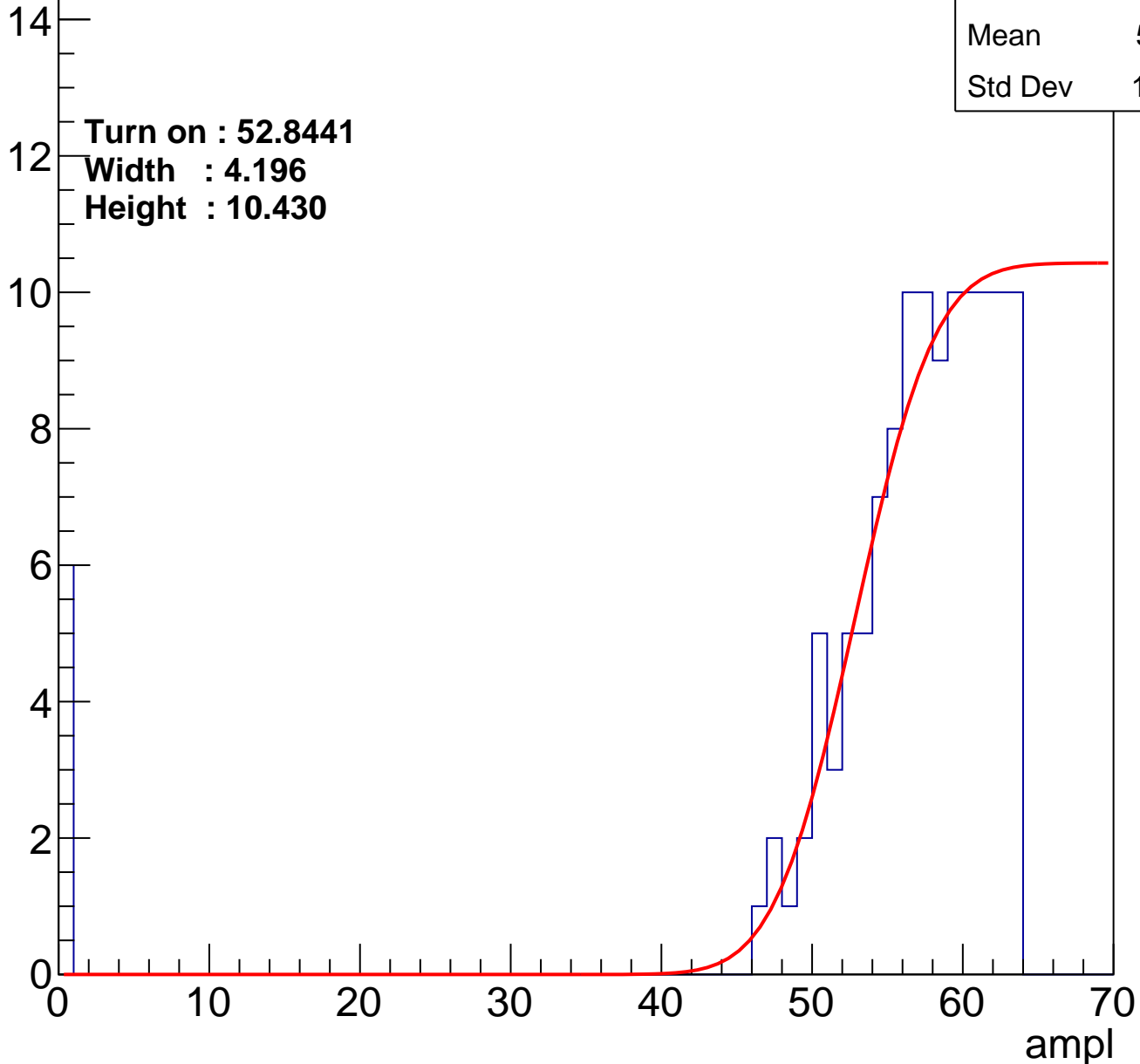
Entries	124
Mean	54.31
Std Dev	12.92

Turn on : 52.8441

Width : 4.196

Height : 10.430

Entry



B0L103S, U11-ch94

calib_packv5_040323_1717.root, FC#2, port C3

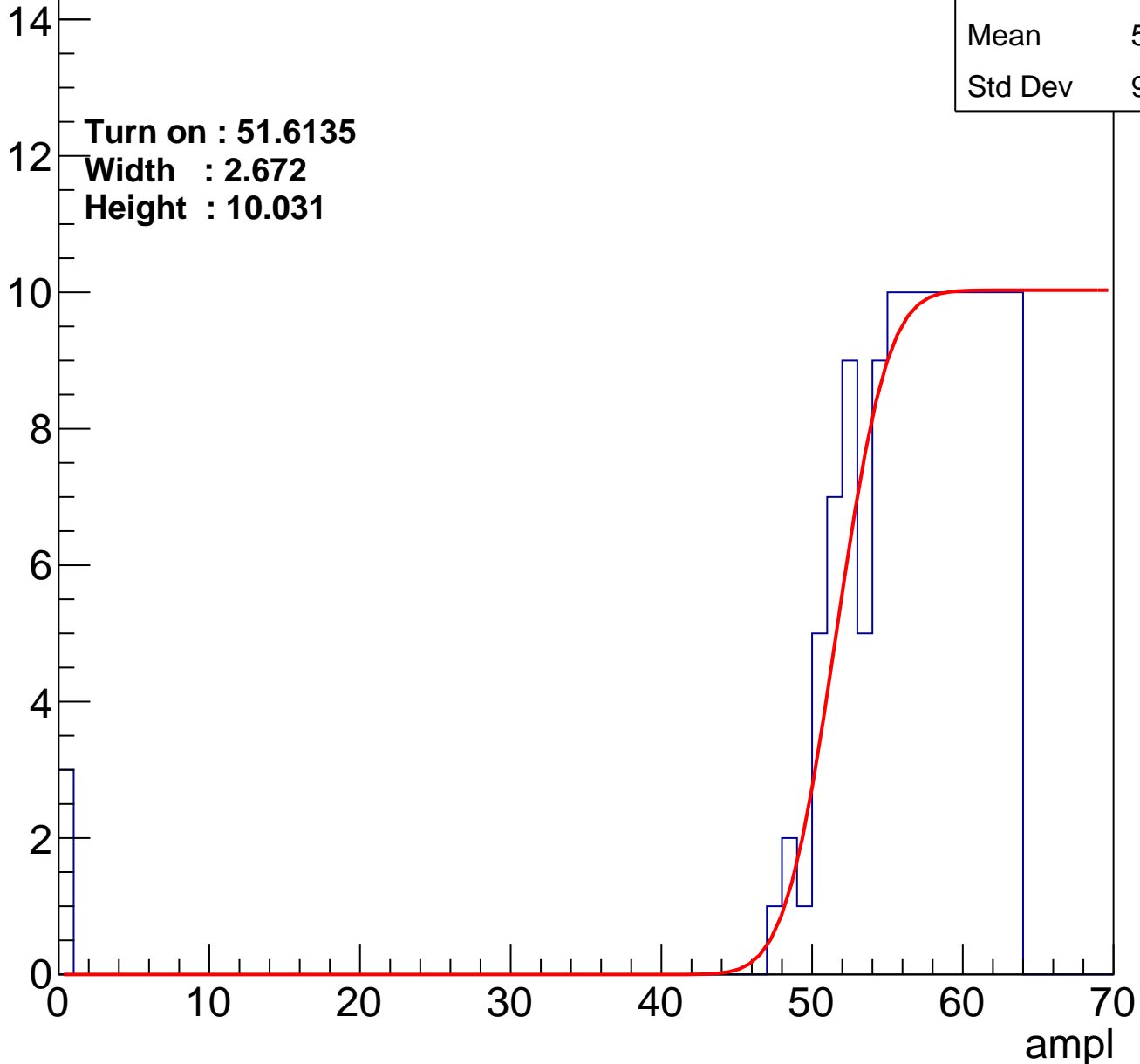
Entries	132
Mean	55.52
Std Dev	9.384

Turn on : 51.6135

Width : 2.672

Height : 10.031

Entry



B0L103S, U11-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.25
Std Dev	8.309

Turn on : 52.1462

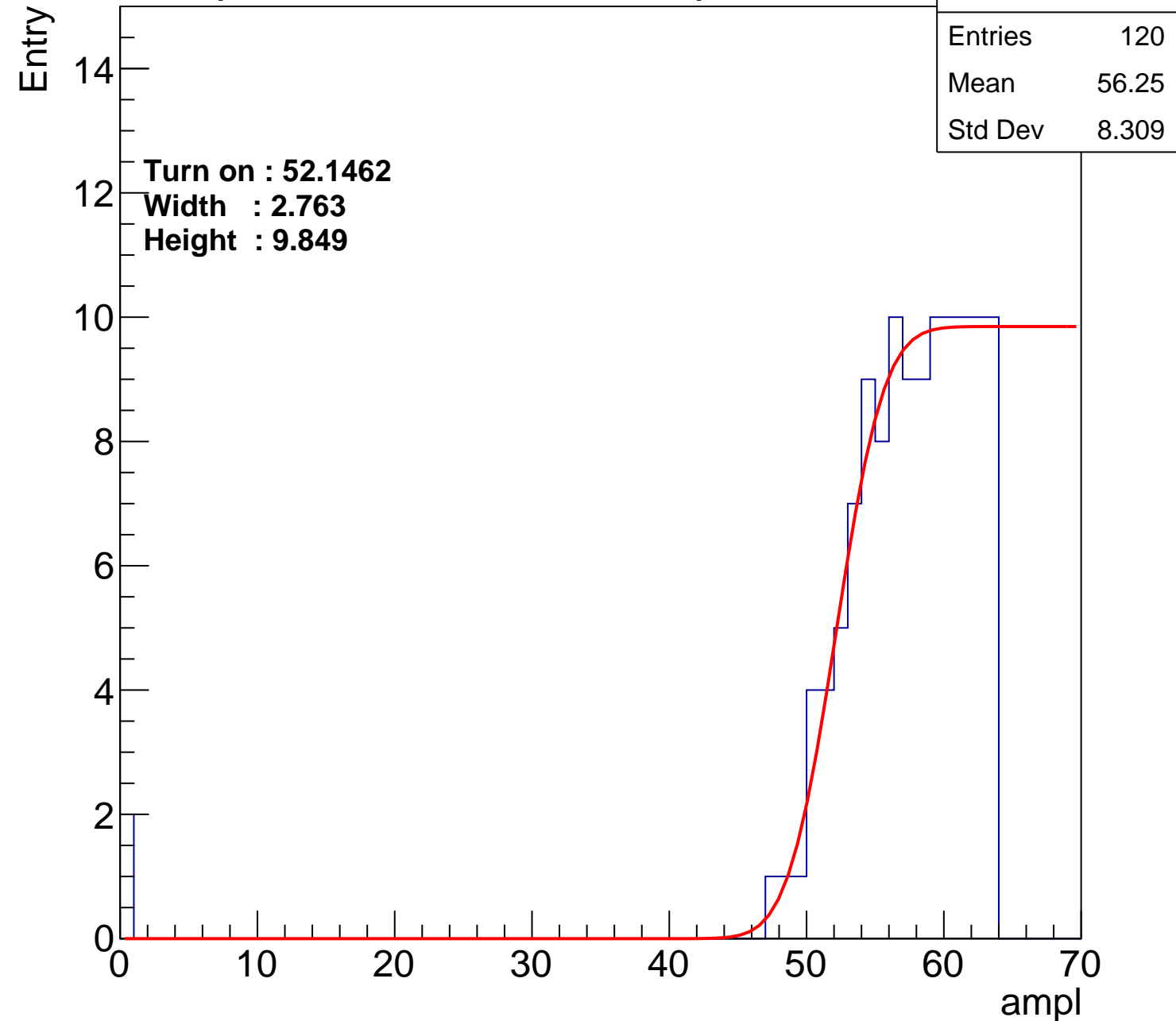
Width : 2.763

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch96

calib_packv5_040323_1717.root, FC#2, port C3

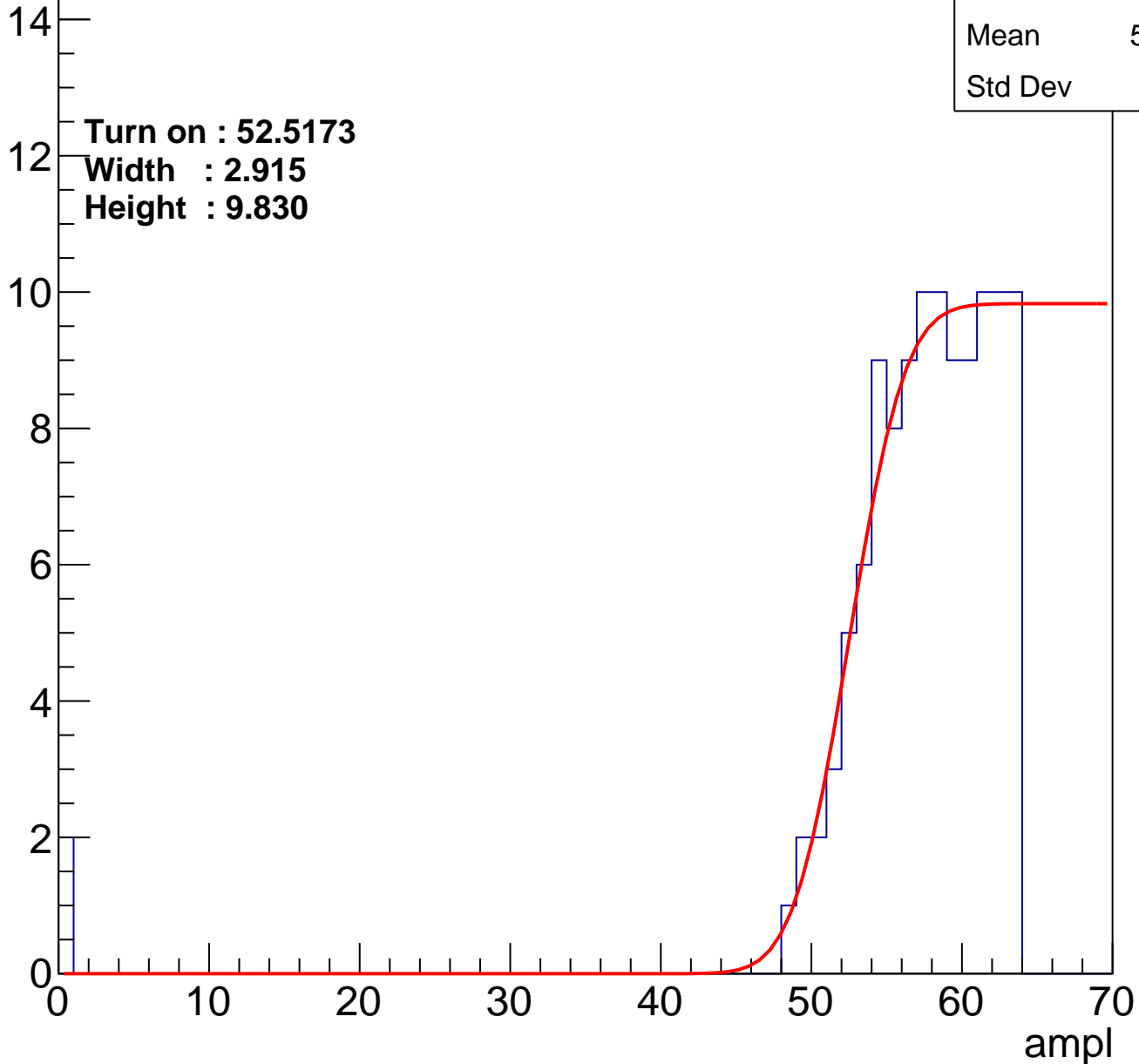
Entries	115
Mean	56.42
Std Dev	8.4

Turn on : 52.5173

Width : 2.915

Height : 9.830

Entry



B0L103S, U11-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.15
Std Dev	10.94

Turn on : 52.7784

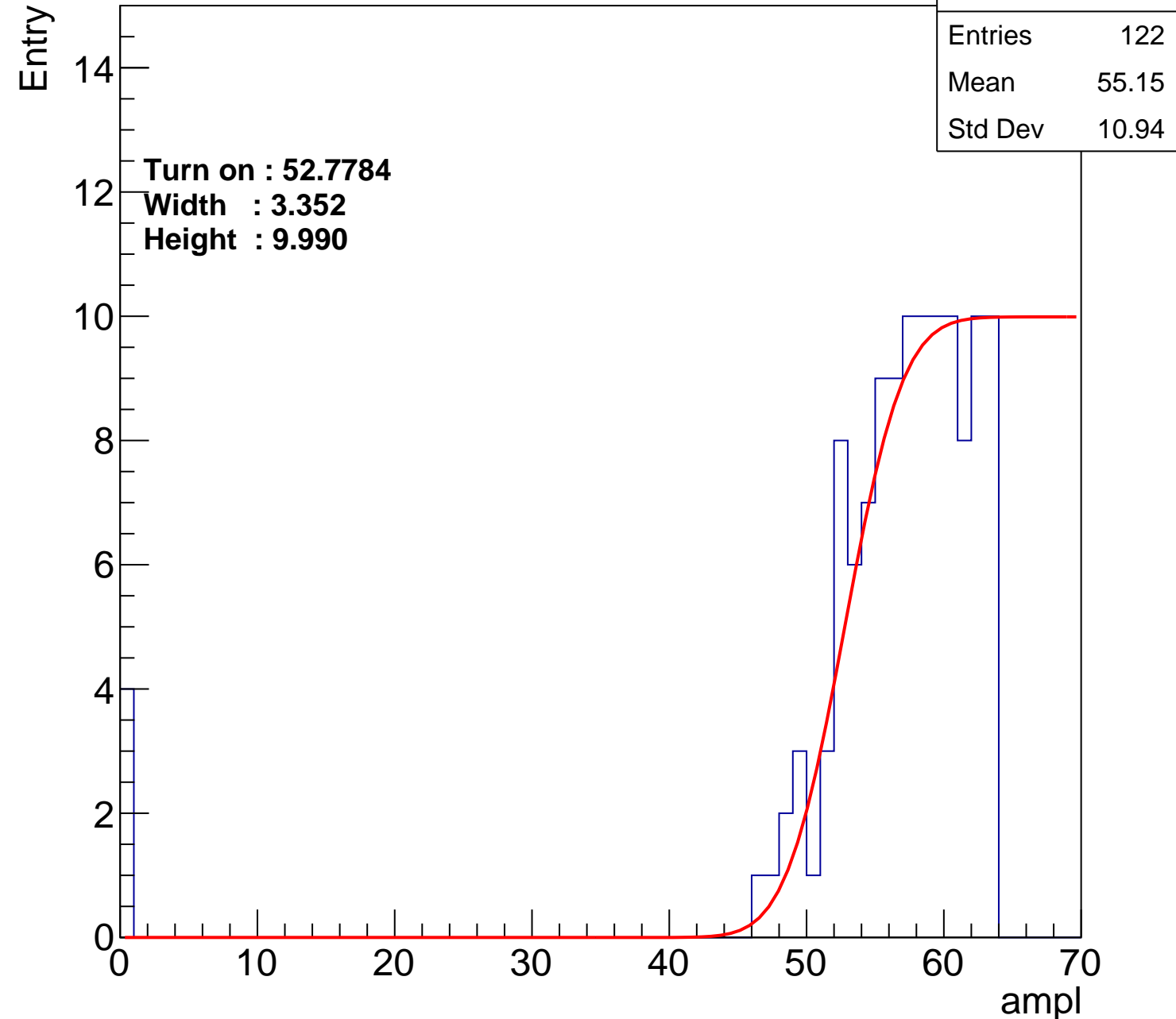
Width : 3.352

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch98

calib_packv5_040323_1717.root, FC#2, port C3

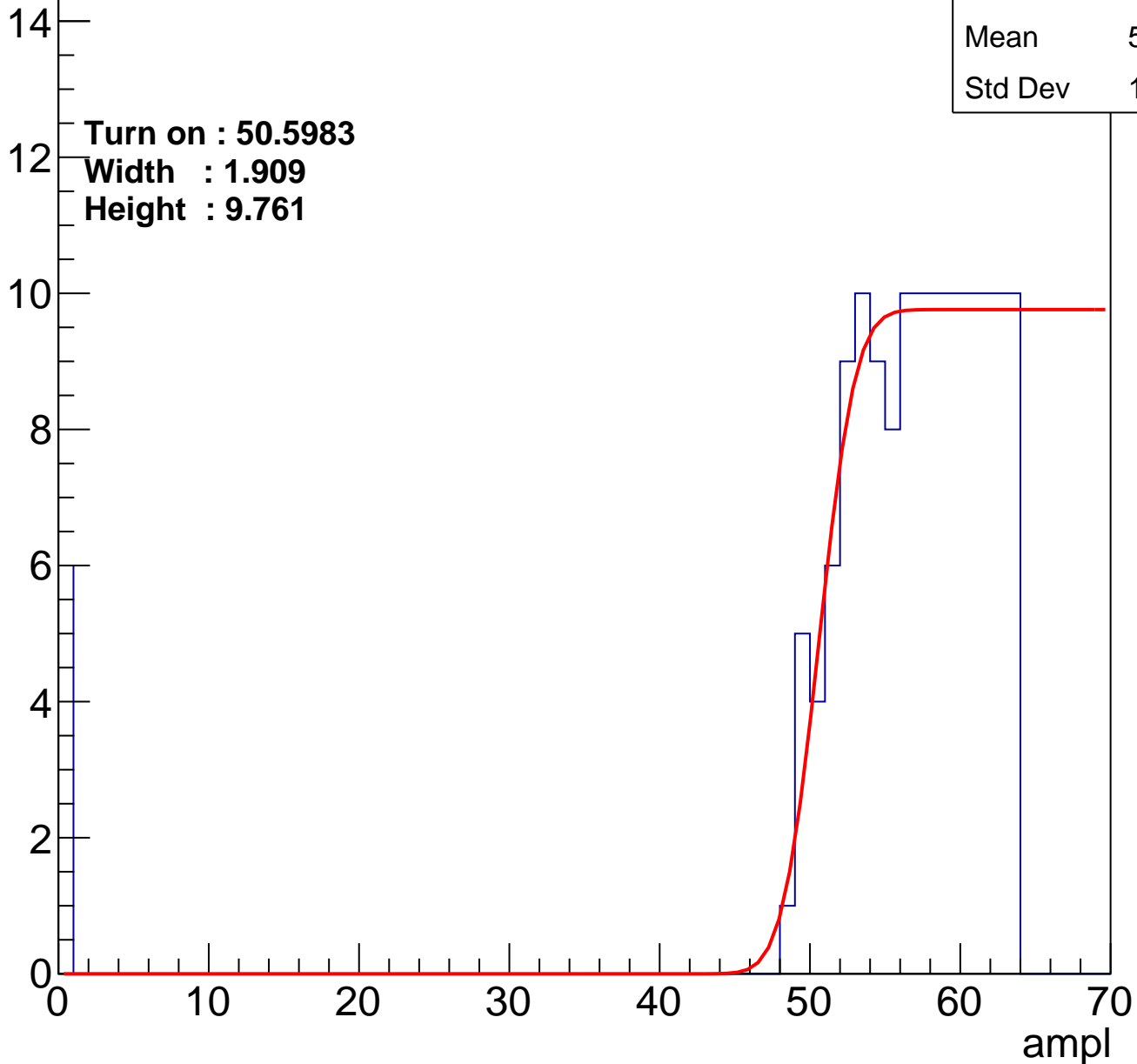
Entries	138
Mean	54.22
Std Dev	12.24

Turn on : 50.5983

Width : 1.909

Height : 9.761

Entry



B0L103S, U11-ch99

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.89
Std Dev	10.42

Turn on : 50.7093

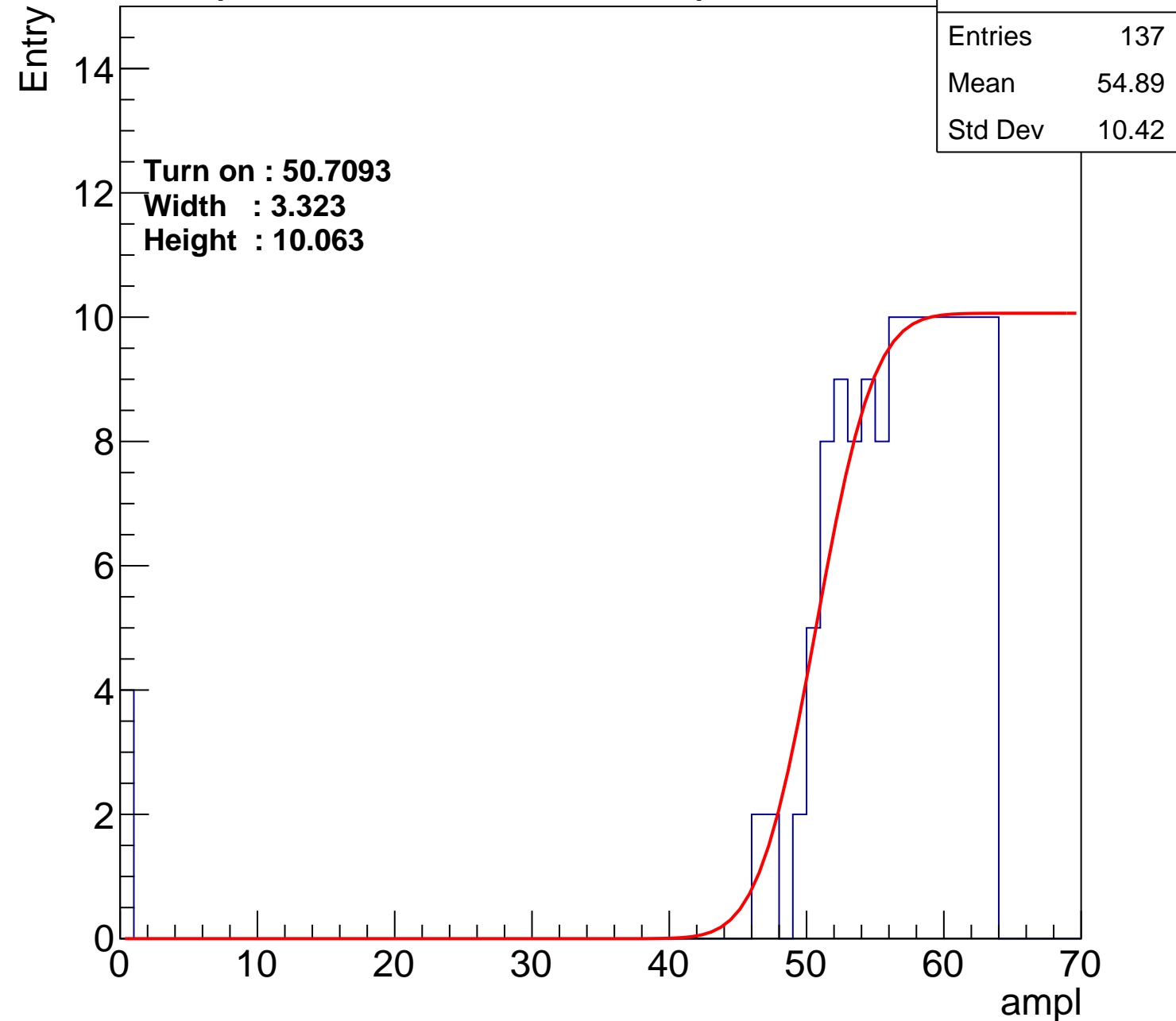
Width : 3.323

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	56.12
Std Dev	9.944

Turn on : 54.0963

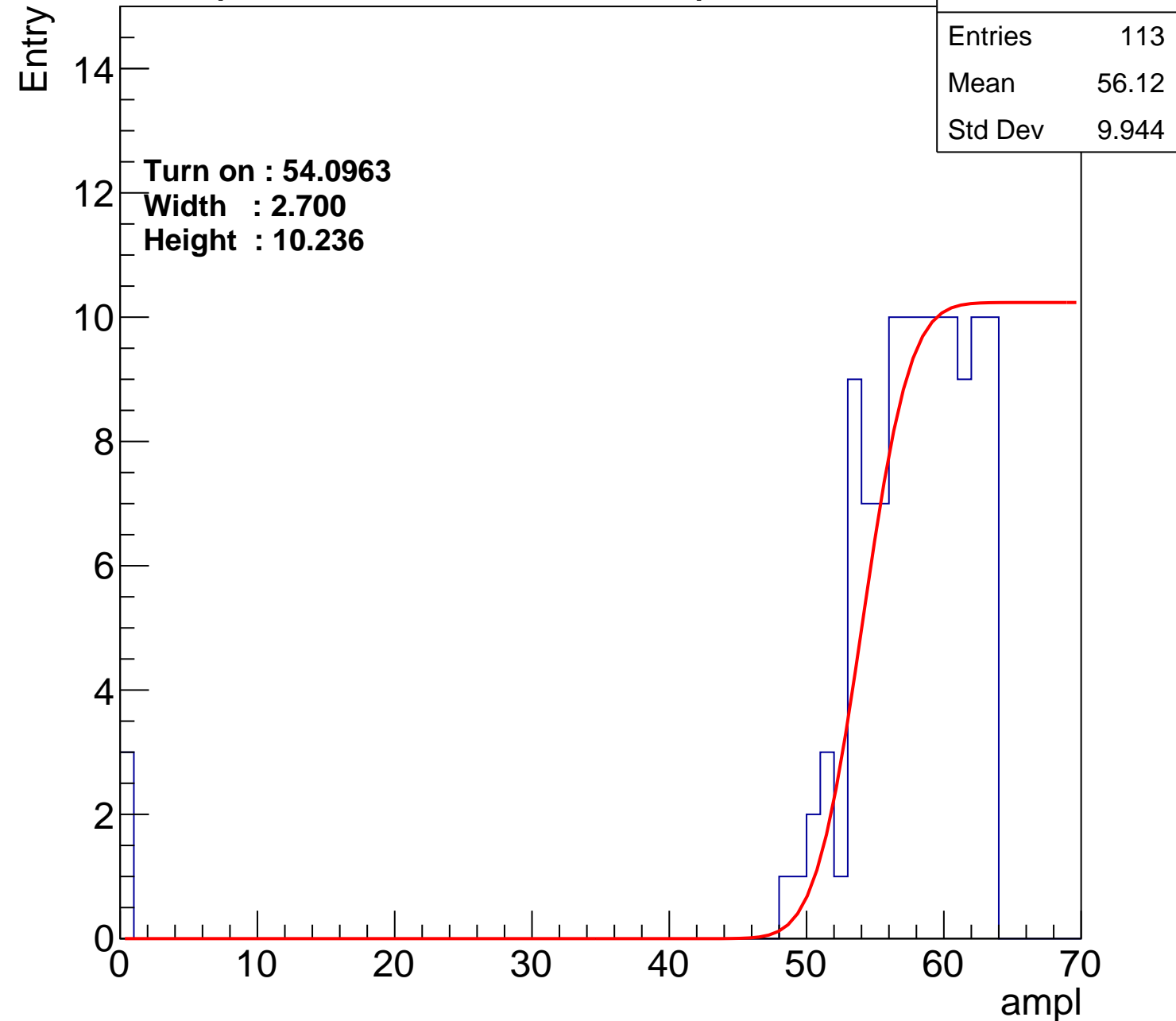
Width : 2.700

Height : 10.236

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch101

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.9
Std Dev	8.083

Turn on : 50.9188

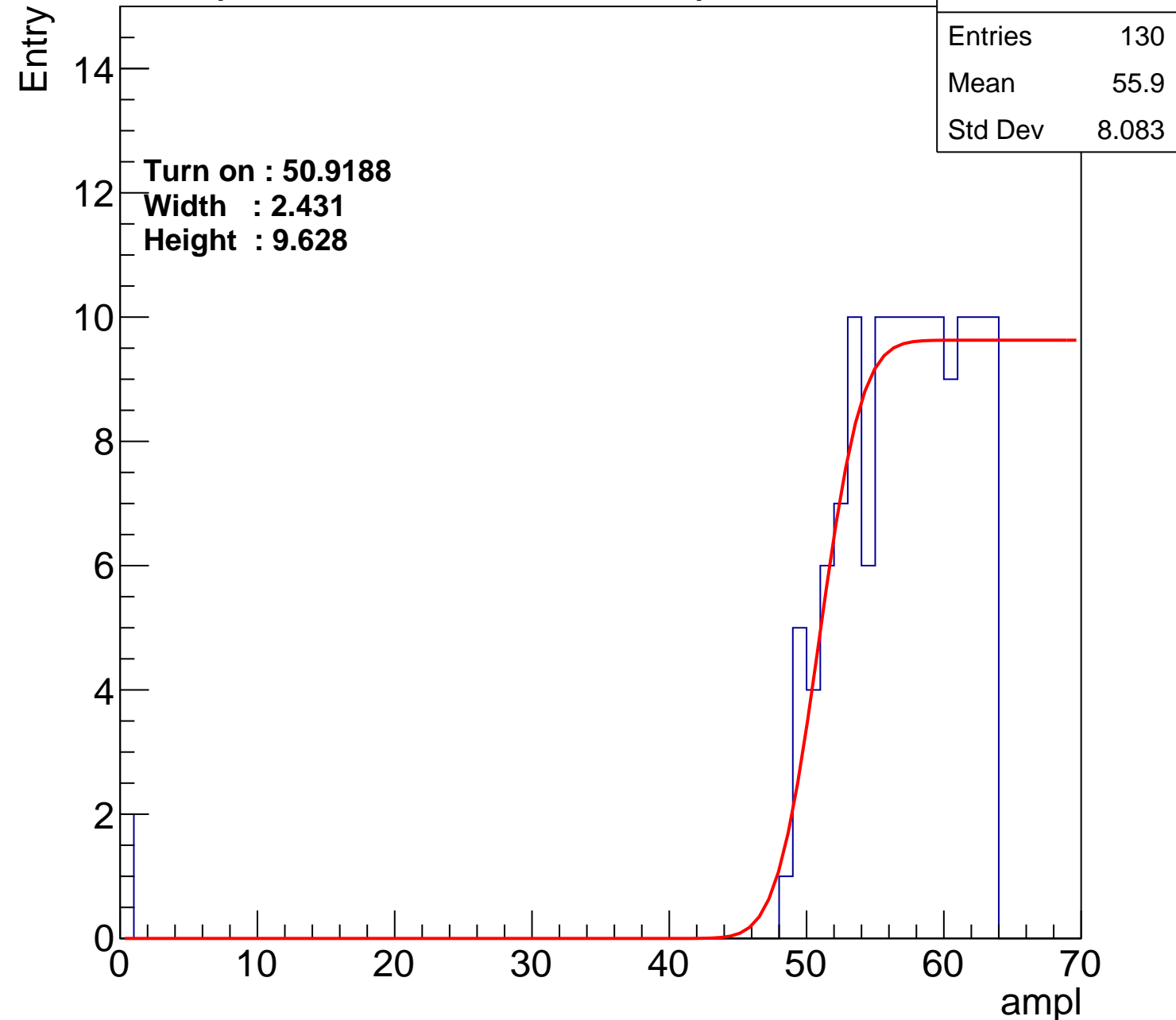
Width : 2.431

Height : 9.628

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch102

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.38
Std Dev	11.08

Turn on : 51.9702

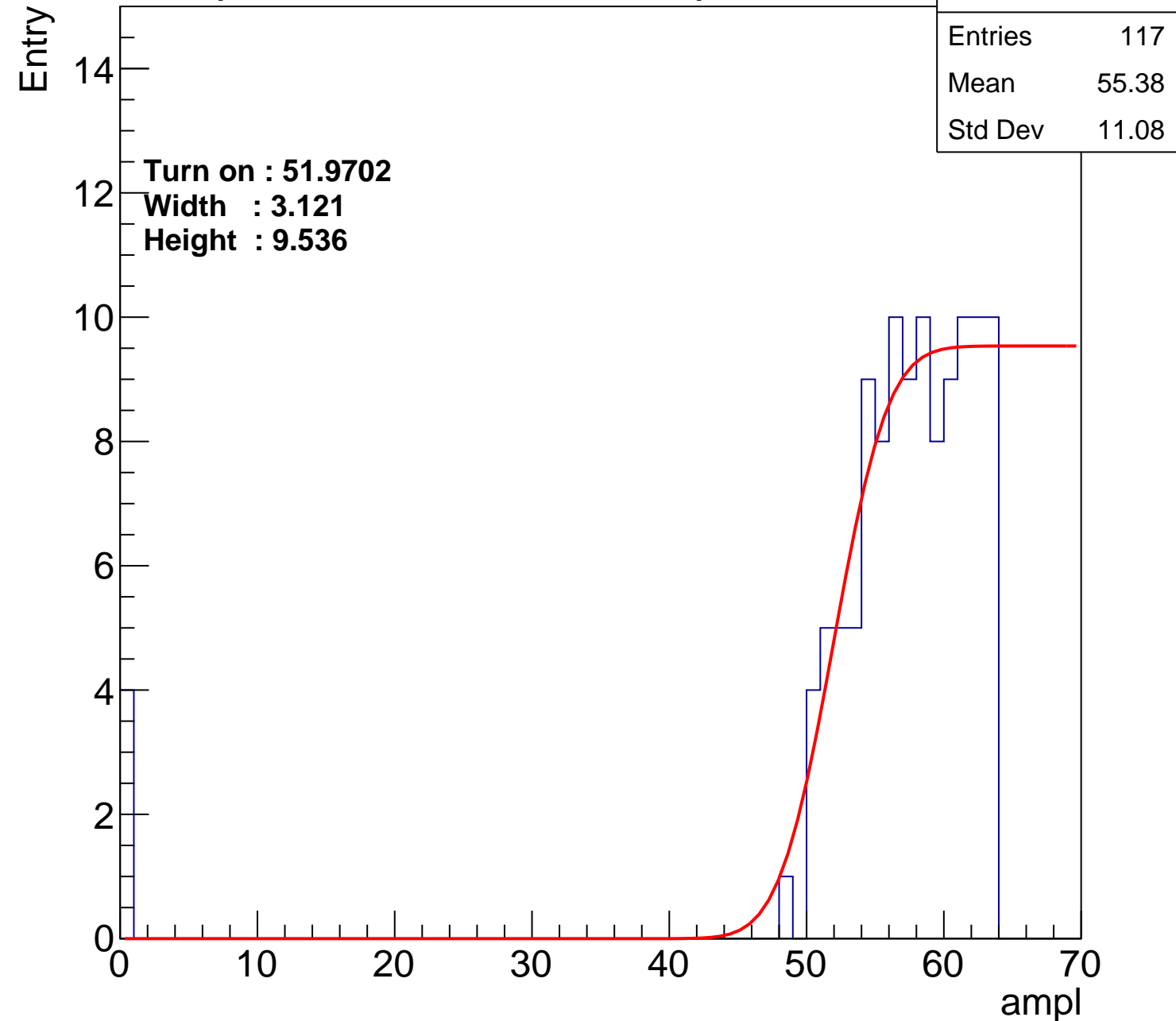
Width : 3.121

Height : 9.536

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch103

calib_packv5_040323_1717.root, FC#2, port C3

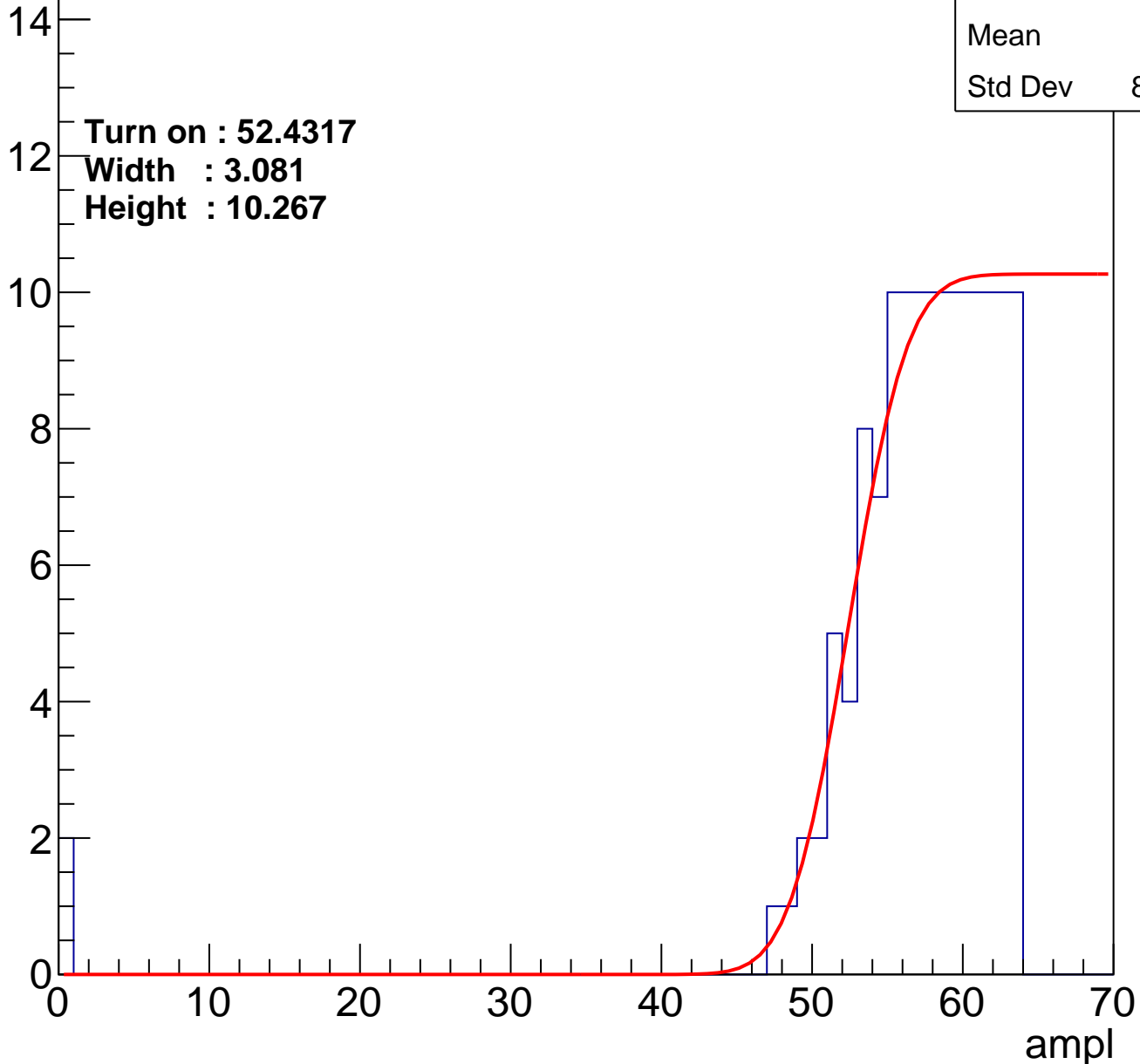
Entries	122
Mean	56.3
Std Dev	8.236

Turn on : 52.4317

Width : 3.081

Height : 10.267

Entry



B0L103S, U11-ch104

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	54.41
Std Dev	13.01

Turn on : 52.1313

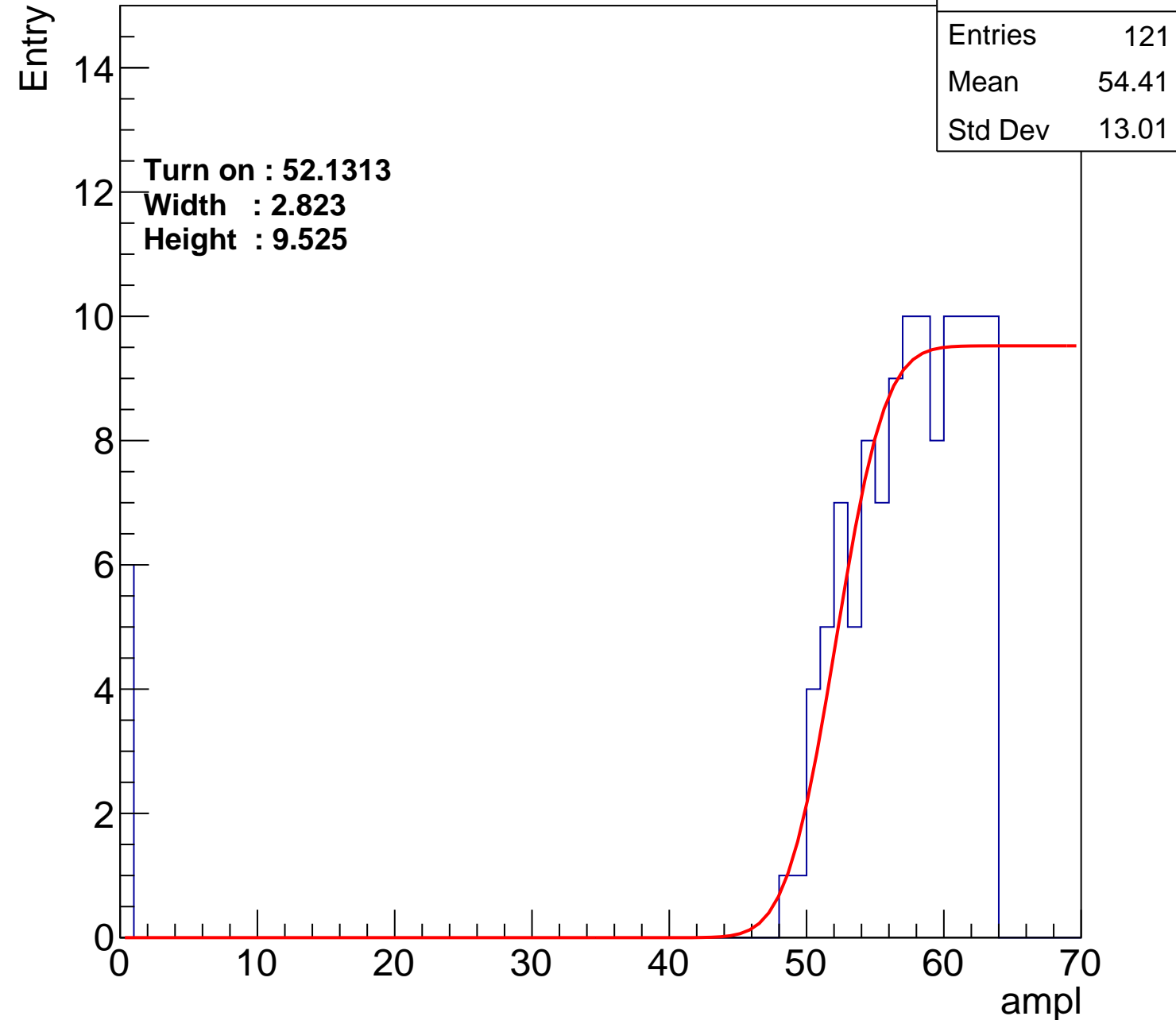
Width : 2.823

Height : 9.525

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	57.05
Std Dev	6.637

Turn on : 53.2043

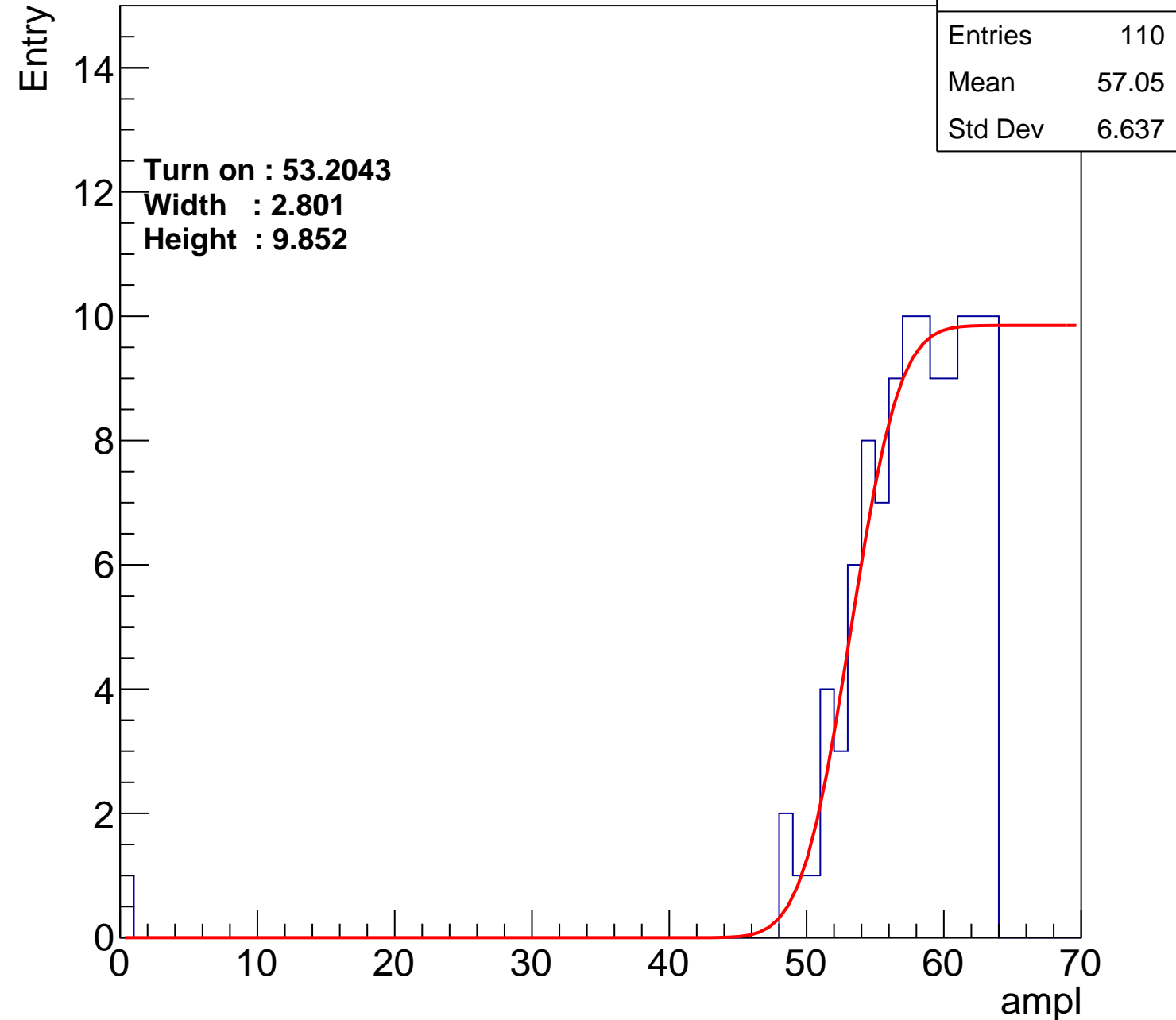
Width : 2.801

Height : 9.852

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.8
Std Dev	9.157

Turn on : 49.3265

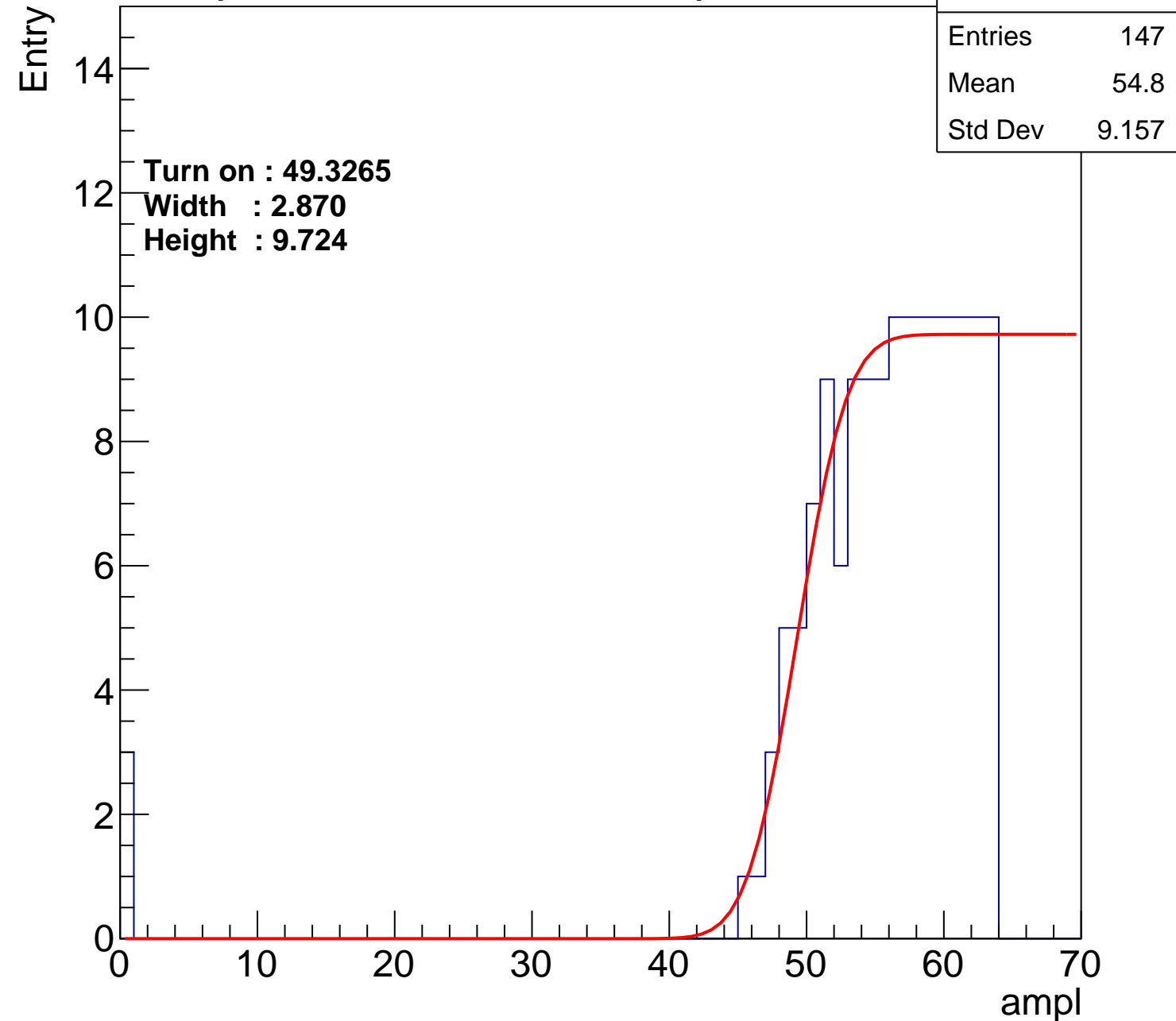
Width : 2.870

Height : 9.724

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	105
Mean	56.73
Std Dev	8.657

Turn on : 53.3788

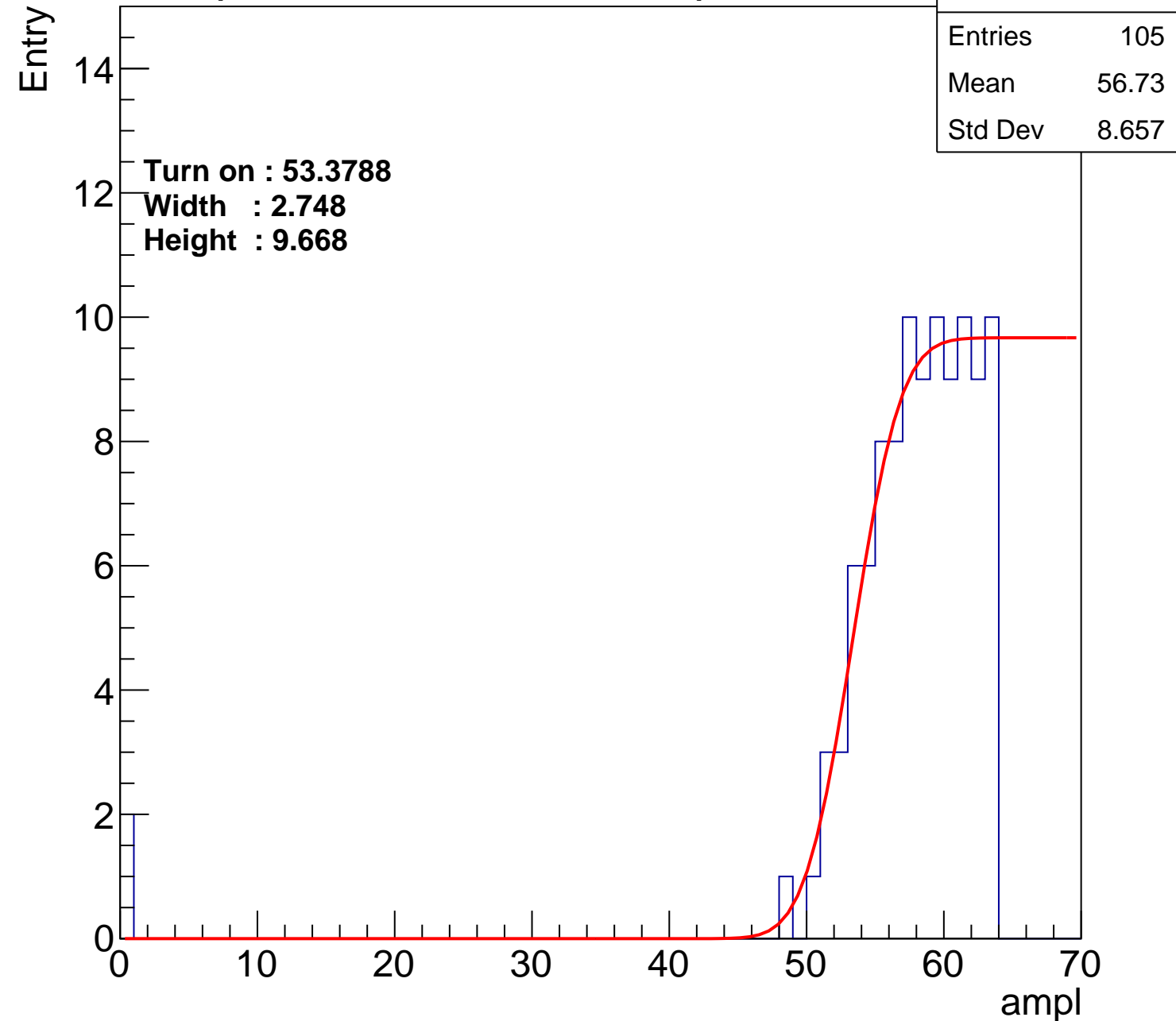
Width : 2.748

Height : 9.668

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch108

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.01
Std Dev	9.288

Turn on : 50.7319

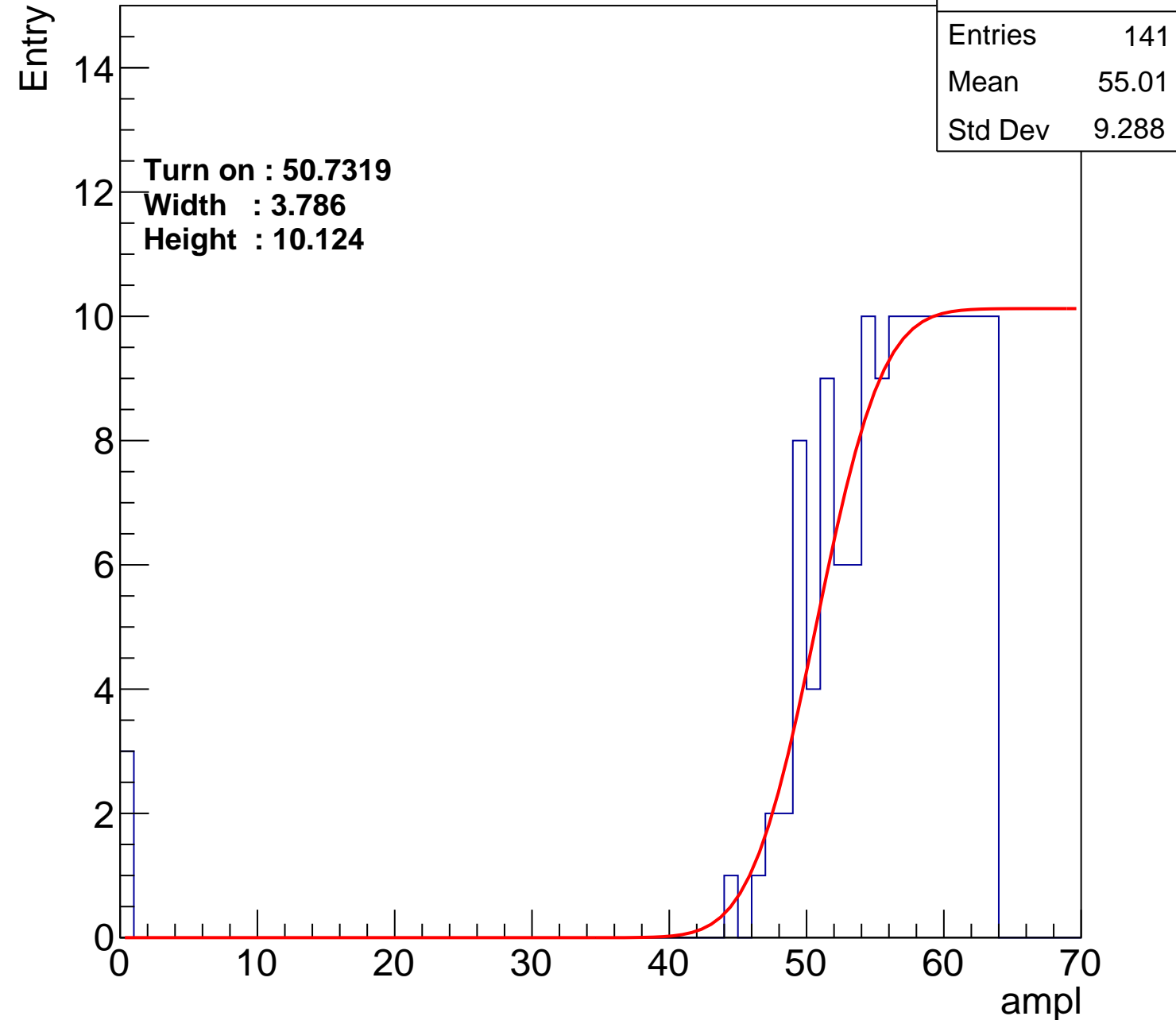
Width : 3.786

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch109

calib_packv5_040323_1717.root, FC#2, port C3

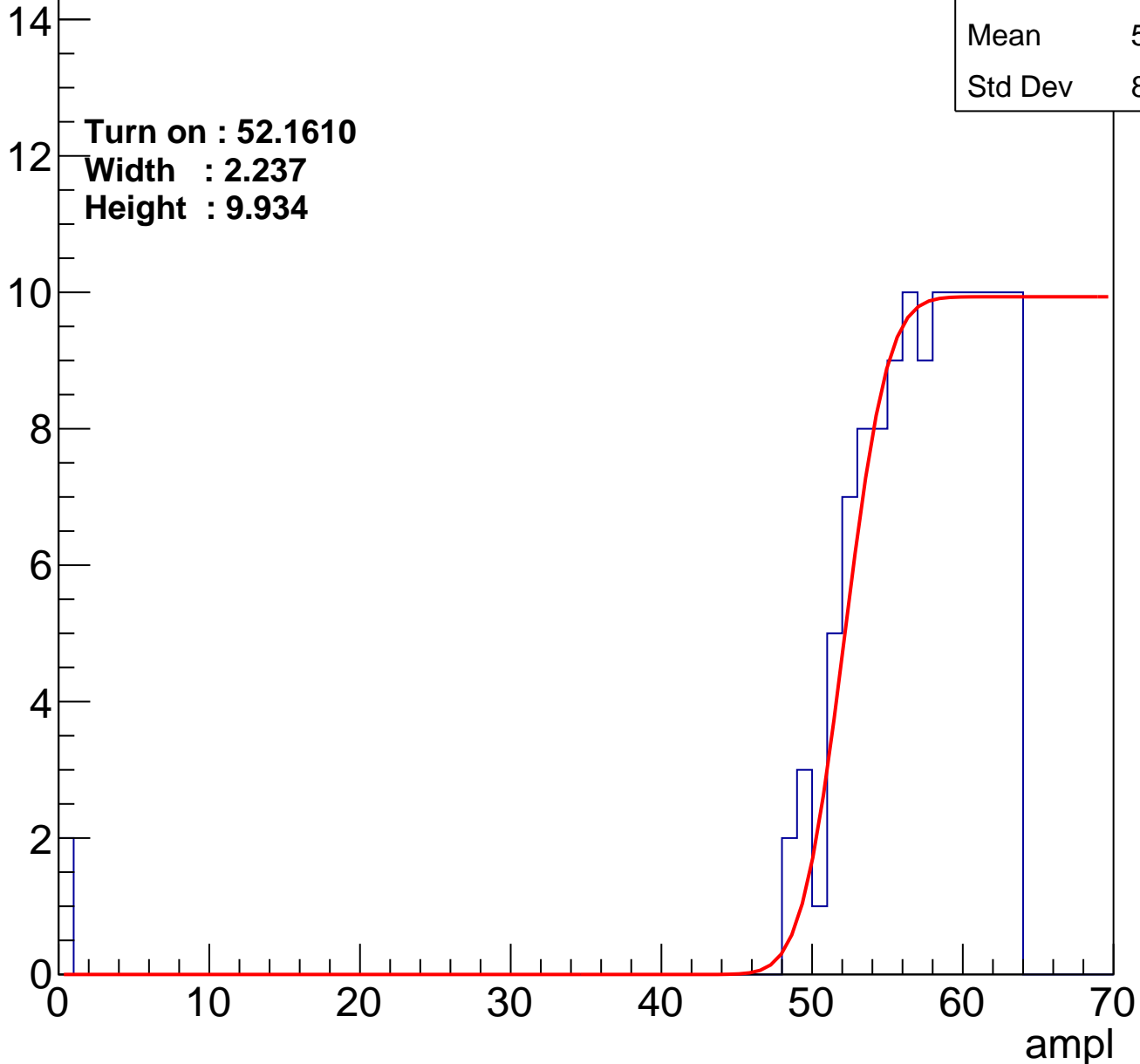
Entries	124
Mean	56.18
Std Dev	8.195

Turn on : 52.1610

Width : 2.237

Height : 9.934

Entry



B0L103S, U11-ch110

calib_packv5_040323_1717.root, FC#2, port C3

Entries	149
Mean	53.37
Std Dev	12.69

Turn on : 50.1395

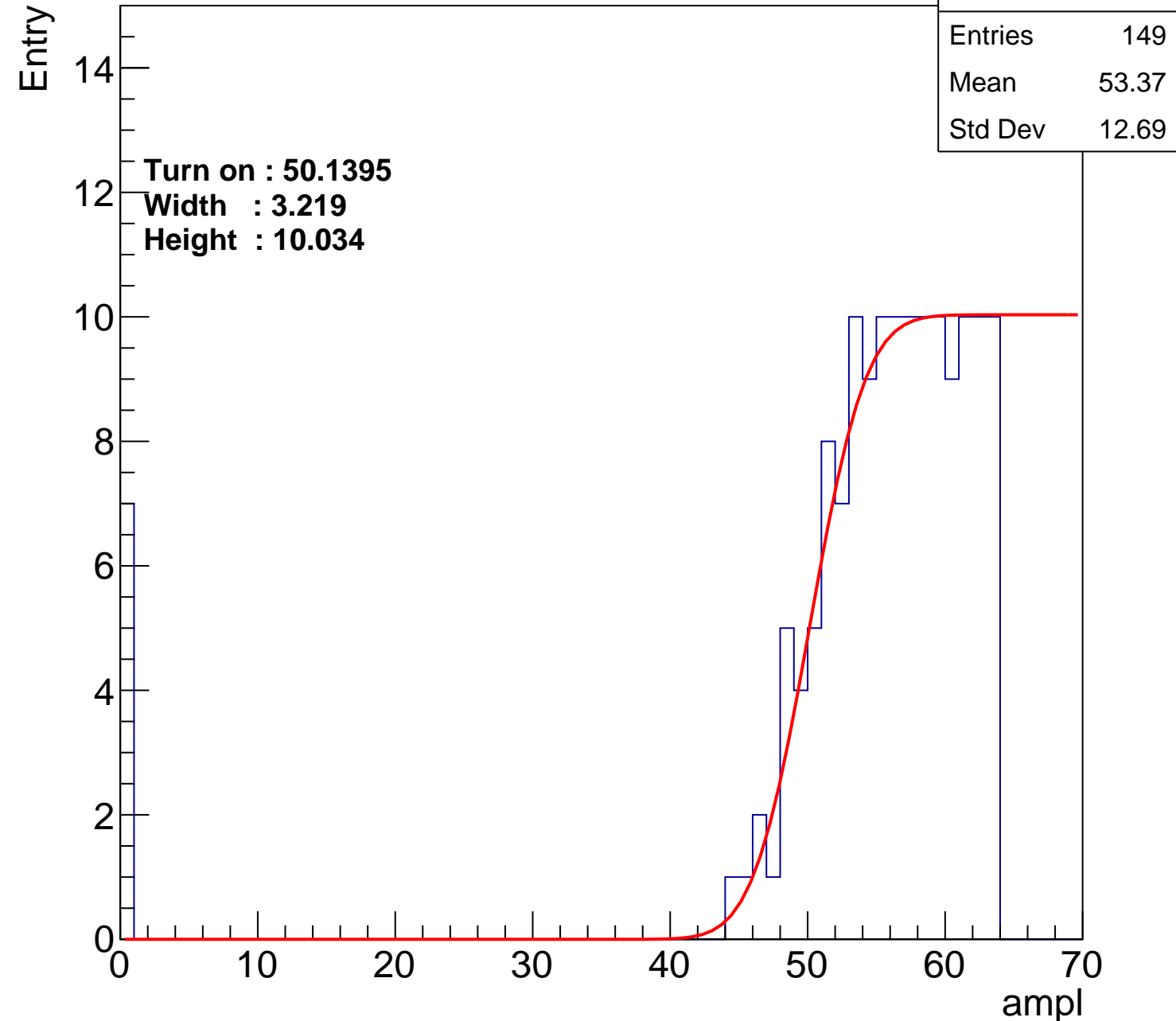
Width : 3.219

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch111

calib_packv5_040323_1717.root, FC#2, port C3

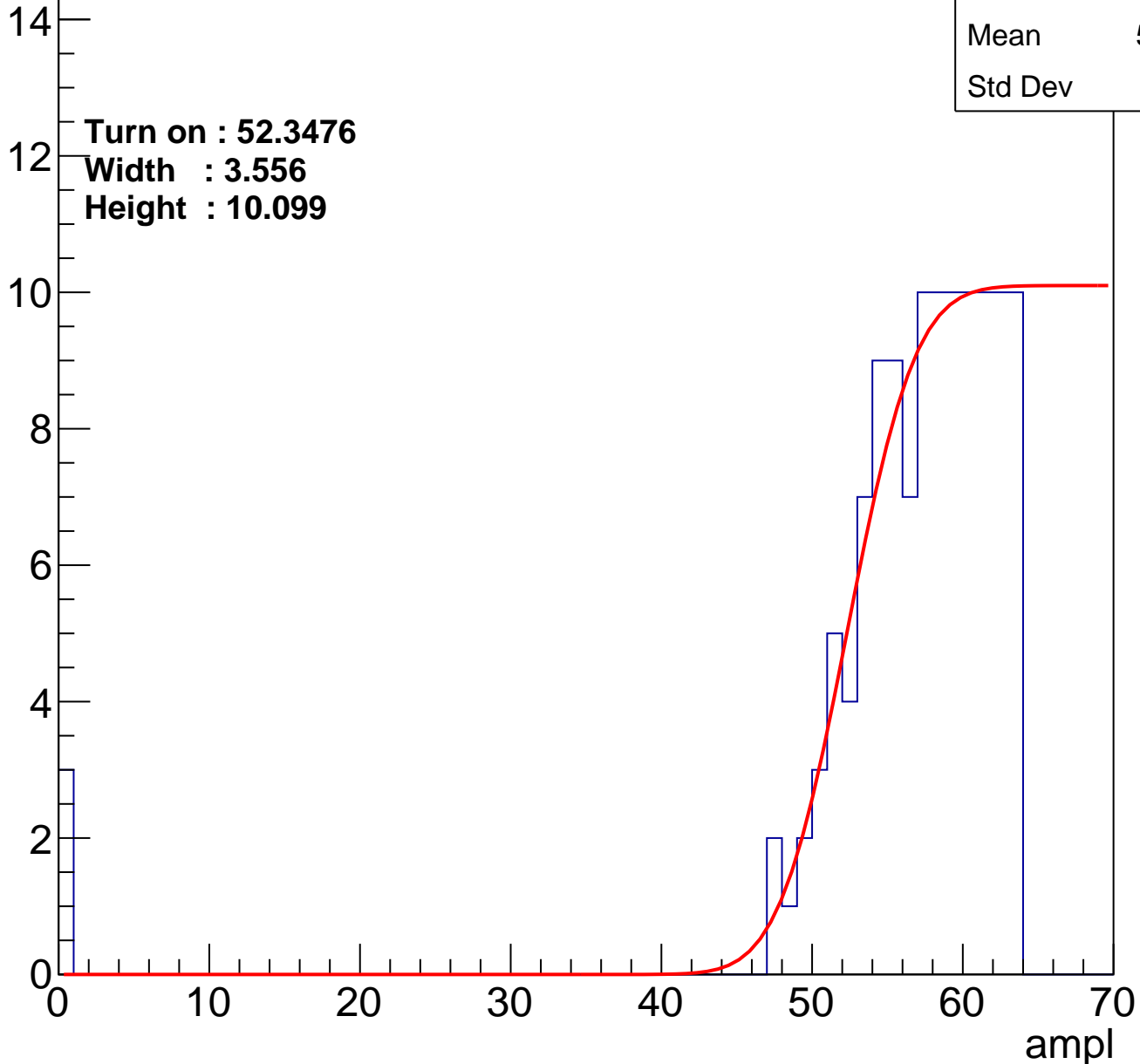
Entries	122
Mean	55.71
Std Dev	9.72

Turn on : 52.3476

Width : 3.556

Height : 10.099

Entry



B0L103S, U11-ch112

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.89
Std Dev	8.226

Turn on : 51.9239

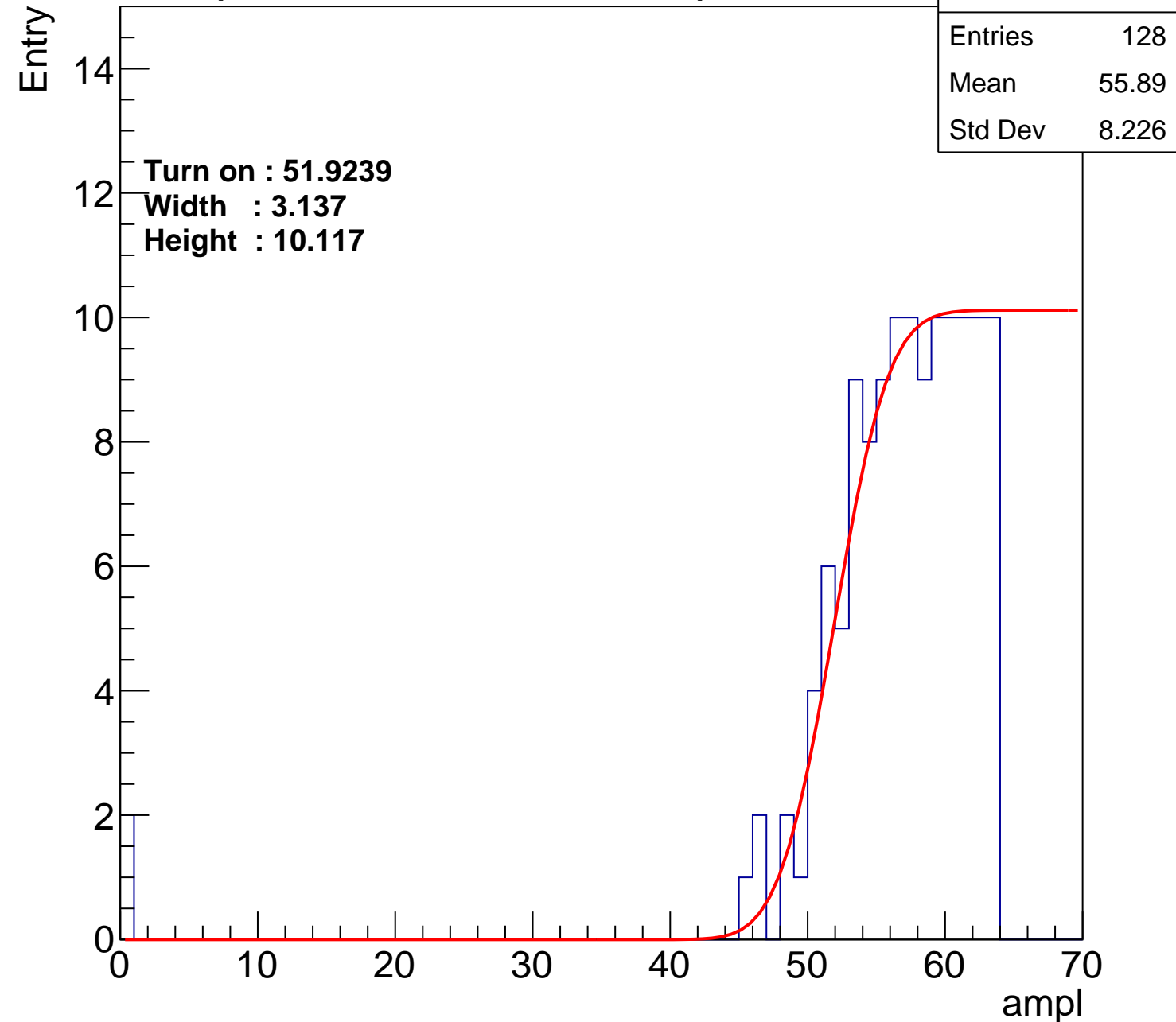
Width : 3.137

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch113

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.09
Std Dev	9.366

Turn on : 50.6794

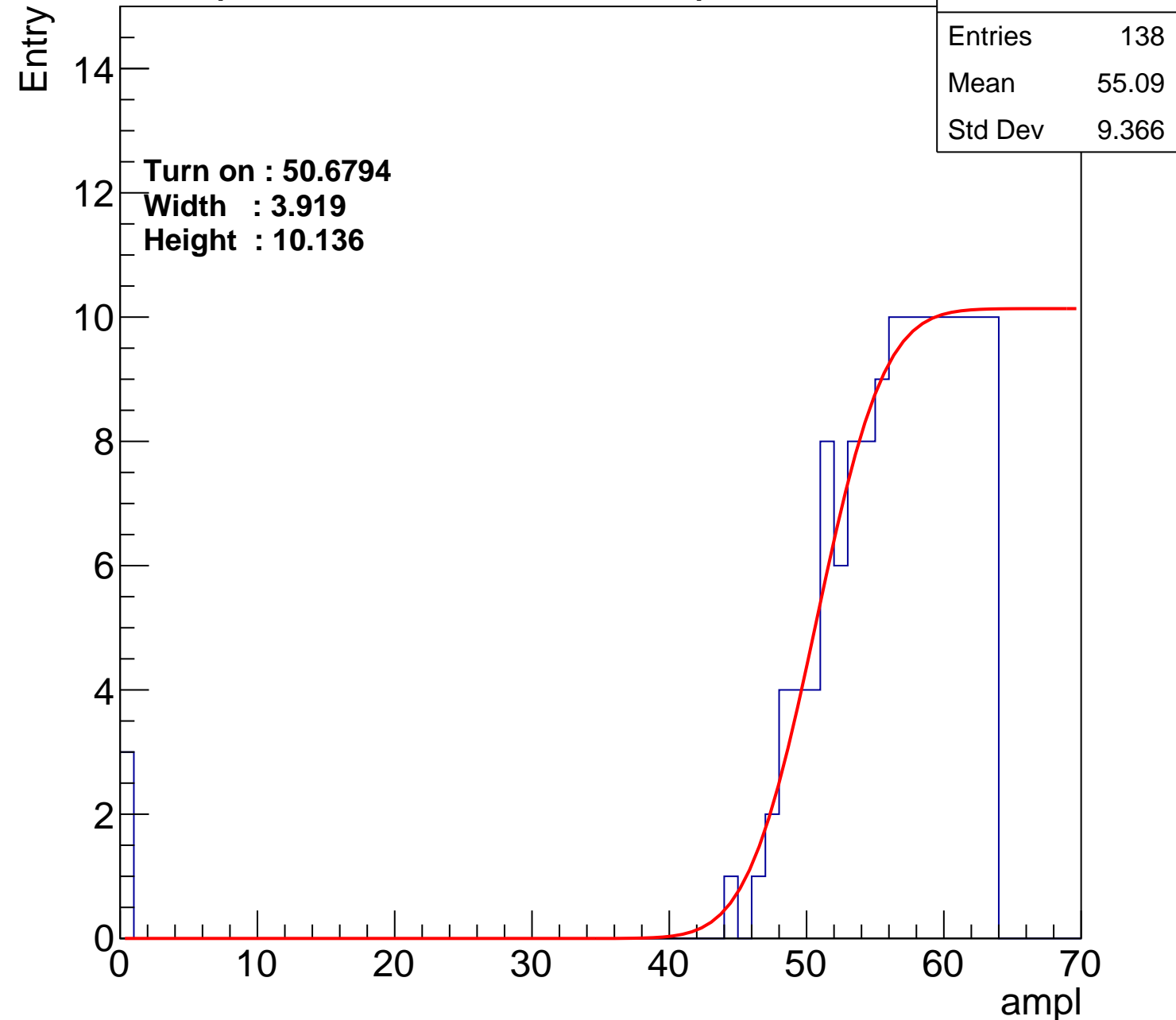
Width : 3.919

Height : 10.136

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.3
Std Dev	10.26

Turn on : 49.8010

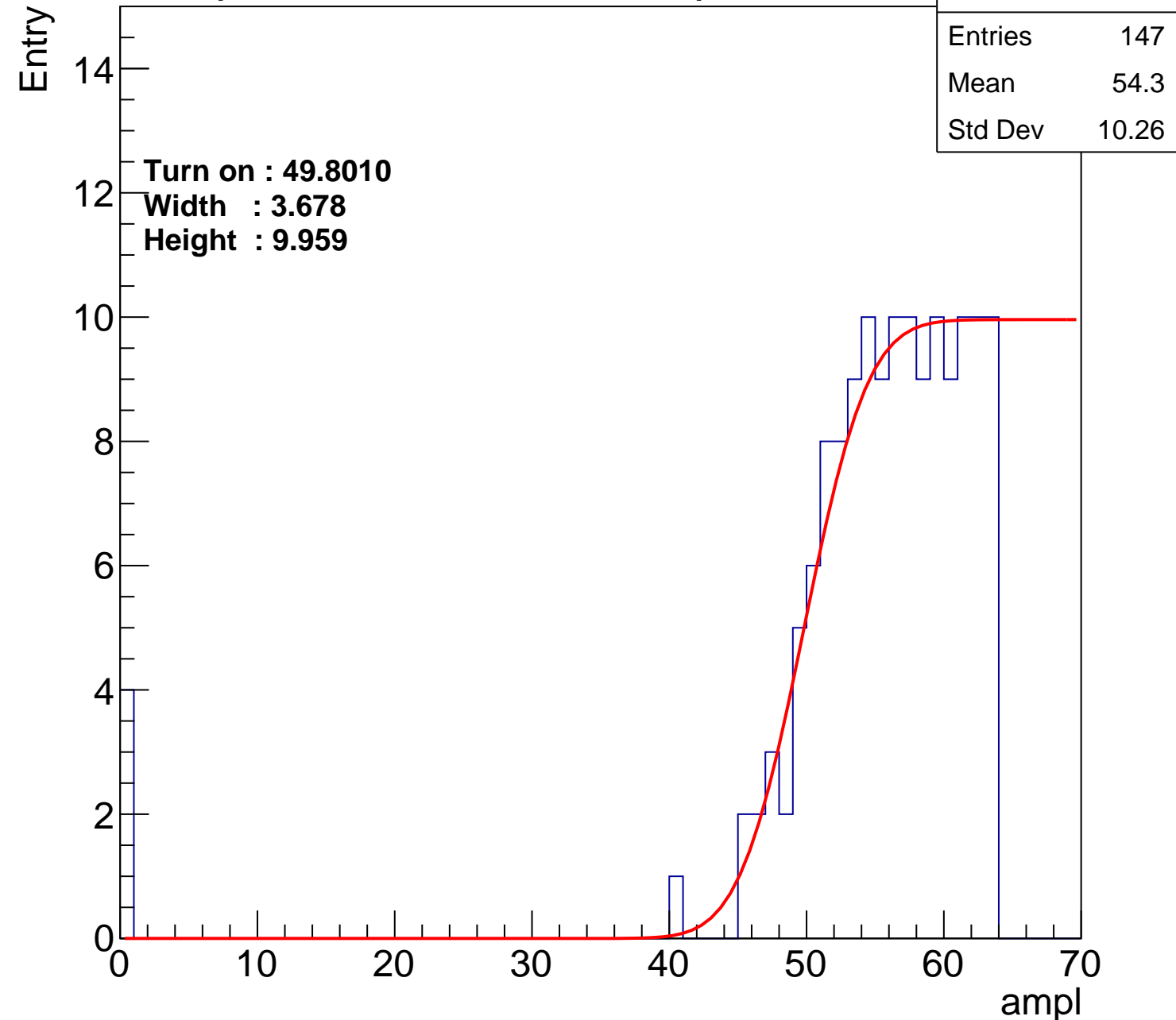
Width : 3.678

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch115

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.14
Std Dev	8.402

Turn on : 52.3697

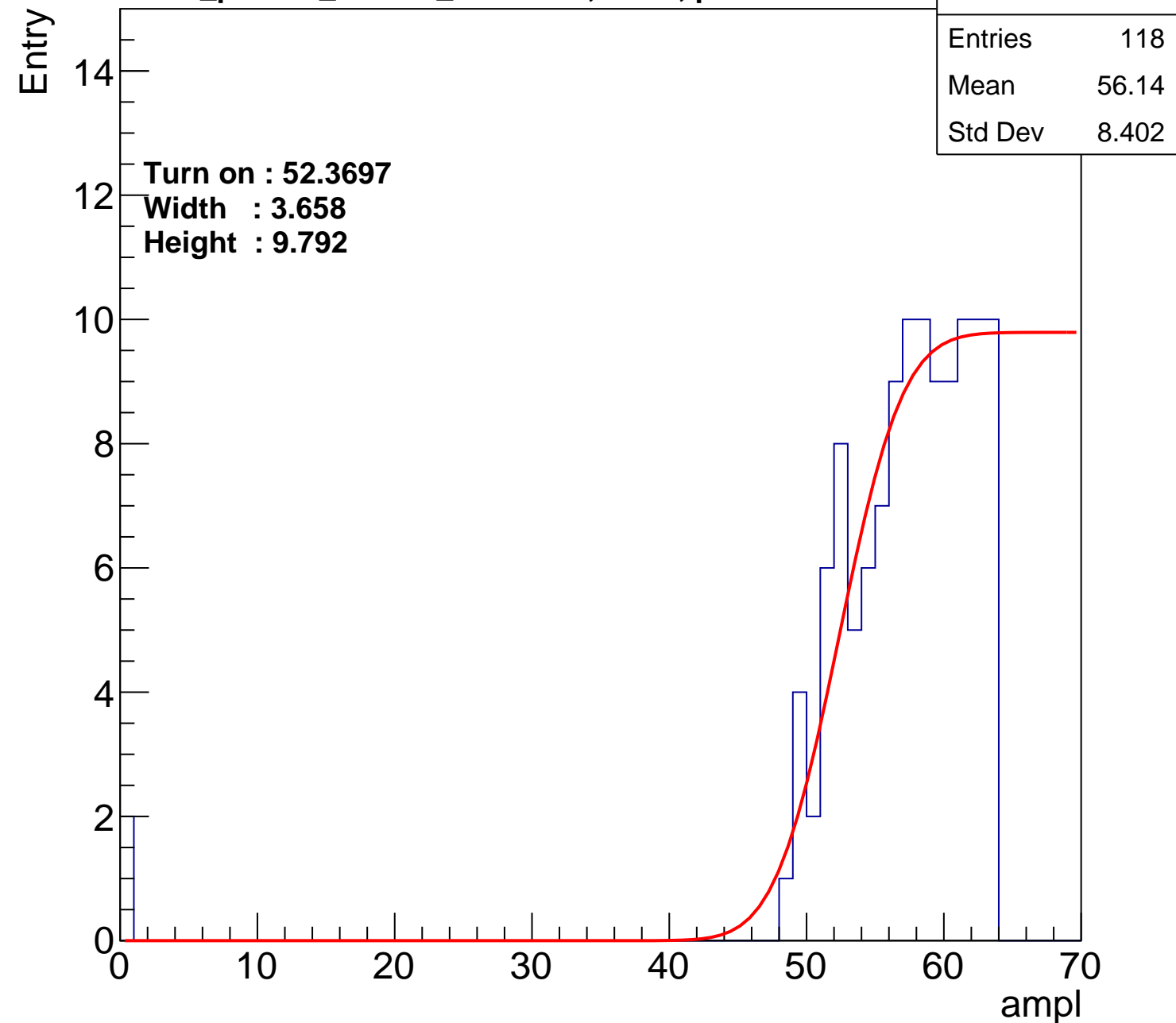
Width : 3.658

Height : 9.792

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.73
Std Dev	9.583

Turn on : 51.7449

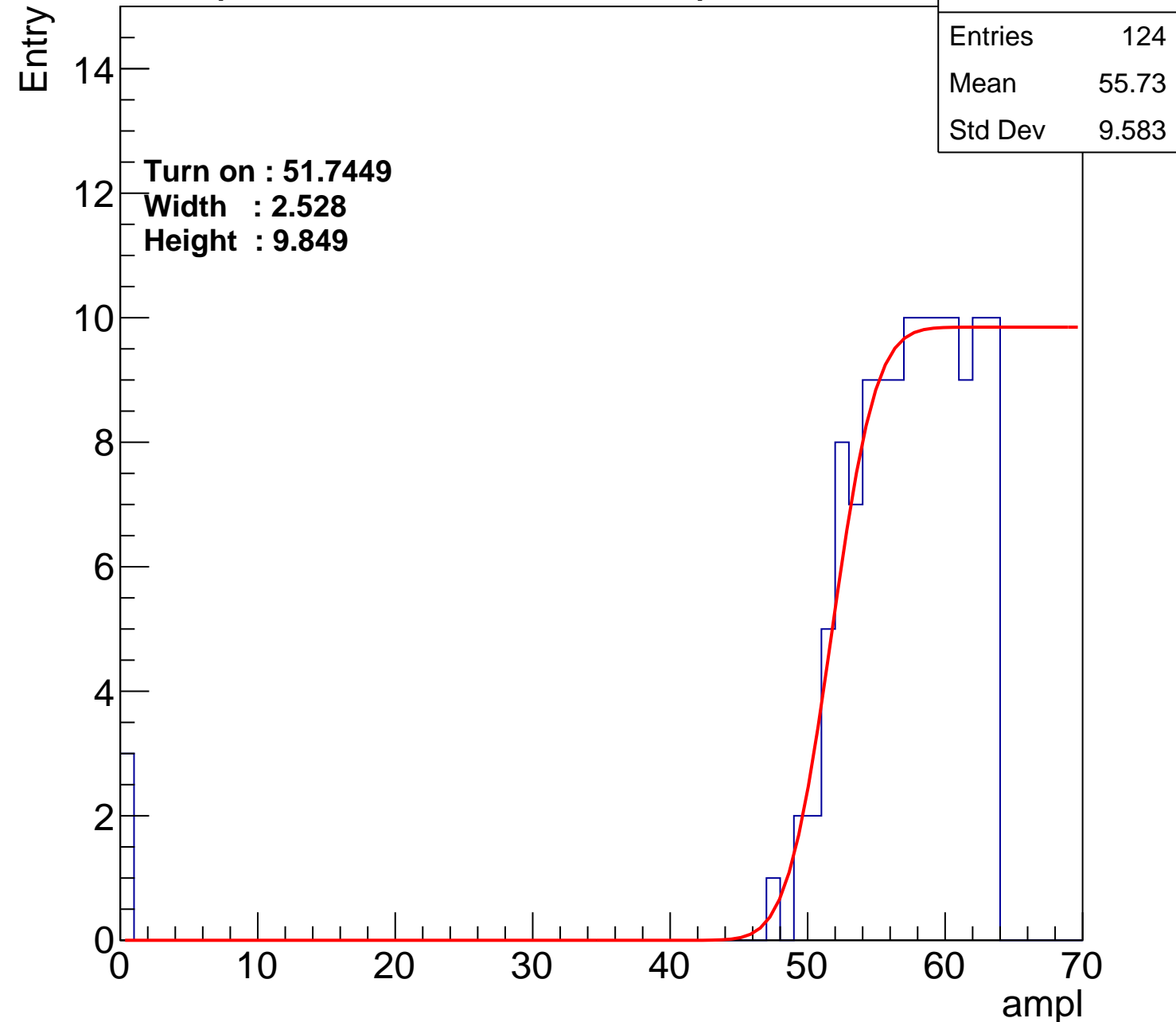
Width : 2.528

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.43
Std Dev	9.626

Turn on : 53.5602

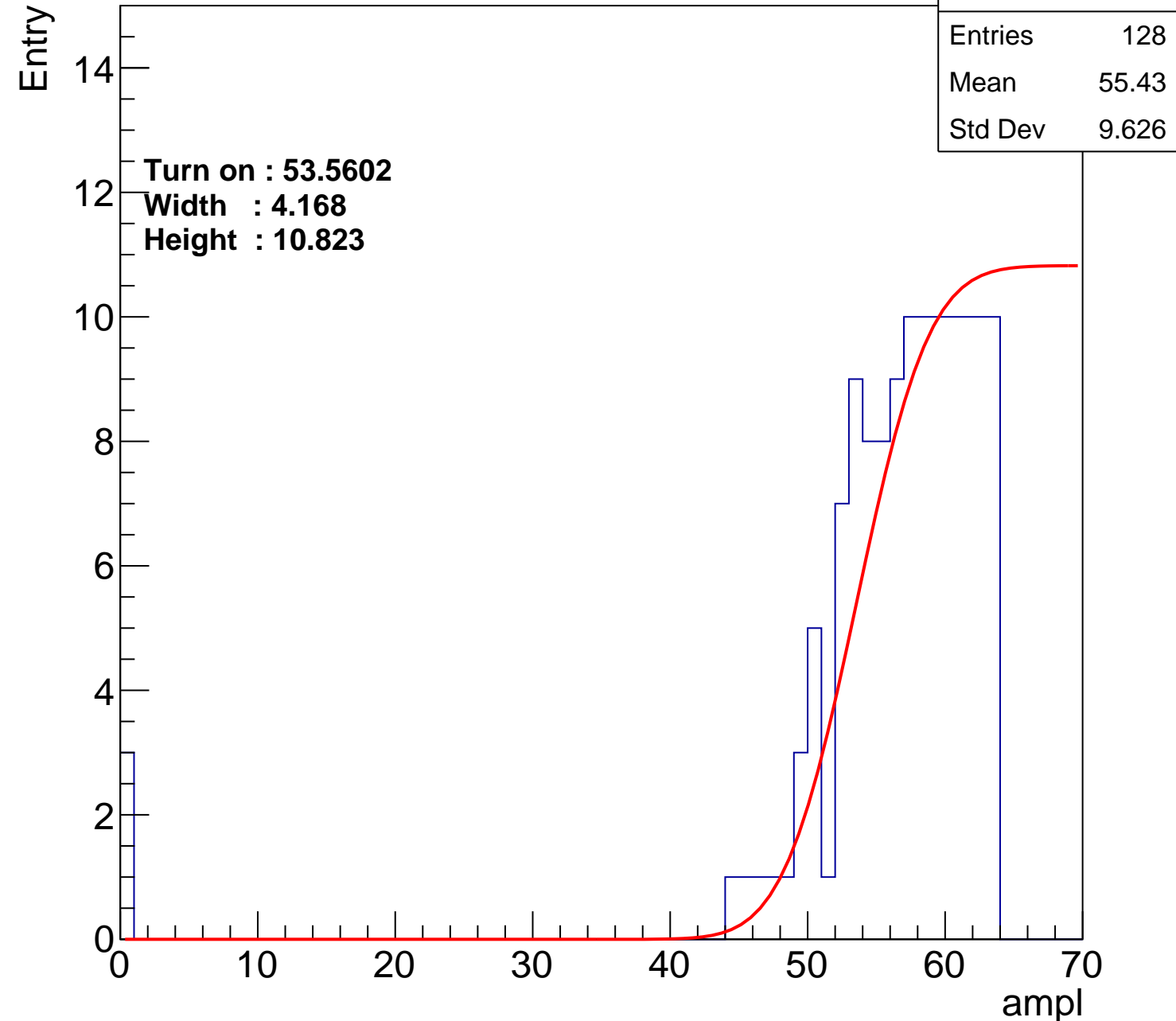
Width : 4.168

Height : 10.823

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.76
Std Dev	8.03

Turn on : 50.9301

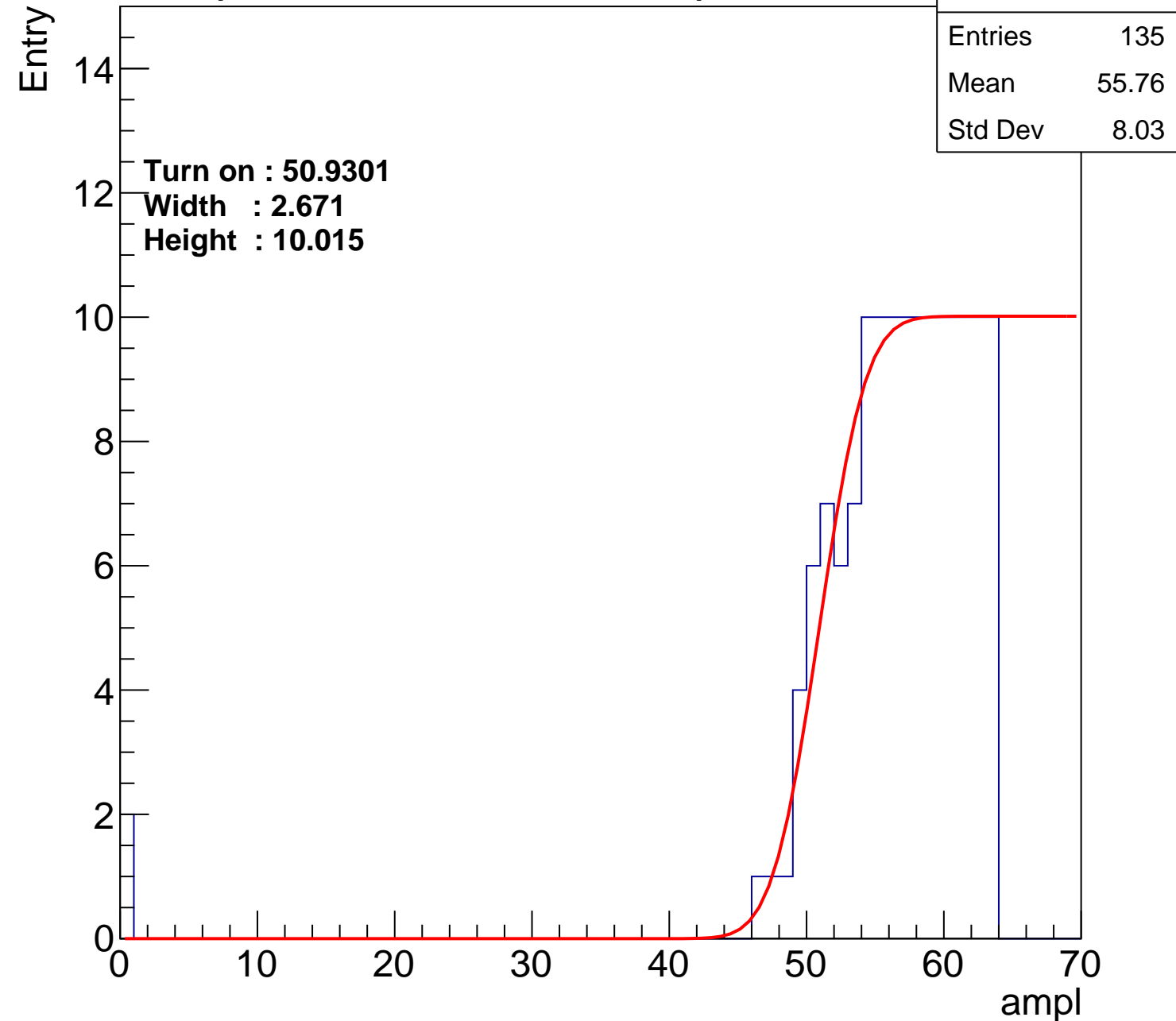
Width : 2.671

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	104
Mean	56.41
Std Dev	10.29

Turn on : 53.8228

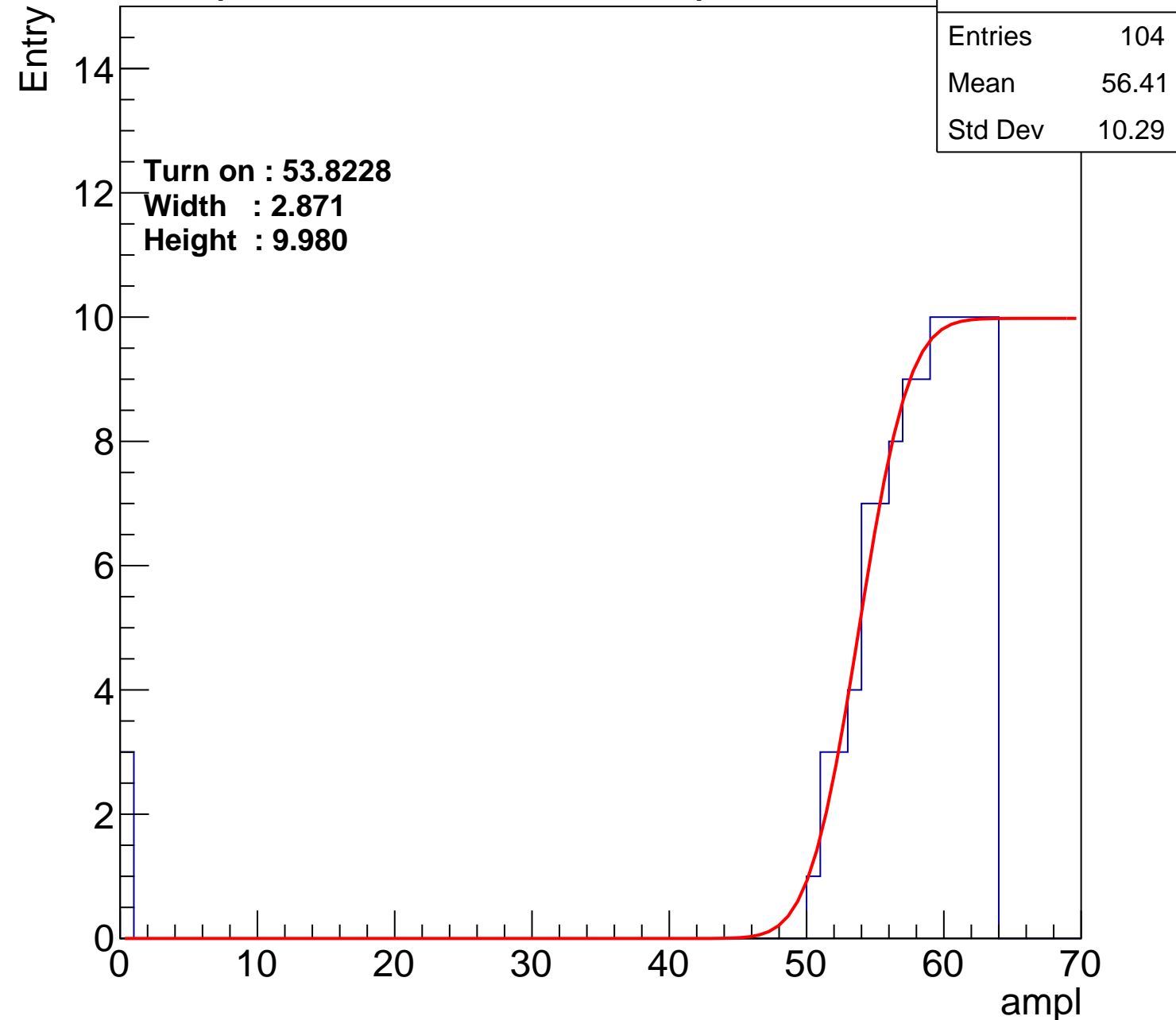
Width : 2.871

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch120

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	56.03
Std Dev	8.139

Turn on : 52.0258

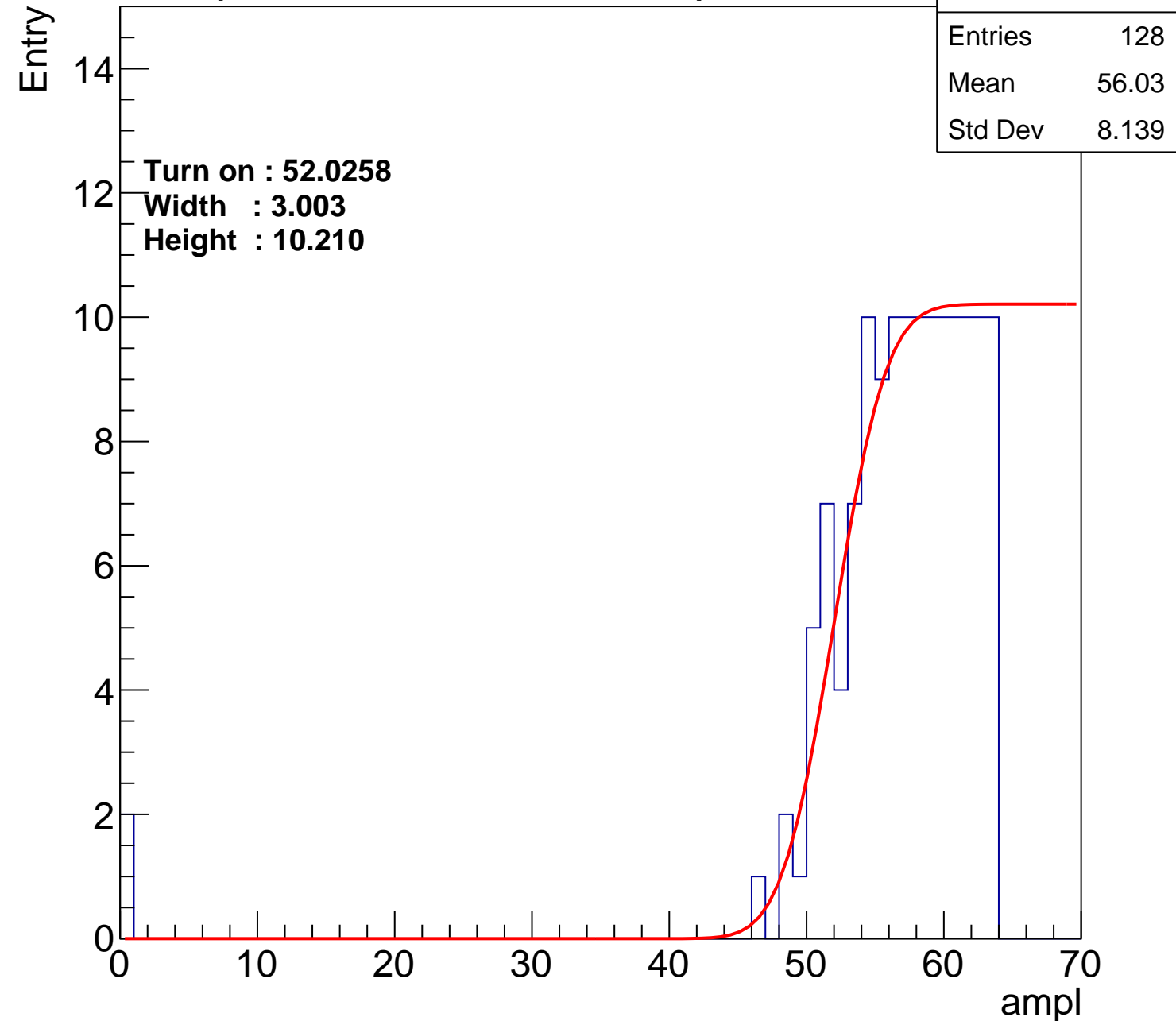
Width : 3.003

Height : 10.210

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.29
Std Dev	10.84

Turn on : 51.7610

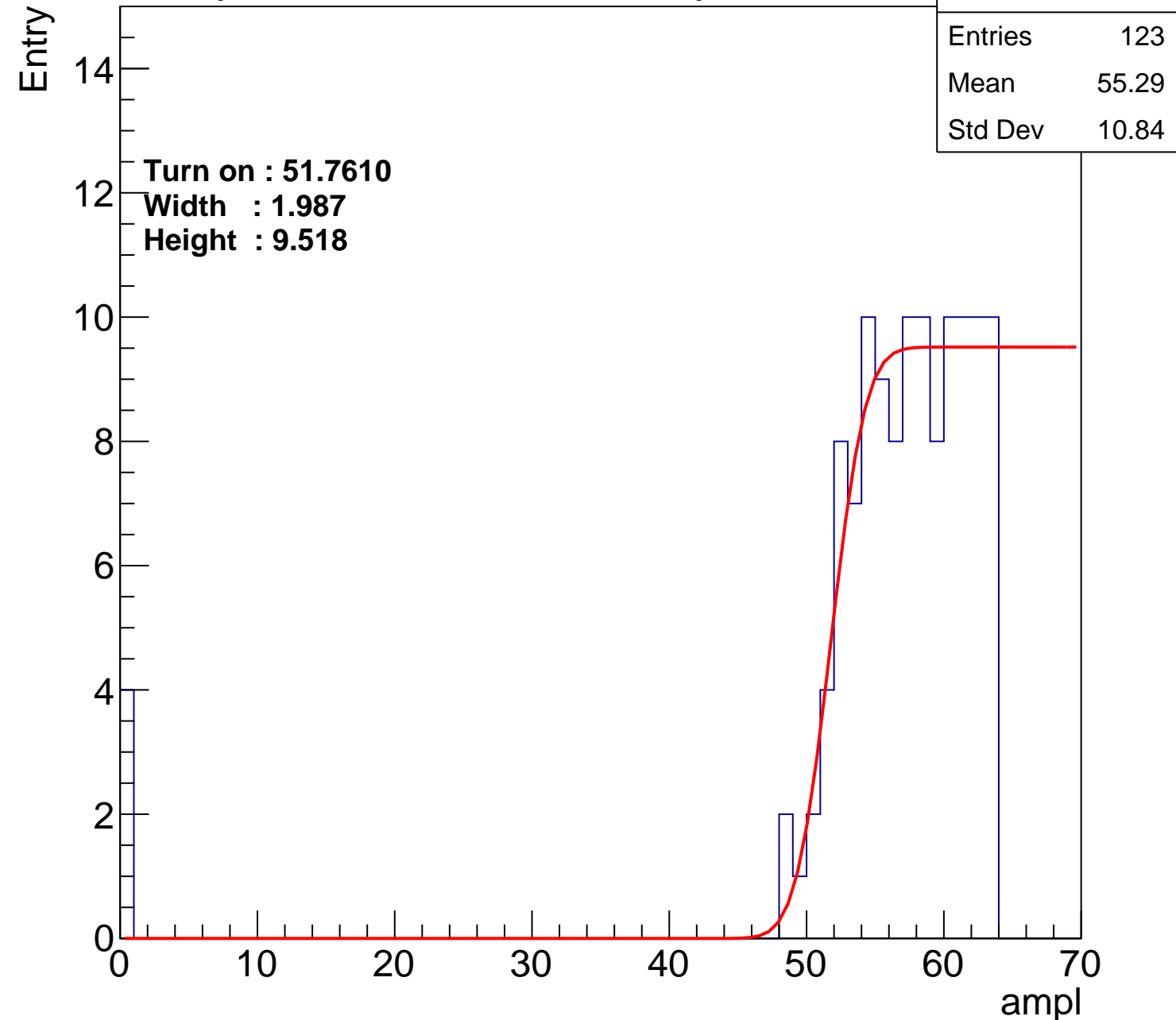
Width : 1.987

Height : 9.518

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch122

calib_packv5_040323_1717.root, FC#2, port C3

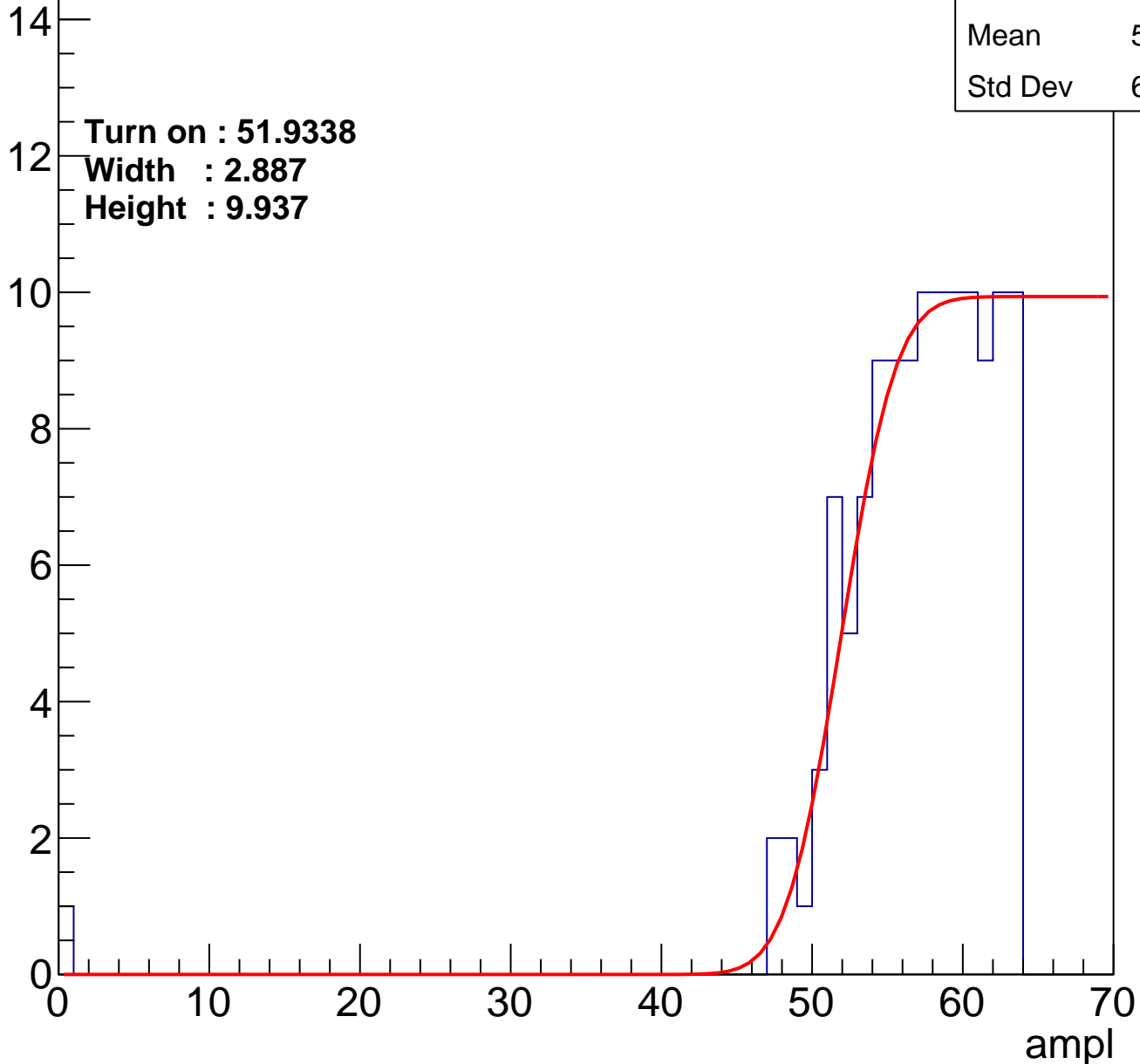
Entries	124
Mean	56.46
Std Dev	6.535

Turn on : 51.9338

Width : 2.887

Height : 9.937

Entry



B0L103S, U11-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.14
Std Dev	9.974

Turn on : 53.1305

Width : 1.568

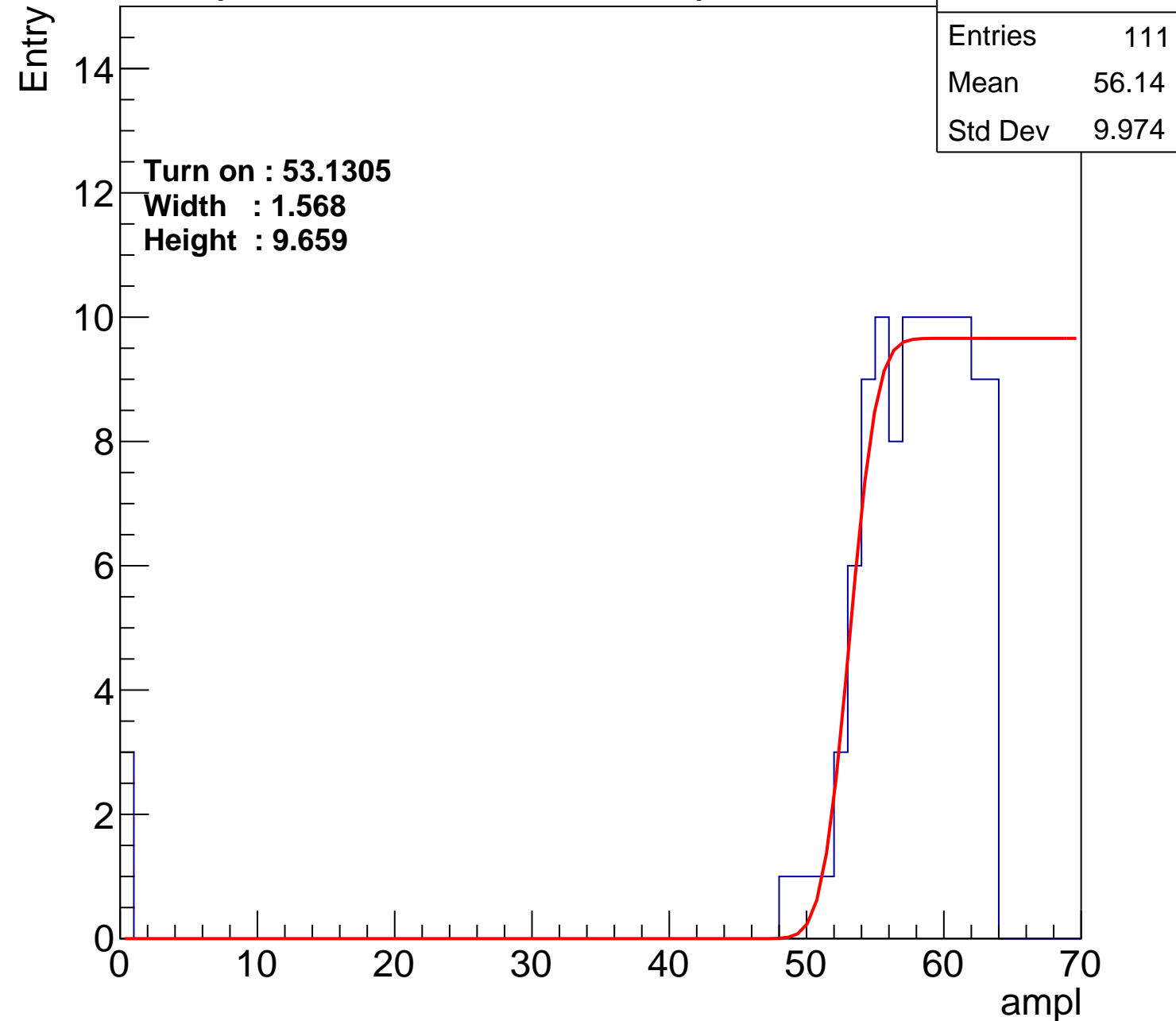
Height : 9.659

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U11-ch124

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	55.47
Std Dev	10.73

Turn on : 52.6013

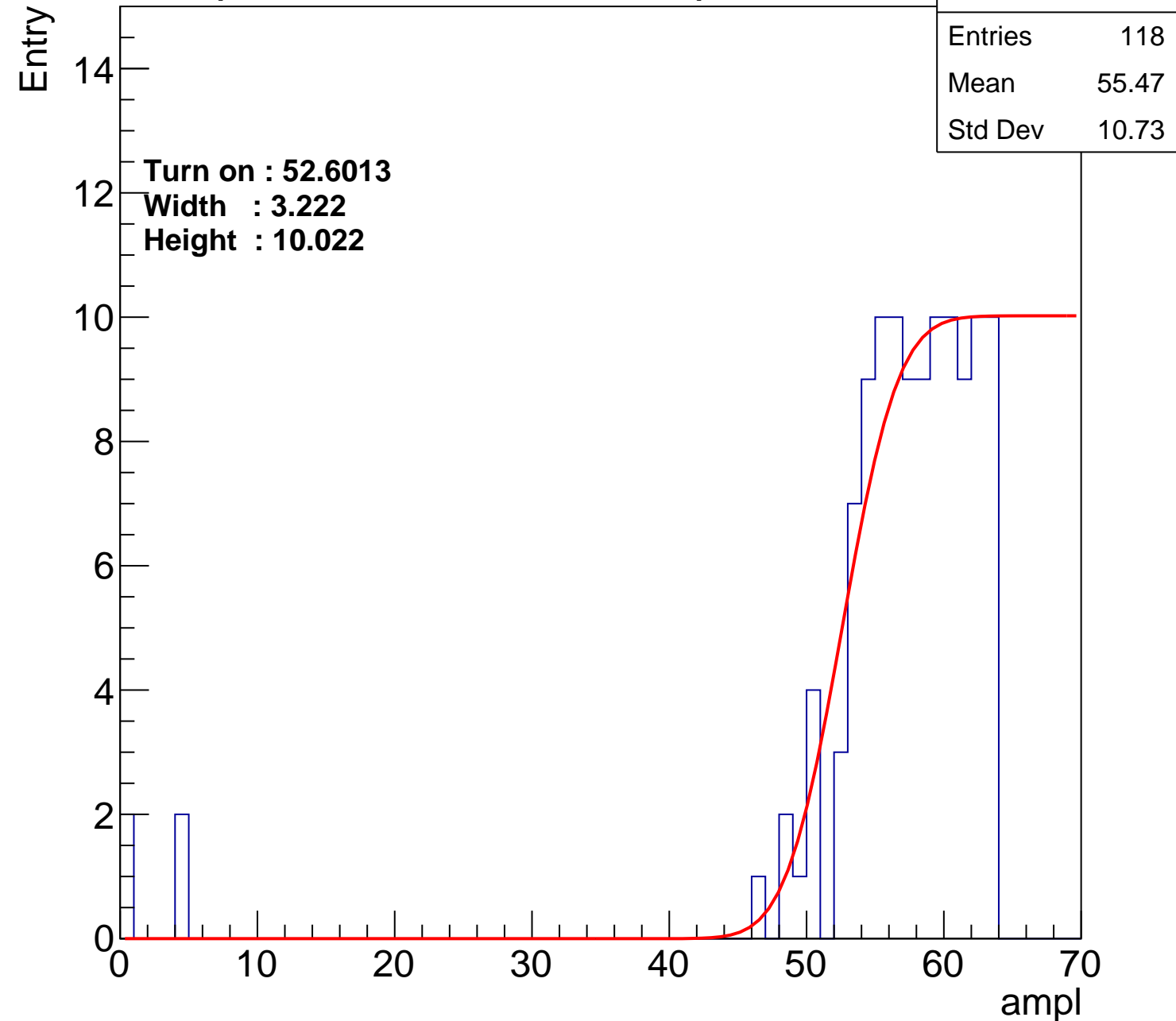
Width : 3.222

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.8
Std Dev	8.419

Turn on : 52.1177

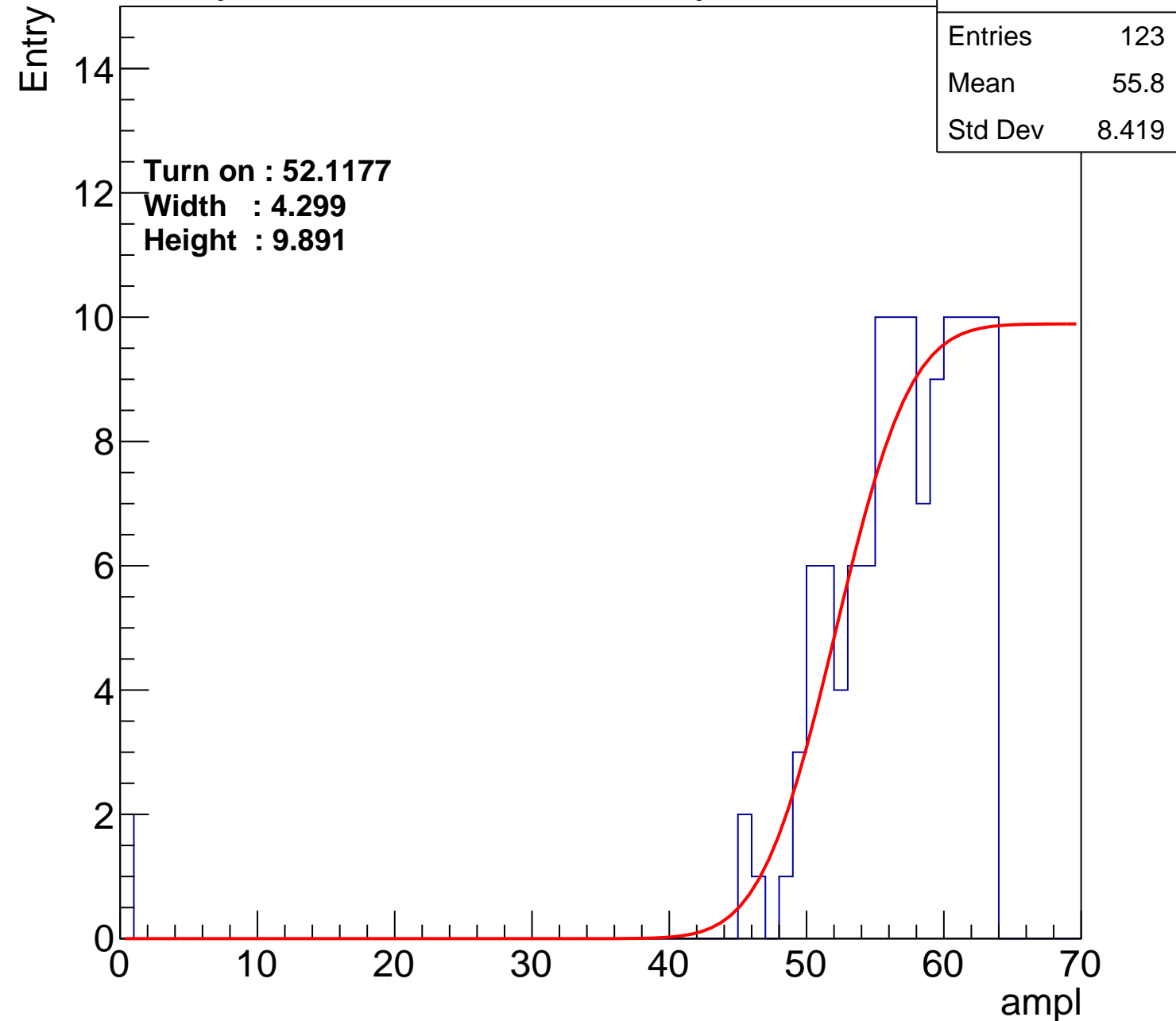
Width : 4.299

Height : 9.891

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.03
Std Dev	9.131

Turn on : 49.9083

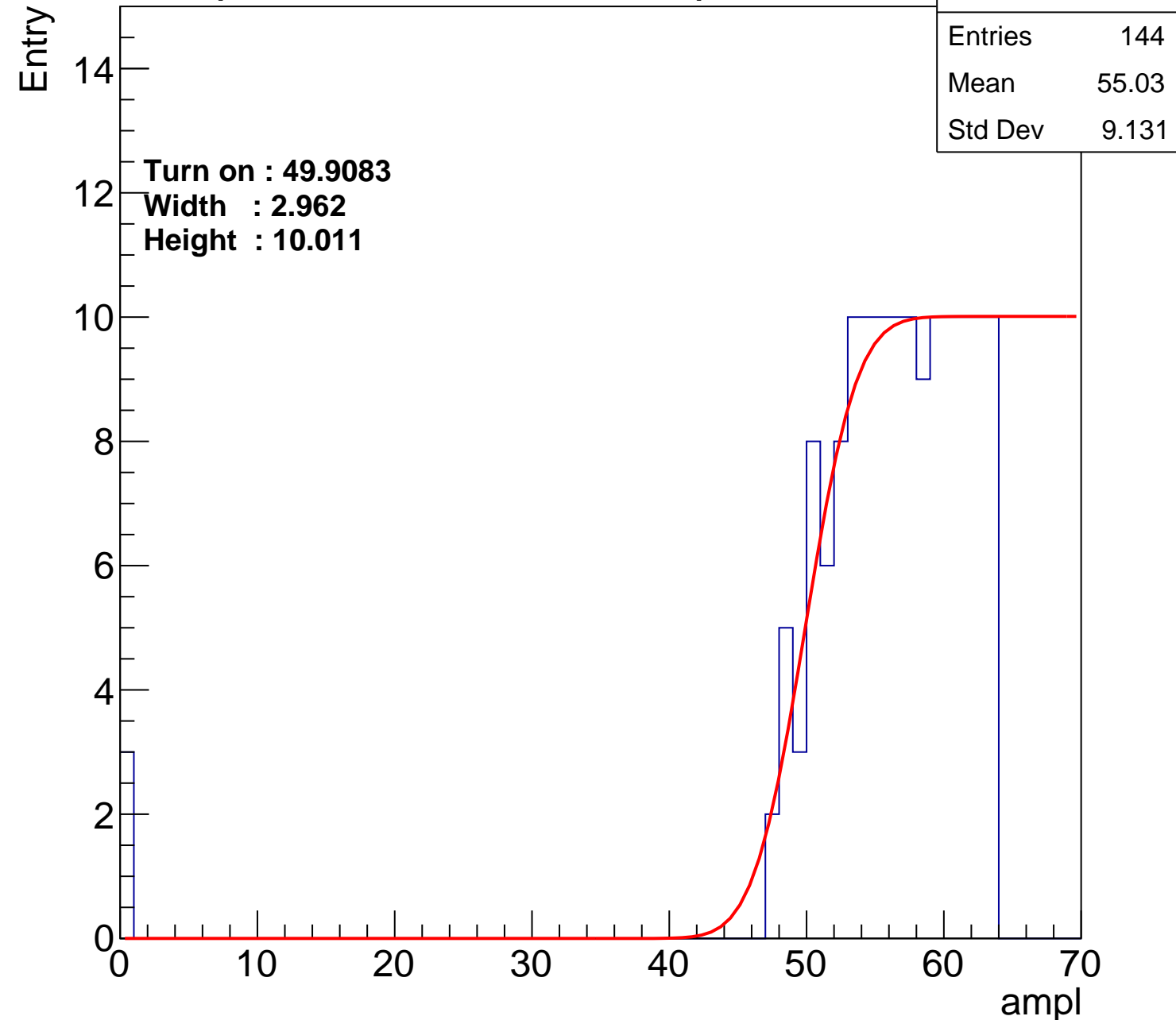
Width : 2.962

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.2
Std Dev	8.267

Turn on : 52.4944

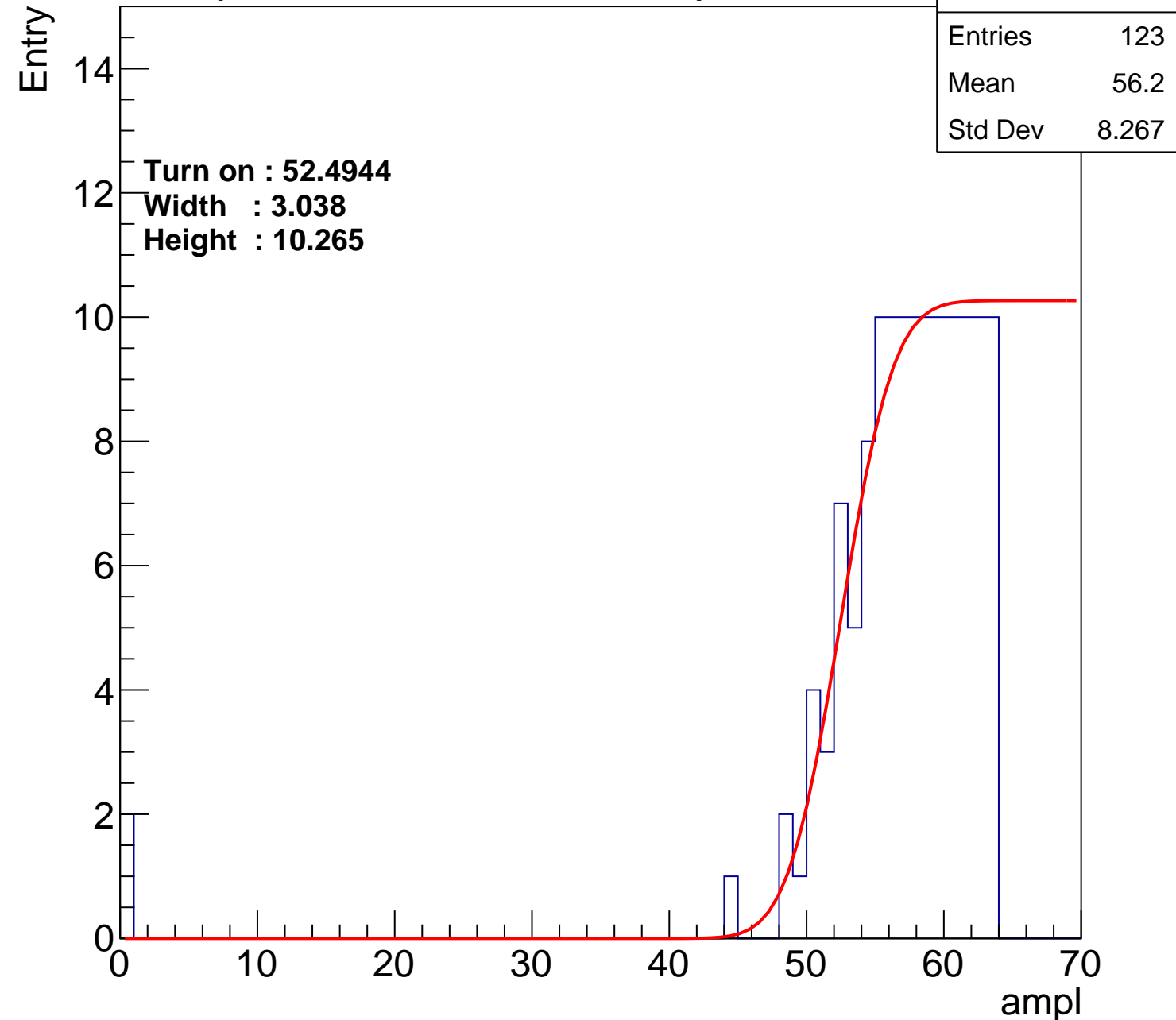
Width : 3.038

Height : 10.265

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U11-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.2
Std Dev	8.267

Turn on : 52.4944

Width : 3.038

Height : 10.265

Entry

14
12
10
8
6
4
2
0

ampl

