



# B0L000S, U17-ch0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 27.1450

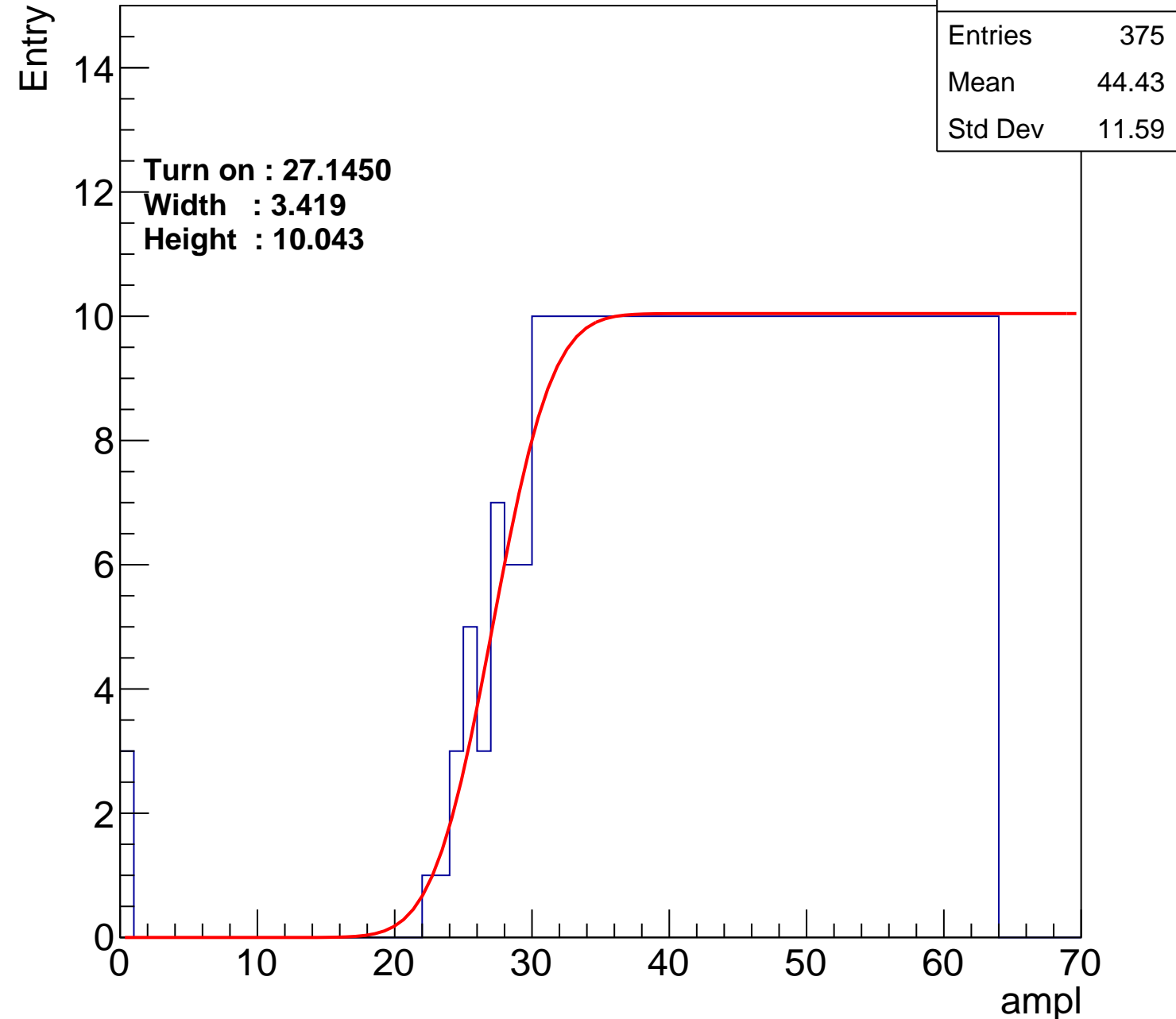
Width : 3.419

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch1

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**Turn on : 26.5449**

**Width : 3.050**

**Height : 10.061**

Entries	377
Mean	44.51
Std Dev	11.2



# B0L000S, U17-ch2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	43.96
Std Dev	12.23

**Turn on : 26.3235**

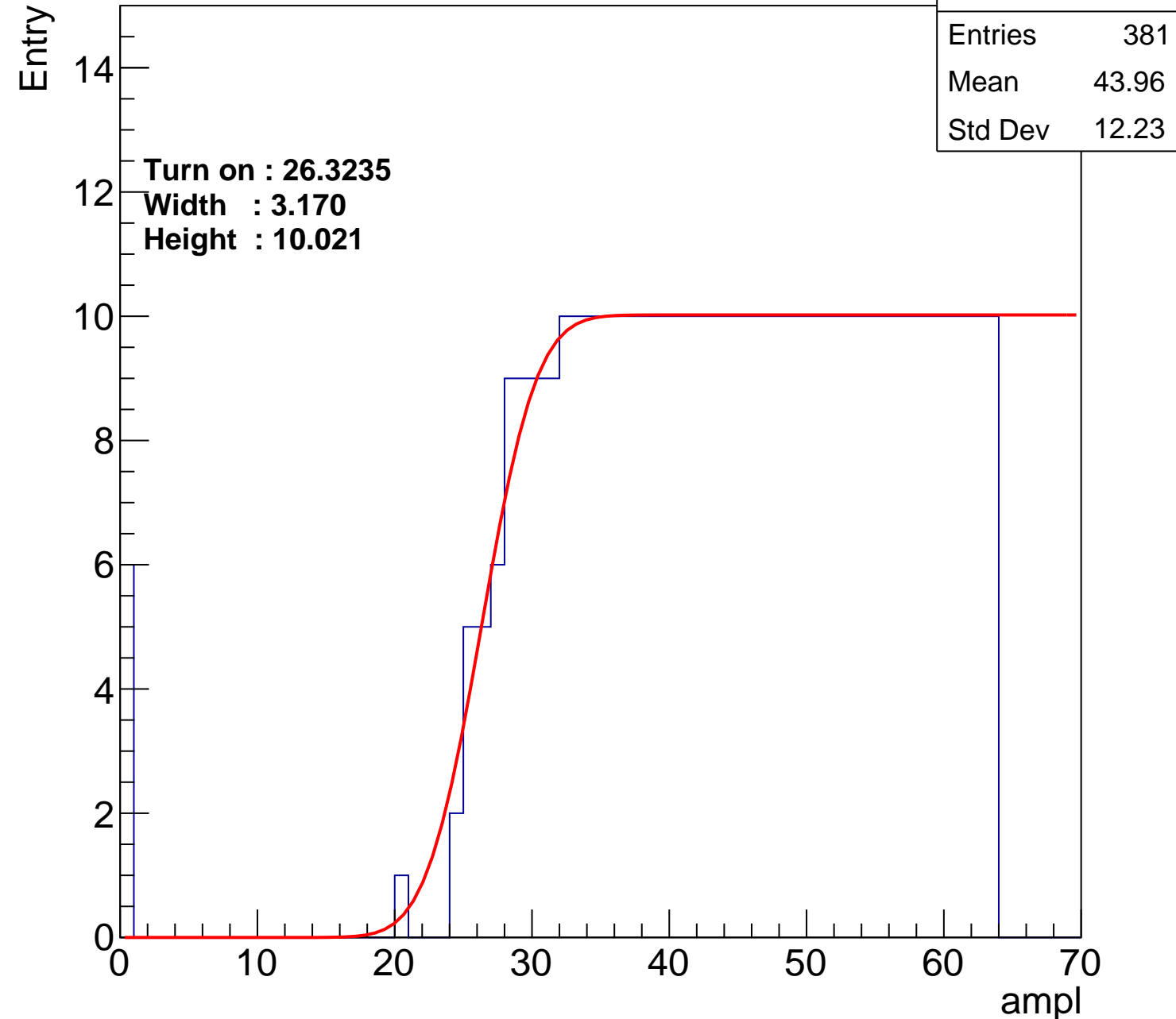
**Width : 3.170**

**Height : 10.021**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch3

calib\_packv5\_042523\_0143.root, FC#5, port B1

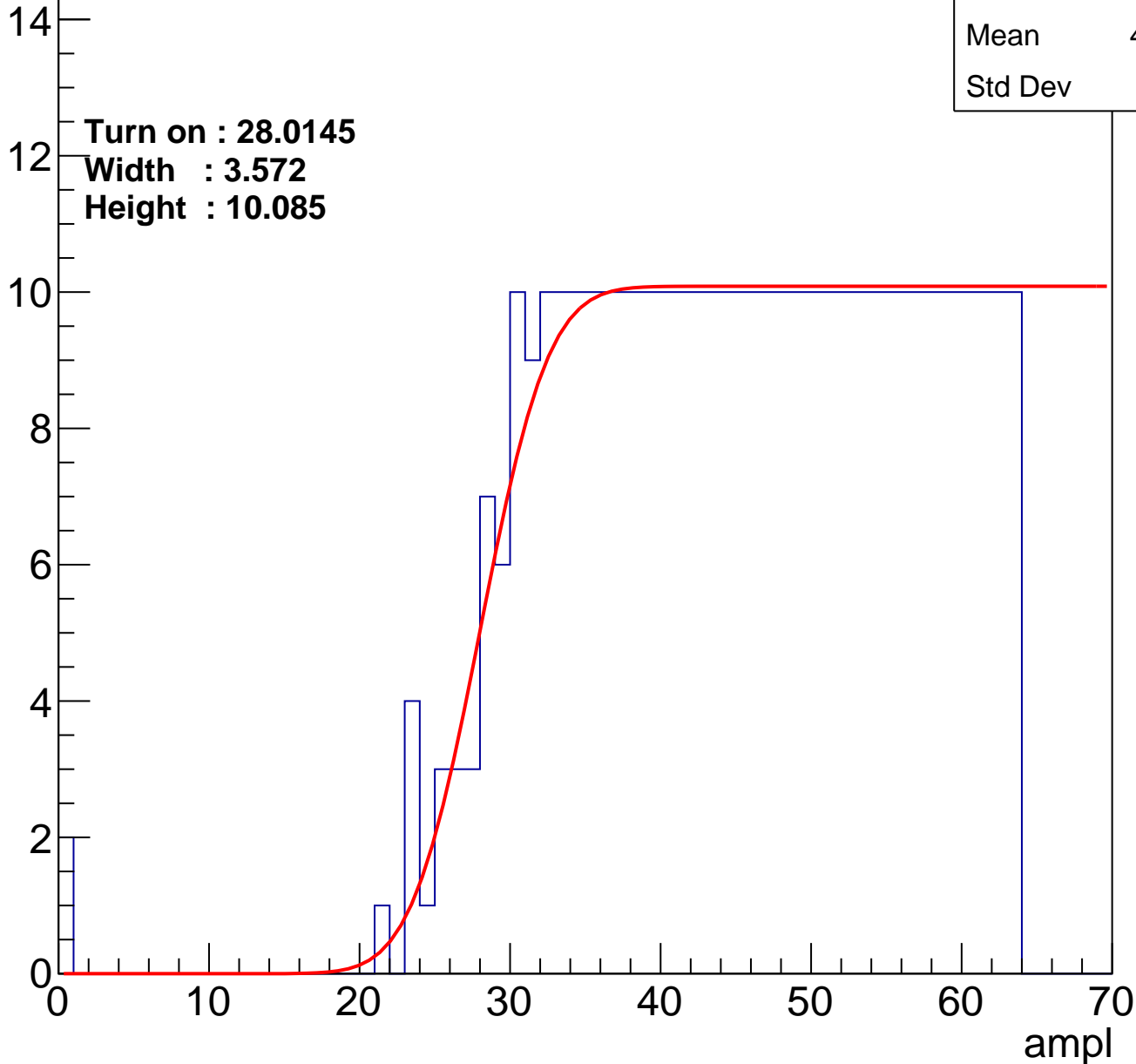
Entry

Entries	369
Mean	44.77
Std Dev	11.3

Turn on : 28.0145

Width : 3.572

Height : 10.085



# B0L000S, U17-ch4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.84
Std Dev	11.09

Turn on : 27.5855

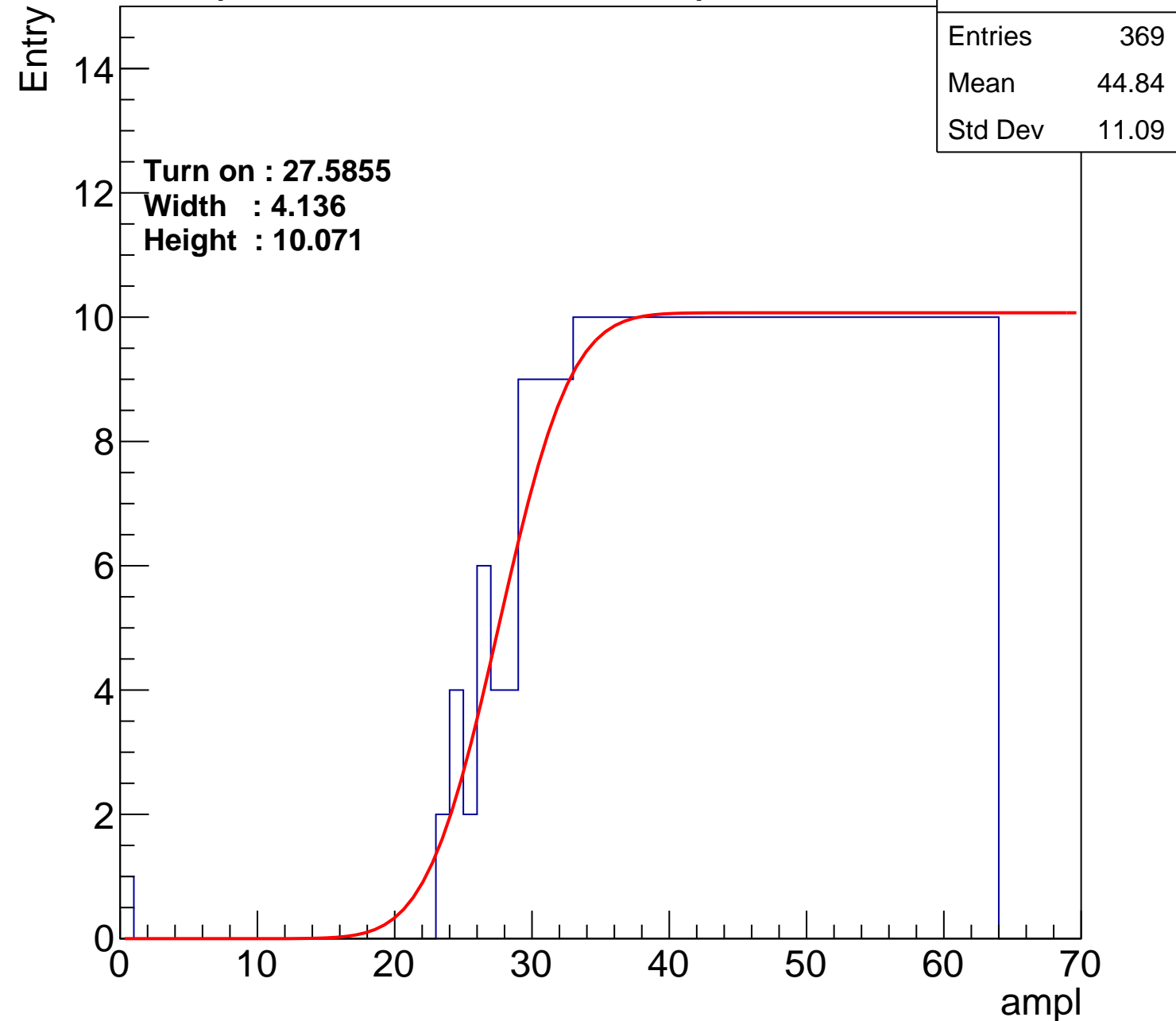
Width : 4.136

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.84
Std Dev	11.88

Turn on : 25.3468

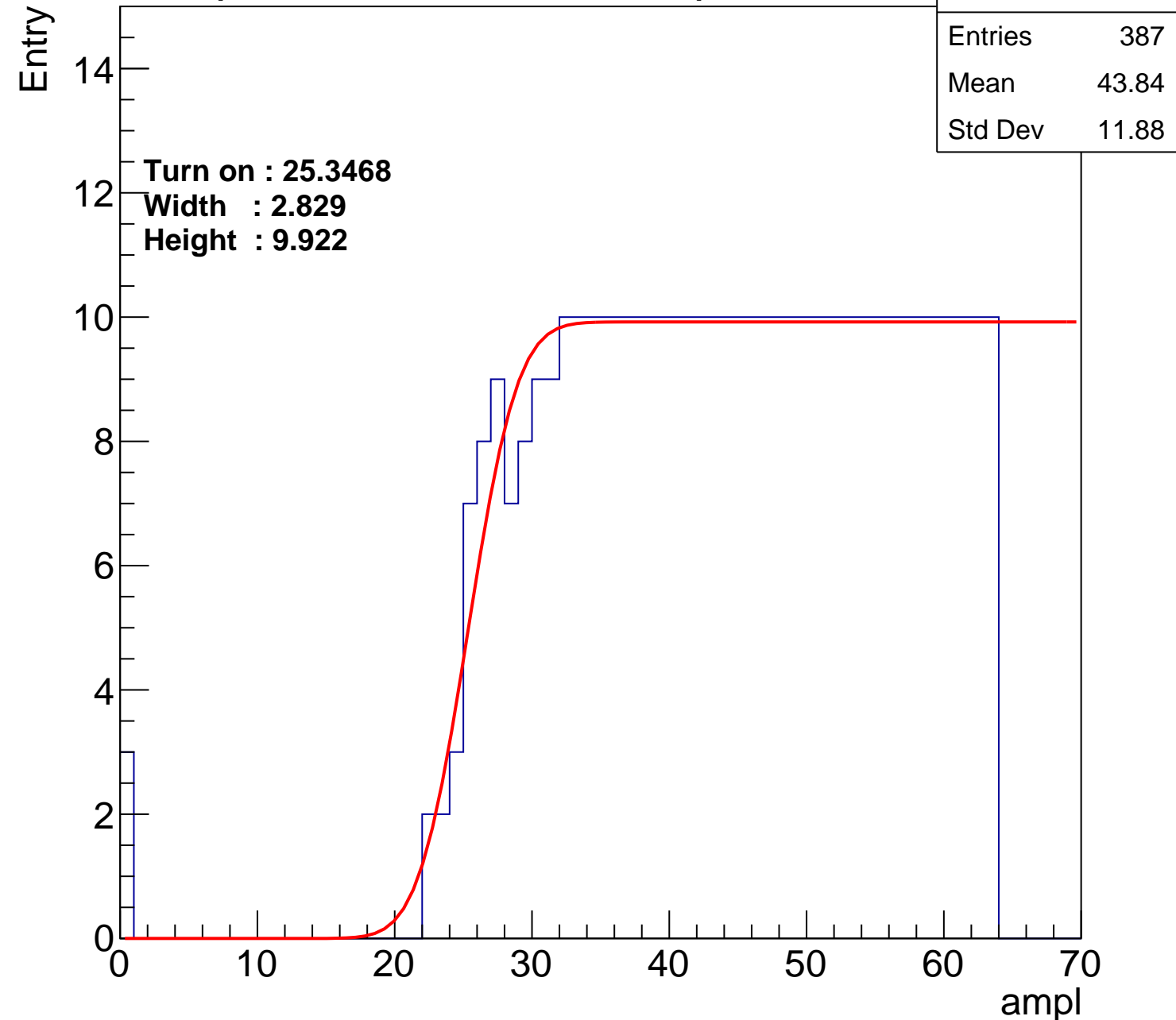
Width : 2.829

Height : 9.922

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	395
Mean	43.61
Std Dev	11.7

**Turn on : 24.7907**

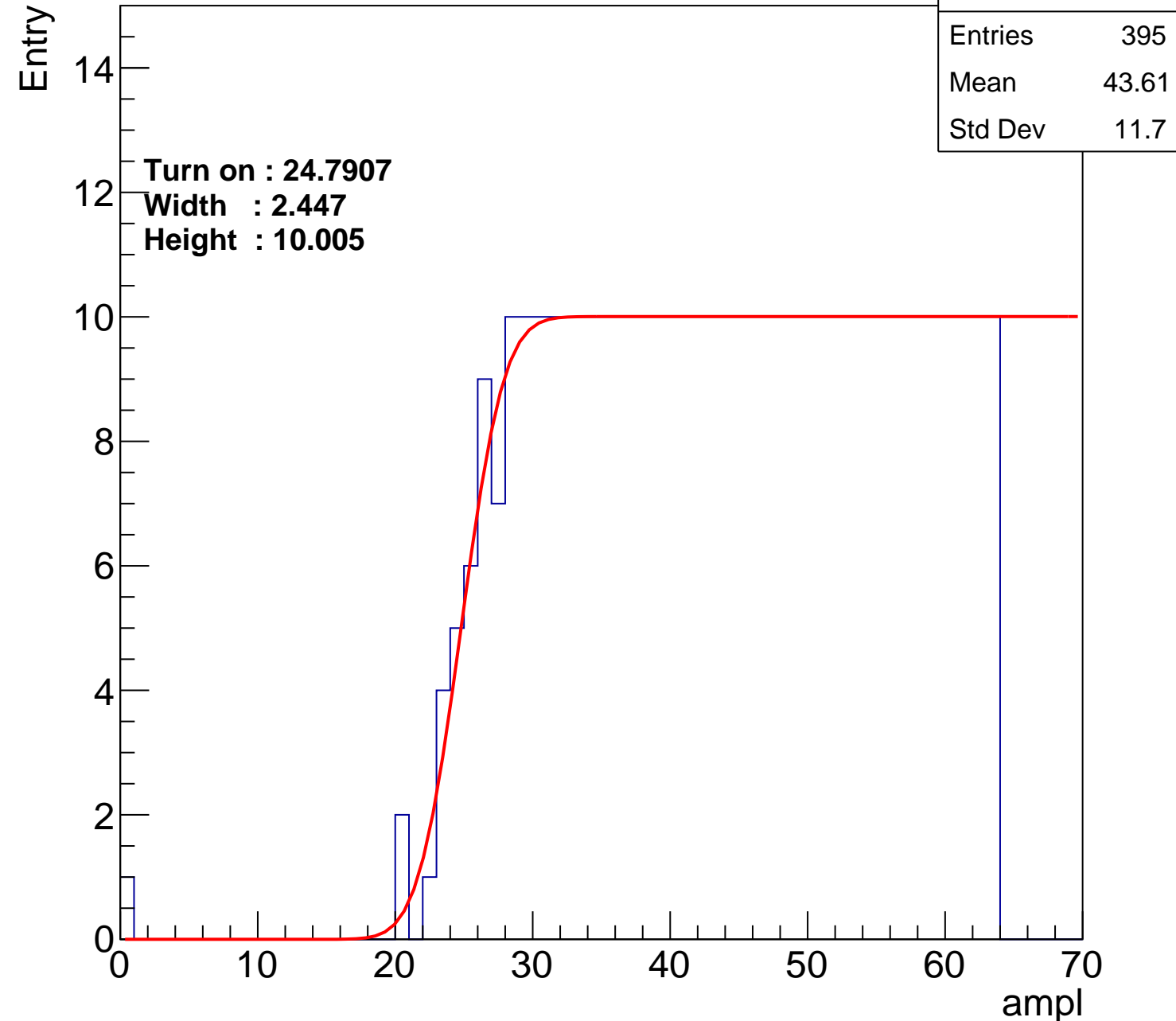
**Width : 2.447**

**Height : 10.005**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch7

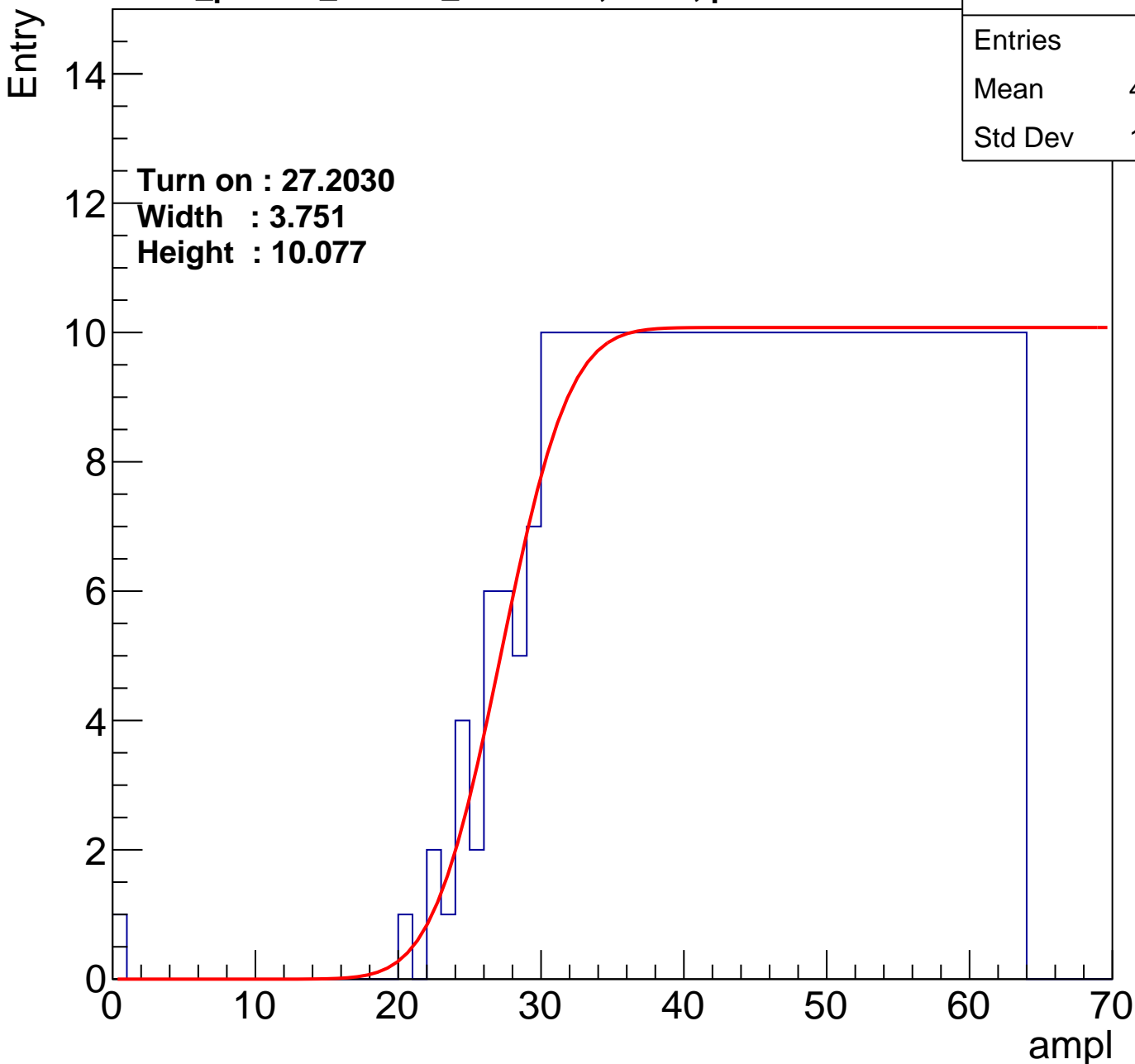
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.54
Std Dev	11.26

Turn on : 27.2030

Width : 3.751

Height : 10.077



# B0L000S, U17-ch8

calib\_packv5\_042523\_0143.root, FC#5, port B1

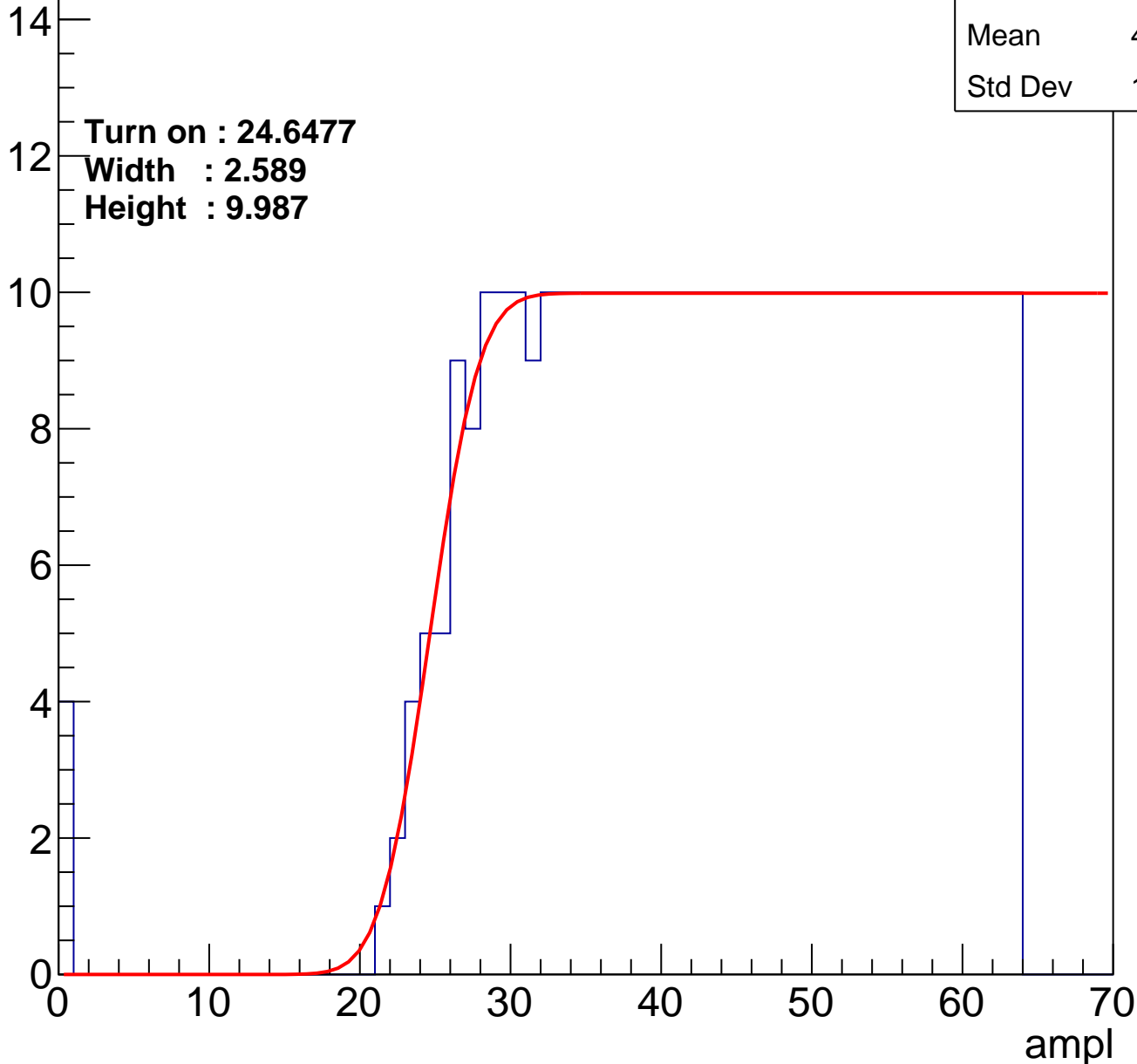
Entry

Entries	397
Mean	43.33
Std Dev	12.23

Turn on : 24.6477

Width : 2.589

Height : 9.987



# B0L000S, U17-ch9

calib\_packv5\_042523\_0143.root, FC#5, port B1

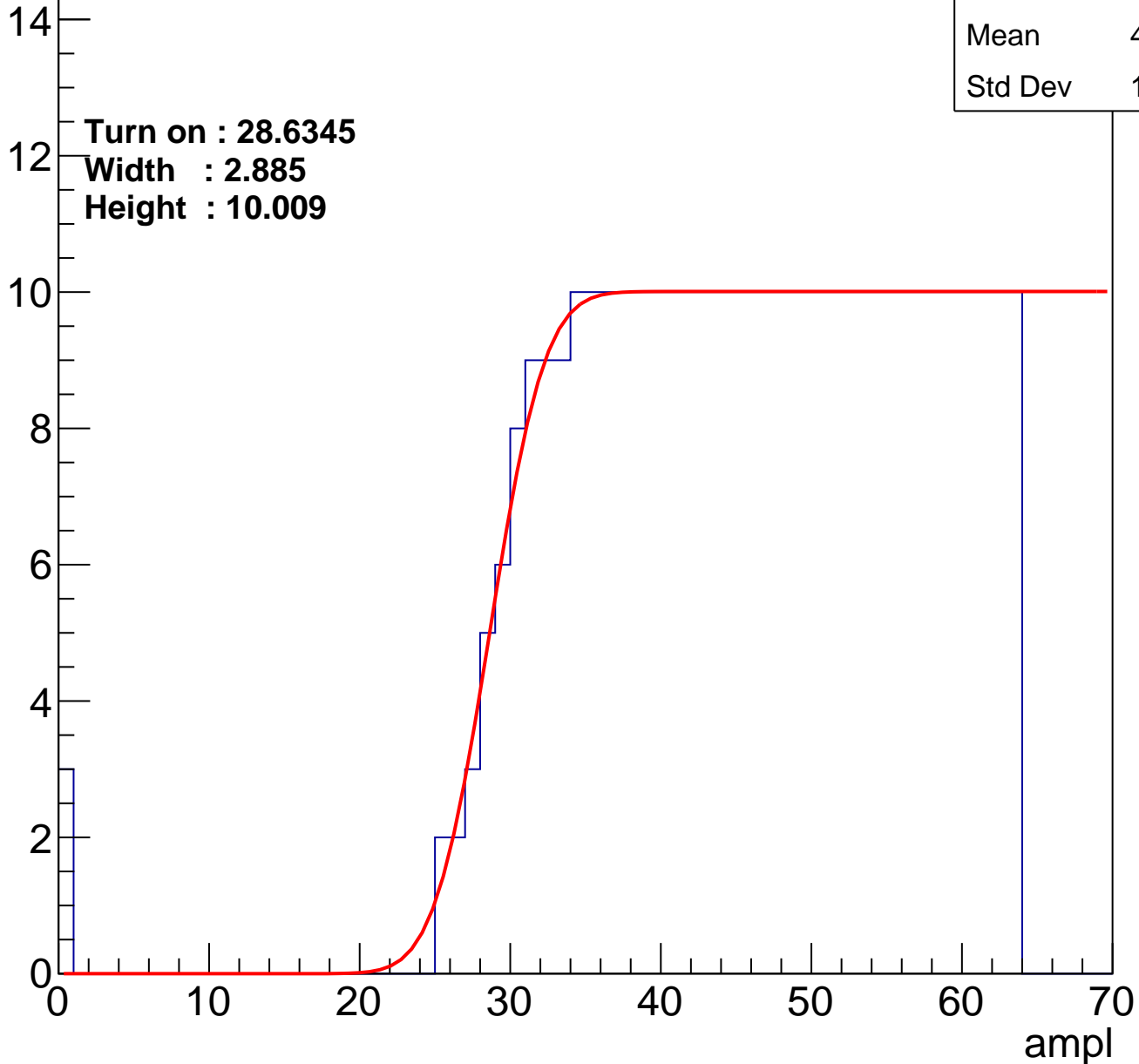
Entries	356
Mean	45.37
Std Dev	11.12

Turn on : 28.6345

Width : 2.885

Height : 10.009

Entry



# B0L000S, U17-ch10

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 26.2142

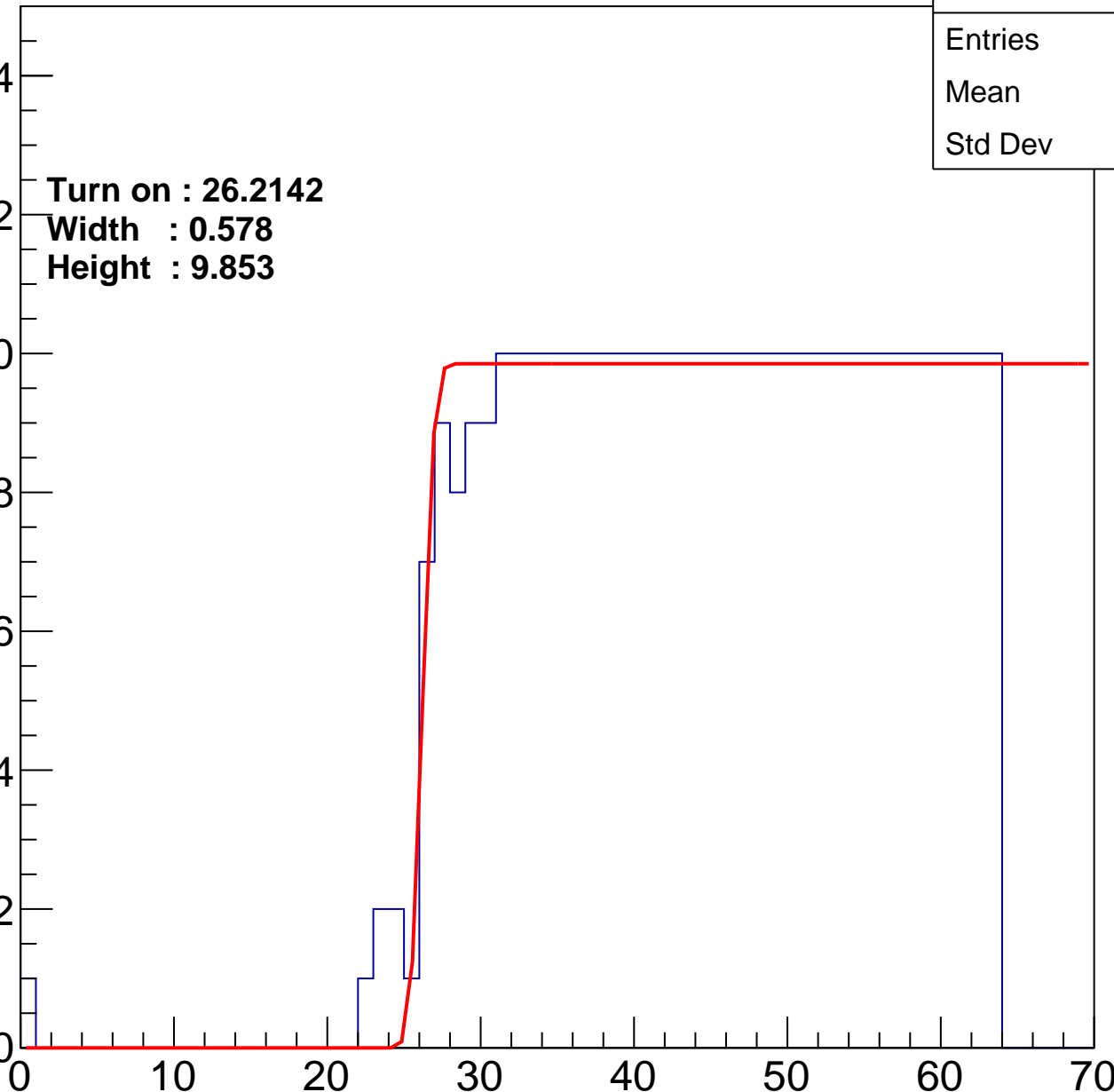
Width : 0.578

Height : 9.853

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch11

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.21
Std Dev	11.86

Turn on : 28.2716

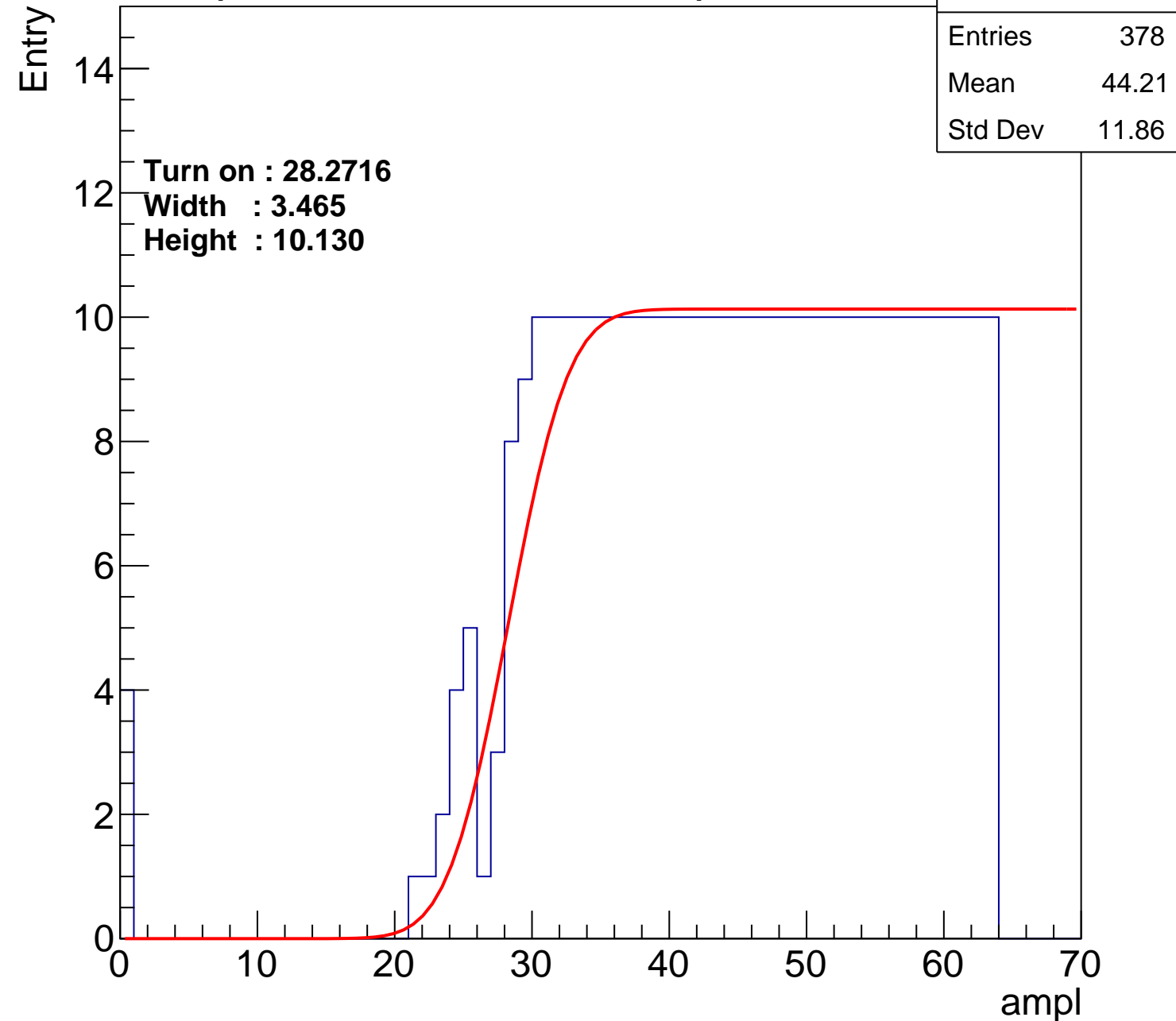
Width : 3.465

Height : 10.130

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch12

calib\_packv5\_042523\_0143.root, FC#5, port B1

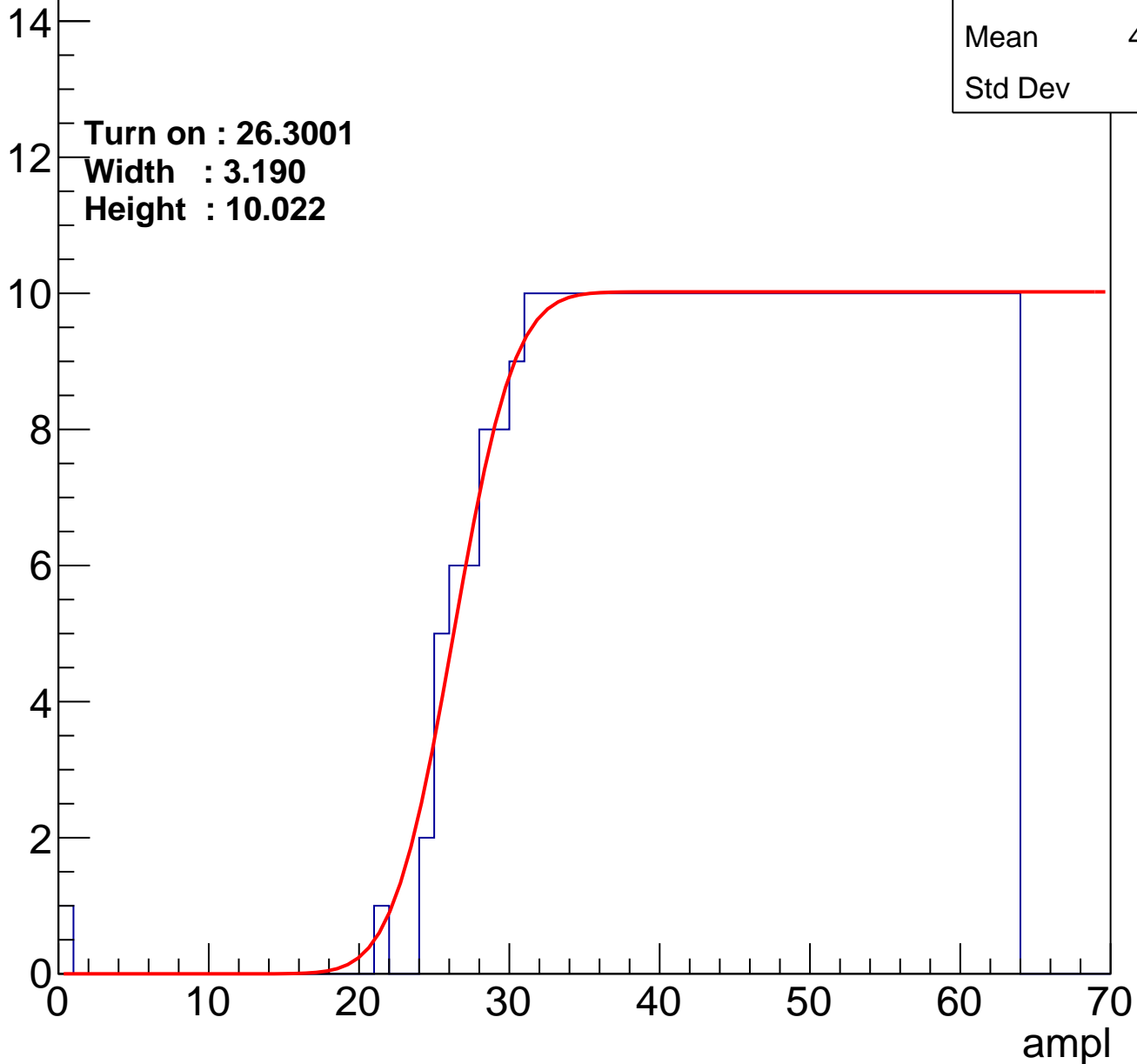
Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.3001

Width : 3.190

Height : 10.022

Entry



# B0L000S, U17-ch13

calib\_packv5\_042523\_0143.root, FC#5, port B1

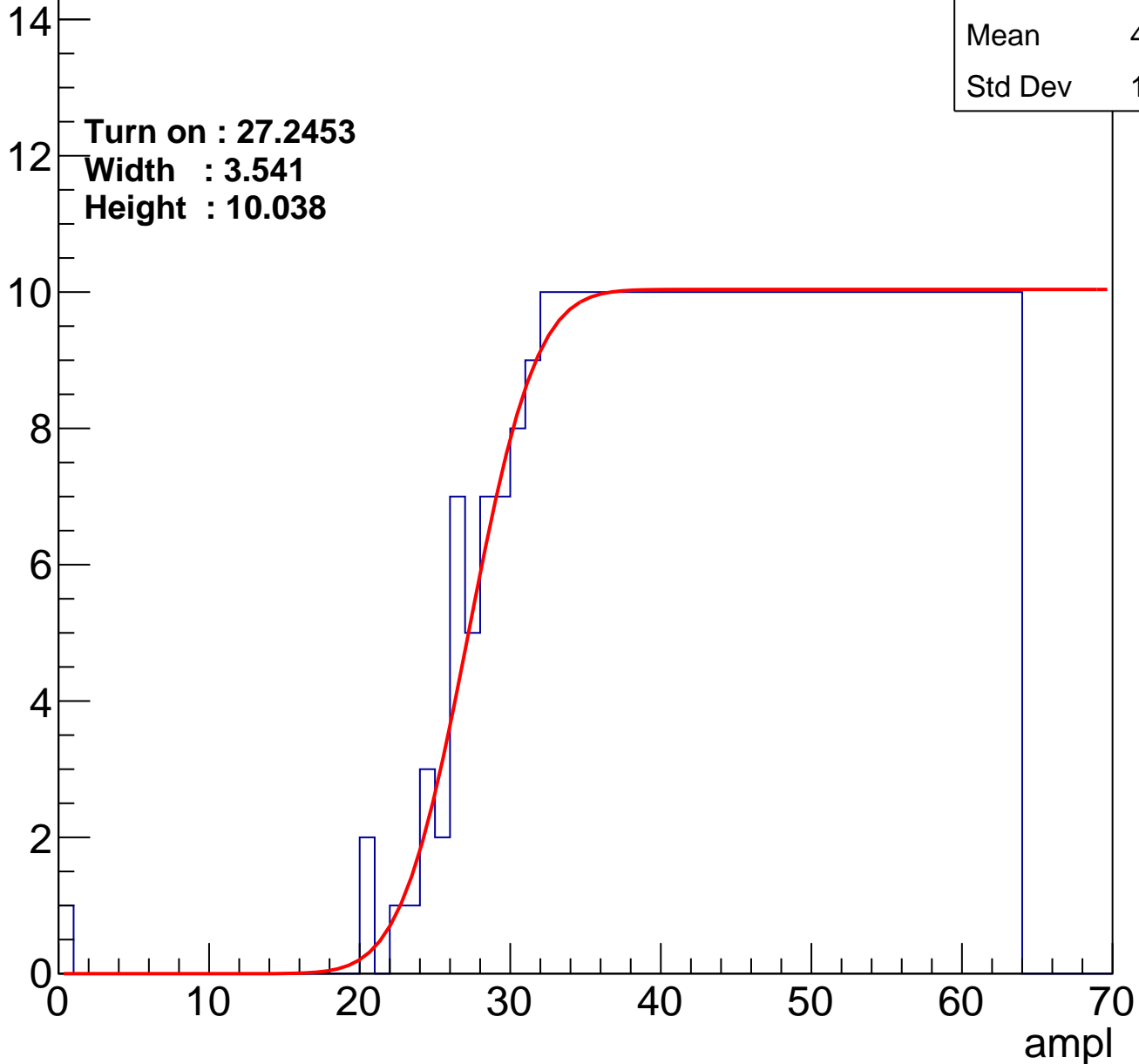
Entries	373
Mean	44.62
Std Dev	11.24

Turn on : 27.2453

Width : 3.541

Height : 10.038

Entry



# B0L000S, U17-ch14

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 27.4825

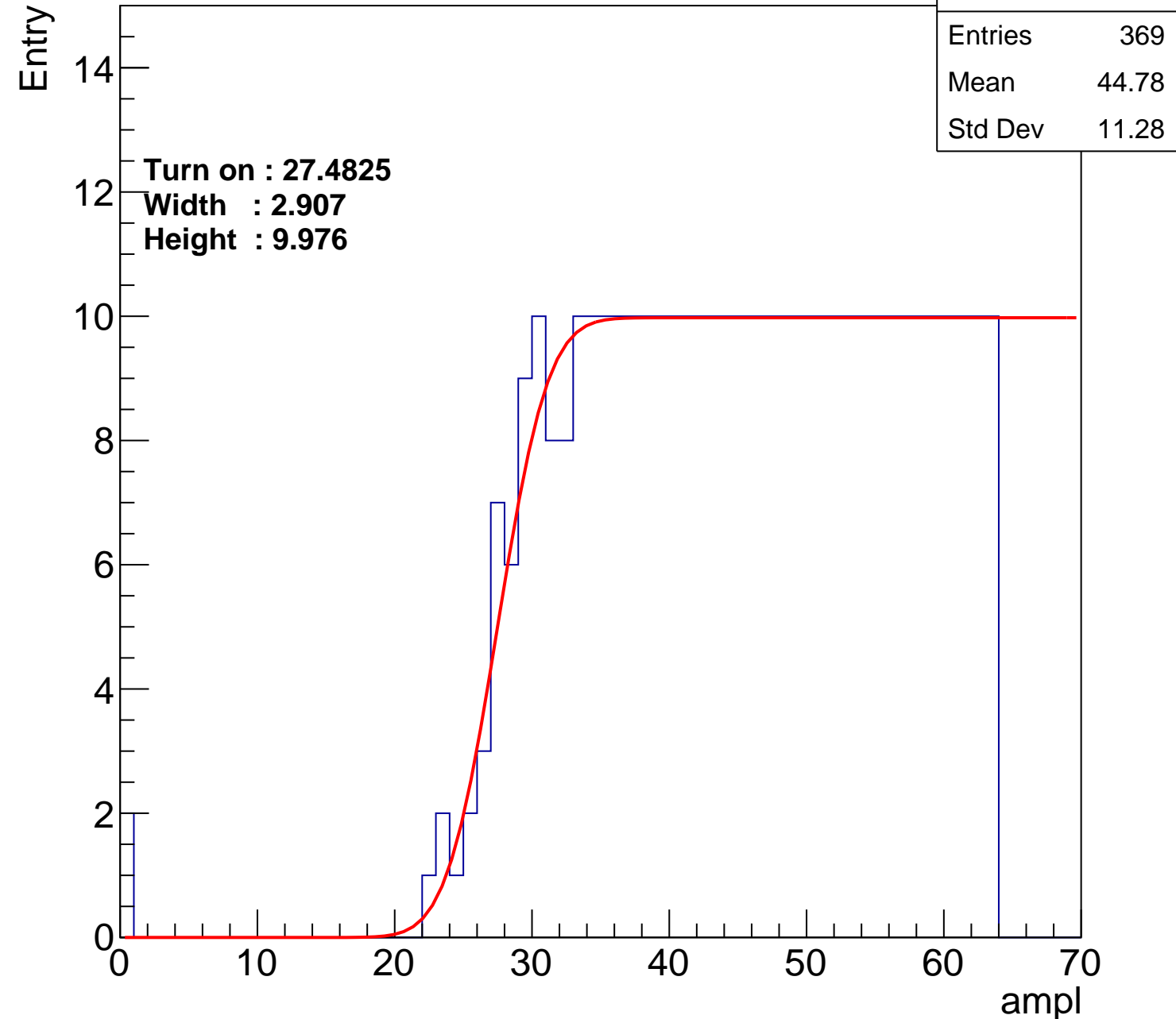
Width : 2.907

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch15

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.47
Std Dev	11.72

Turn on : 27.9304

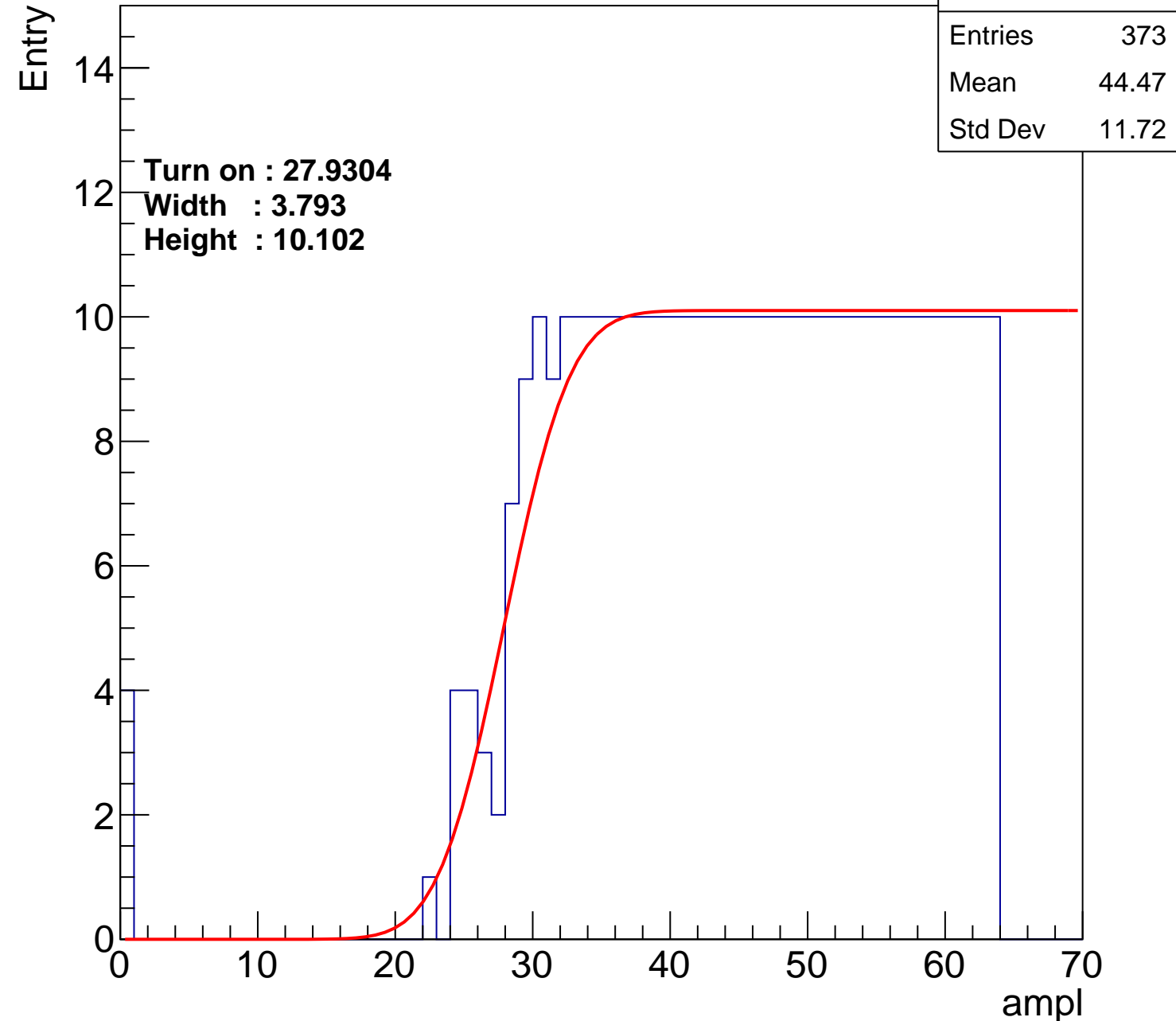
Width : 3.793

Height : 10.102

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch16

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	409
Mean	42.92
Std Dev	12.08

Turn on : 23.1796

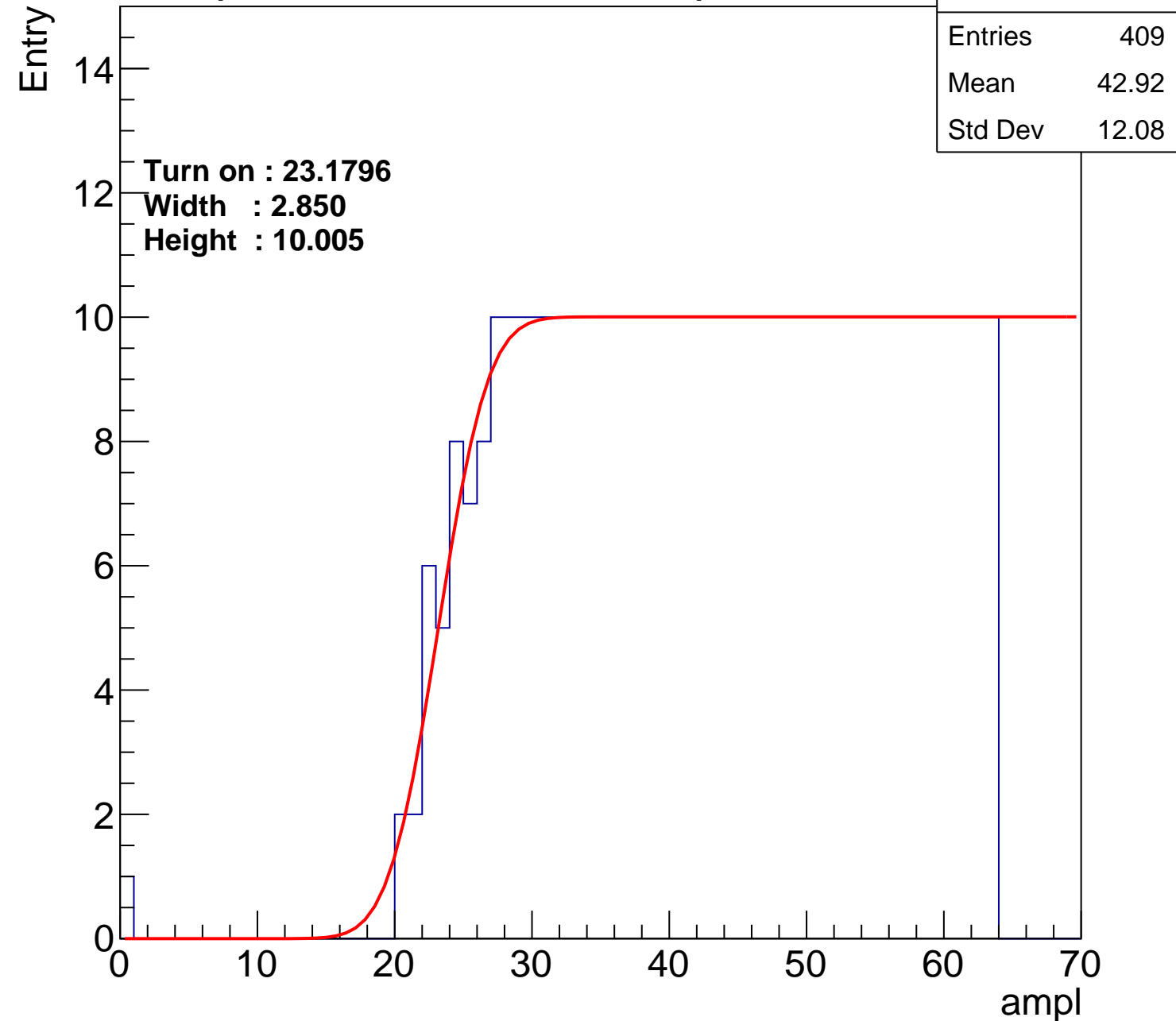
Width : 2.850

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch17

calib\_packv5\_042523\_0143.root, FC#5, port B1

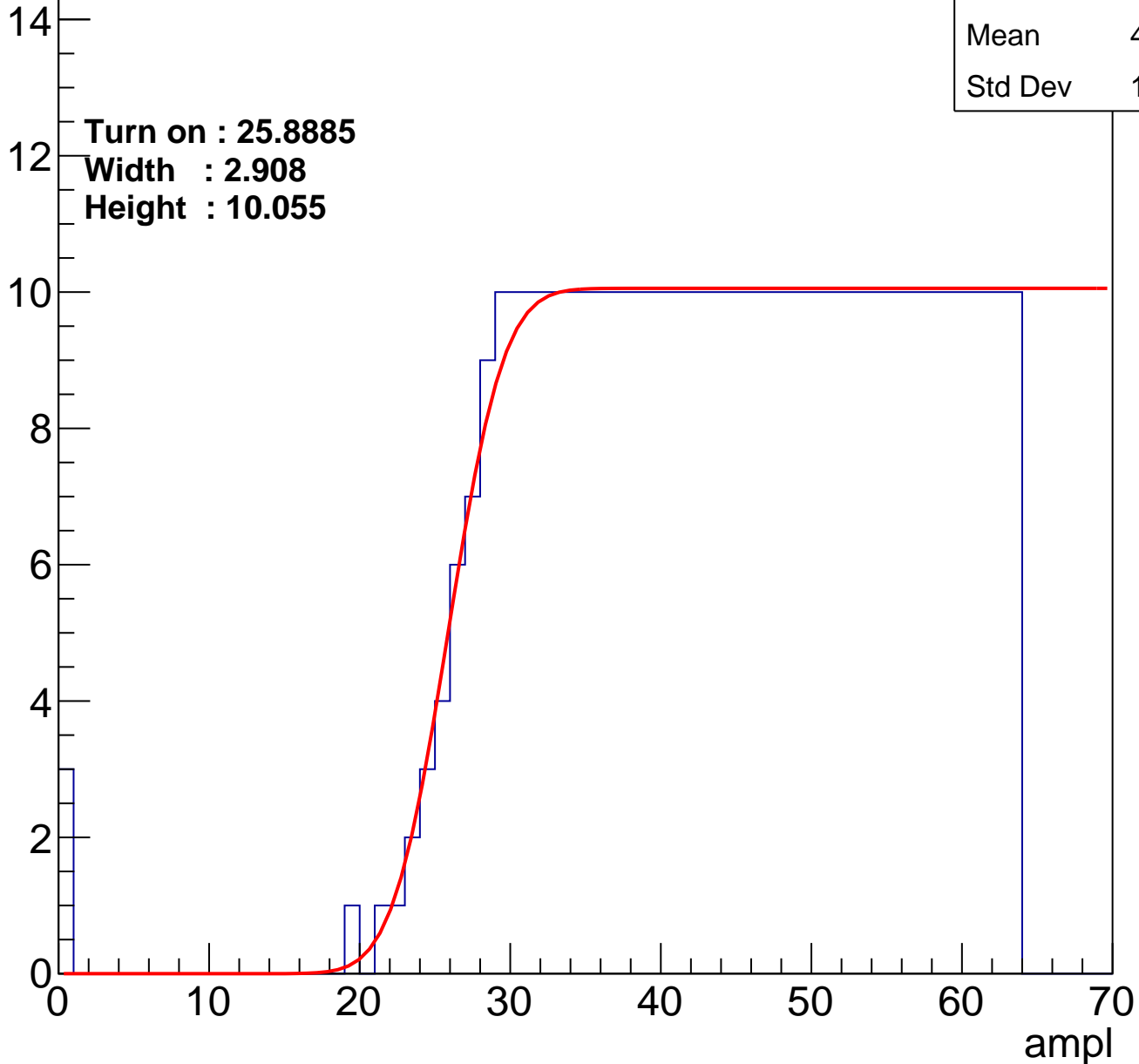
Entries	387
Mean	43.87
Std Dev	11.85

Turn on : 25.8885

Width : 2.908

Height : 10.055

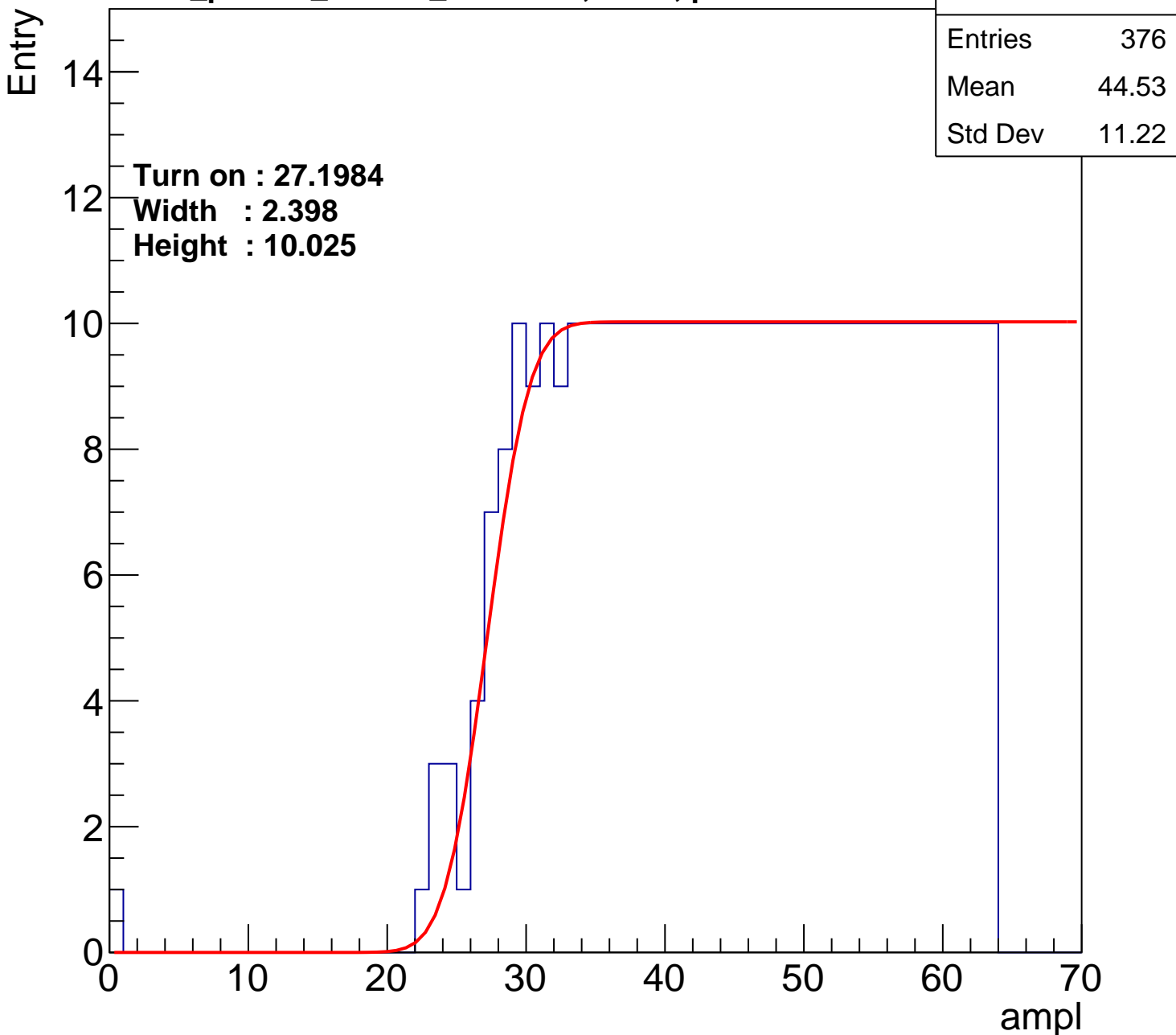
Entry



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 27.1984  
Width : 2.398  
Height : 10.025



# B0L000S, U17-ch19

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.82
Std Dev	12.04

Turn on : 26.1396

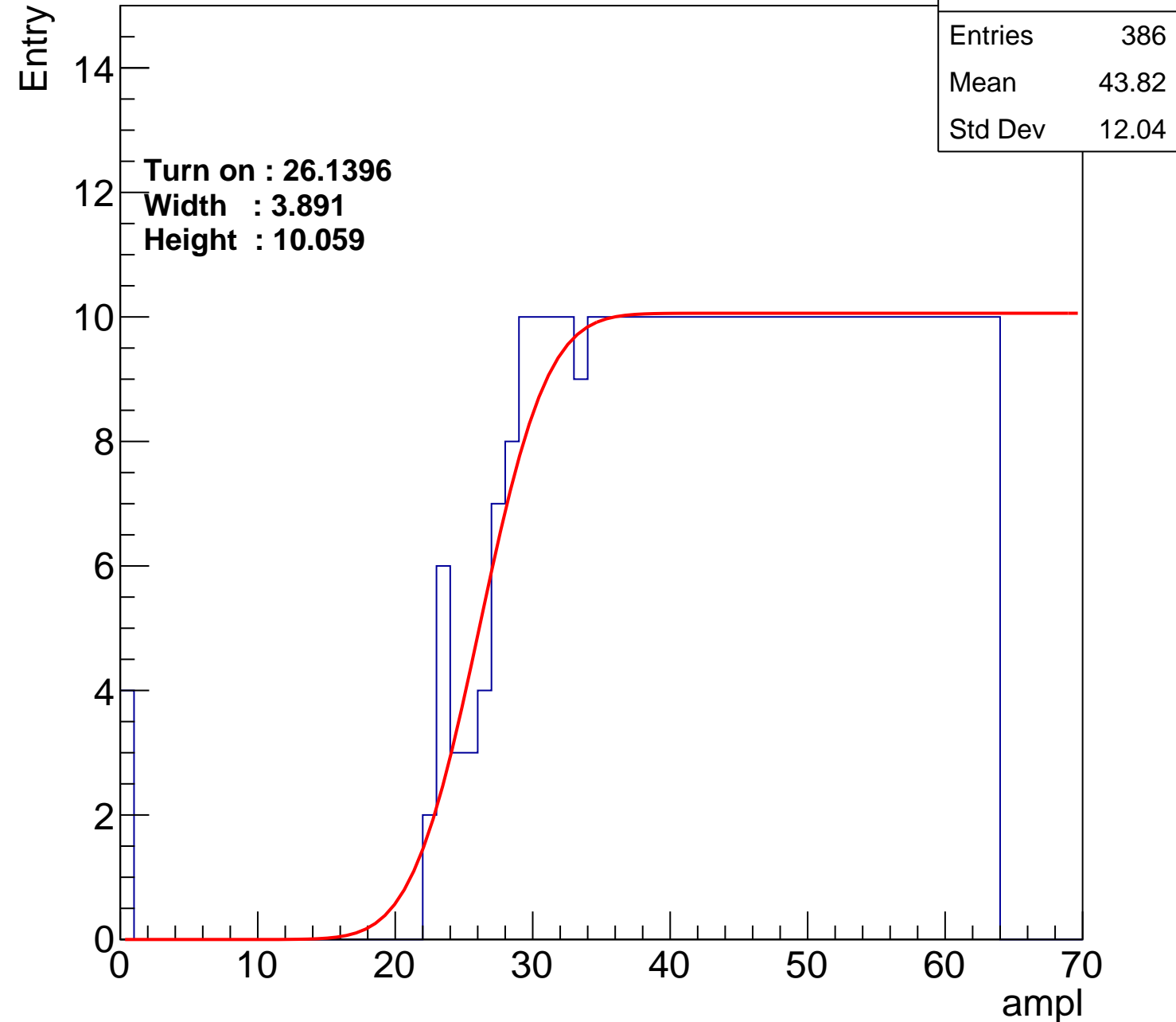
Width : 3.891

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch20

calib\_packv5\_042523\_0143.root, FC#5, port B1

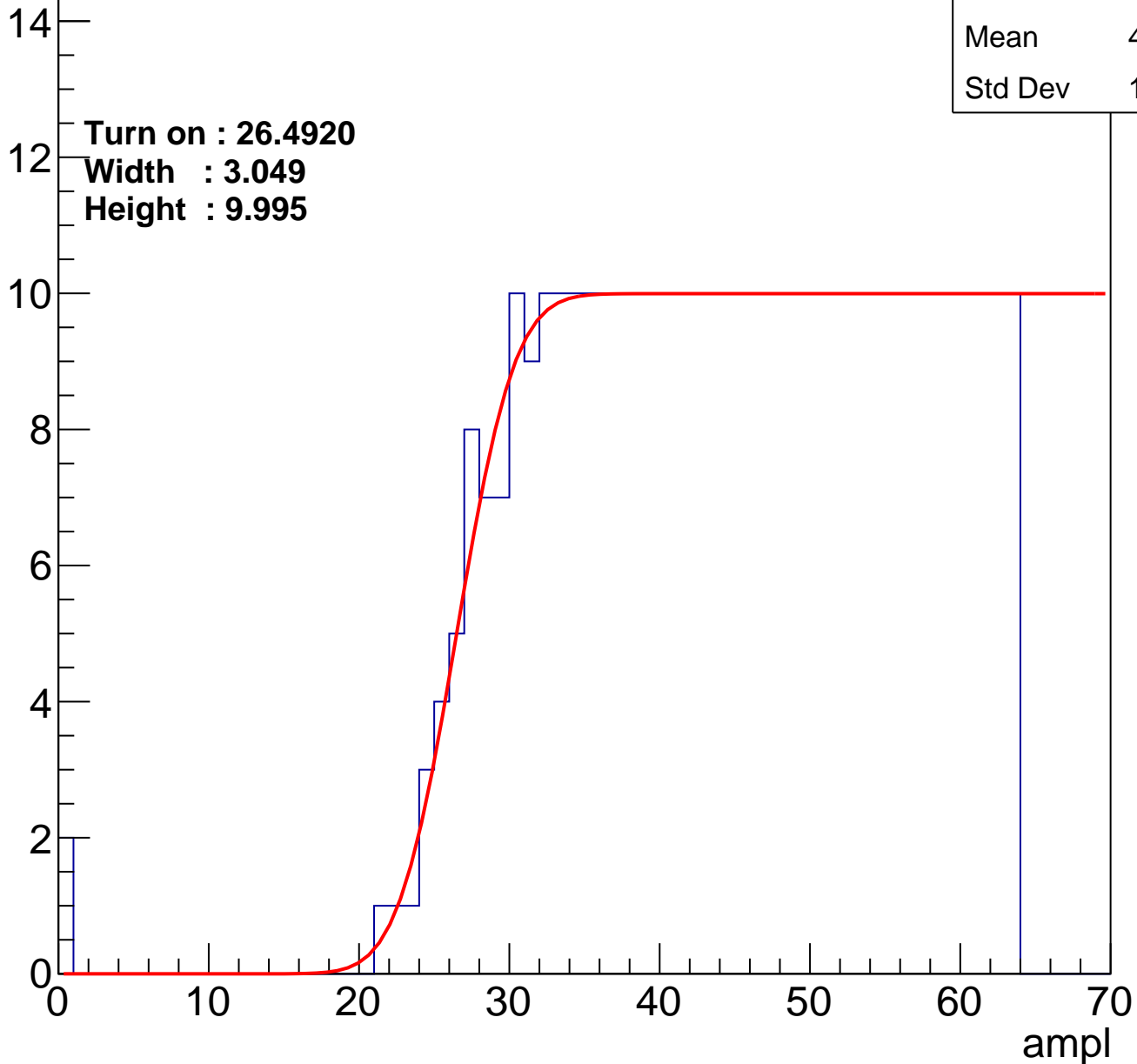
Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.4920

Width : 3.049

Height : 9.995

Entry



# B0L000S, U17-ch21

calib\_packv5\_042523\_0143.root, FC#5, port B1

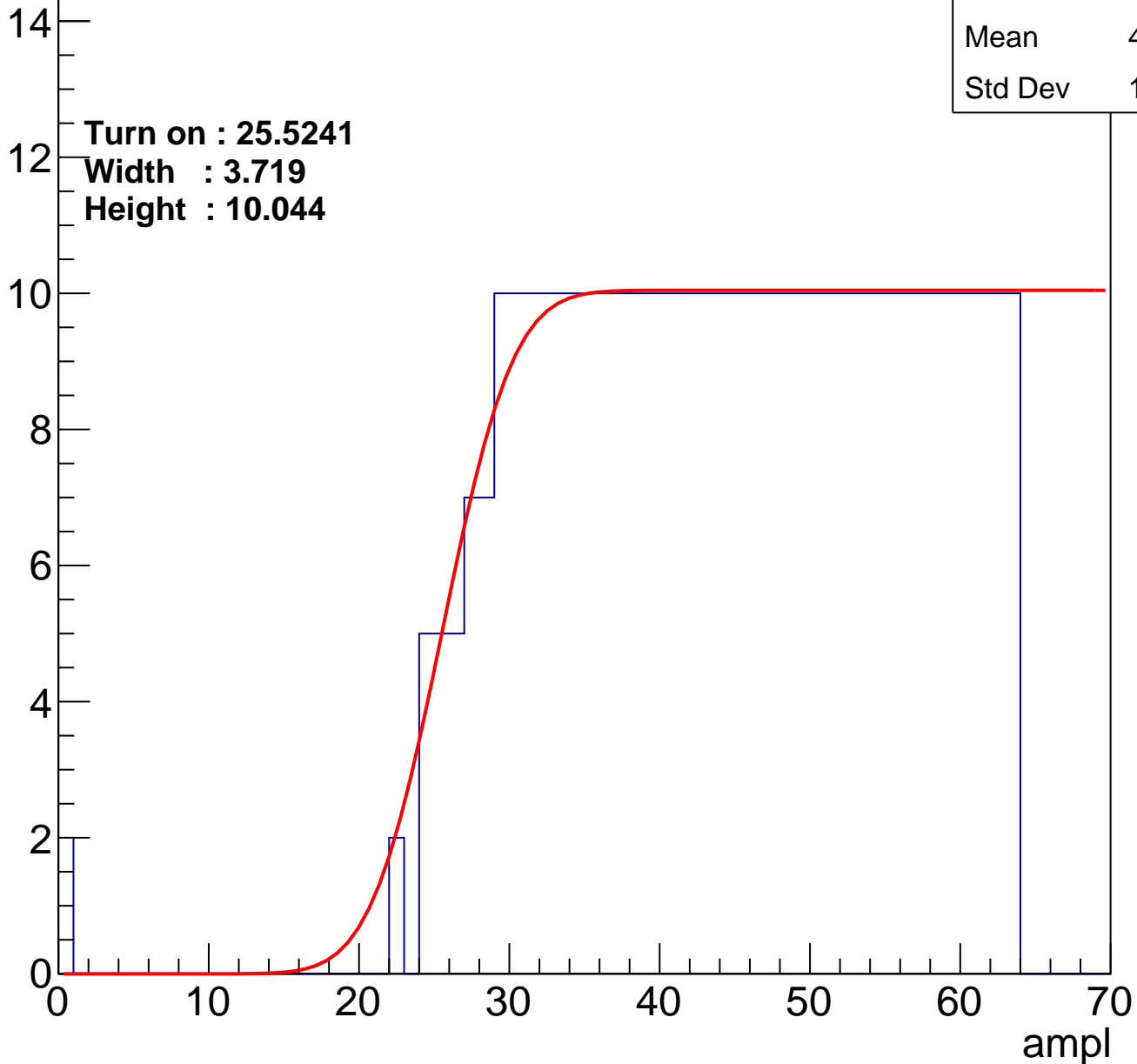
Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 25.5241

Width : 3.719

Height : 10.044

Entry



# B0L000S, U17-ch22

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.65
Std Dev	11.19

Turn on : 27.1324

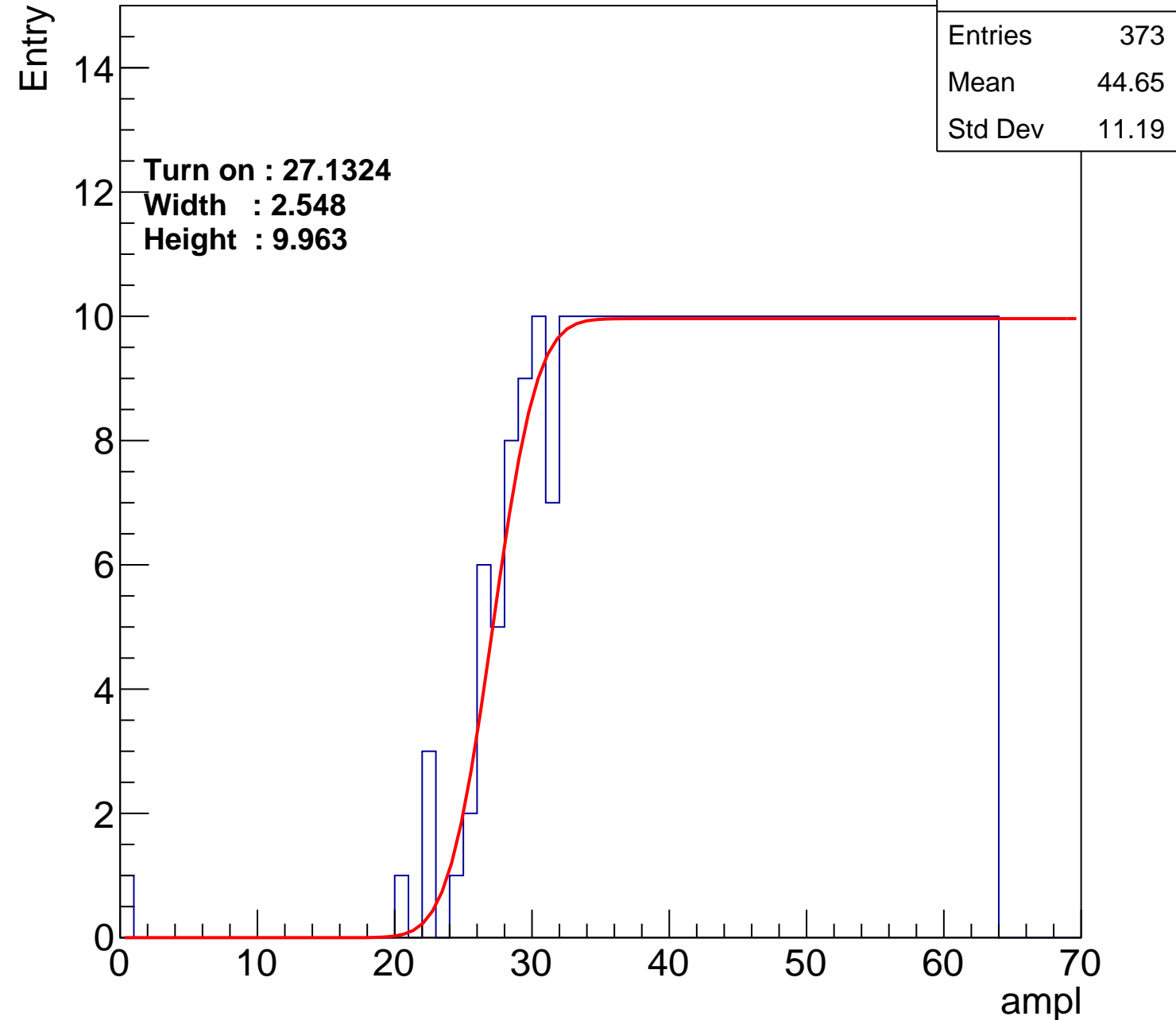
Width : 2.548

Height : 9.963

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch23

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	45.14
Std Dev	10.86

Turn on : 27.8489

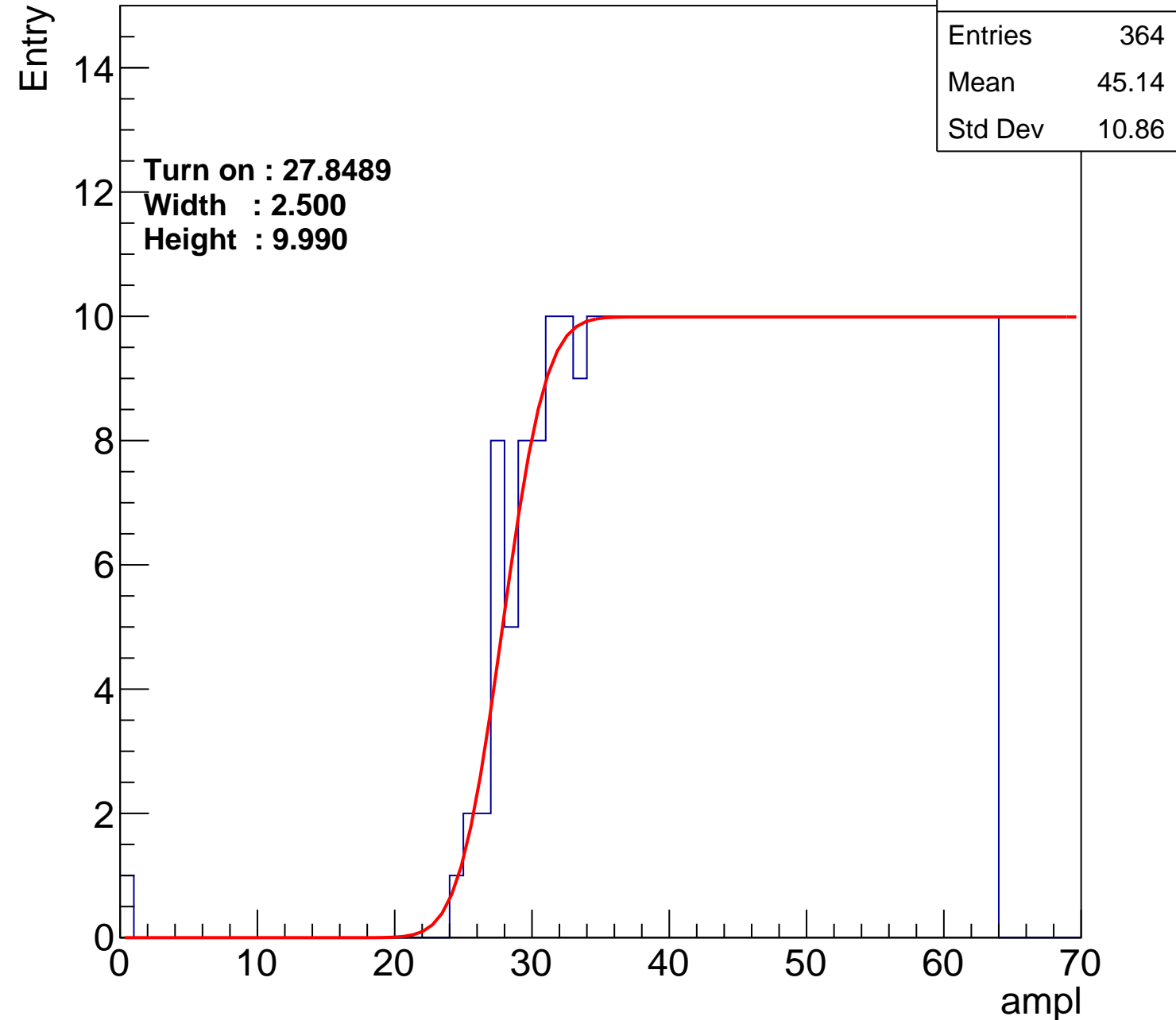
Width : 2.500

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**B0L000S, U17-ch24**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	382
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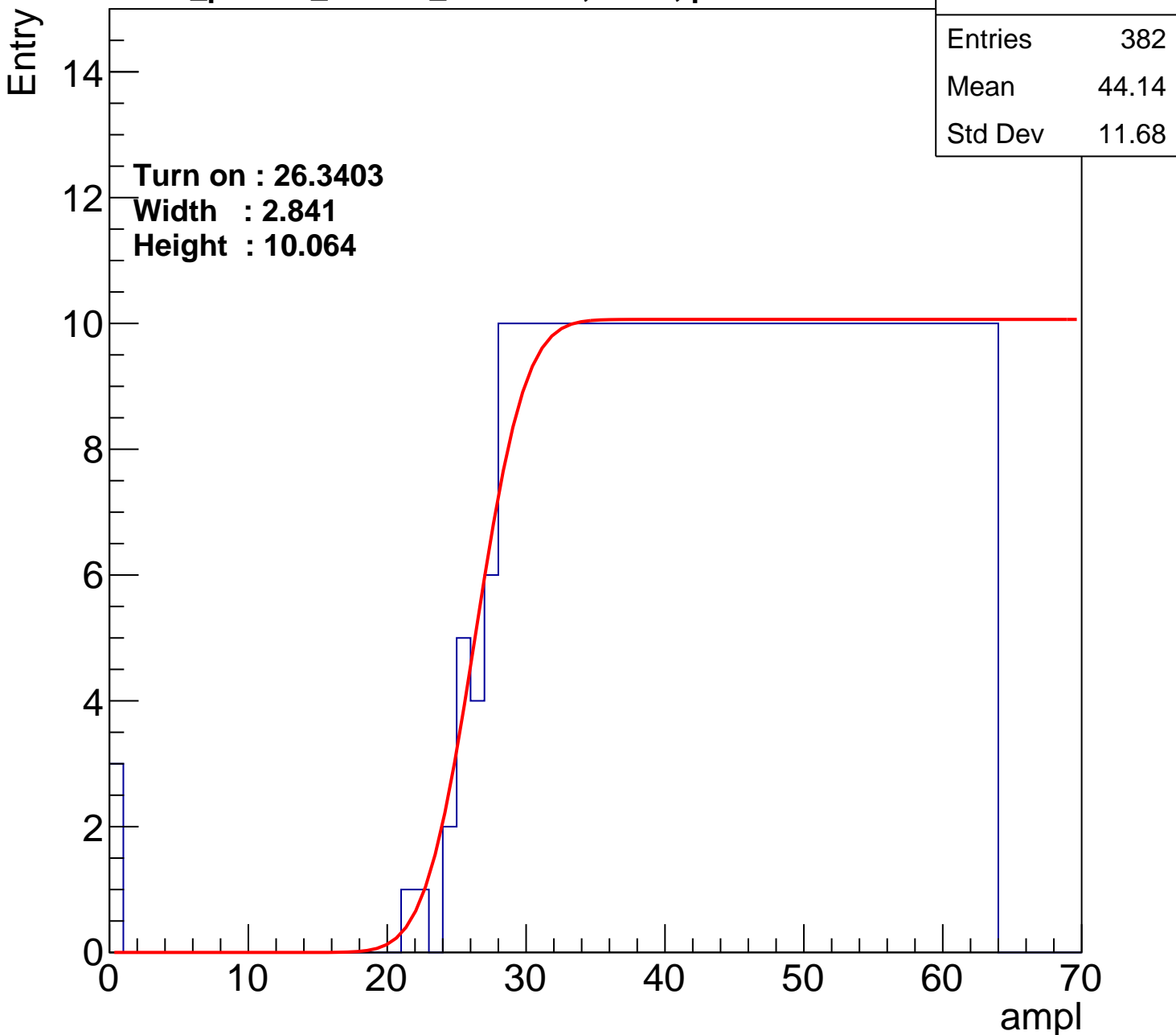
Mean	44.14
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Std Dev	11.68
---------	-------

**Turn on : 26.3403**

**Width : 2.841**

**Height : 10.064**



# B0L000S, U17-ch25

calib\_packv5\_042523\_0143.root, FC#5, port B1

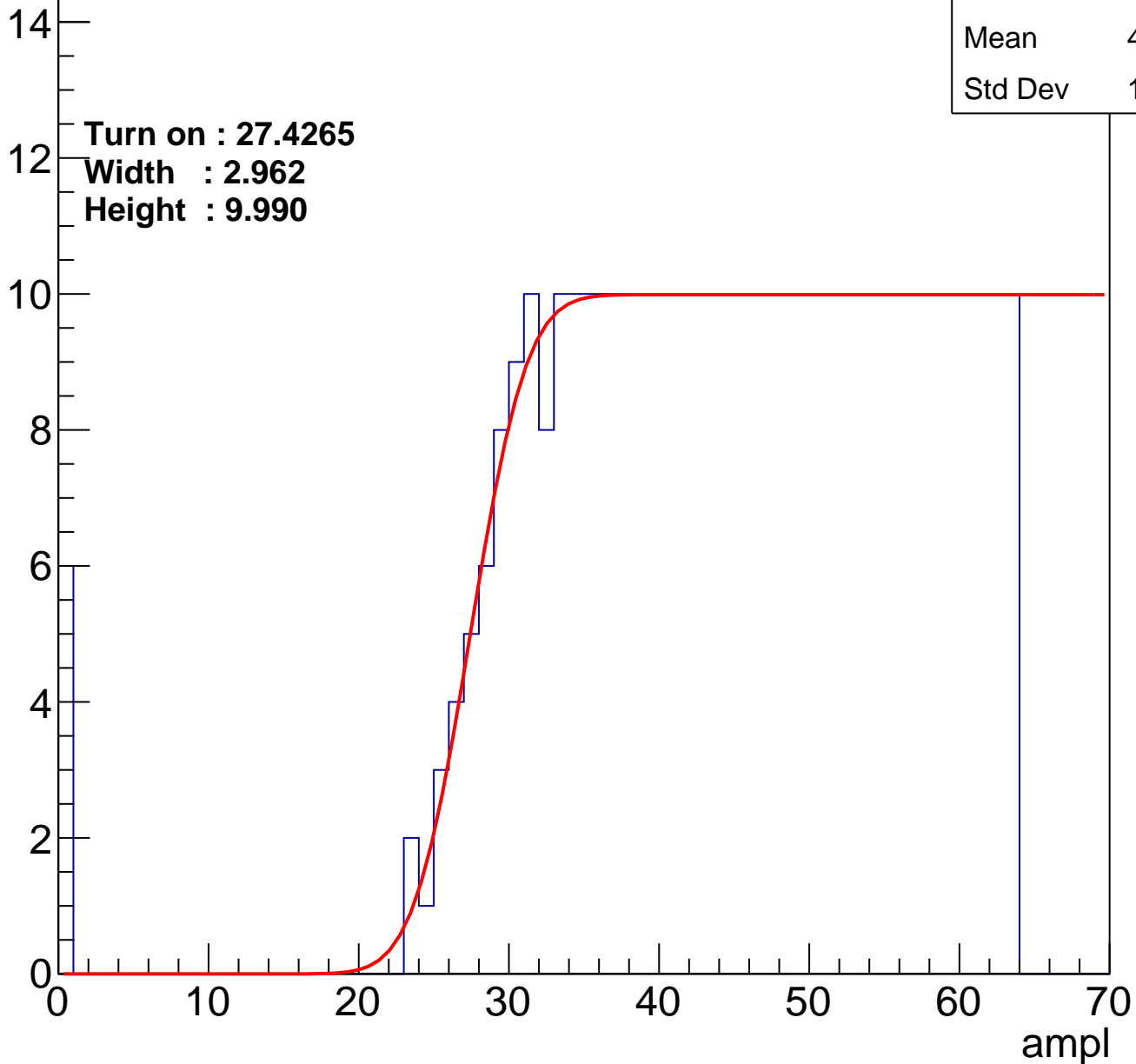
Entries	372
Mean	44.35
Std Dev	12.09

**Turn on : 27.4265**

**Width : 2.962**

**Height : 9.990**

Entry



# B0L000S, U17-ch26

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	45.16
Std Dev	10.83

**Turn on : 27.4882**

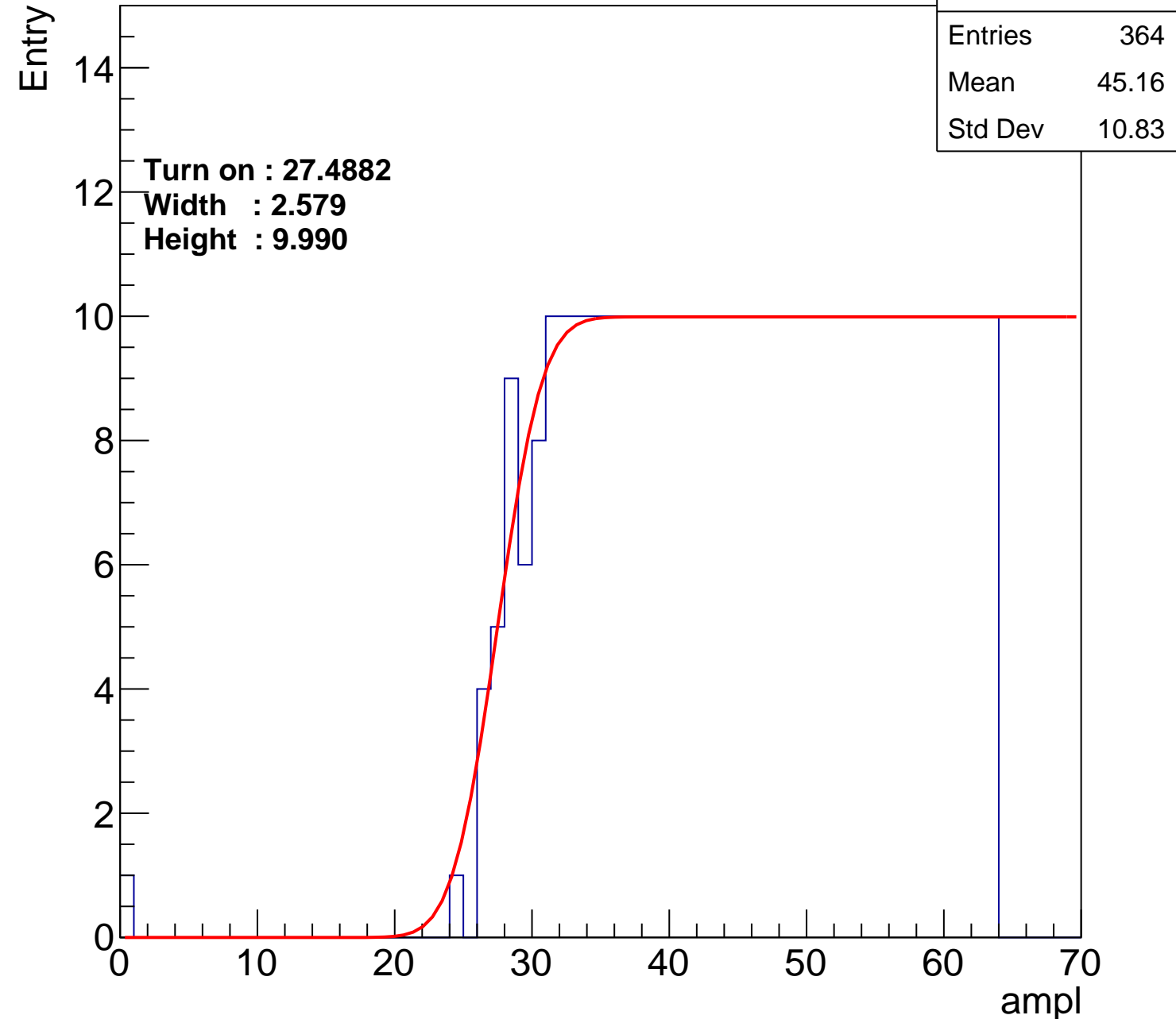
**Width : 2.579**

**Height : 9.990**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch27

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 25.7434

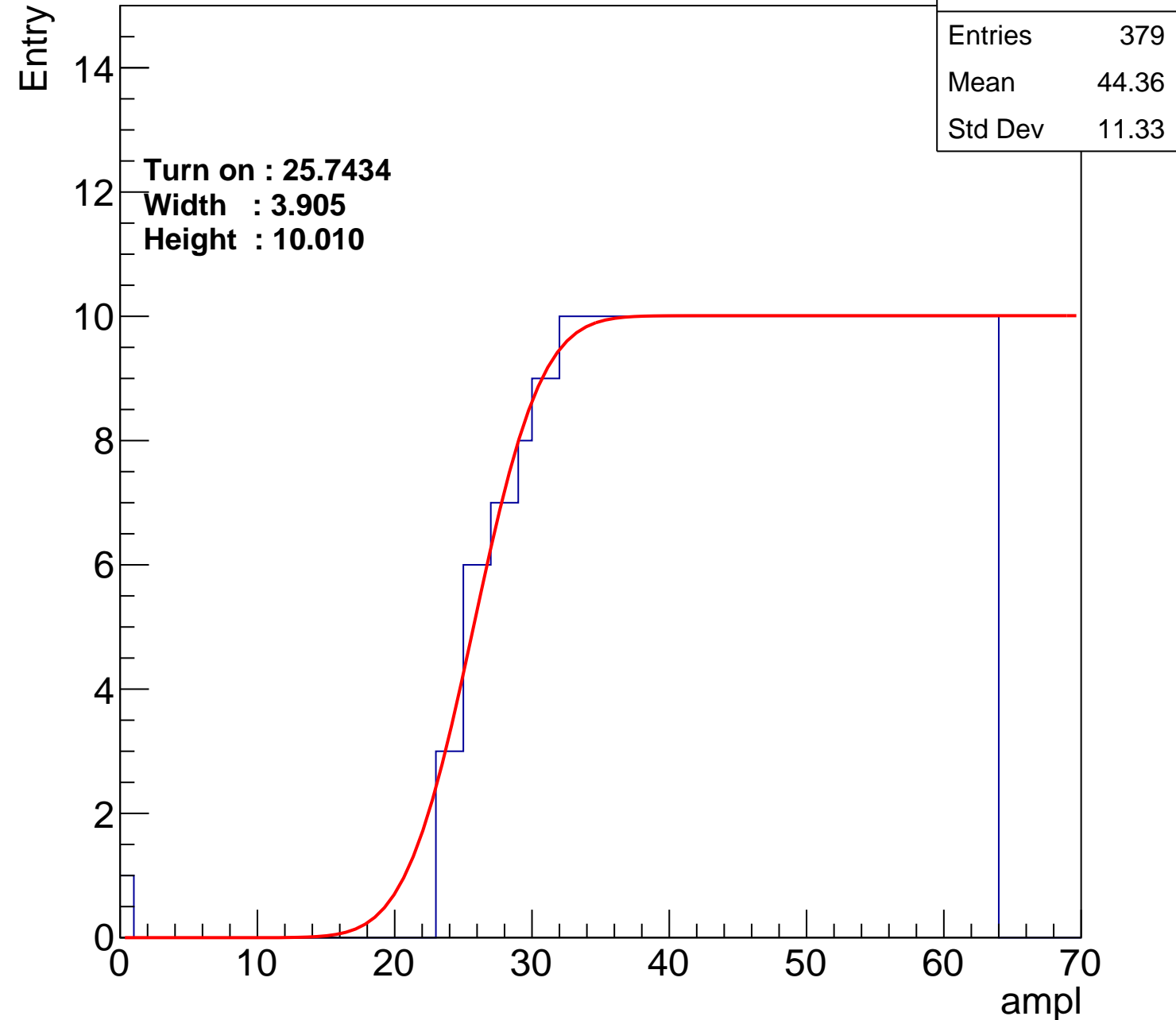
Width : 3.905

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch28

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.08
Std Dev	11.55

**Turn on : 25.8125**

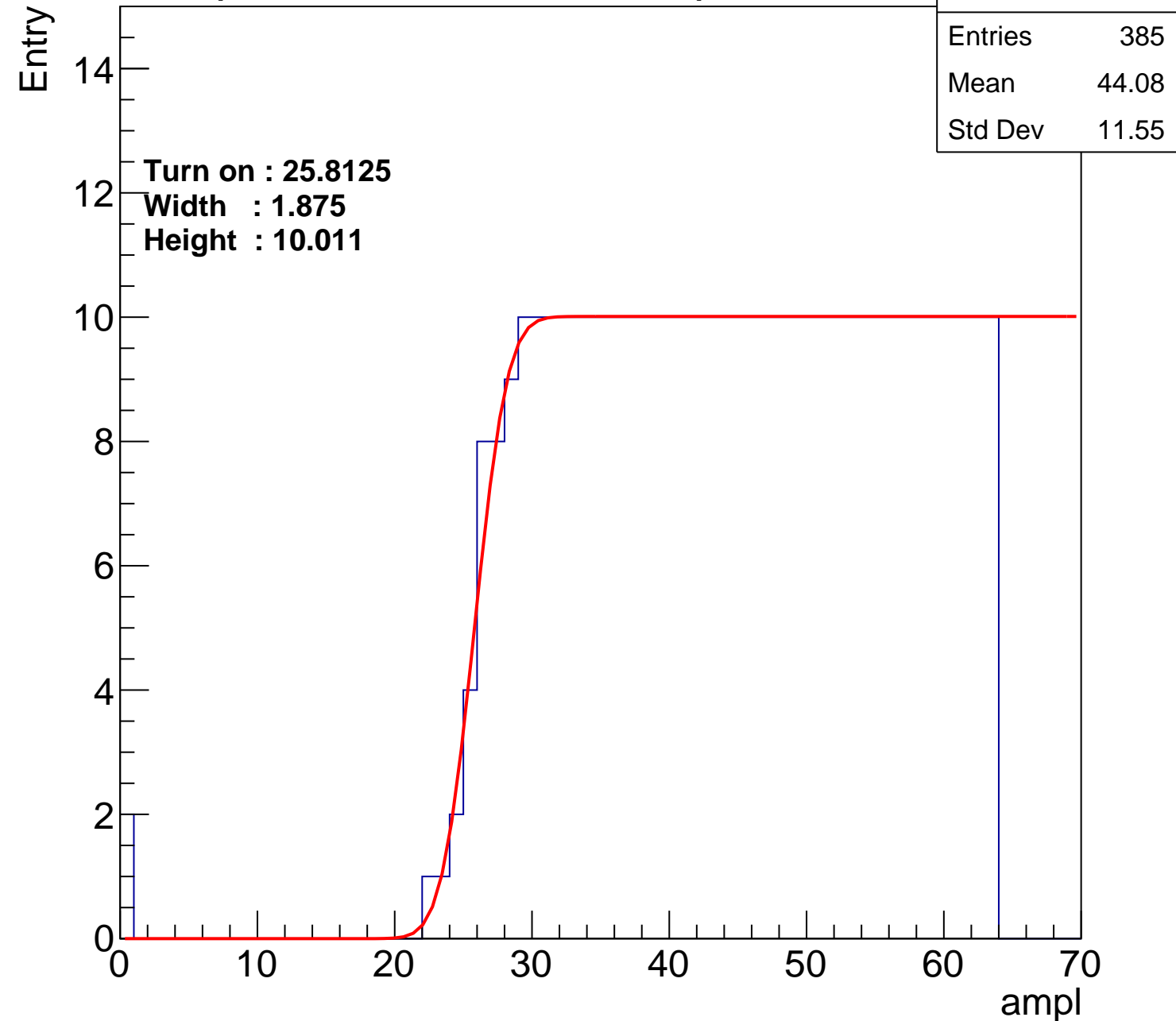
**Width : 1.875**

**Height : 10.011**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch29

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.12
Std Dev	11.53

**Turn on : 26.8403**

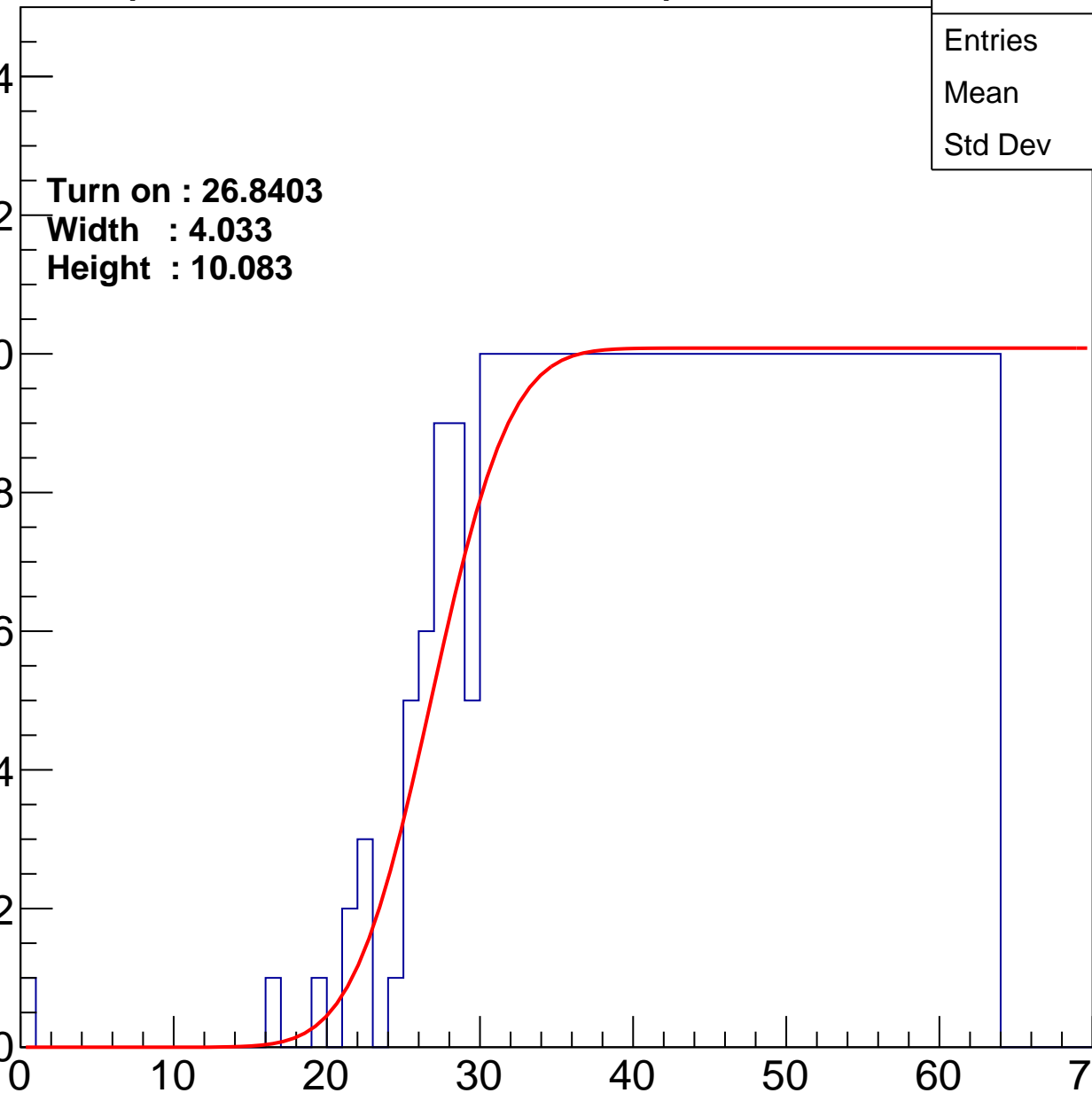
**Width : 4.033**

**Height : 10.083**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch30

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.06
Std Dev	11.82

**Turn on : 26.6833**

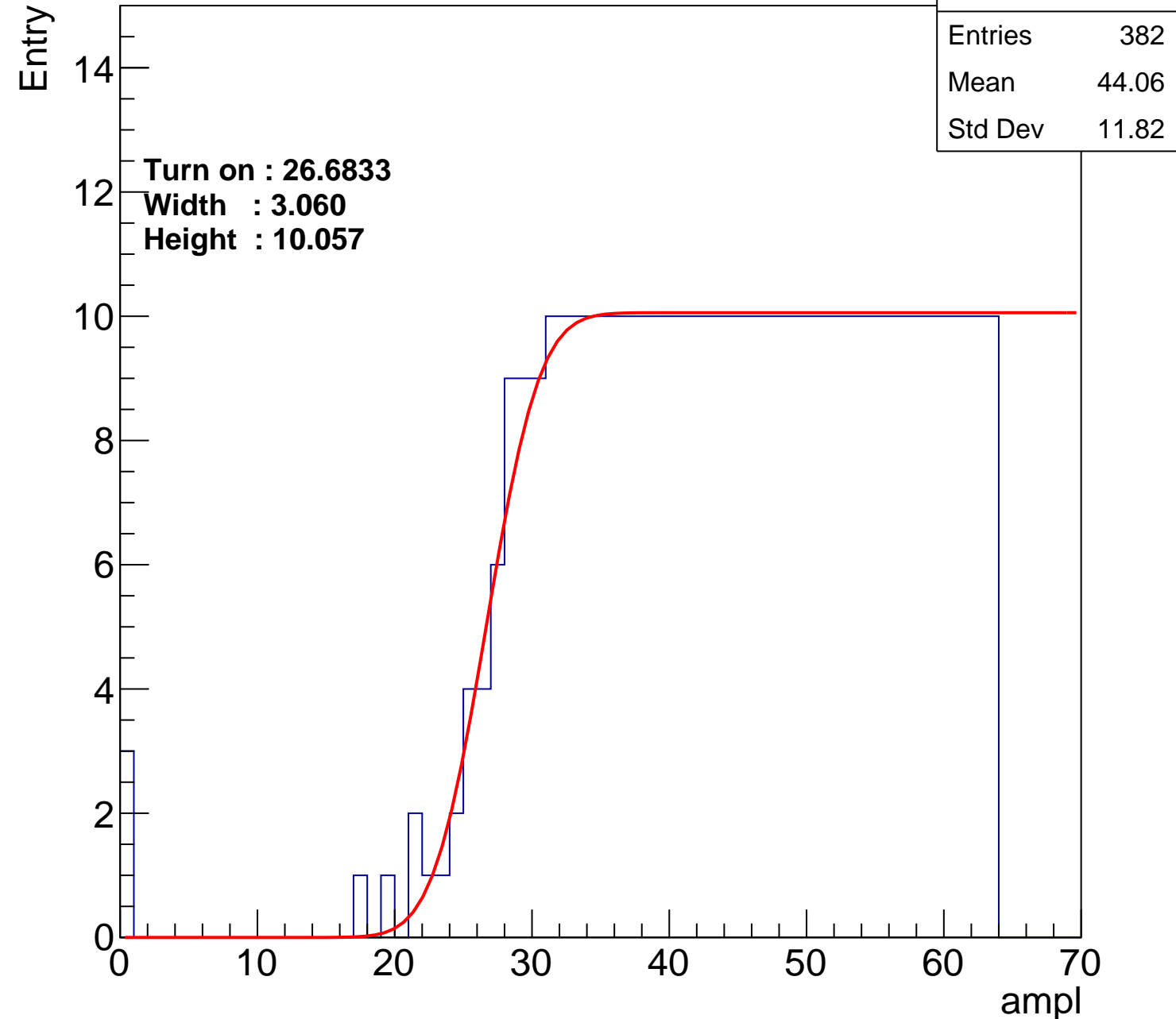
**Width : 3.060**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch31

calib\_packv5\_042523\_0143.root, FC#5, port B1

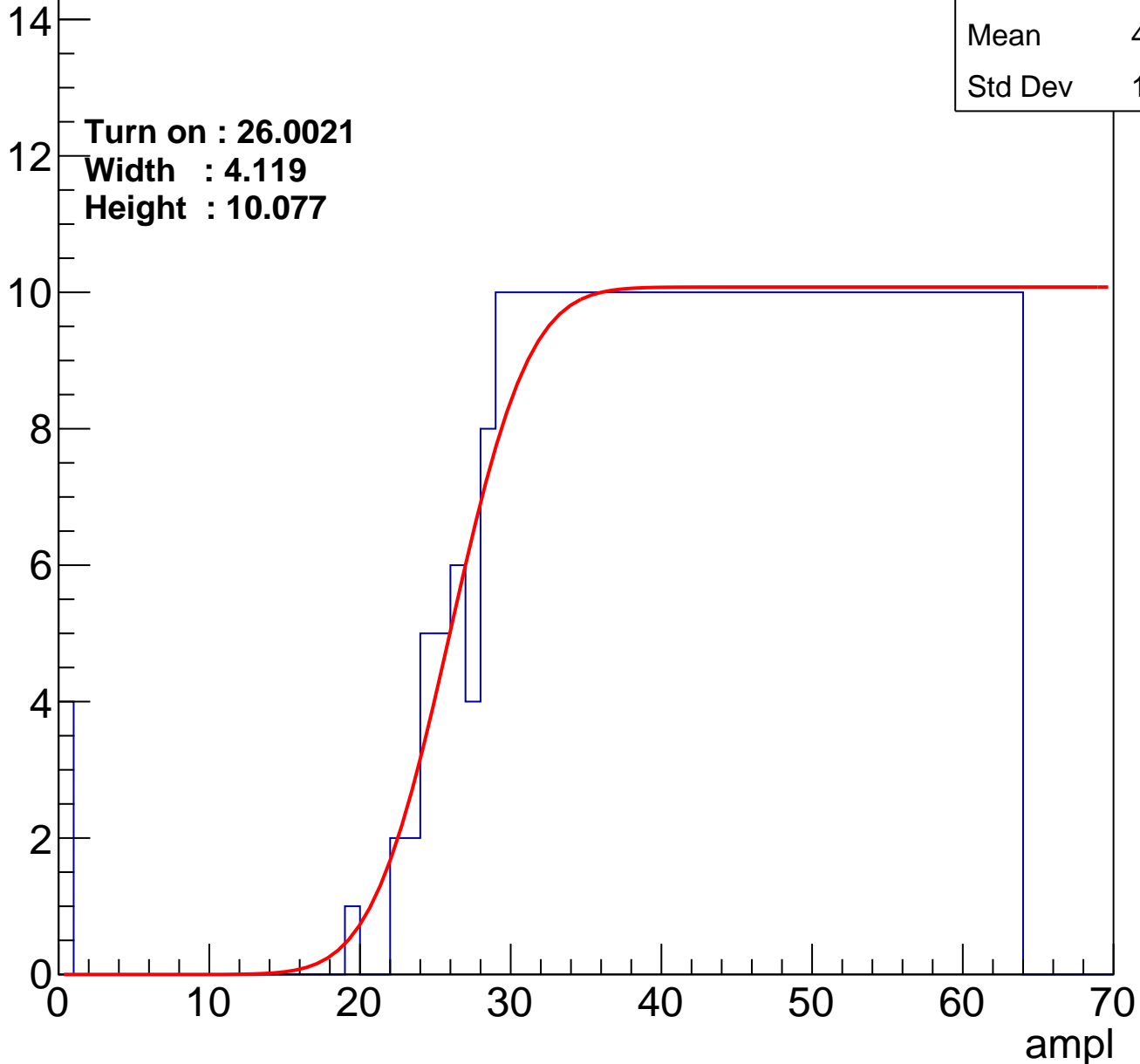
Entries	387
Mean	43.78
Std Dev	12.06

Turn on : 26.0021

Width : 4.119

Height : 10.077

Entry



# B0L000S, U17-ch32

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.31
Std Dev	11.43

**Turn on : 26.5123**

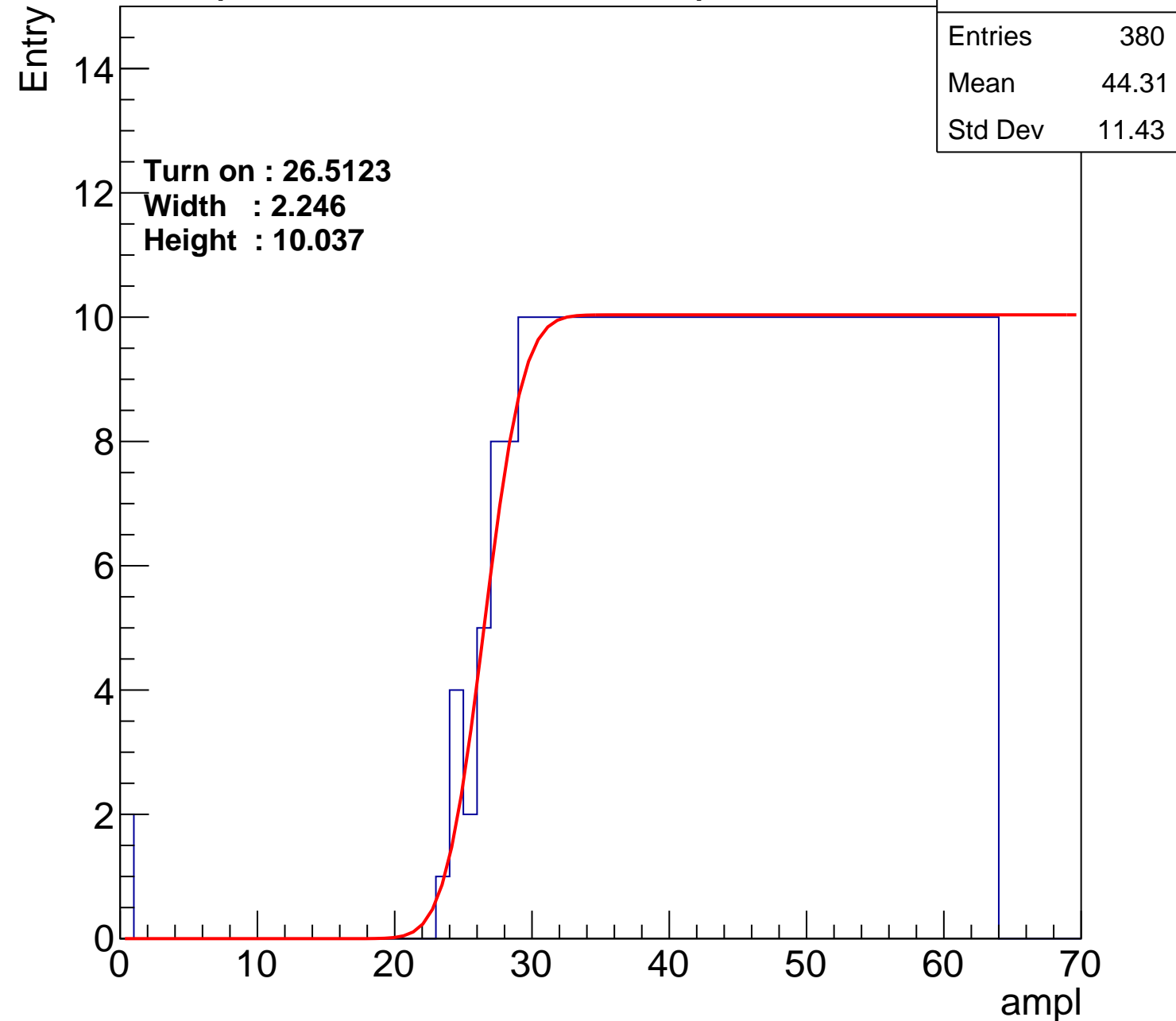
**Width : 2.246**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch33

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.63
Std Dev	11.48

Turn on : 27.2363

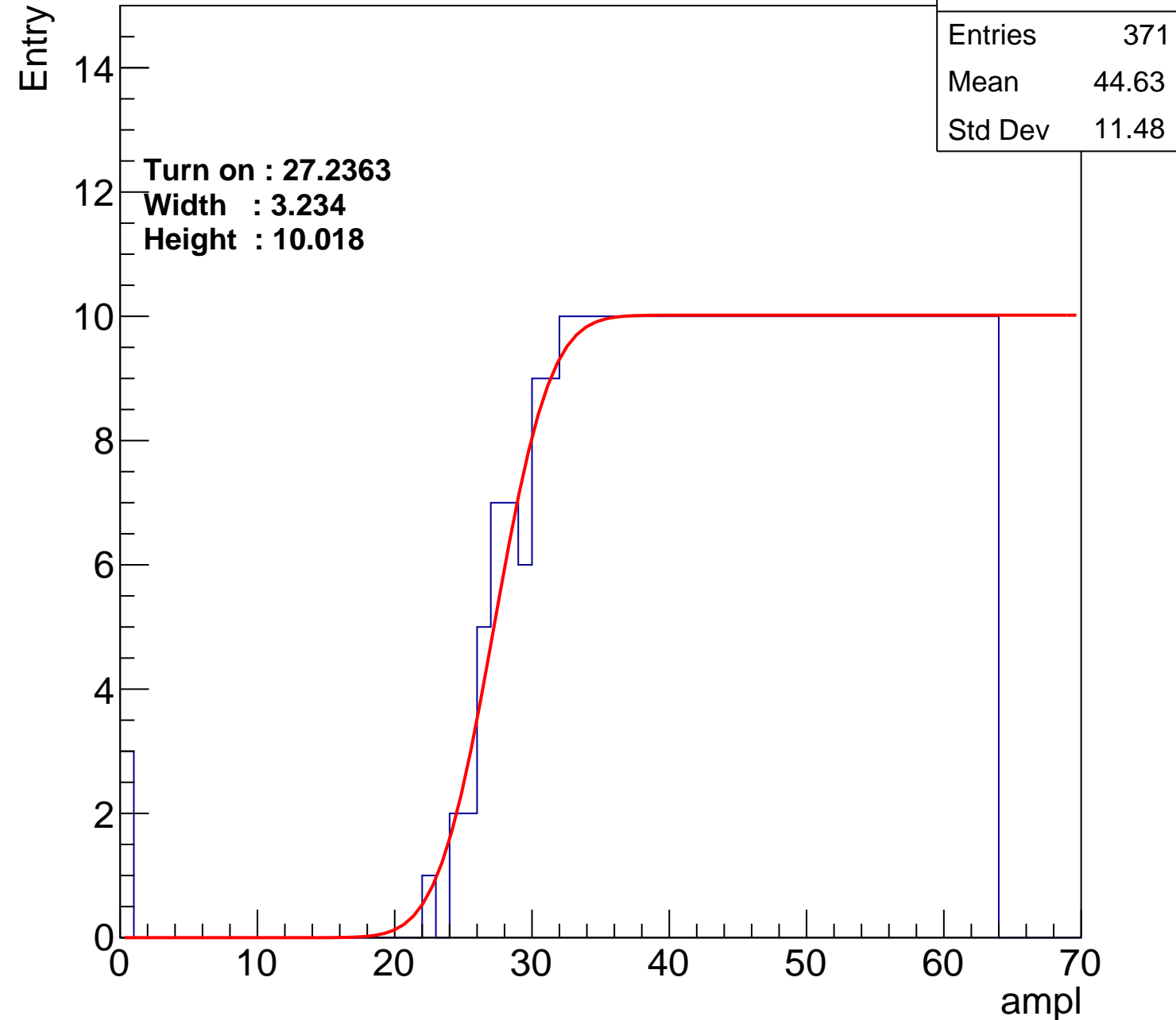
Width : 3.234

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch34

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.41
Std Dev	11.75

**Turn on : 27.3757**

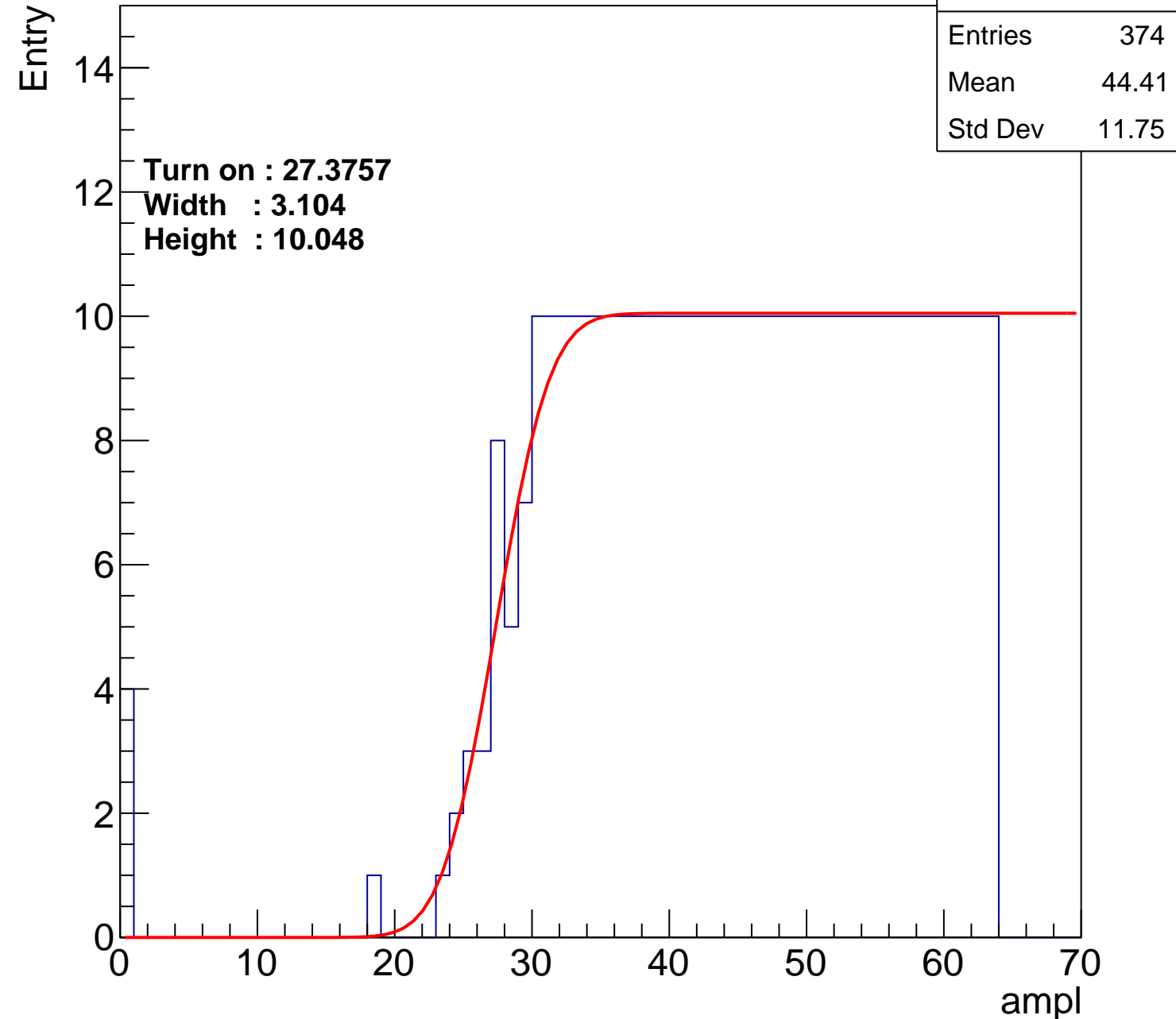
**Width : 3.104**

**Height : 10.048**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 26.4238  
Width : 2.280  
Height : 9.999



# B0L000S, U17-ch36

calib\_packv5\_042523\_0143.root, FC#5, port B1

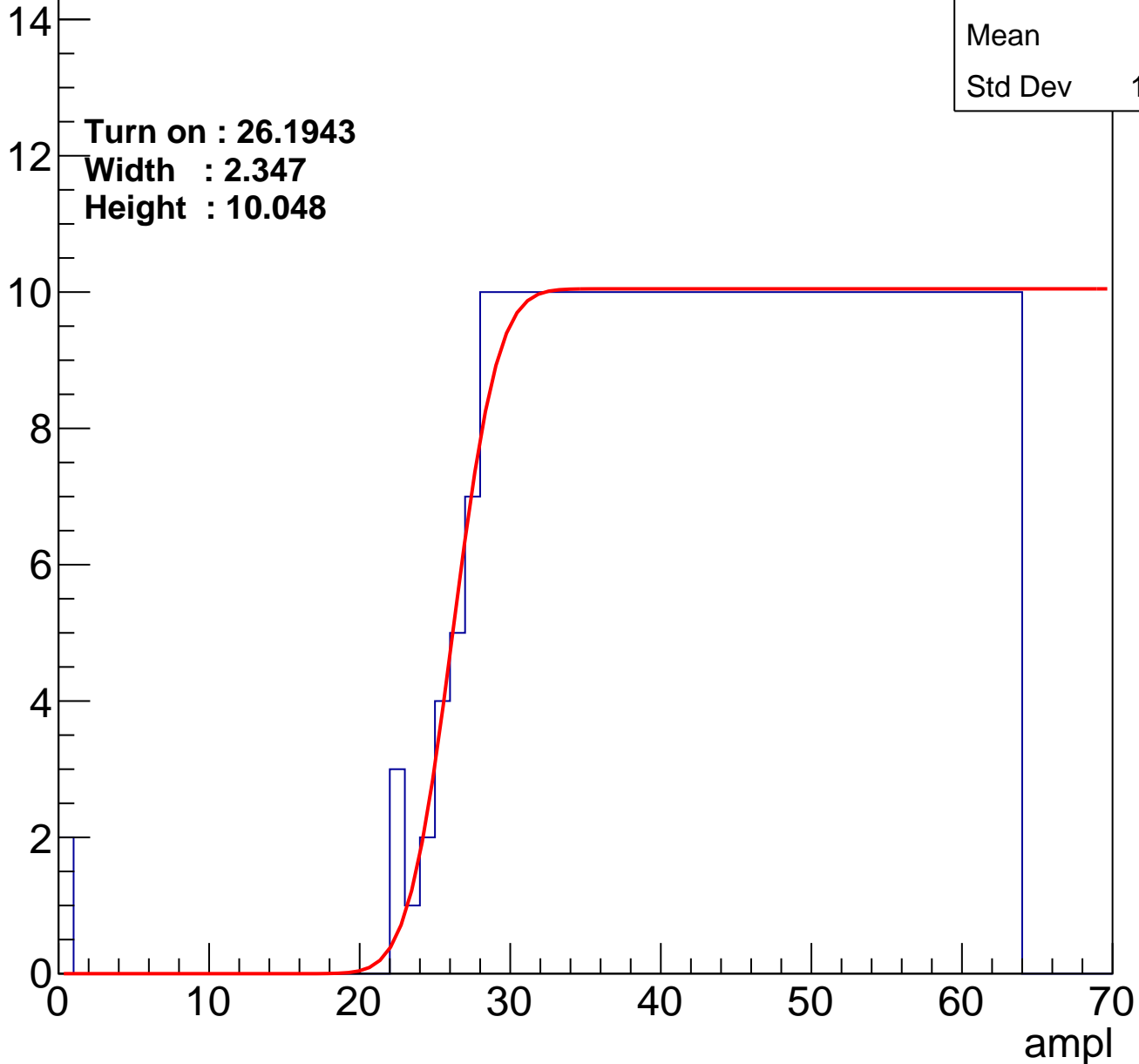
Entries	384
Mean	44.1
Std Dev	11.56

**Turn on : 26.1943**

**Width : 2.347**

**Height : 10.048**

Entry



# B0L000S, U17-ch37

calib\_packv5\_042523\_0143.root, FC#5, port B1

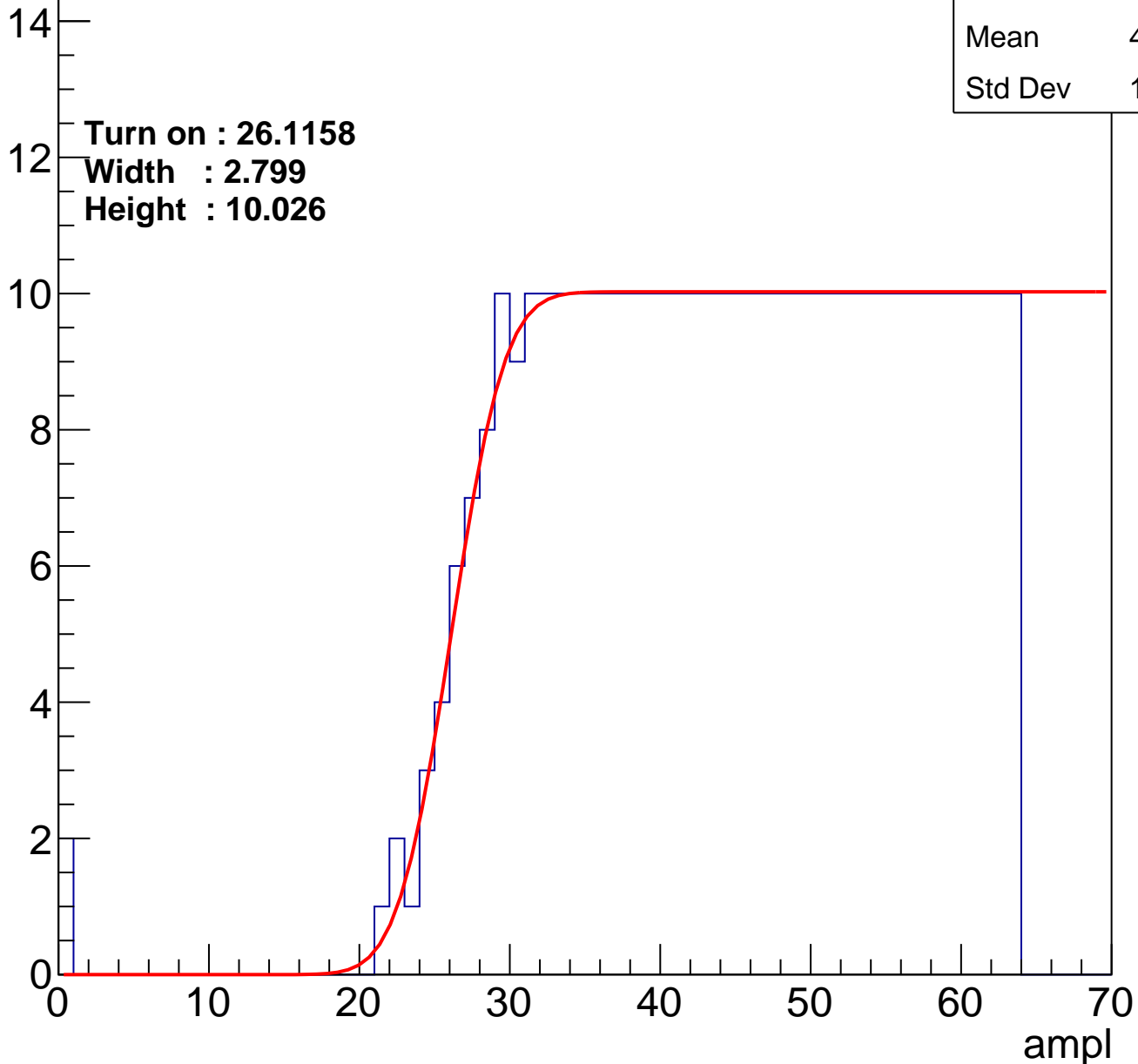
Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.1158

Width : 2.799

Height : 10.026

Entry



# B0L000S, U17-ch38

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.89
Std Dev	11.73

Turn on : 25.5750

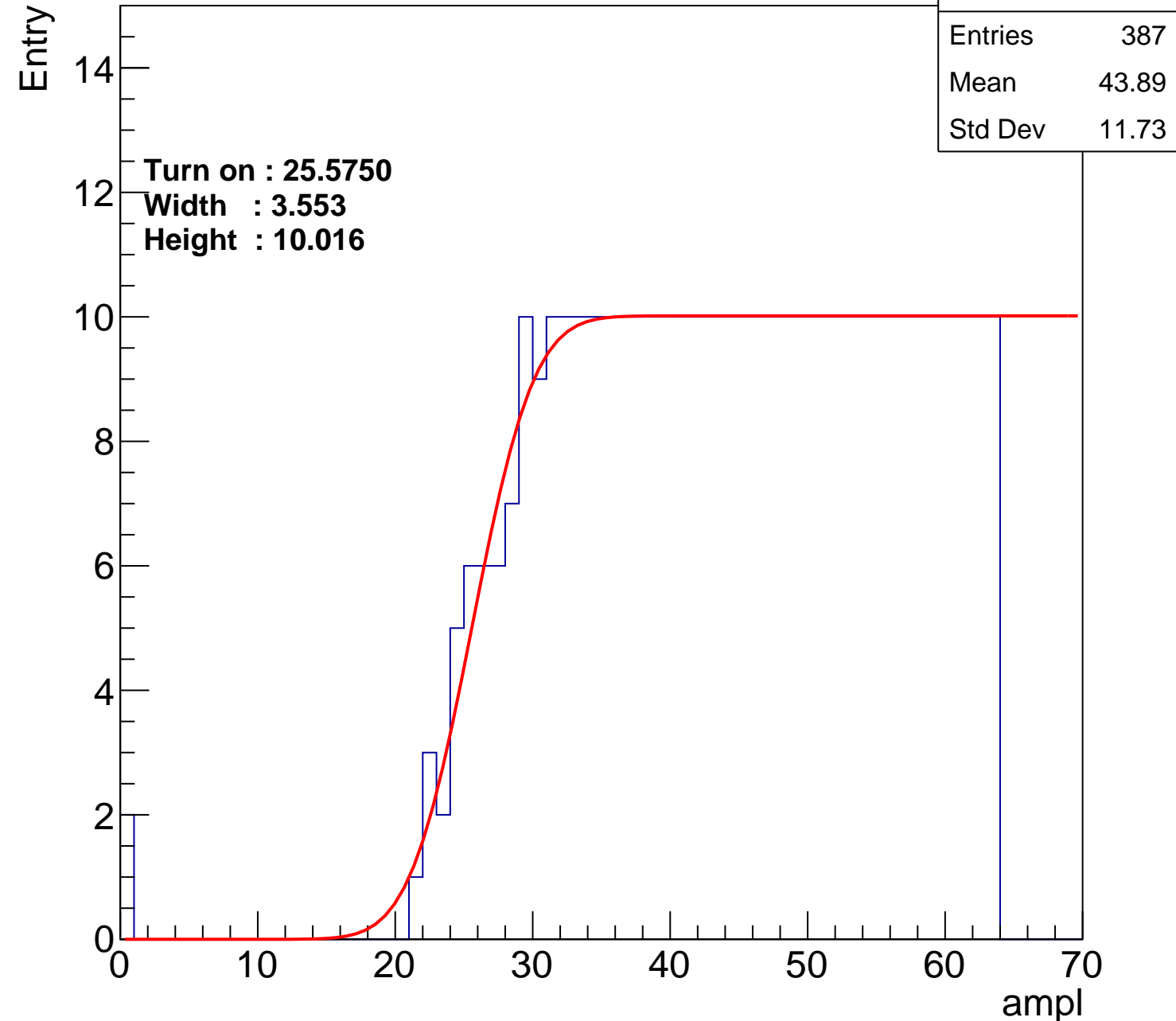
Width : 3.553

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch39

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 25.6553

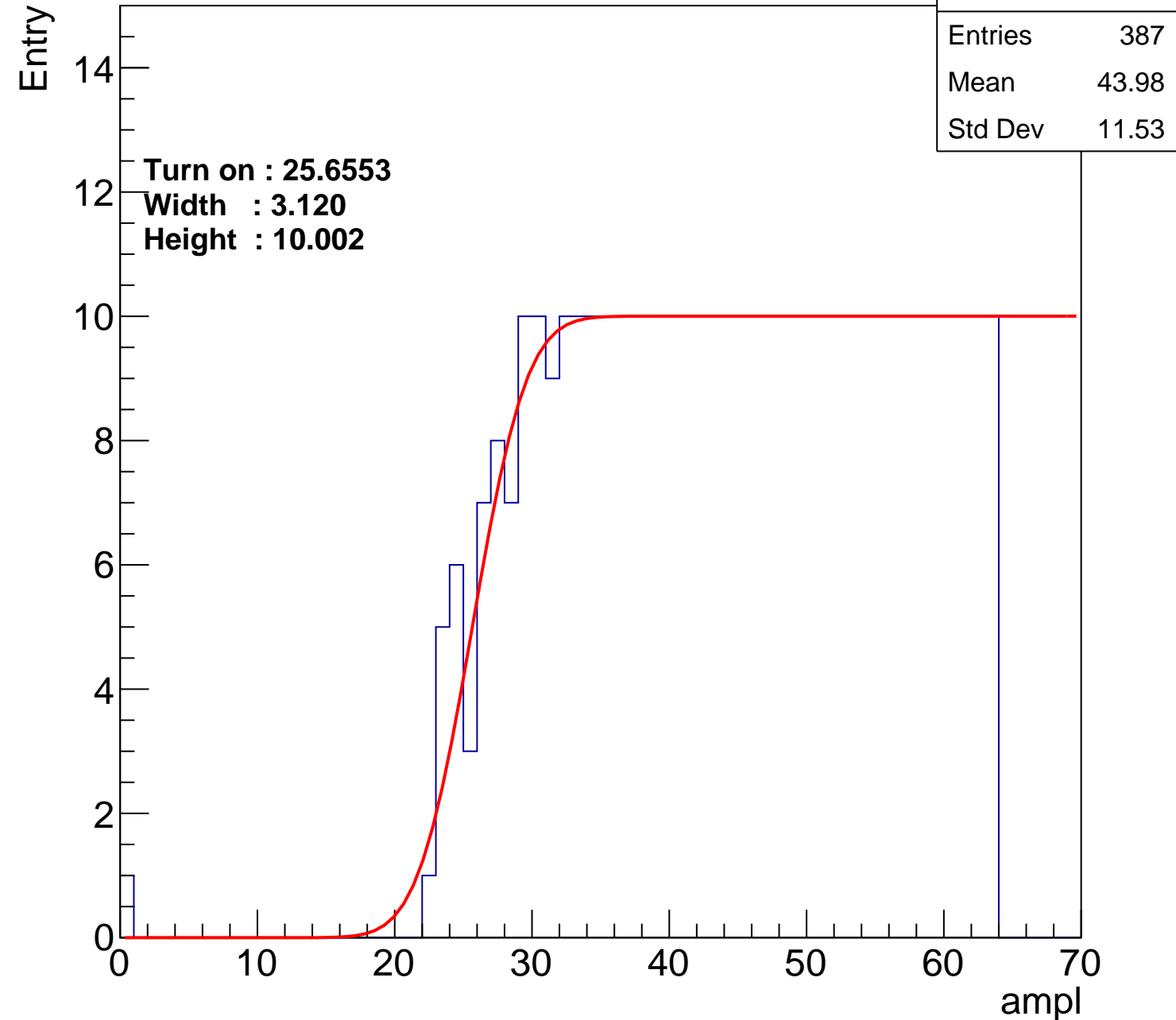
Width : 3.120

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 26.1399  
Width : 2.851  
Height : 10.048



# B0L000S, U17-ch41

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	407
Mean	42.63
Std Dev	12.94

Turn on : 24.3202

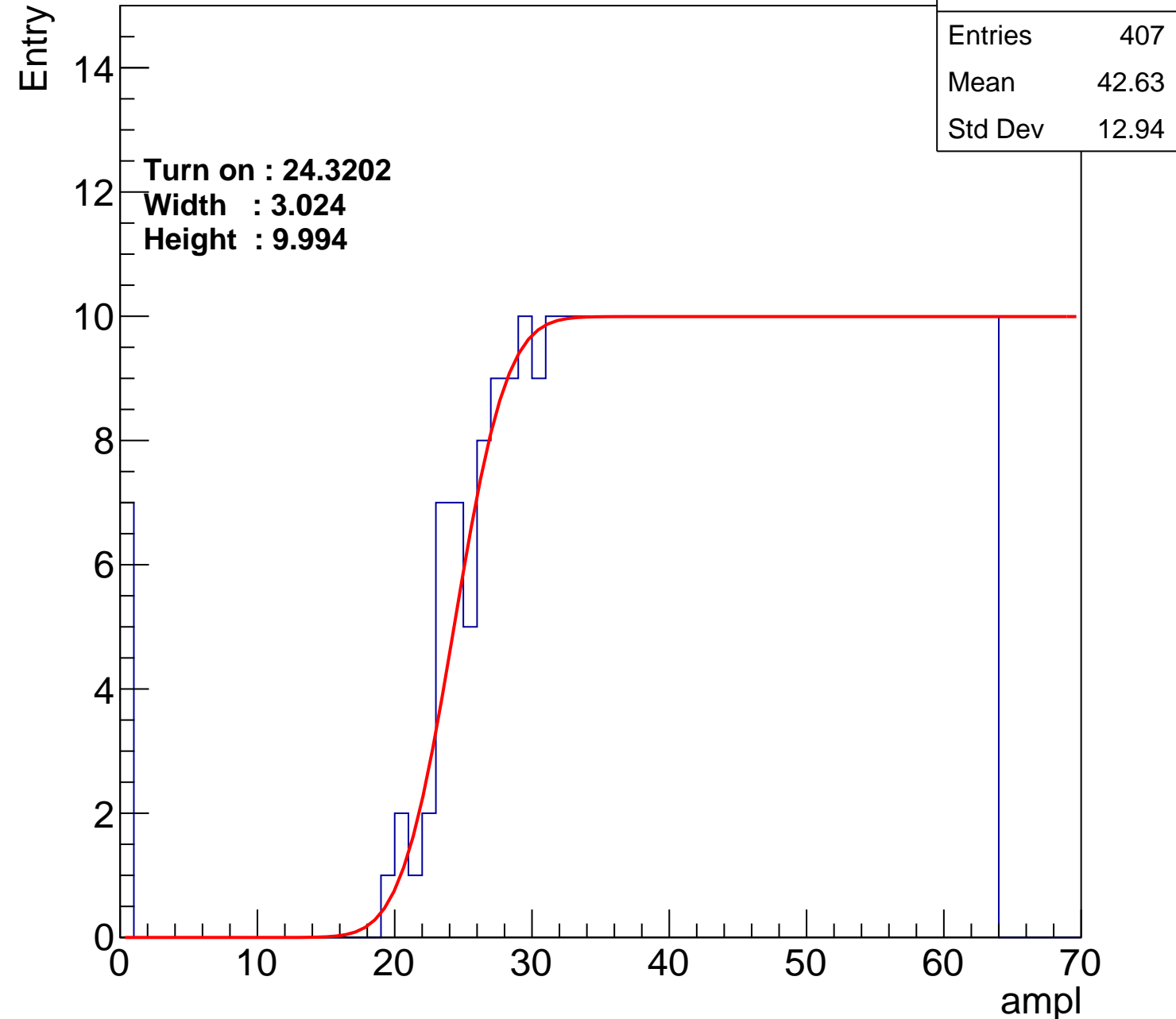
Width : 3.024

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch42

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 25.9026

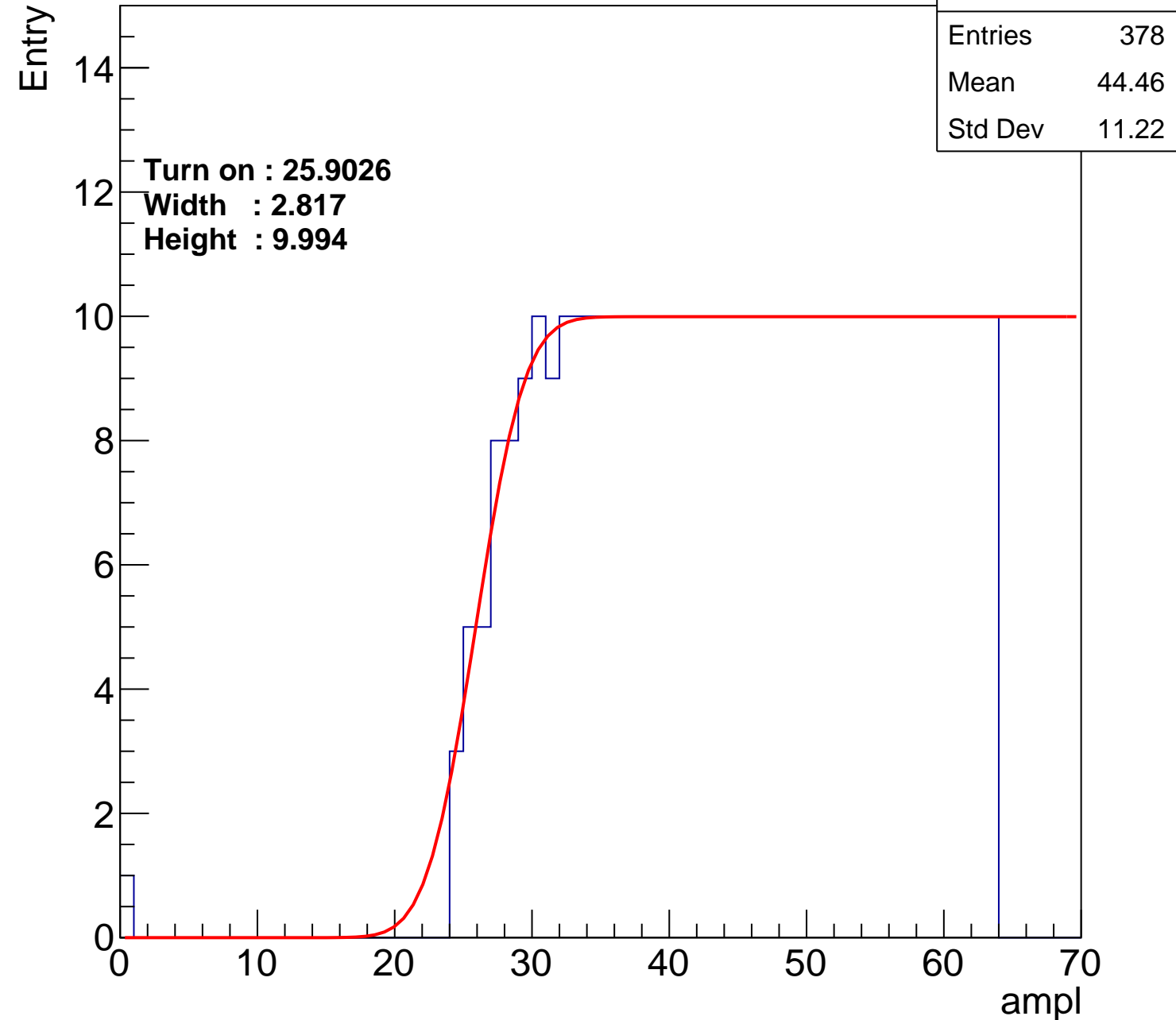
Width : 2.817

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch43

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.06
Std Dev	11.64

**Turn on : 26.6268**

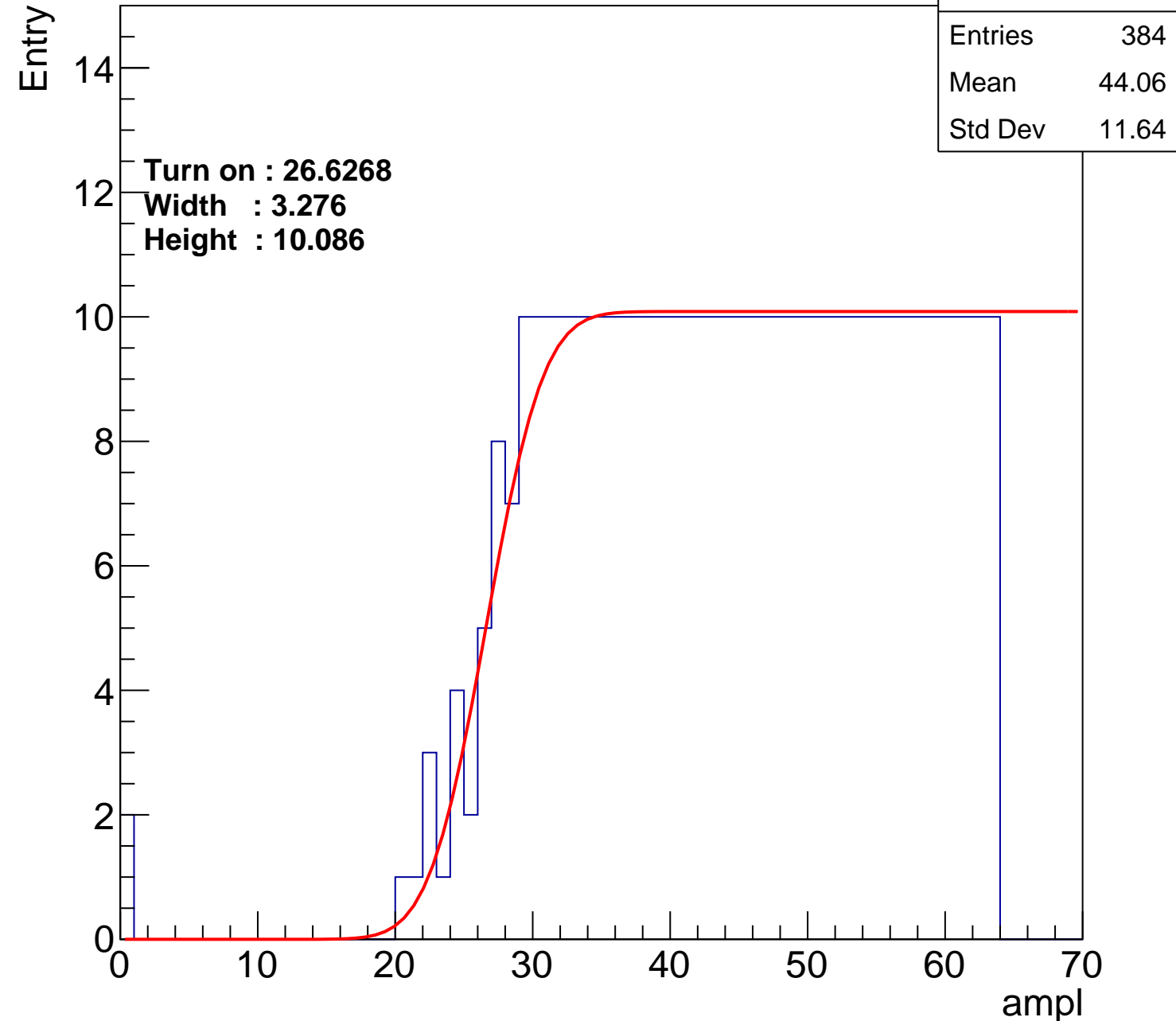
**Width : 3.276**

**Height : 10.086**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch44

calib\_packv5\_042523\_0143.root, FC#5, port B1

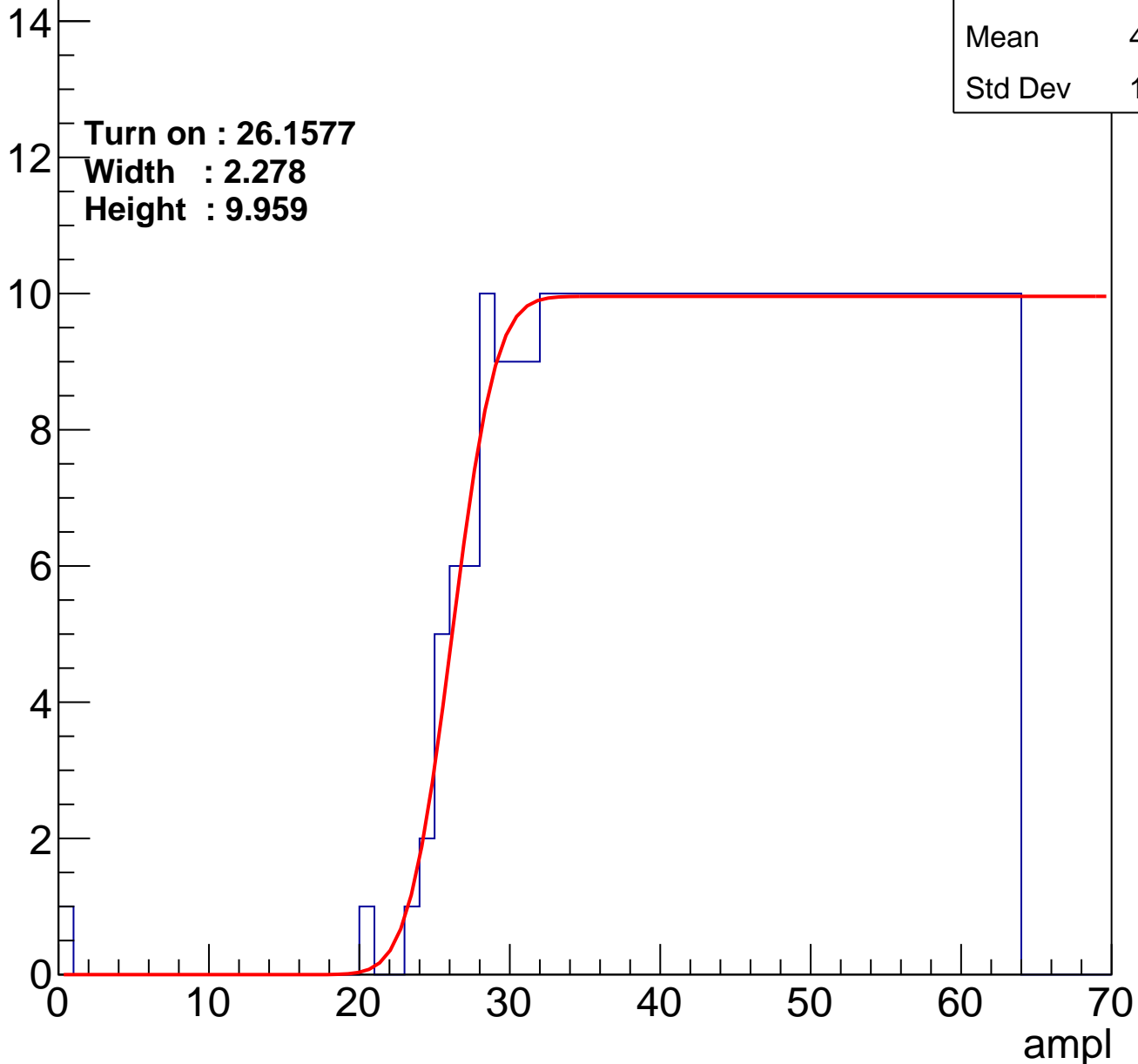
Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.1577

Width : 2.278

Height : 9.959

Entry



# B0L000S, U17-ch45

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.3
Std Dev	11.81

Turn on : 26.6314

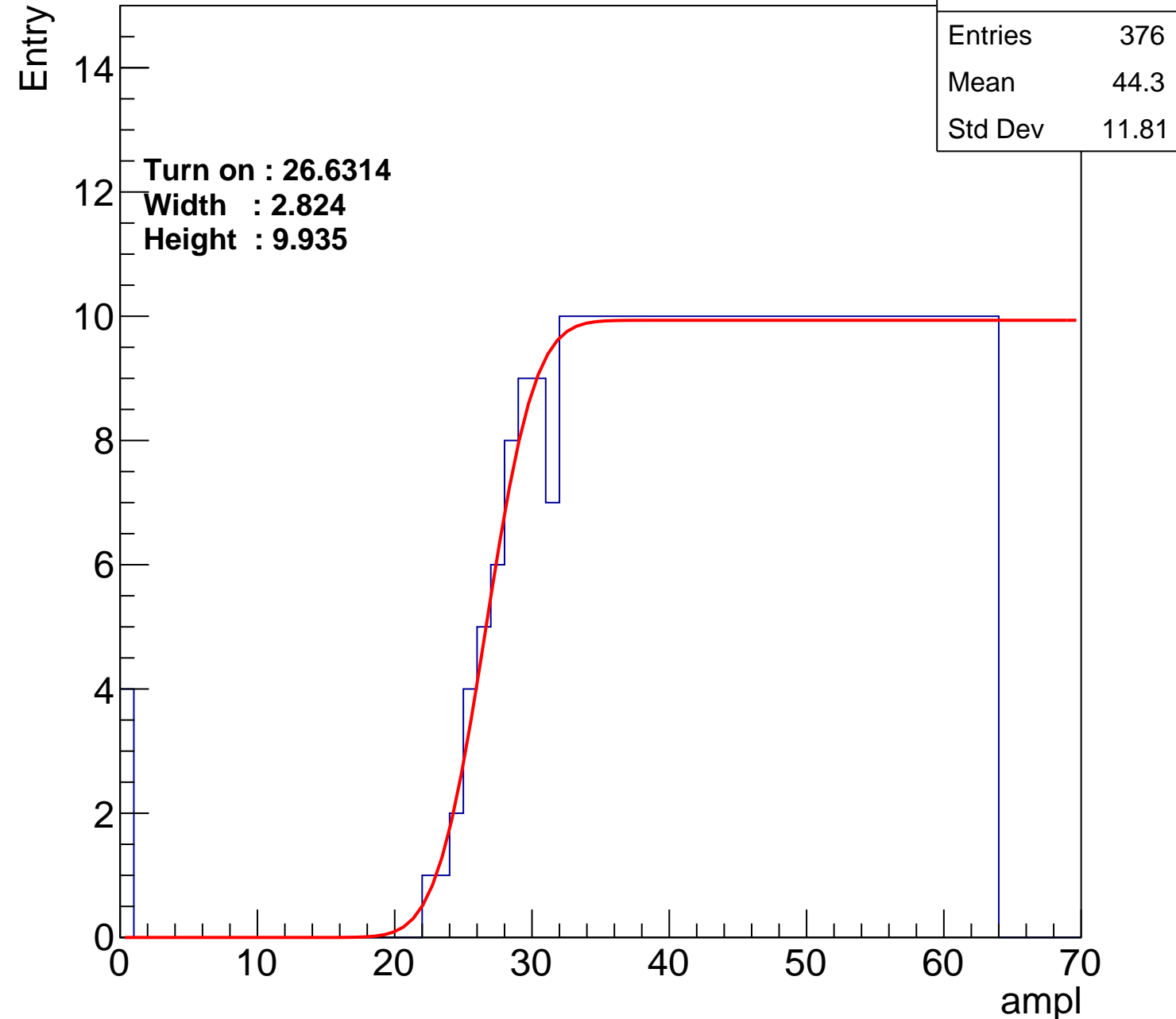
Width : 2.824

Height : 9.935

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch46

calib\_packv5\_042523\_0143.root, FC#5, port B1

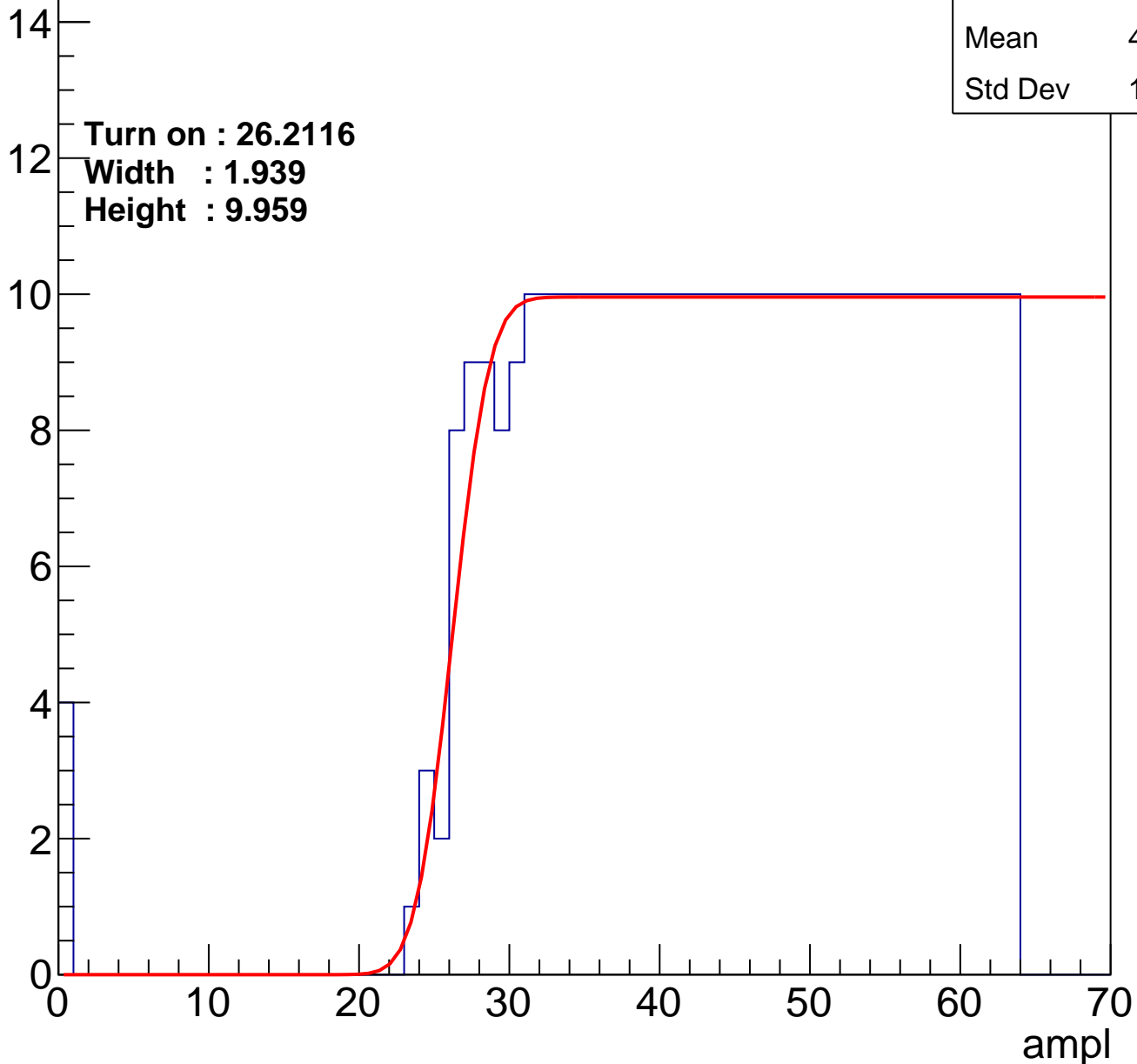
Entries	383
Mean	44.02
Std Dev	11.88

Turn on : 26.2116

Width : 1.939

Height : 9.959

Entry





# B0L000S, U17-ch47

calib\_packv5\_042523\_0143.root, FC#5, port B1

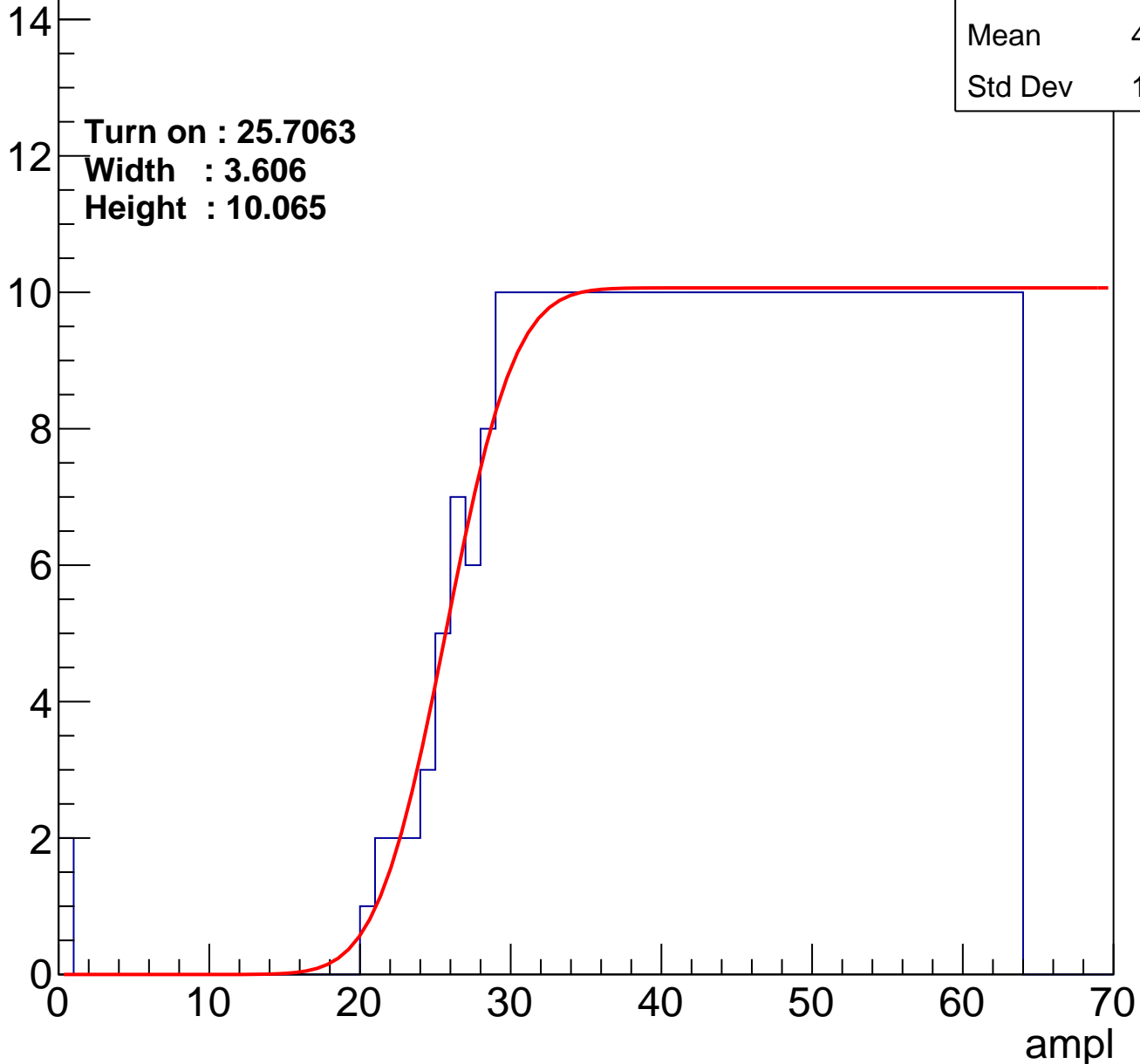
Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 25.7063

Width : 3.606

Height : 10.065

Entry



# B0L000S, U17-ch48

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	404
Mean	43.04
Std Dev	12.26

**Turn on : 23.9867**

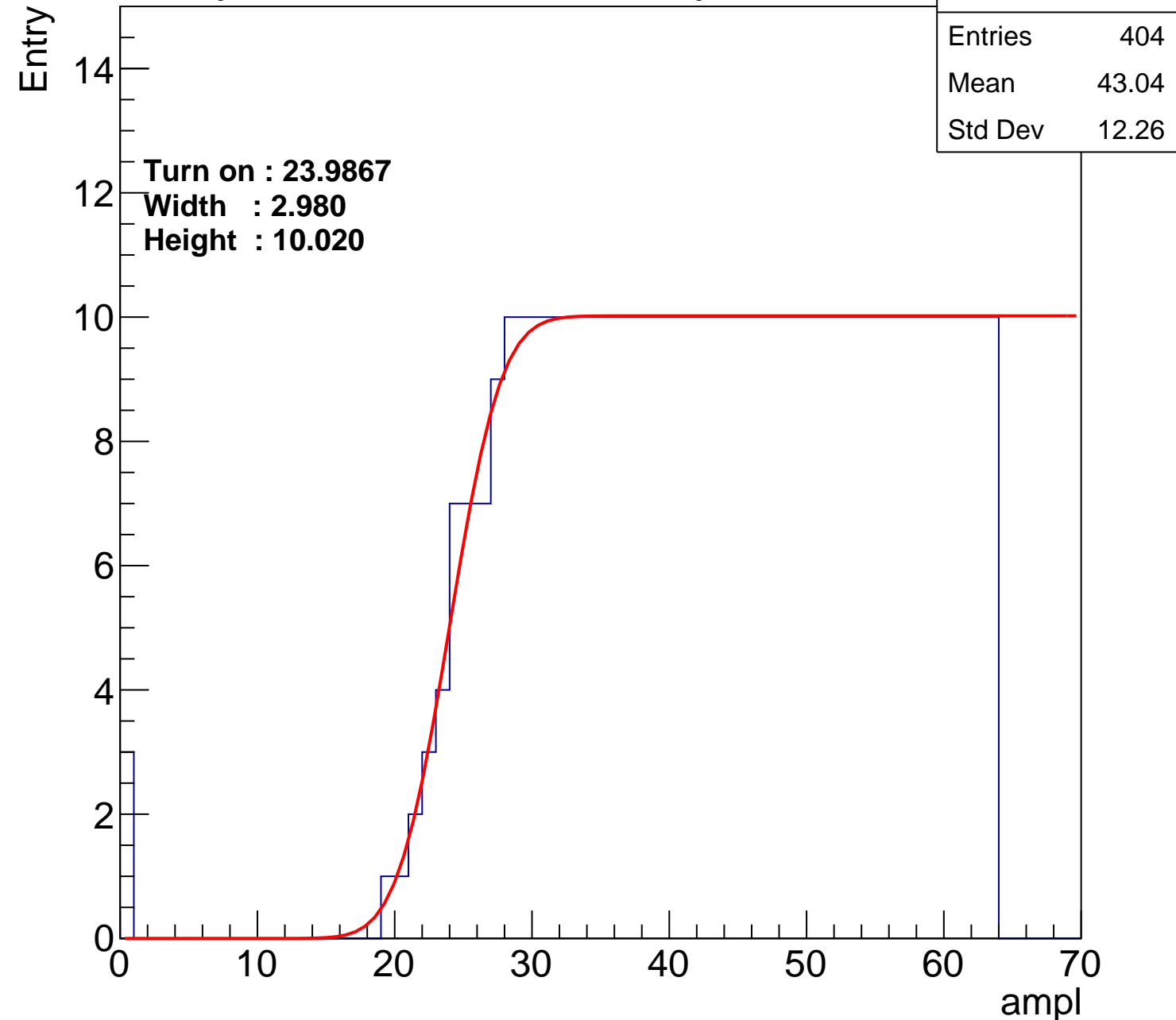
**Width : 2.980**

**Height : 10.020**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch49

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.55
Std Dev	11.83

**Turn on : 27.6045**

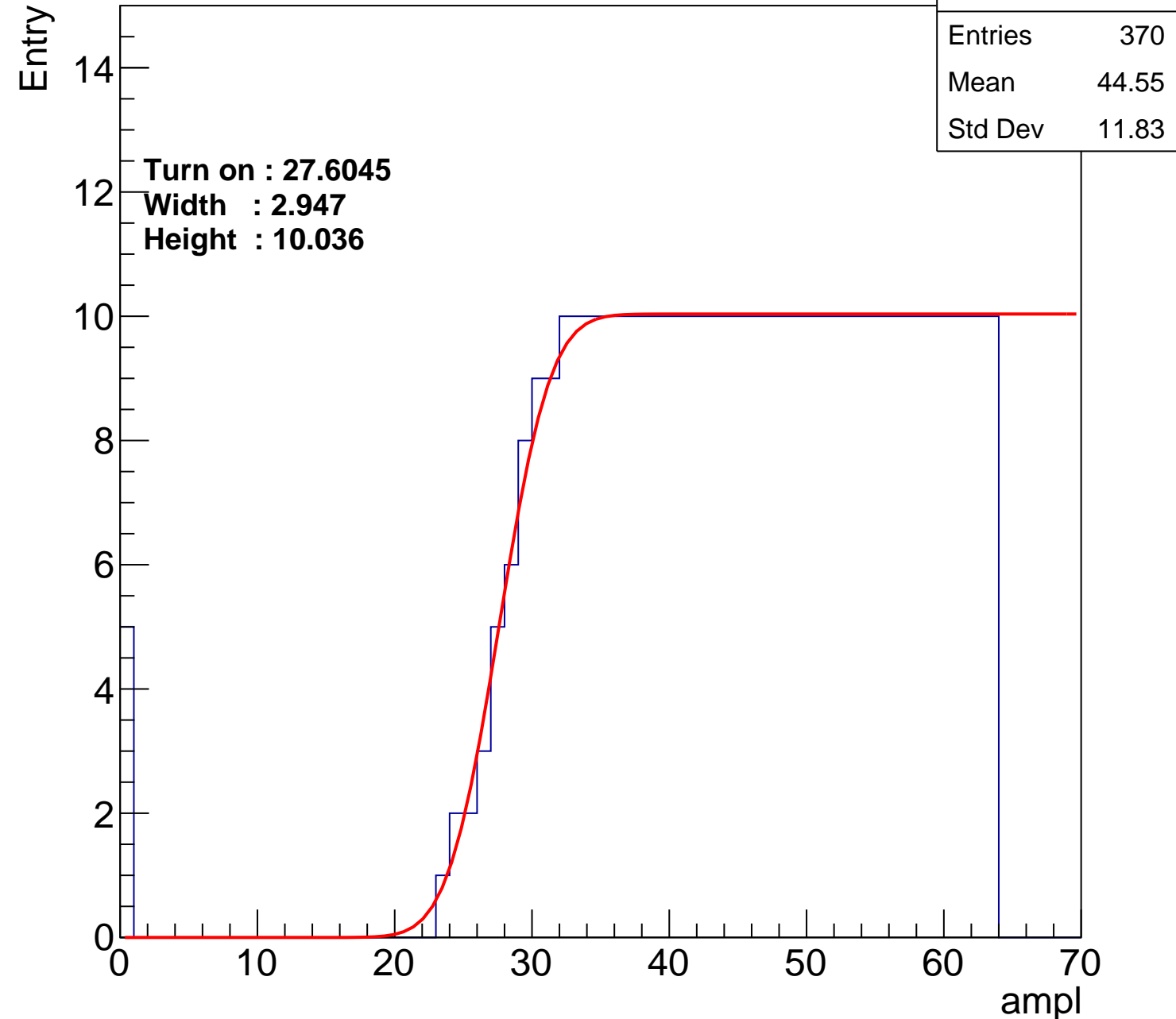
**Width : 2.947**

**Height : 10.036**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch50

calib\_packv5\_042523\_0143.root, FC#5, port B1

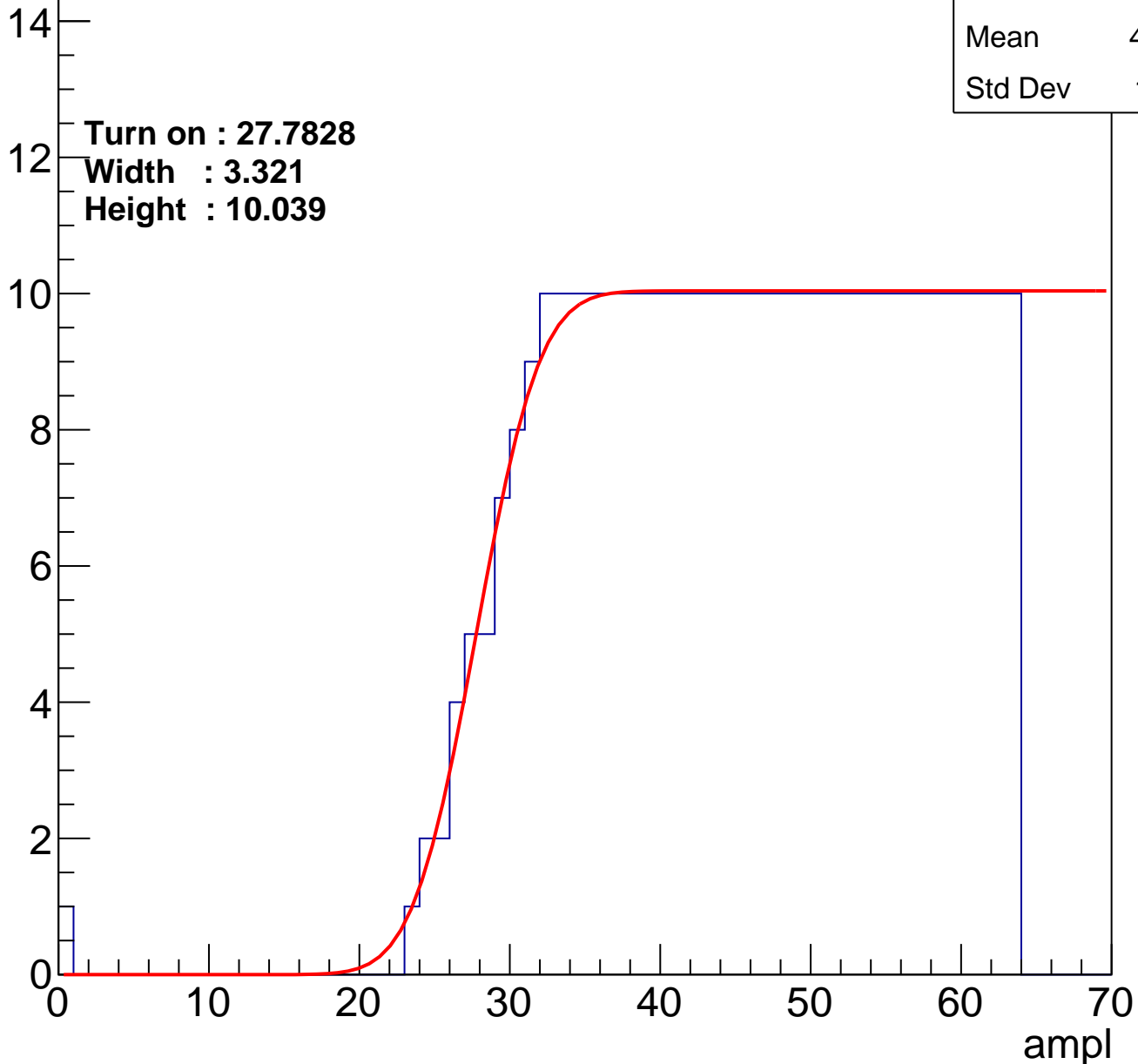
Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 27.7828

Width : 3.321

Height : 10.039

Entry



# B0L000S, U17-ch51

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.91
Std Dev	11.18

Turn on : 27.9081

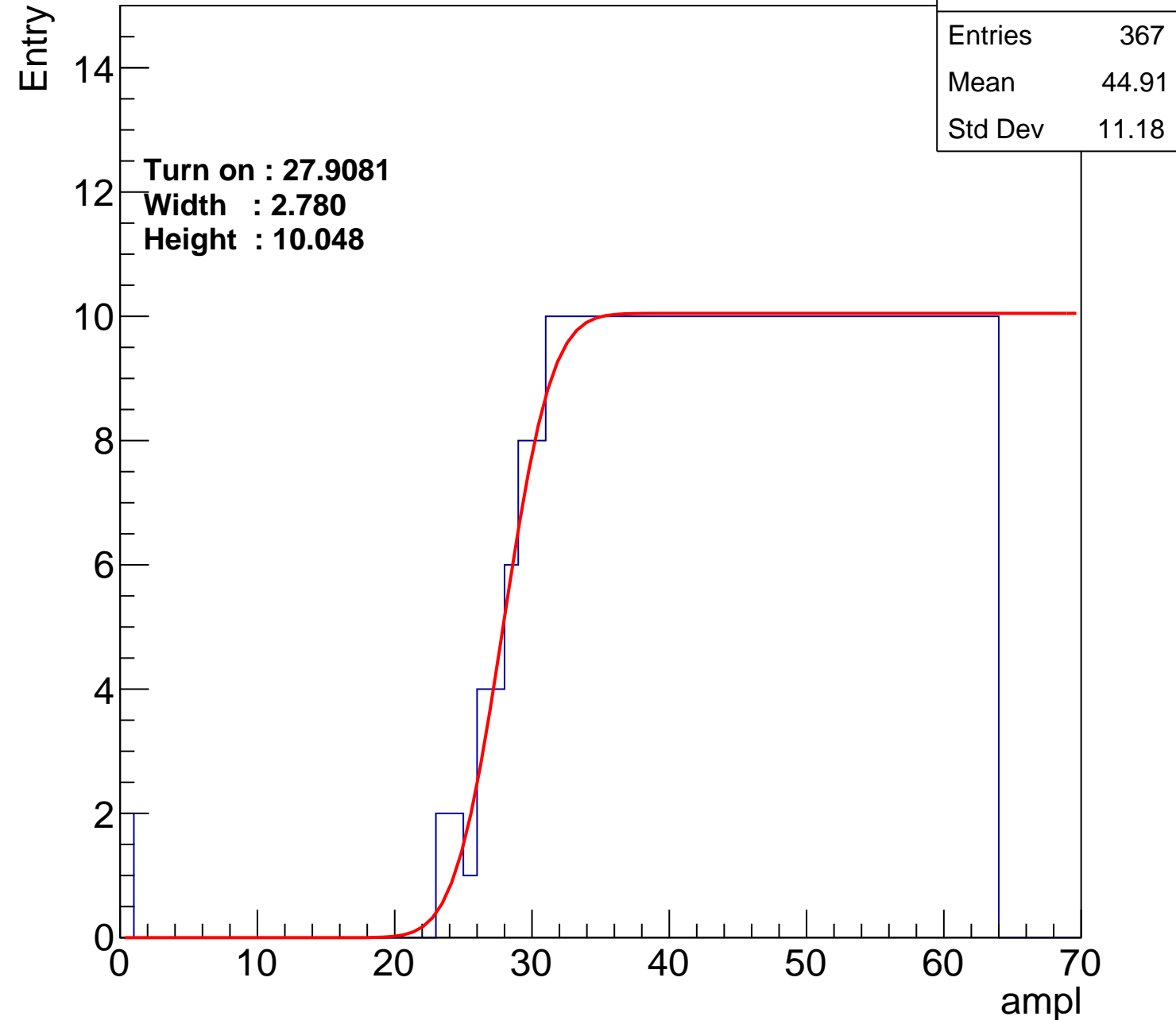
Width : 2.780

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch52

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.12
Std Dev	11.45

**Turn on : 25.7638**

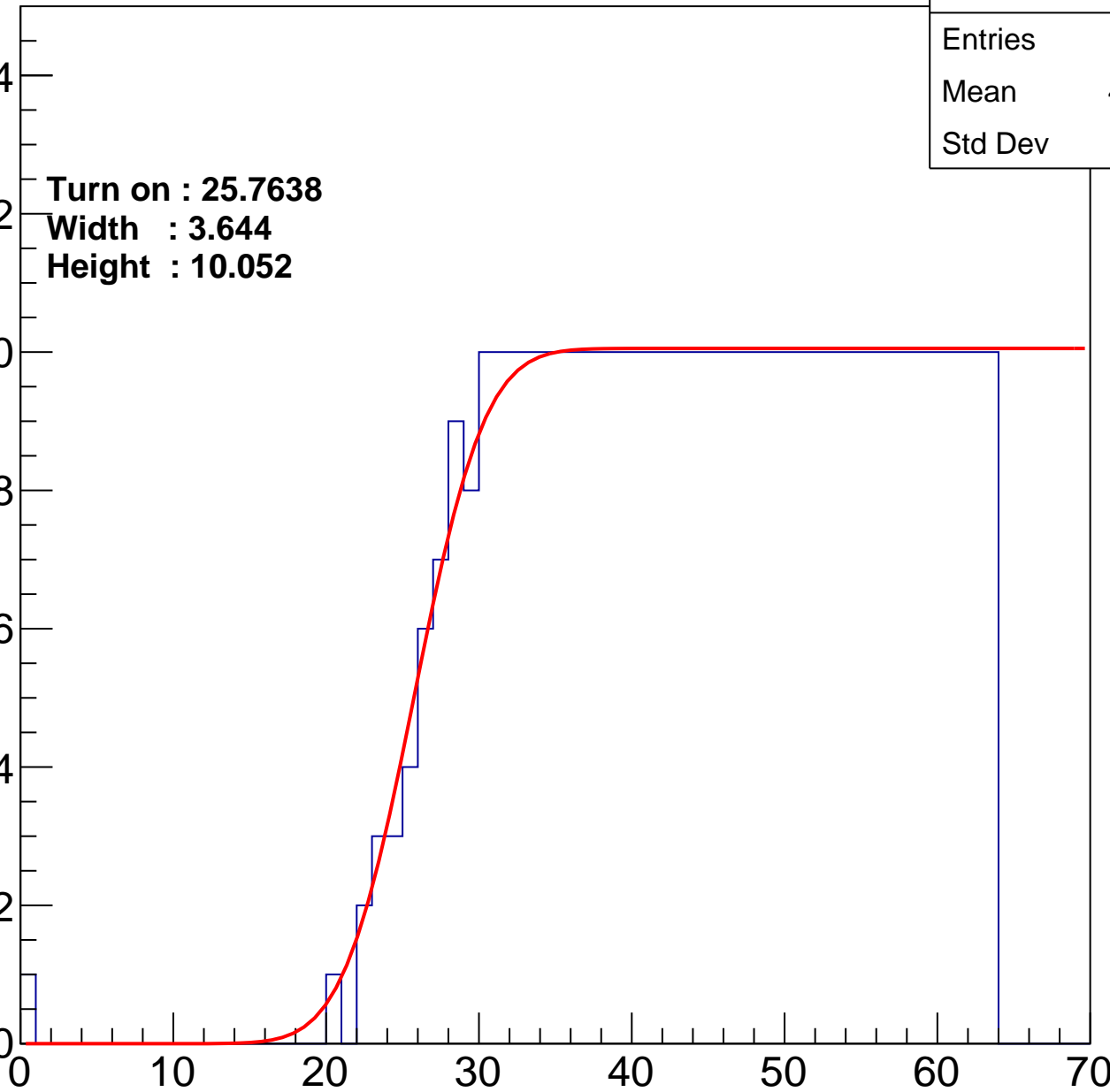
**Width : 3.644**

**Height : 10.052**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch53

calib\_packv5\_042523\_0143.root, FC#5, port B1

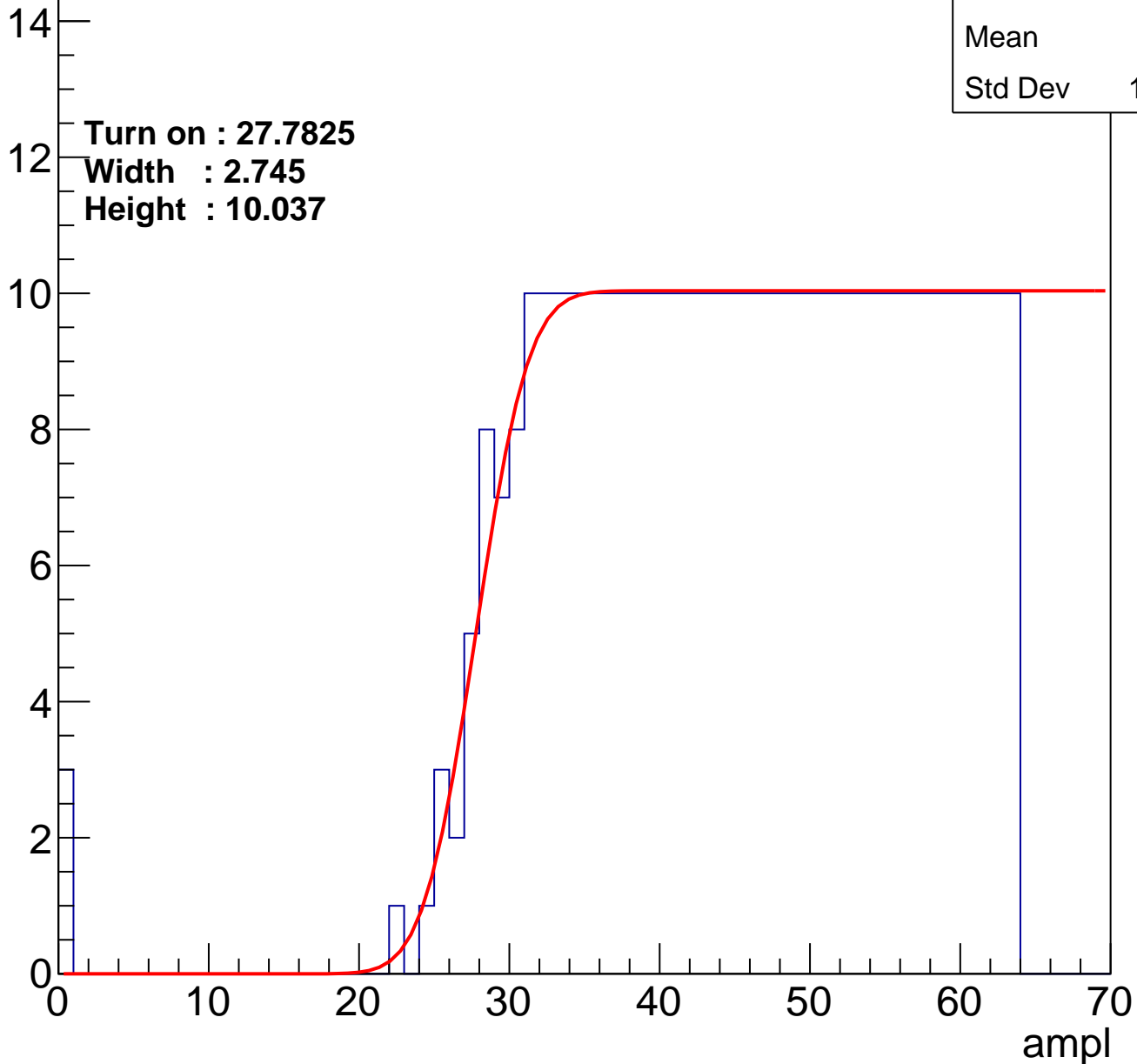
Entries	368
Mean	44.8
Std Dev	11.39

**Turn on : 27.7825**

**Width : 2.745**

**Height : 10.037**

Entry



# B0L000S, U17-ch54

calib\_packv5\_042523\_0143.root, FC#5, port B1

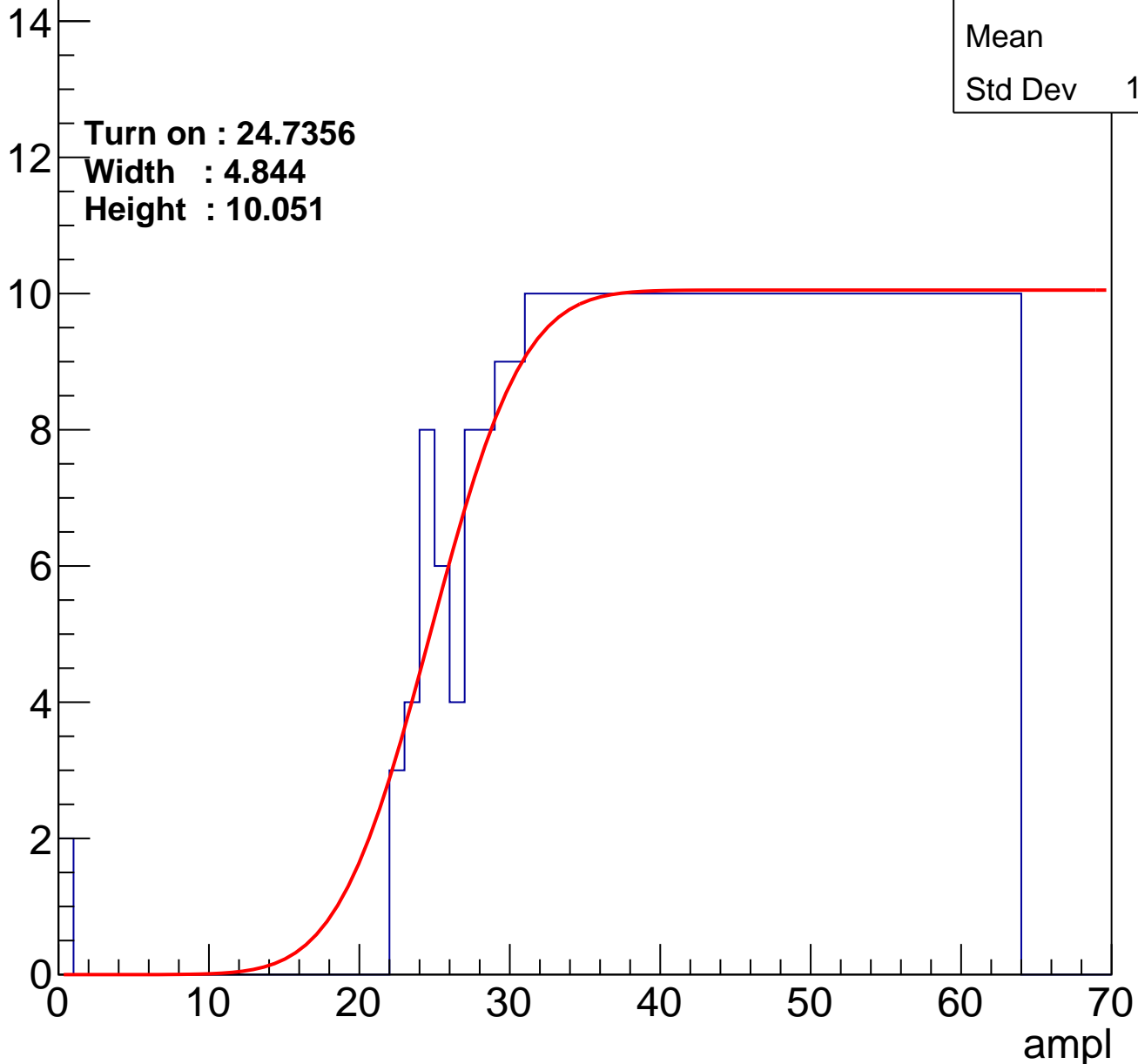
Entries	391
Mean	43.7
Std Dev	11.83

Turn on : 24.7356

Width : 4.844

Height : 10.051

Entry





# B0L000S, U17-ch55

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.3327

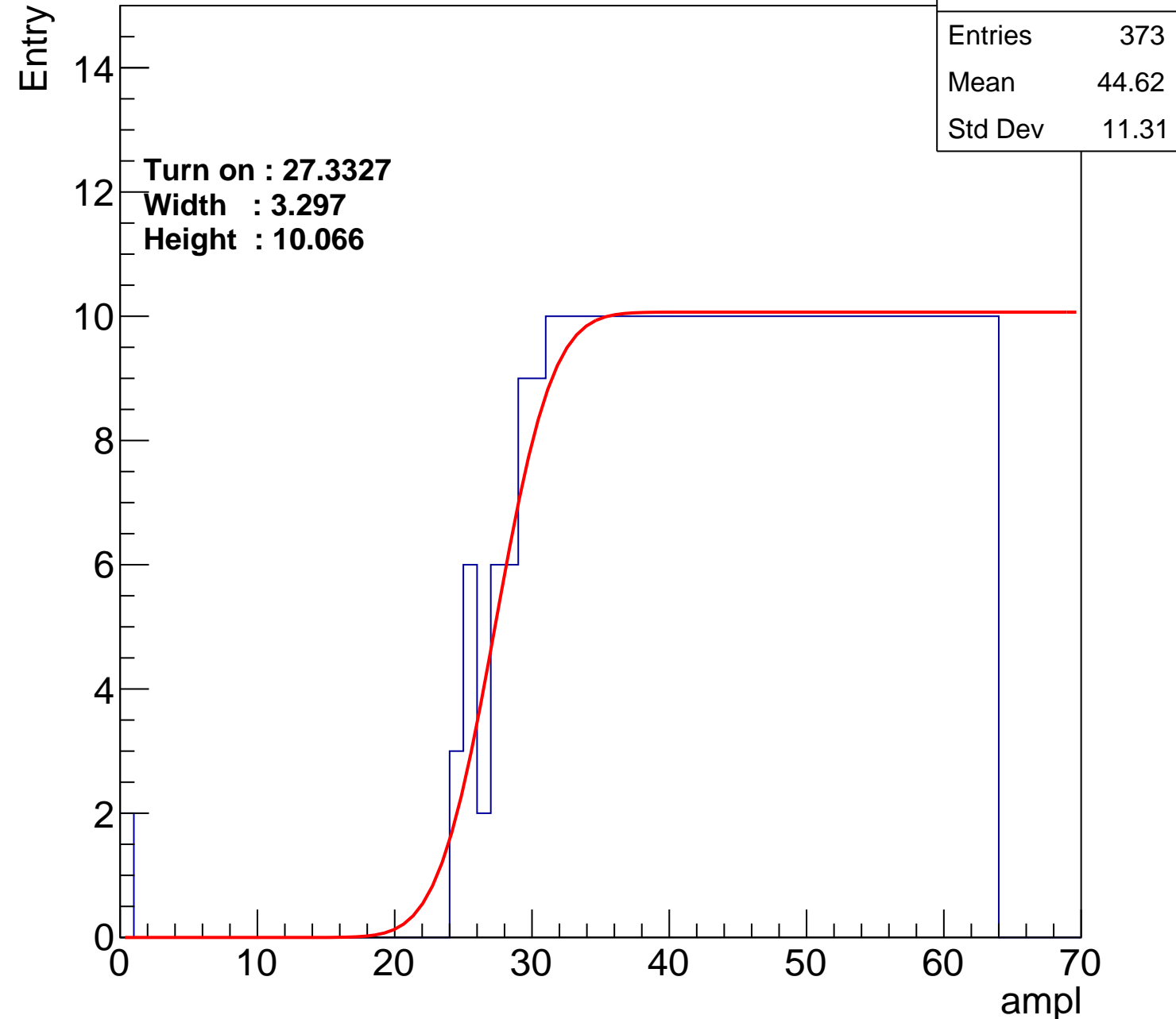
Width : 3.297

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch56

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.17
Std Dev	11.86

Turn on : 26.9801

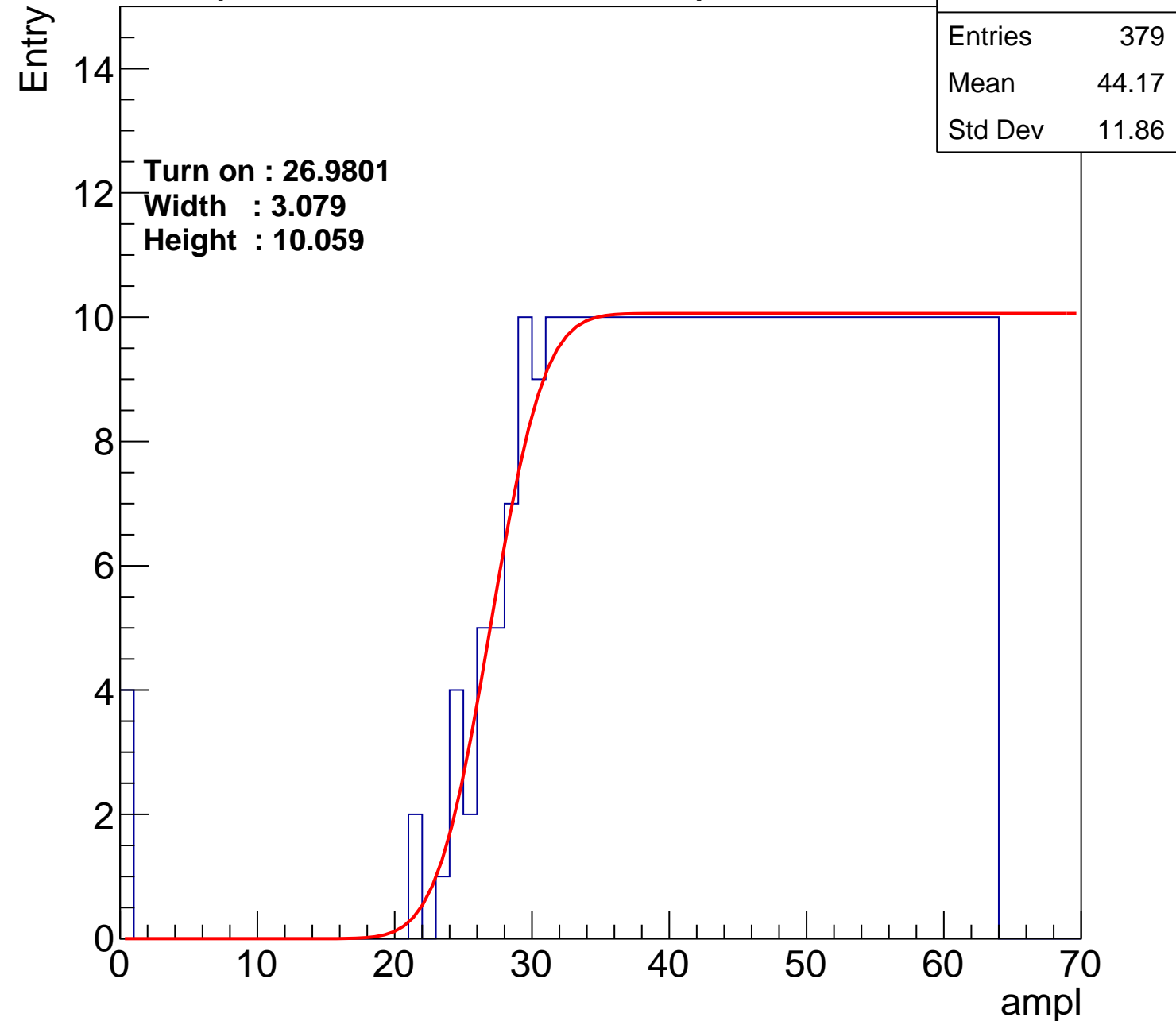
Width : 3.079

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch57

calib\_packv5\_042523\_0143.root, FC#5, port B1

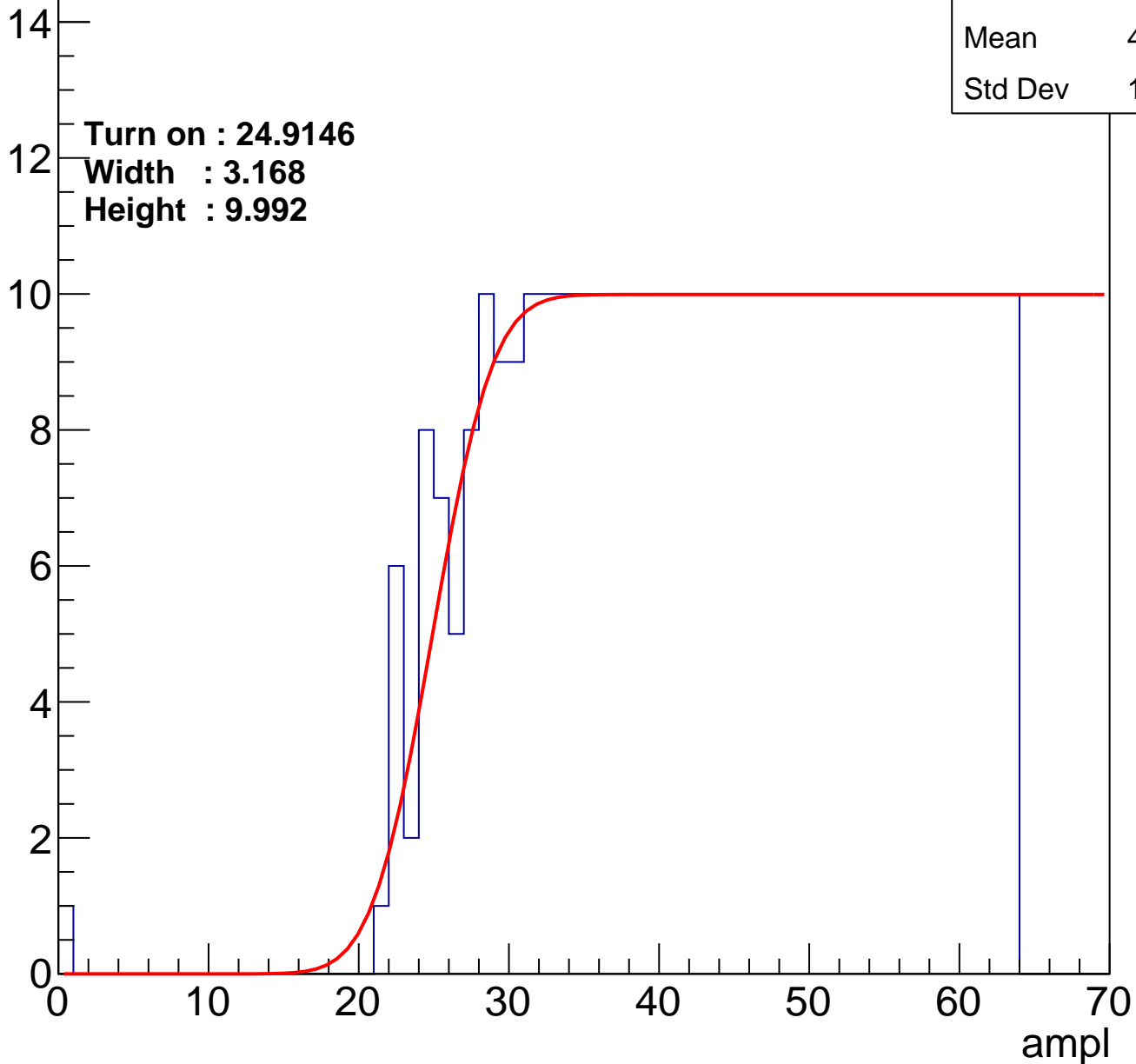
Entries	396
Mean	43.52
Std Dev	11.79

Turn on : 24.9146

Width : 3.168

Height : 9.992

Entry



# B0L000S, U17-ch58

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.84
Std Dev	11.56

Turn on : 28.0495

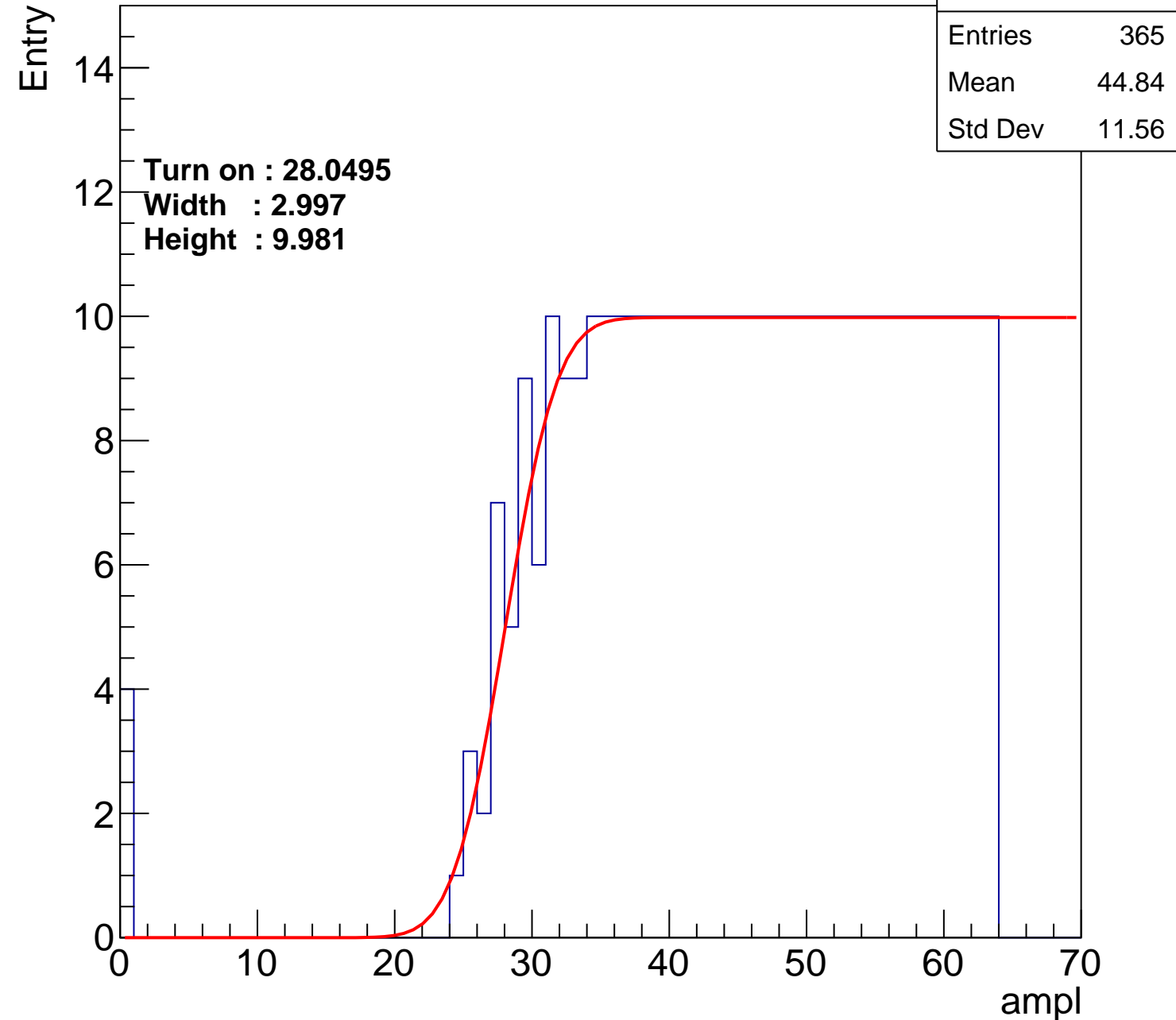
Width : 2.997

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch59

calib\_packv5\_042523\_0143.root, FC#5, port B1

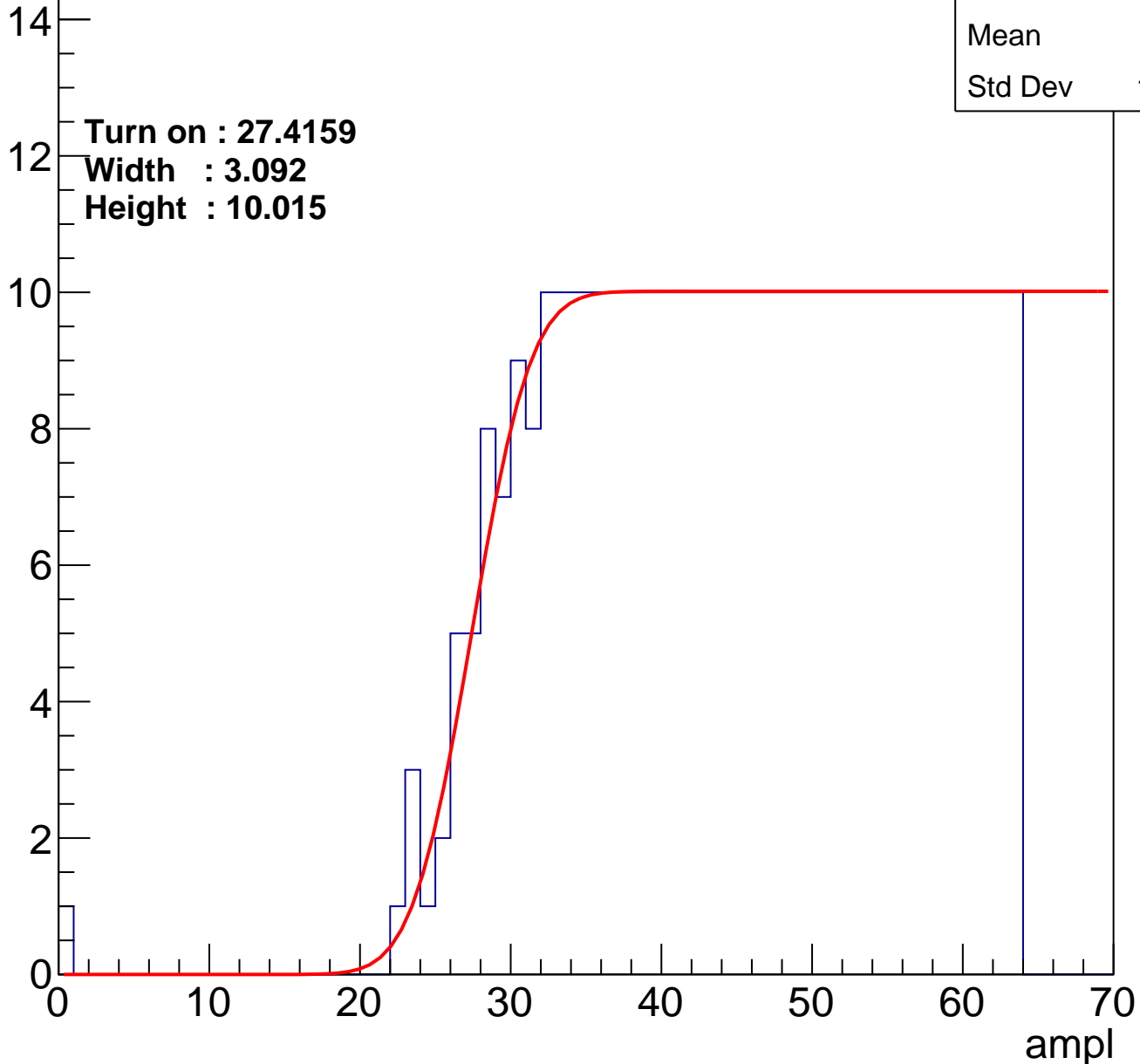
Entries	370
Mean	44.8
Std Dev	11.11

Turn on : 27.4159

Width : 3.092

Height : 10.015

Entry



# B0L000S, U17-ch60

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.48
Std Dev	11.44

**Turn on : 26.7502**

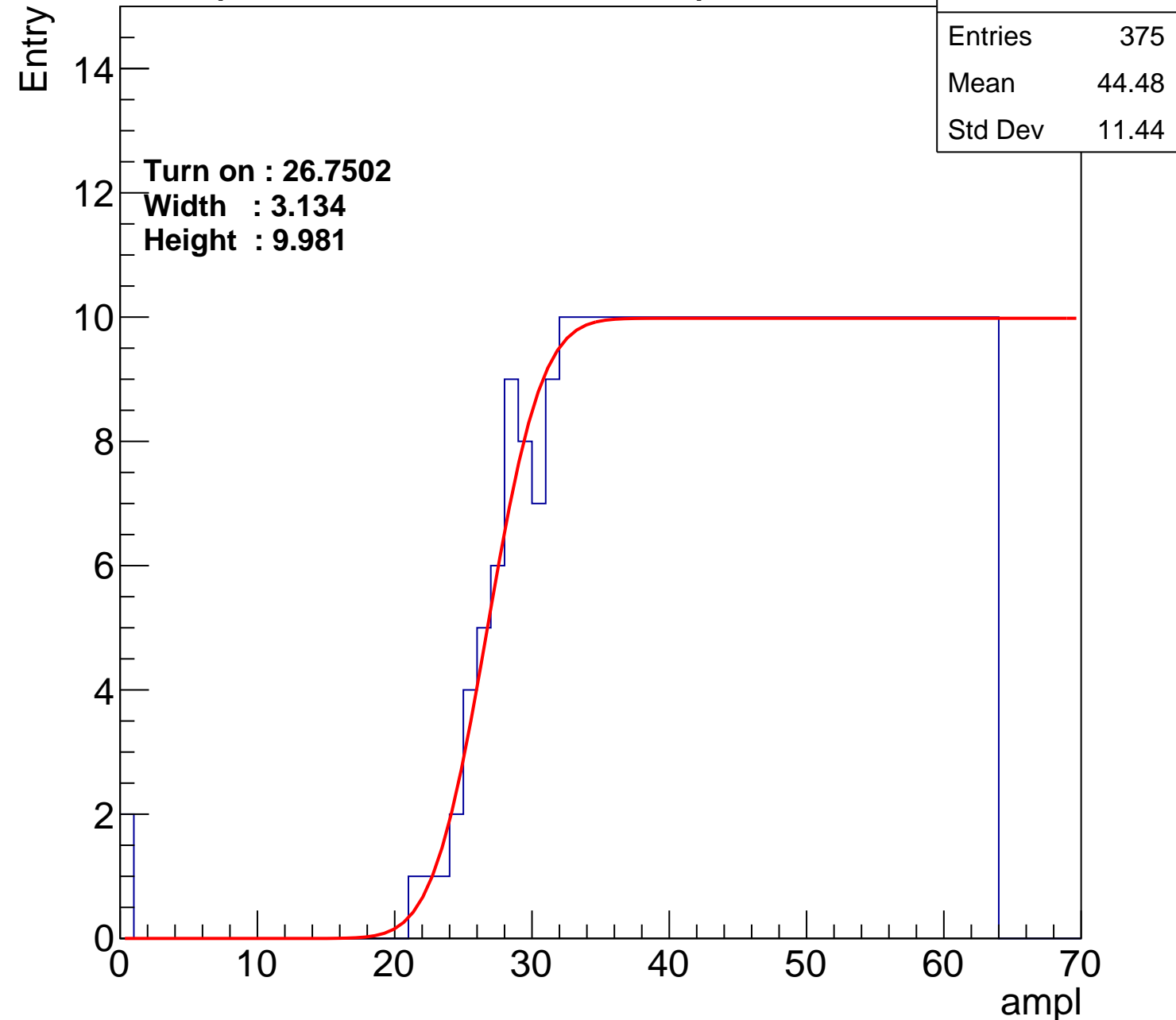
**Width : 3.134**

**Height : 9.981**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch61

calib\_packv5\_042523\_0143.root, FC#5, port B1

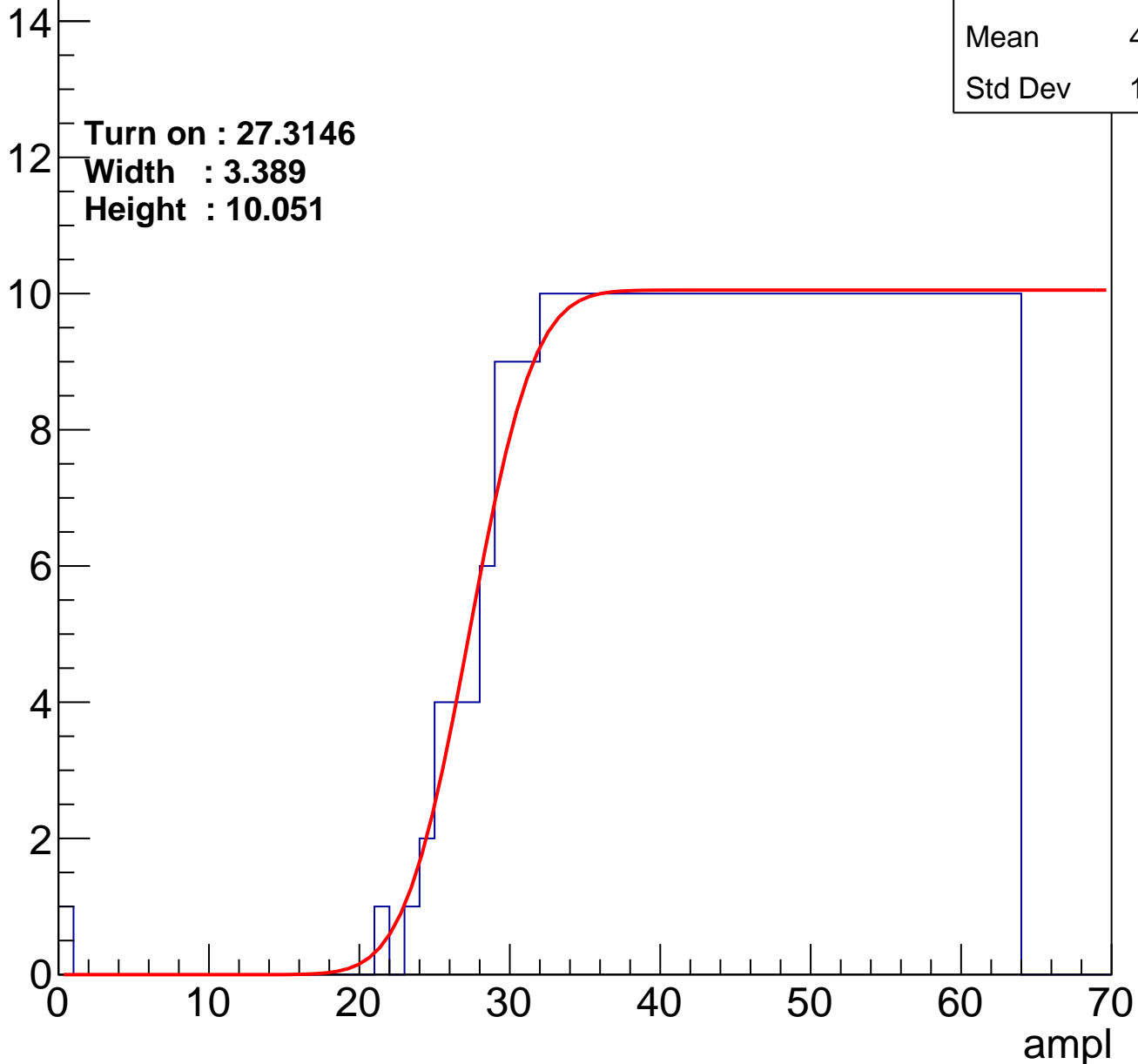
Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 27.3146

Width : 3.389

Height : 10.051

Entry



# B0L000S, U17-ch62

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.11
Std Dev	11.59

Turn on : 26.2182

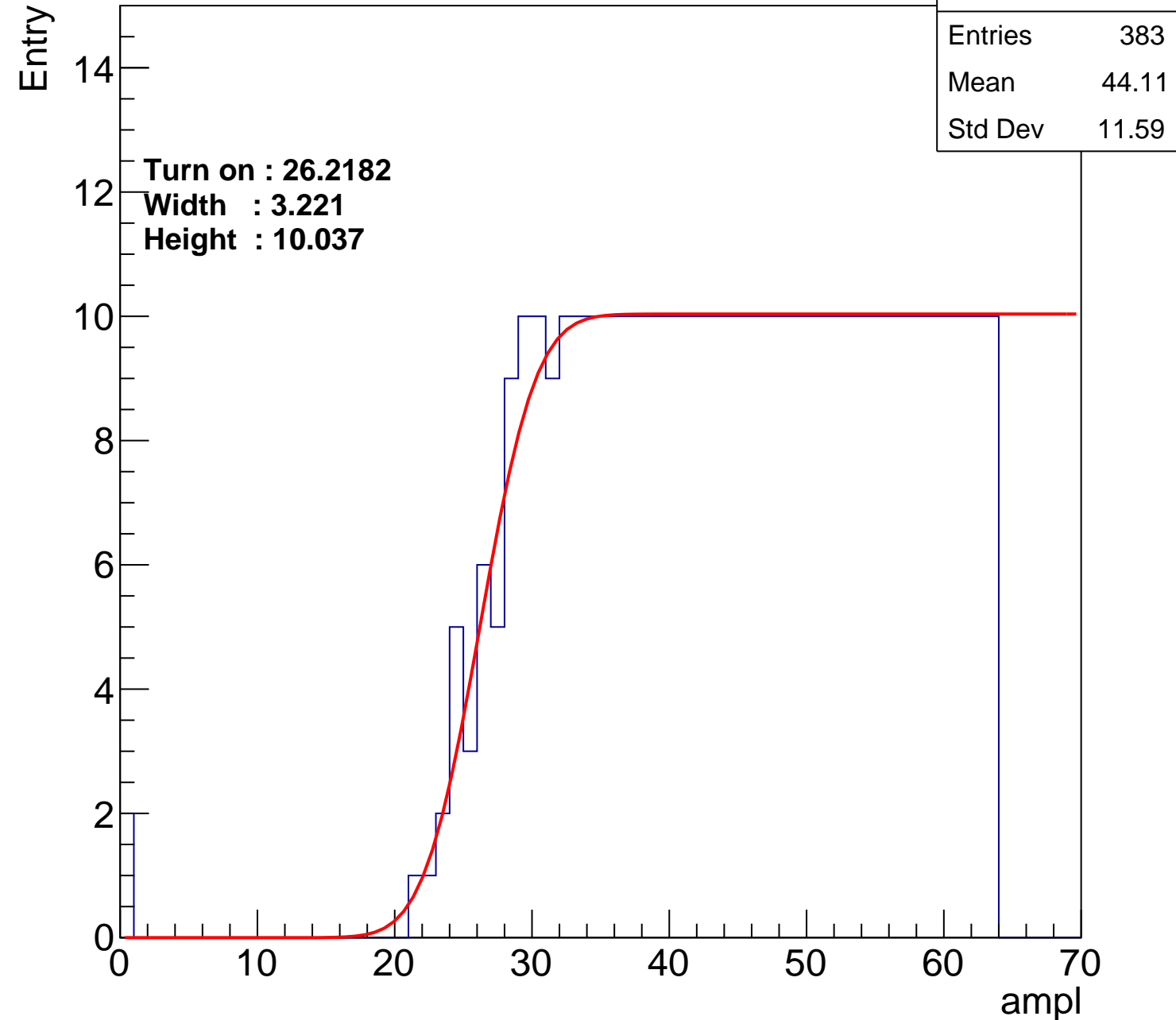
Width : 3.221

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch63

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 27.0890

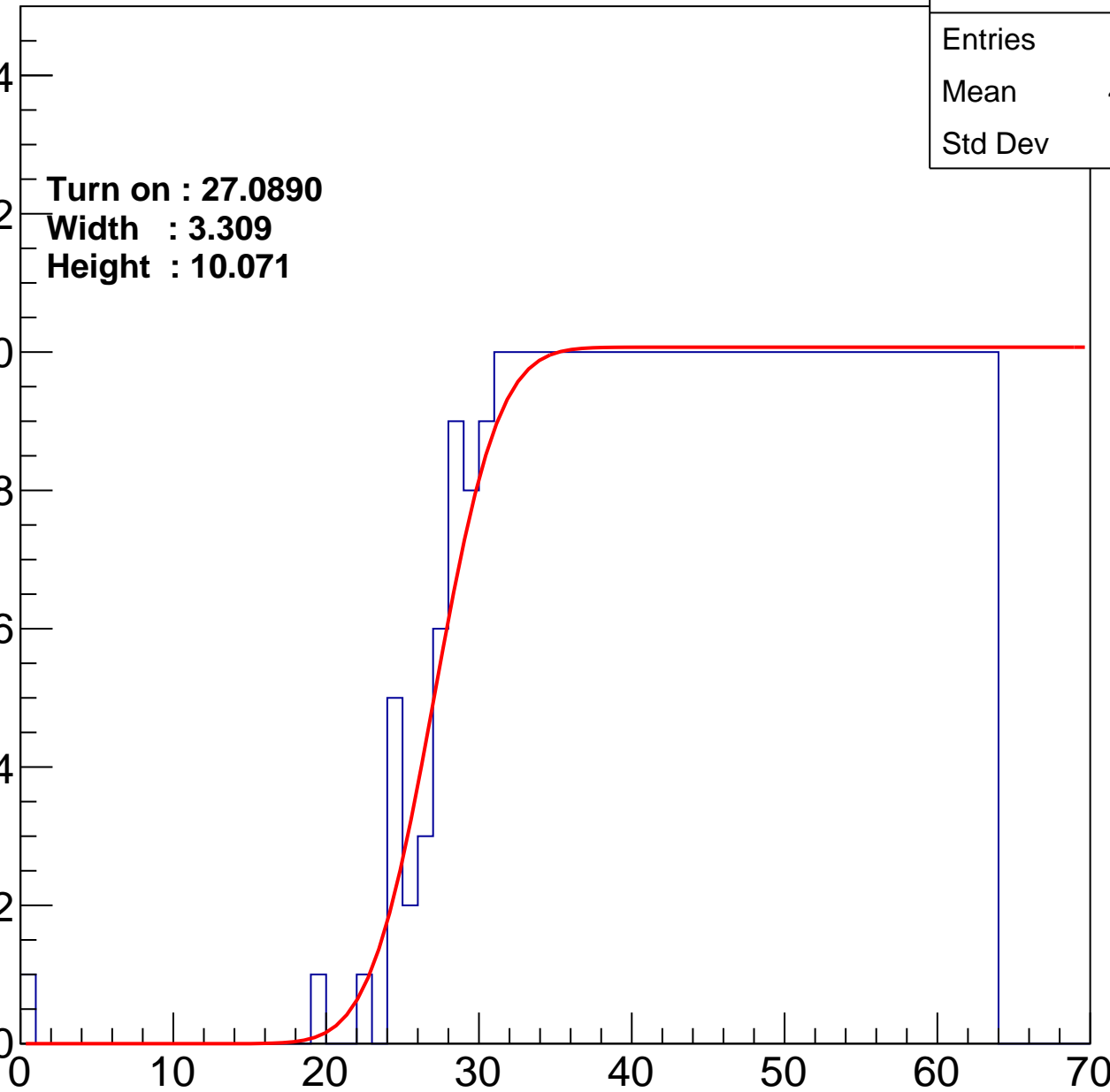
Width : 3.309

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch64

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	396
Mean	43.51
Std Dev	11.87

Turn on : 24.3348

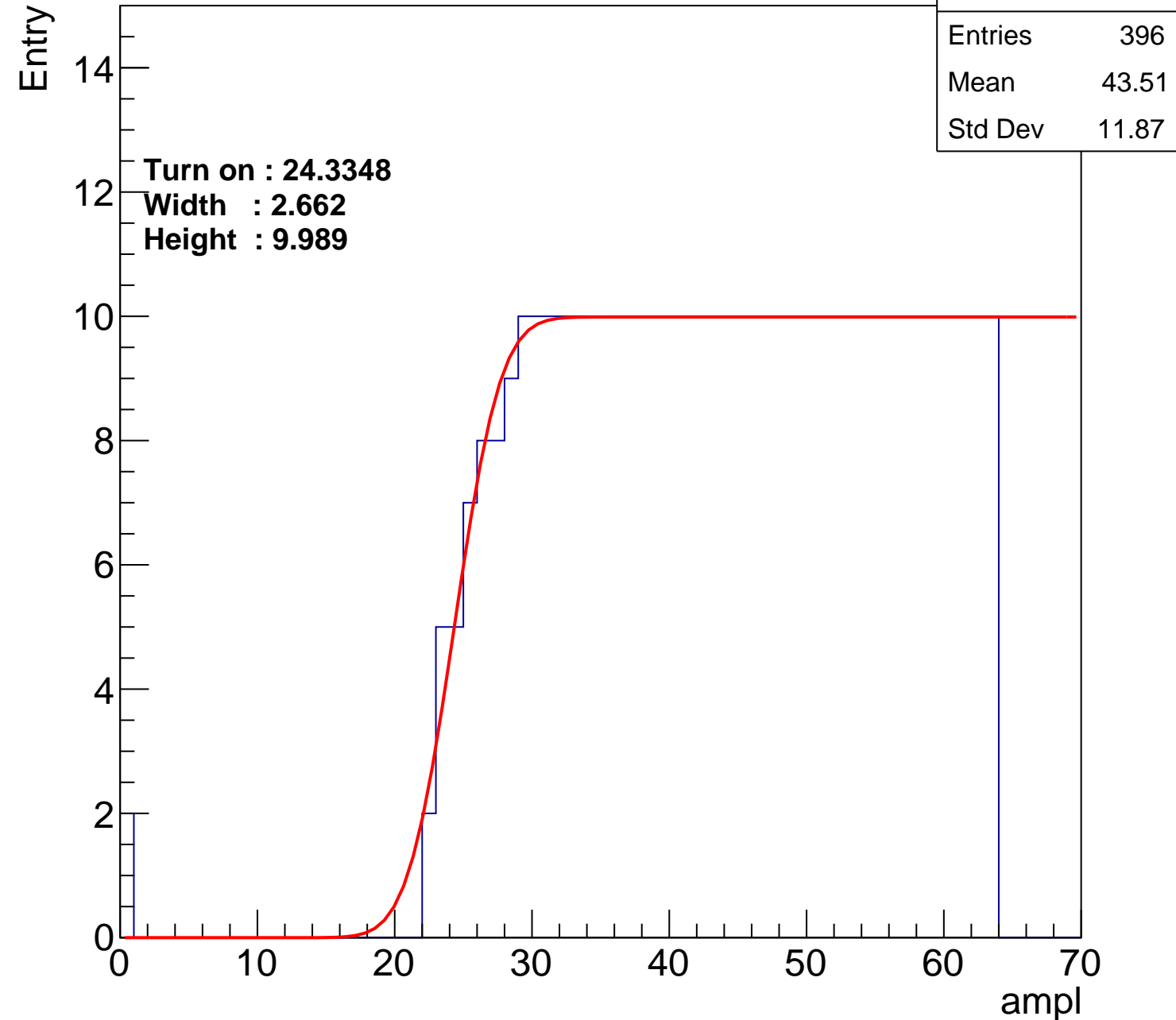
Width : 2.662

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch65

calib\_packv5\_042523\_0143.root, FC#5, port B1

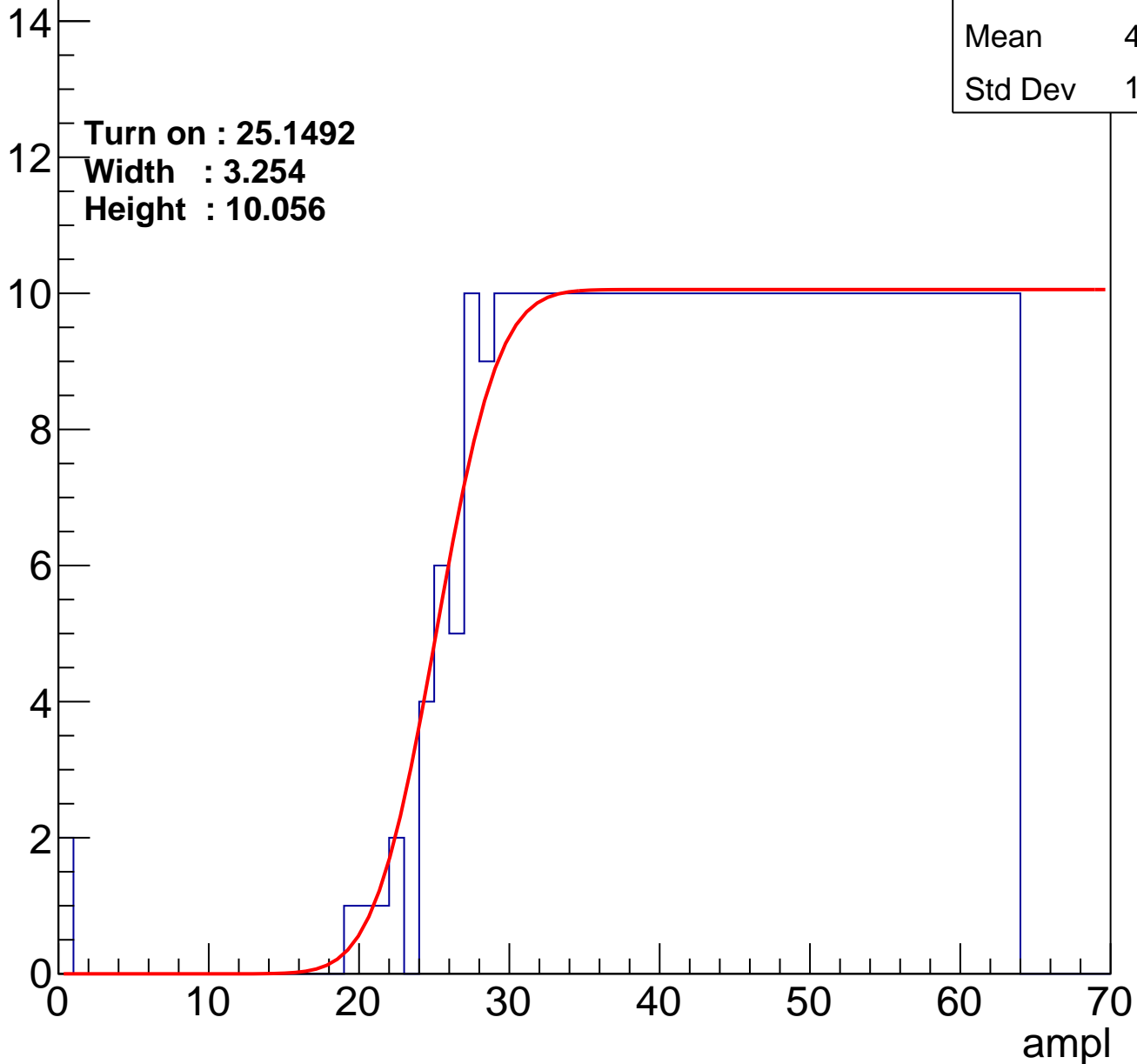
Entries	391
Mean	43.74
Std Dev	11.78

Turn on : 25.1492

Width : 3.254

Height : 10.056

Entry



# B0L000S, U17-ch66

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.98
Std Dev	11.17

Turn on : 28.4233

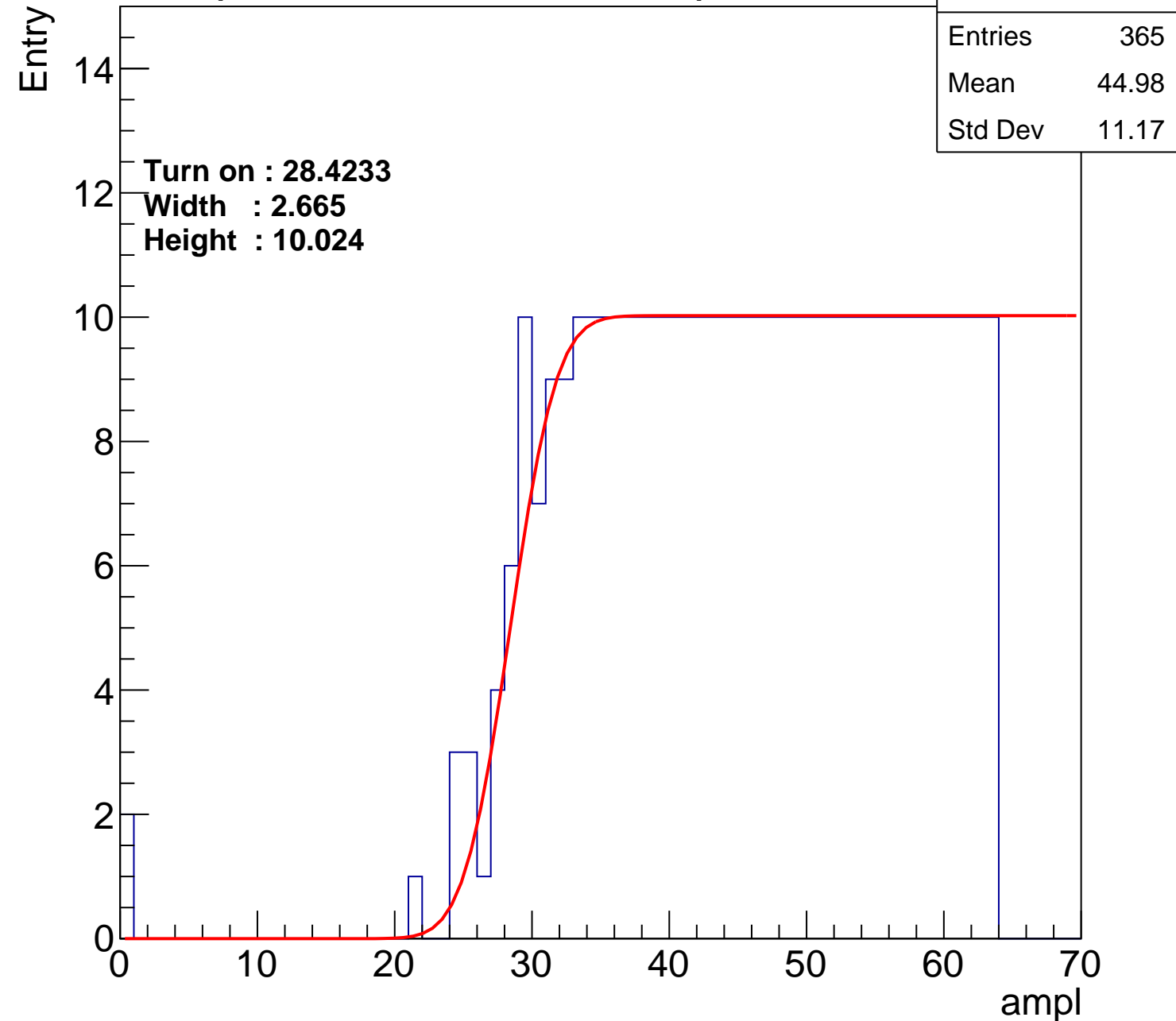
Width : 2.665

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch67

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	403
Mean	43.03
Std Dev	12.38

Turn on : 24.0355

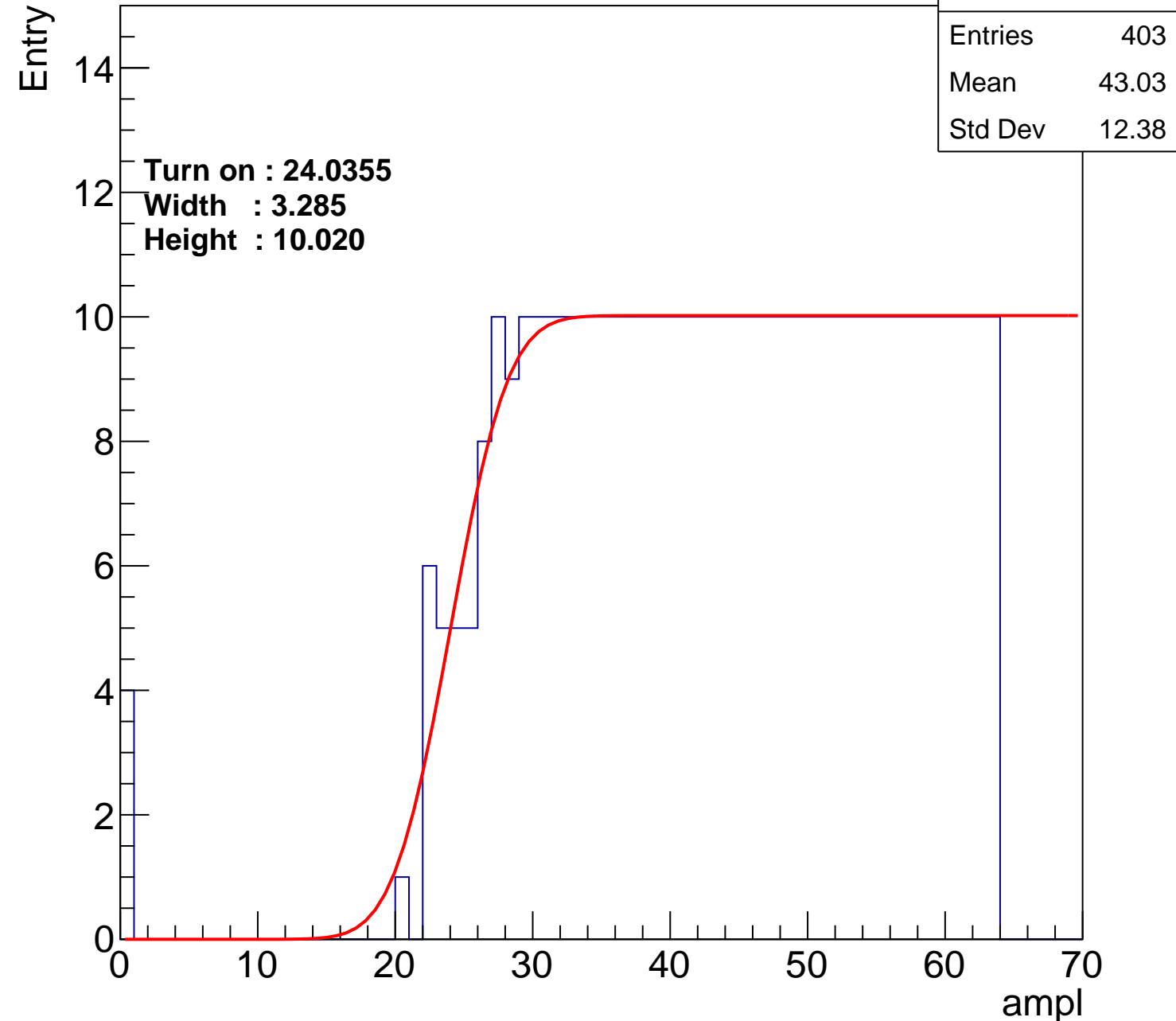
Width : 3.285

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch68

calib\_packv5\_042523\_0143.root, FC#5, port B1

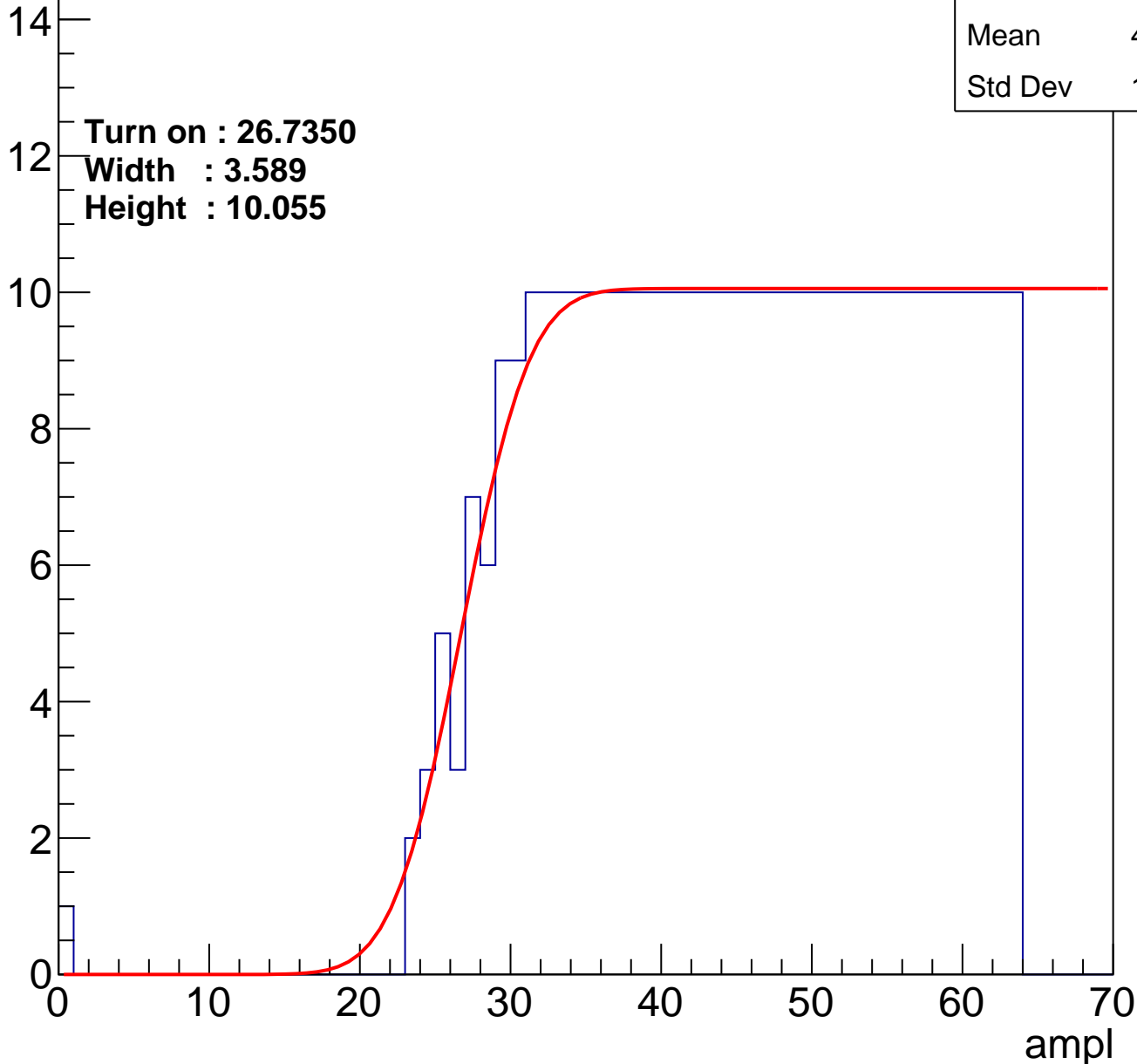
Entries	375
Mean	44.58
Std Dev	11.18

Turn on : 26.7350

Width : 3.589

Height : 10.055

Entry



# B0L000S, U17-ch69

calib\_packv5\_042523\_0143.root, FC#5, port B1

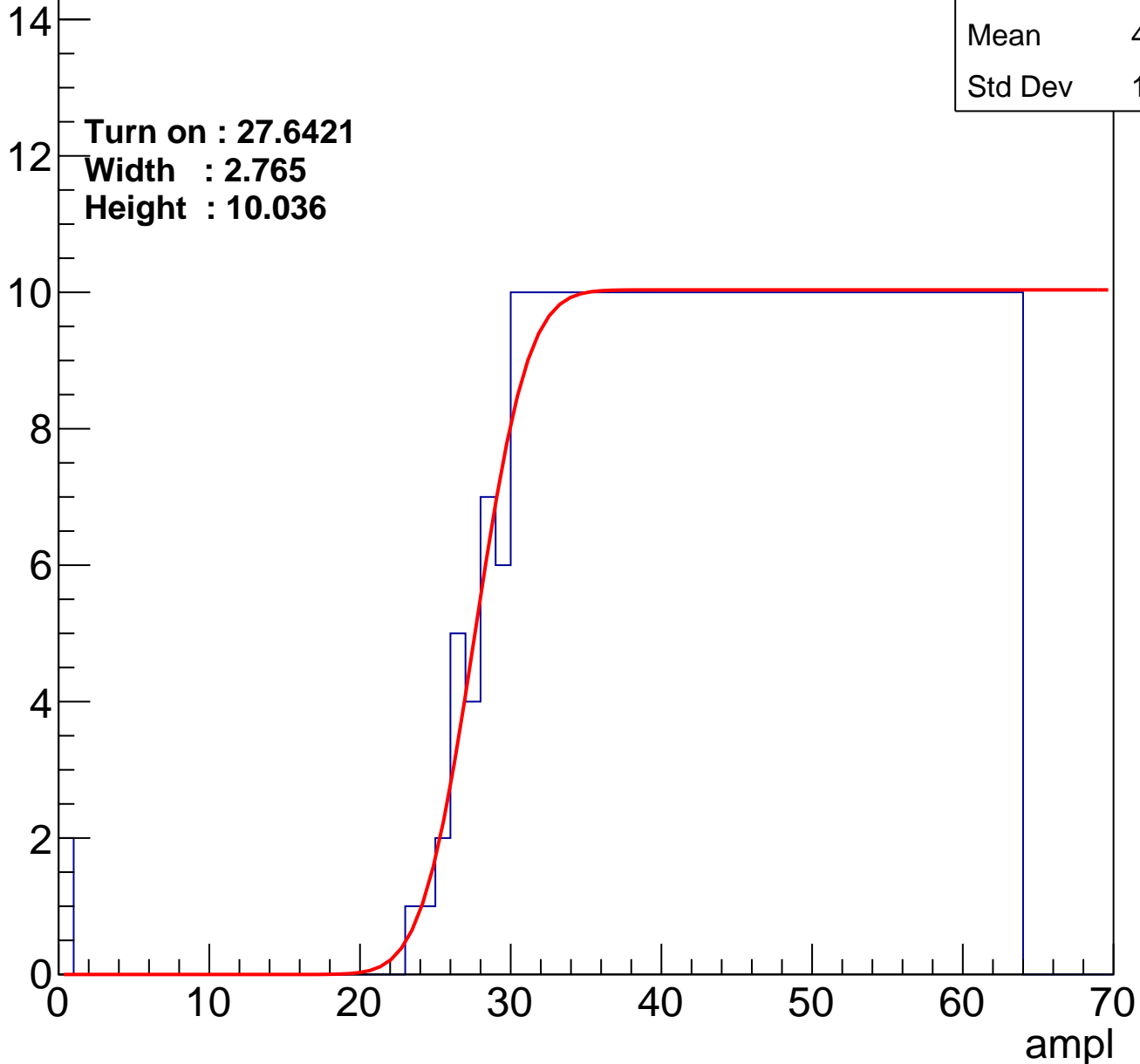
Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.6421

Width : 2.765

Height : 10.036

Entry



# B0L000S, U17-ch70

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	400
Mean	43.19
Std Dev	12.3

Turn on : 25.1613

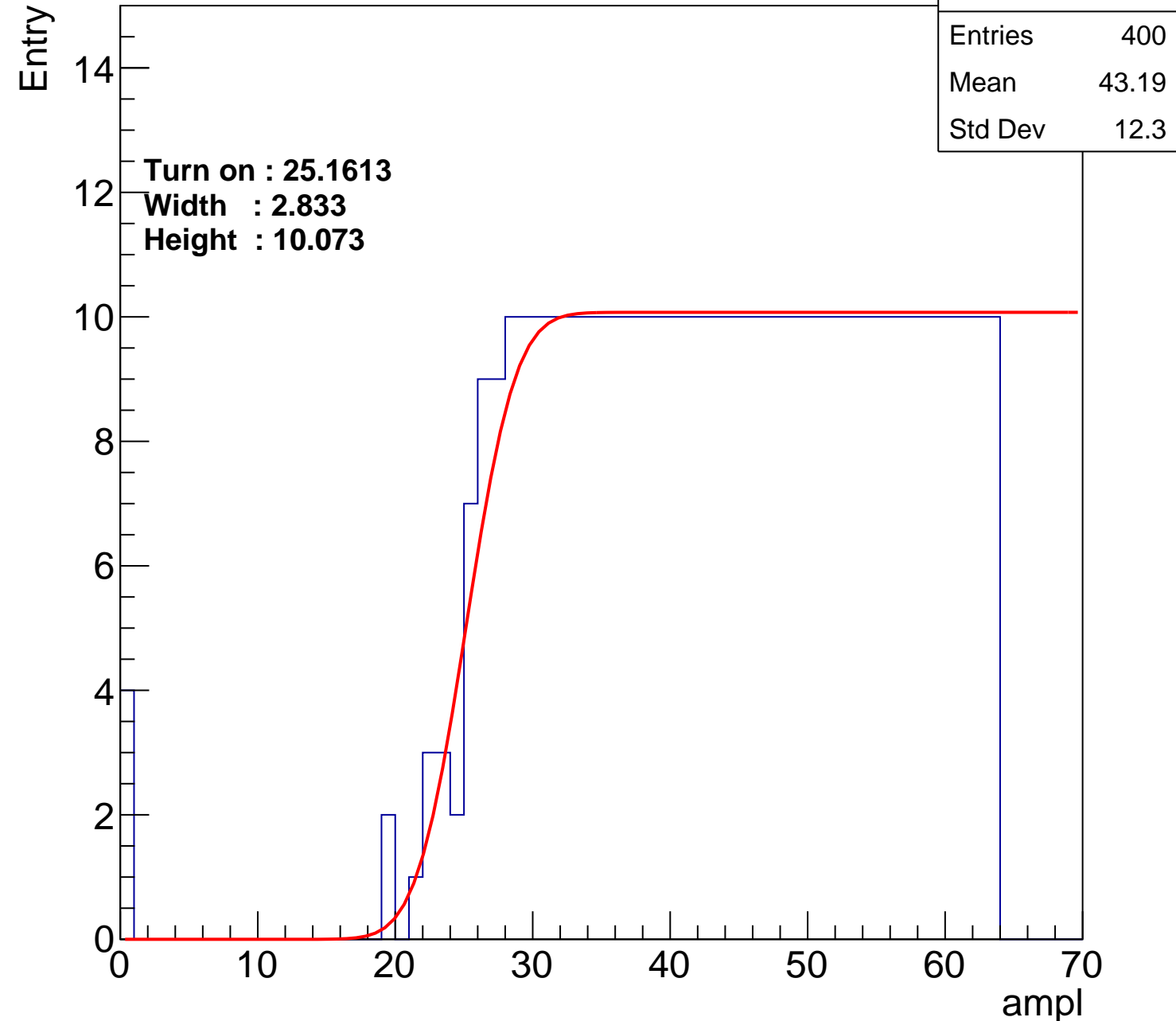
Width : 2.833

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch71

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	399
Mean	43.14
Std Dev	12.47

Turn on : 24.7537

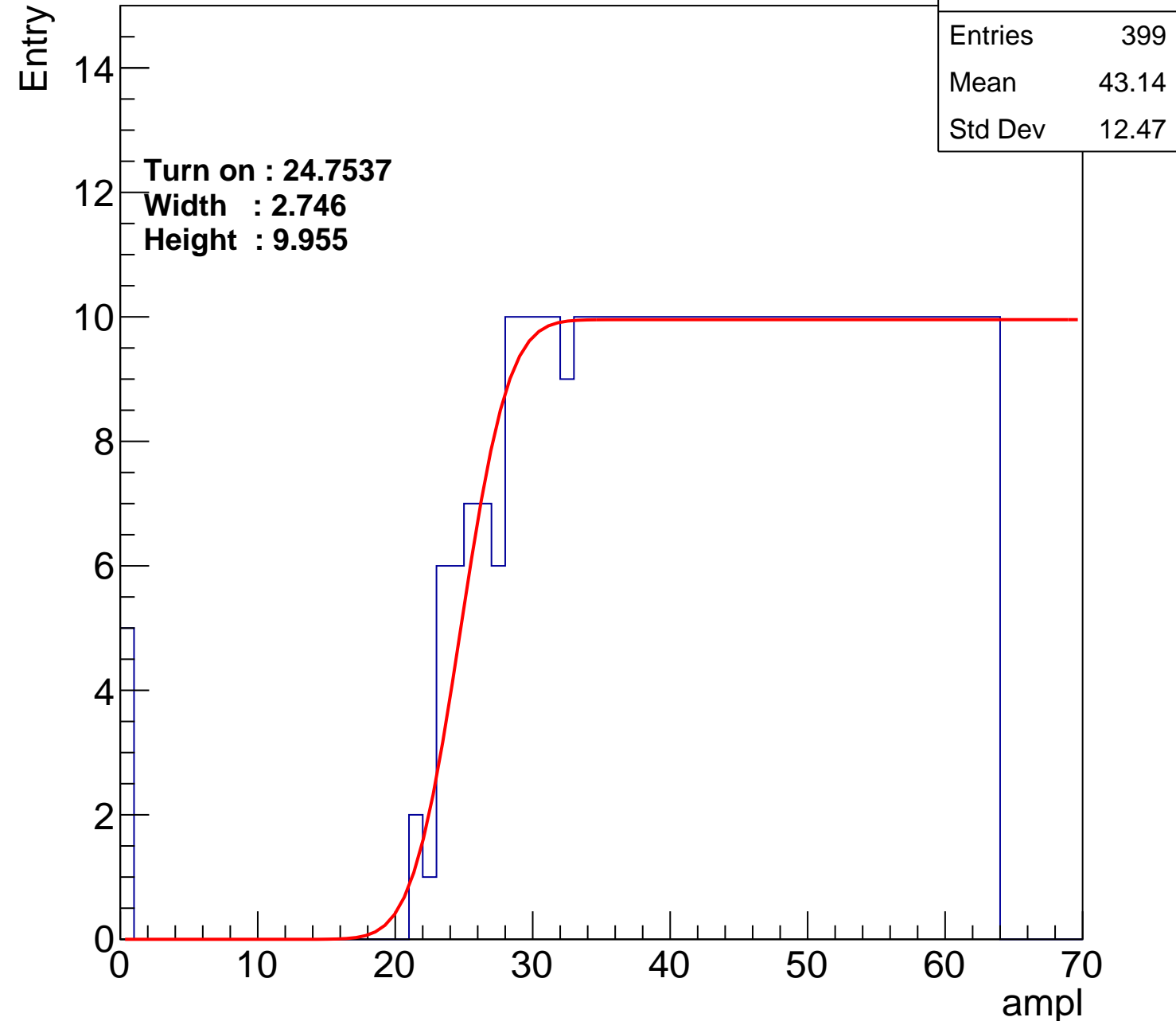
Width : 2.746

Height : 9.955

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch72

calib\_packv5\_042523\_0143.root, FC#5, port B1

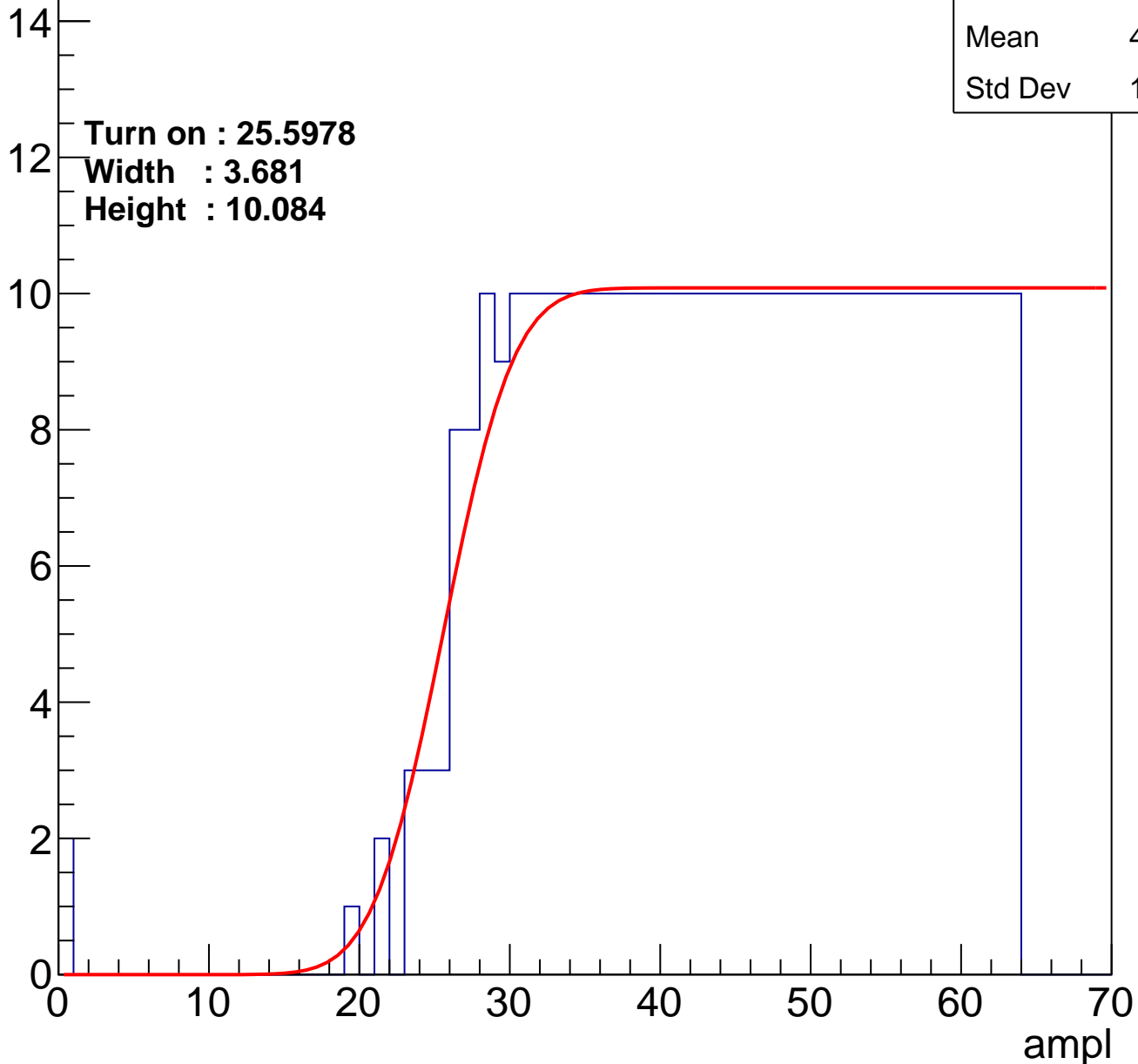
Entries	389
Mean	43.84
Std Dev	11.73

Turn on : 25.5978

Width : 3.681

Height : 10.084

Entry



# B0L000S, U17-ch73

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.94
Std Dev	11.66

**Turn on : 25.7406**

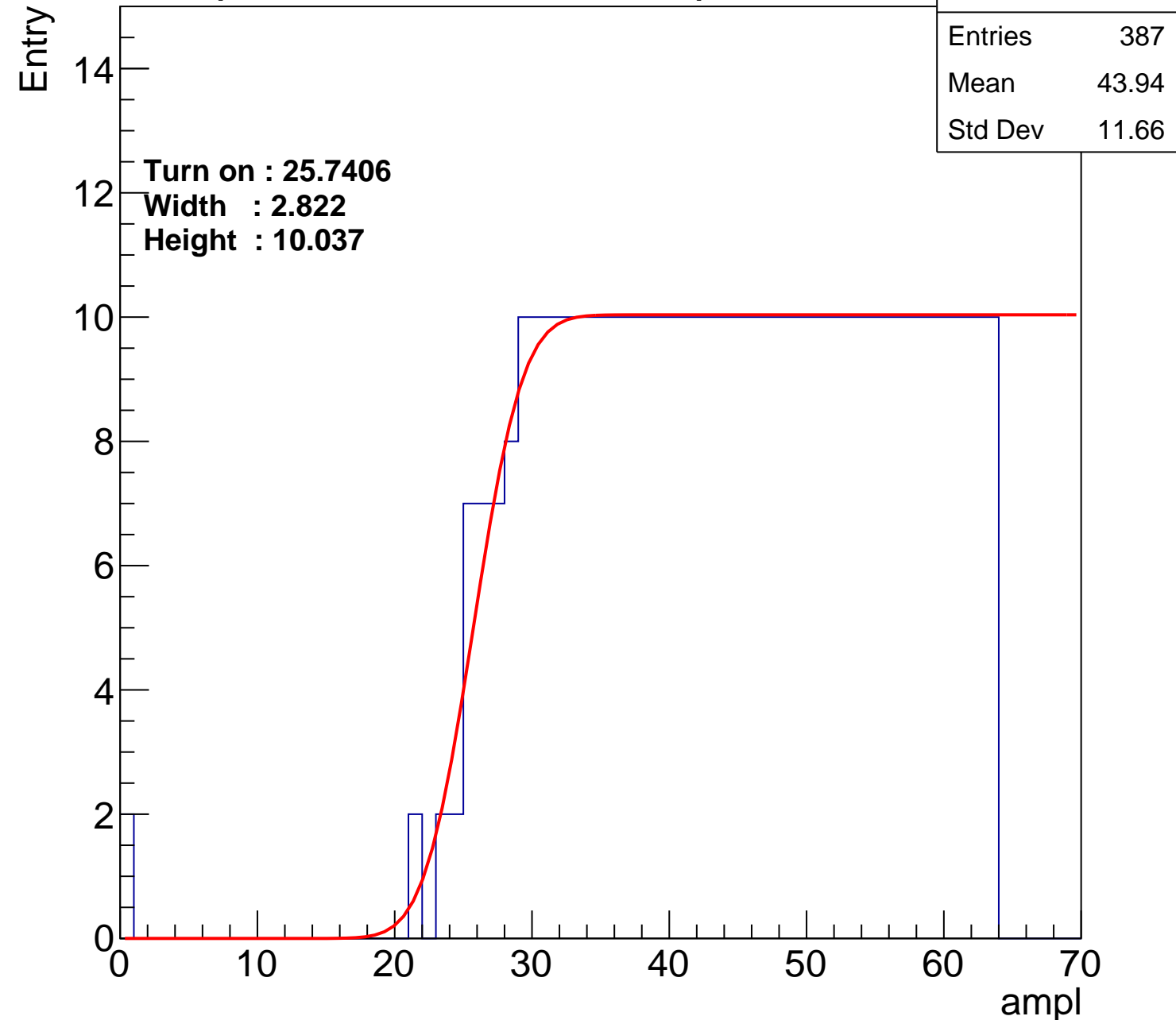
**Width : 2.822**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch74

calib\_packv5\_042523\_0143.root, FC#5, port B1

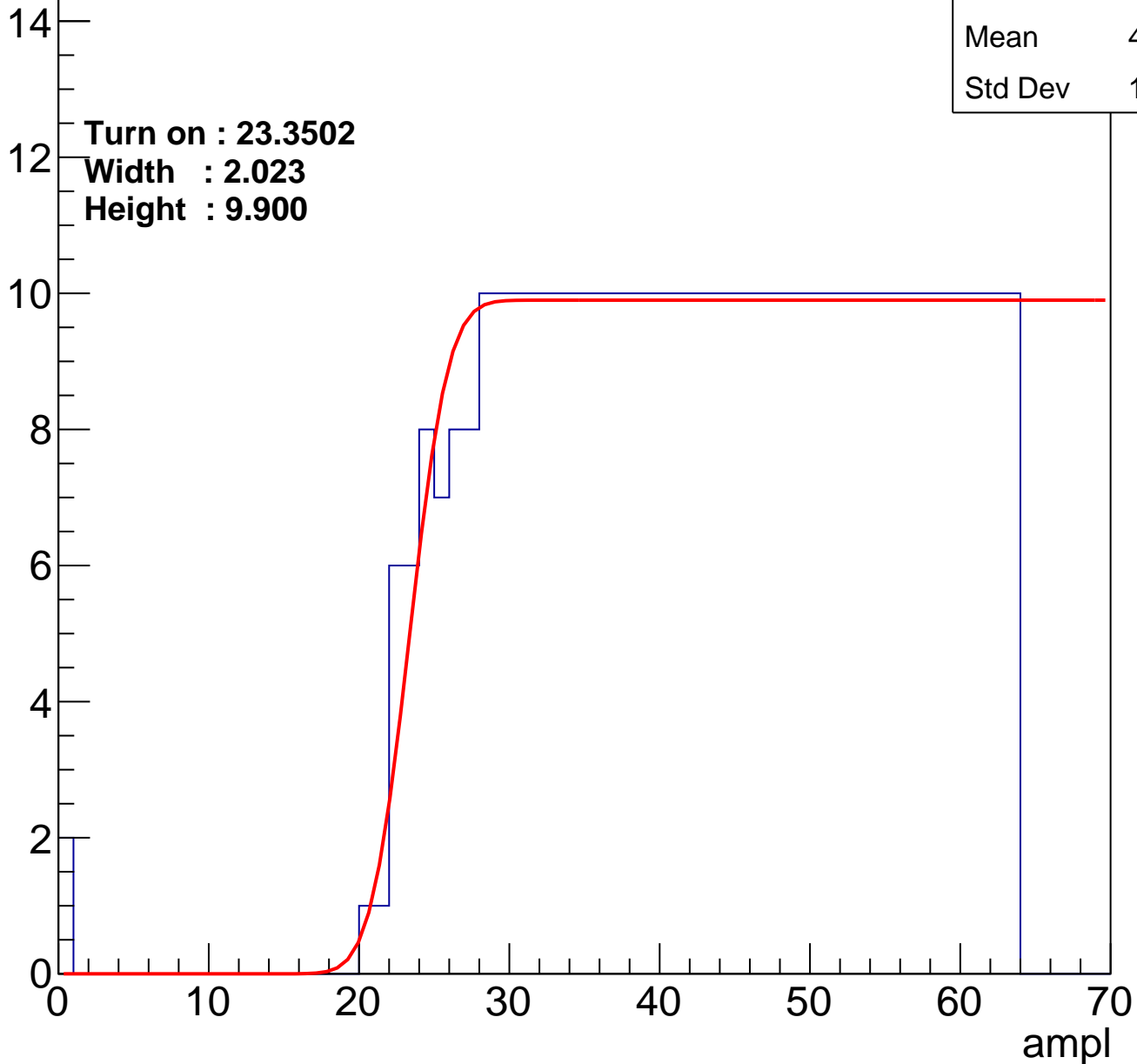
Entries	407
Mean	42.95
Std Dev	12.18

Turn on : 23.3502

Width : 2.023

Height : 9.900

Entry



# B0L000S, U17-ch75

calib\_packv5\_042523\_0143.root, FC#5, port B1

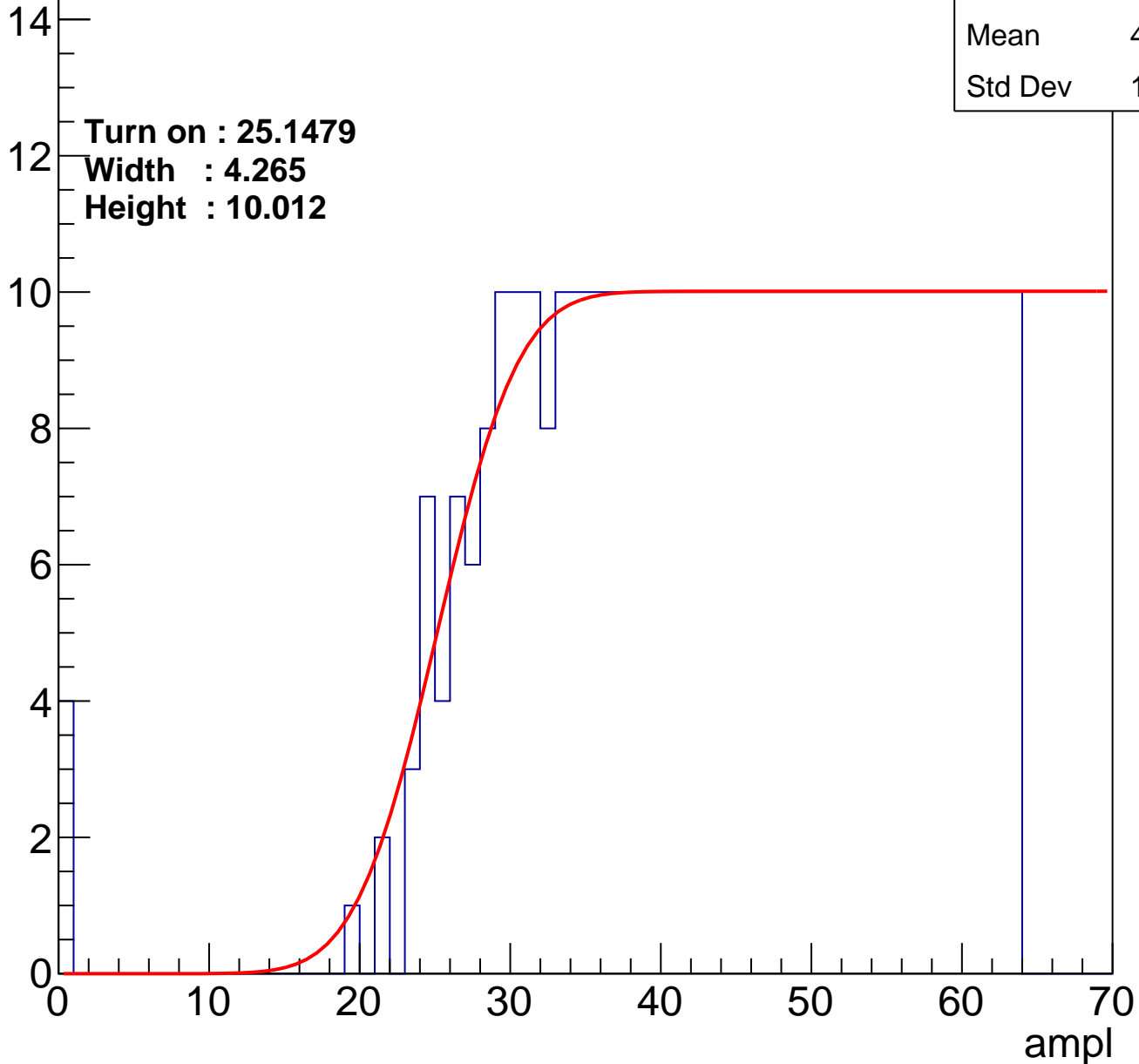
Entries	390
Mean	43.59
Std Dev	12.17

Turn on : 25.1479

Width : 4.265

Height : 10.012

Entry



# B0L000S, U17-ch76

calib\_packv5\_042523\_0143.root, FC#5, port B1

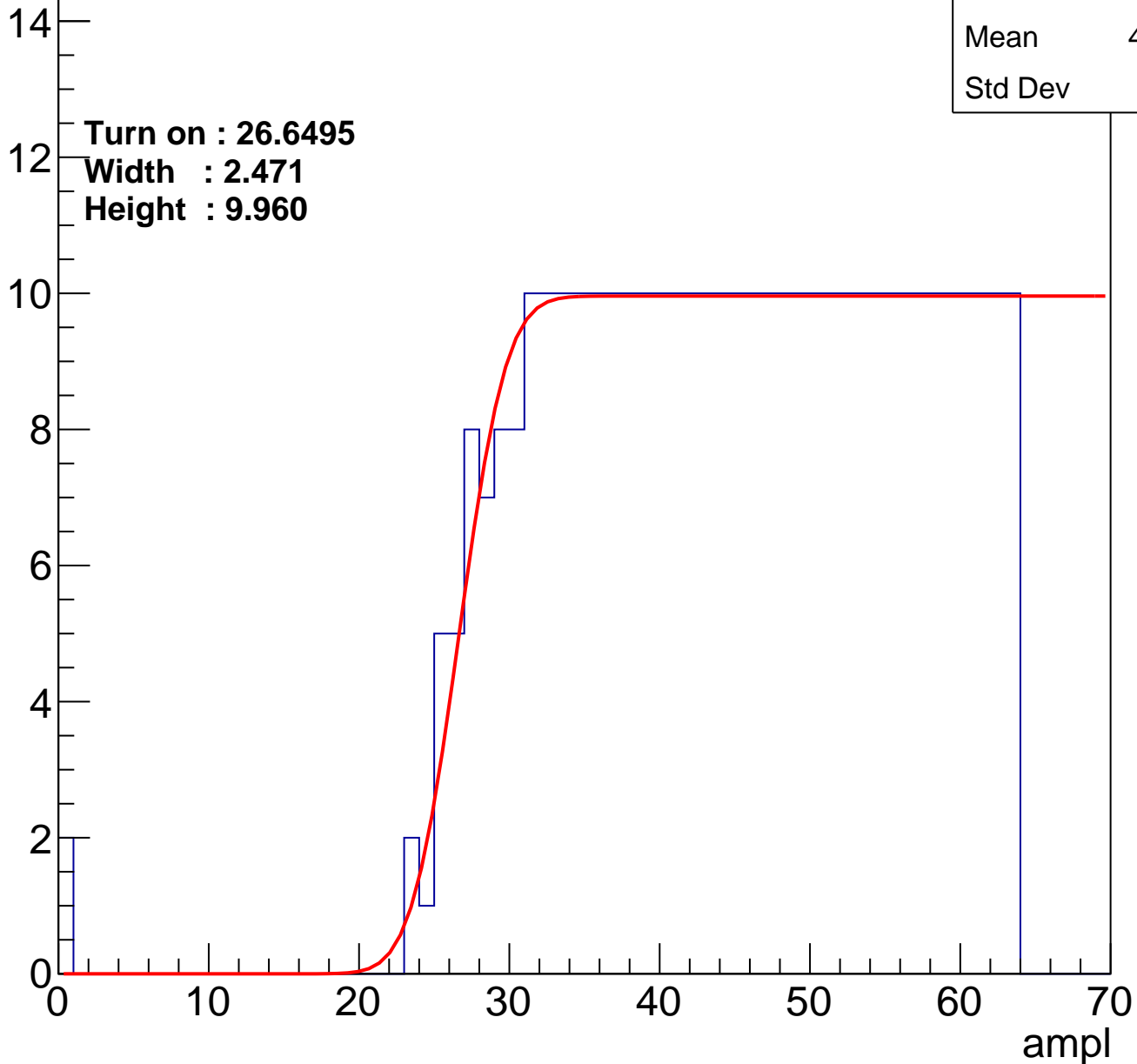
Entries	376
Mean	44.47
Std Dev	11.4

**Turn on : 26.6495**

**Width : 2.471**

**Height : 9.960**

Entry



# B0L000S, U17-ch77

calib\_packv5\_042523\_0143.root, FC#5, port B1

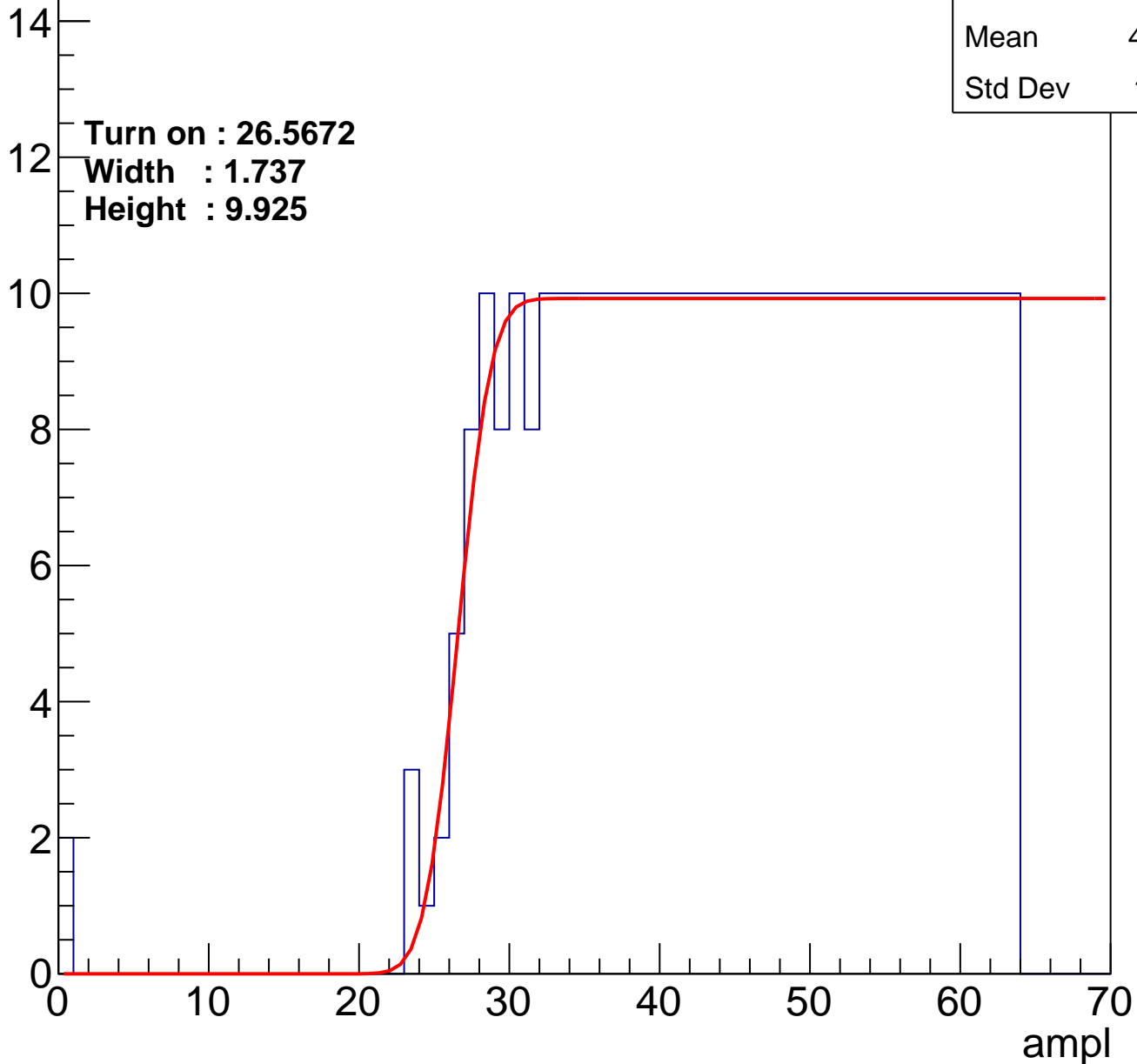
Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 26.5672

Width : 1.737

Height : 9.925

Entry



# B0L000S, U17-ch78

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	43.95
Std Dev	12.07

Turn on : 26.3836

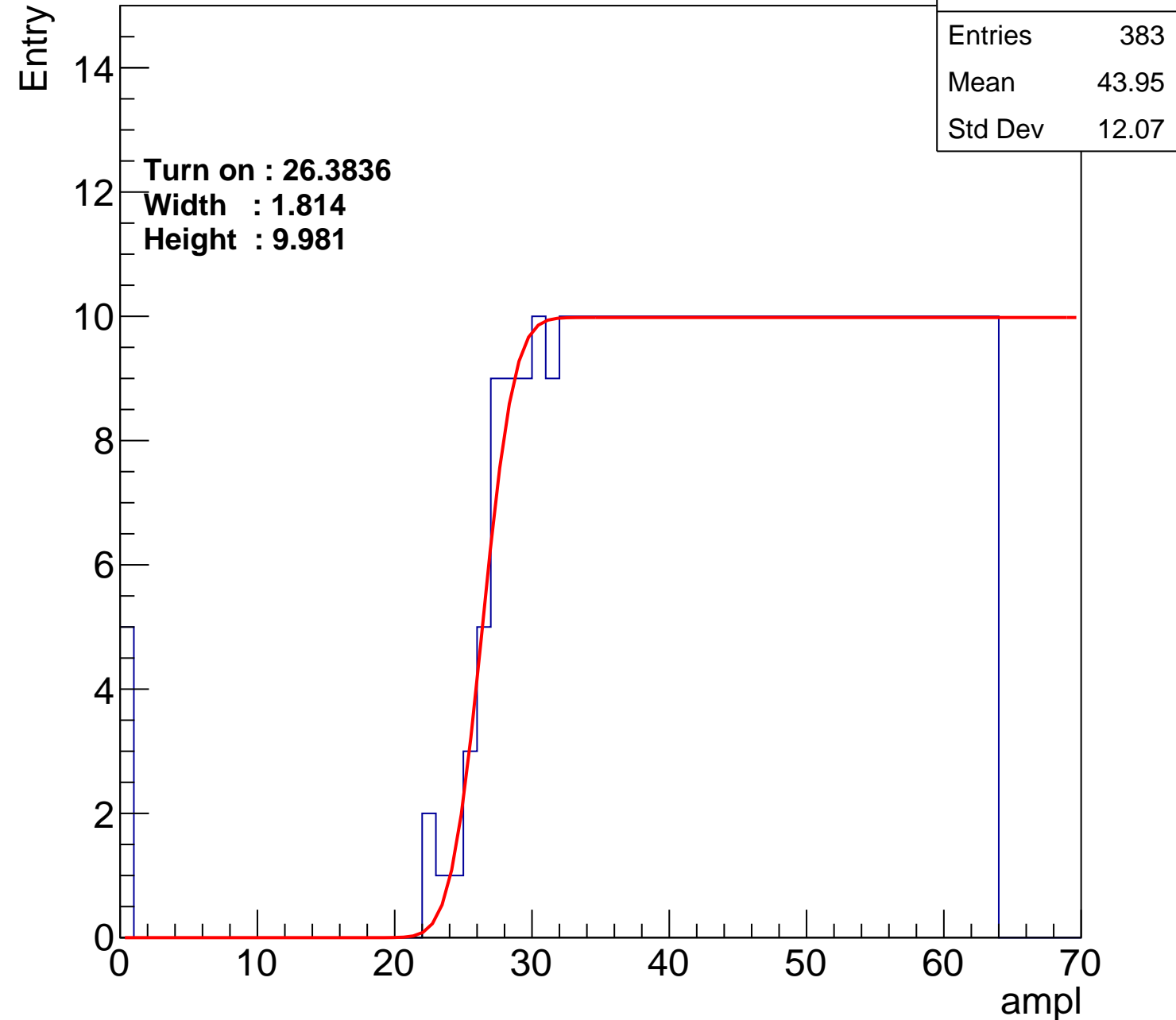
Width : 1.814

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch79

calib\_packv5\_042523\_0143.root, FC#5, port B1

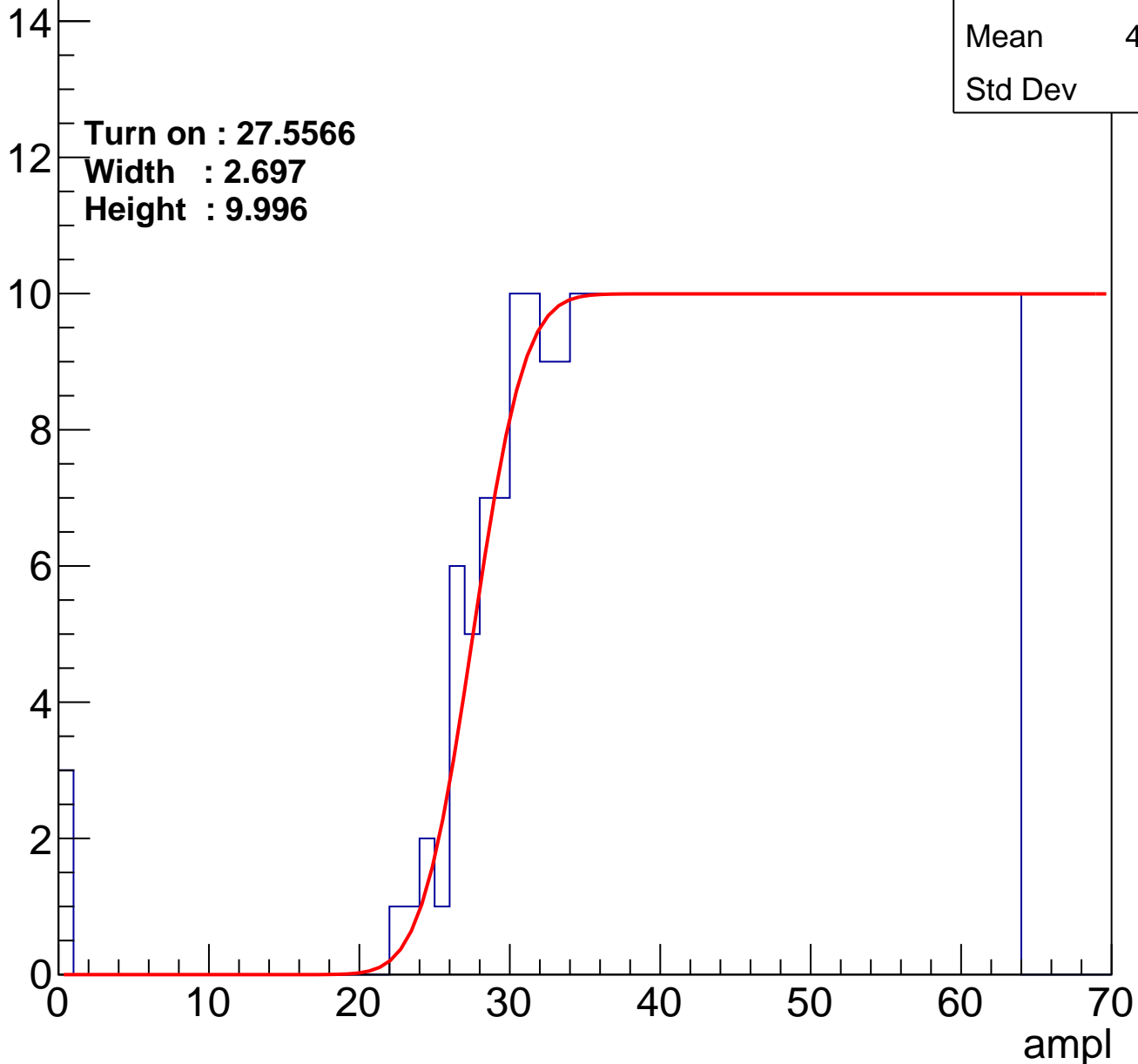
Entries	371
Mean	44.62
Std Dev	11.5

Turn on : 27.5566

Width : 2.697

Height : 9.996

Entry



# B0L000S, U17-ch80

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	395
Mean	43.44
Std Dev	12.1

Turn on : 24.9896

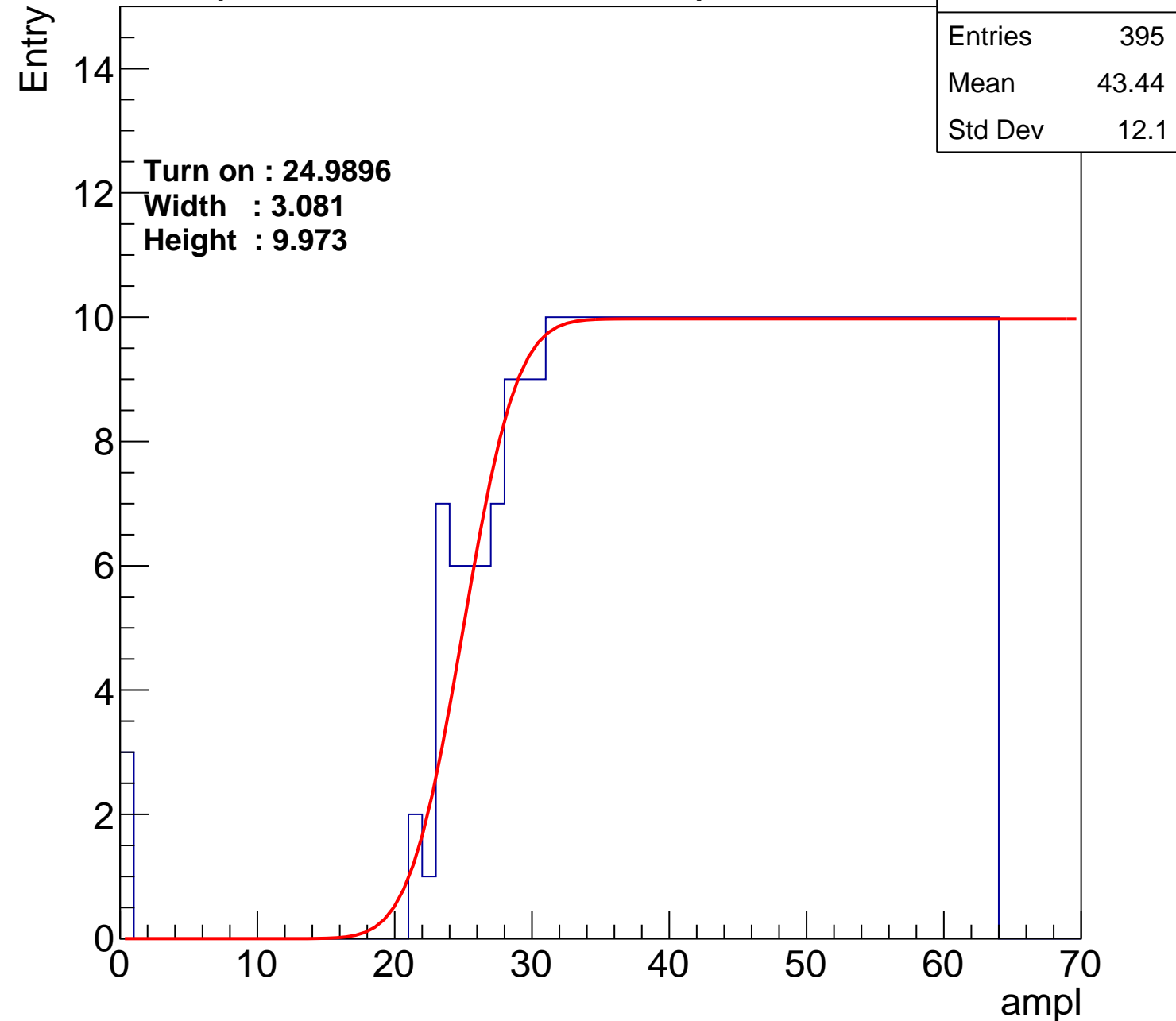
Width : 3.081

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch81

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.62
Std Dev	11.51

**Turn on : 27.7128**

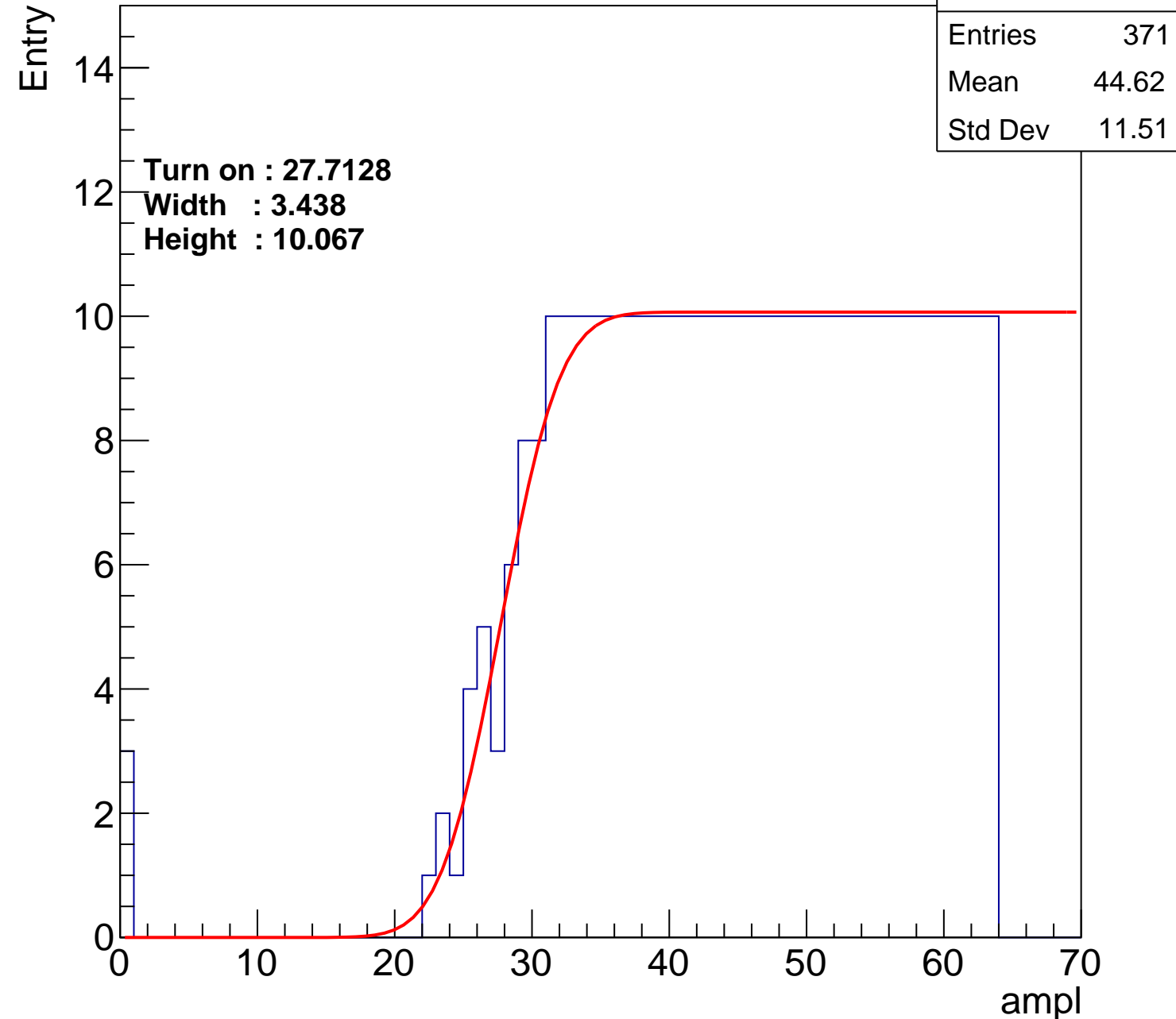
**Width : 3.438**

**Height : 10.067**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch82

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 25.8724

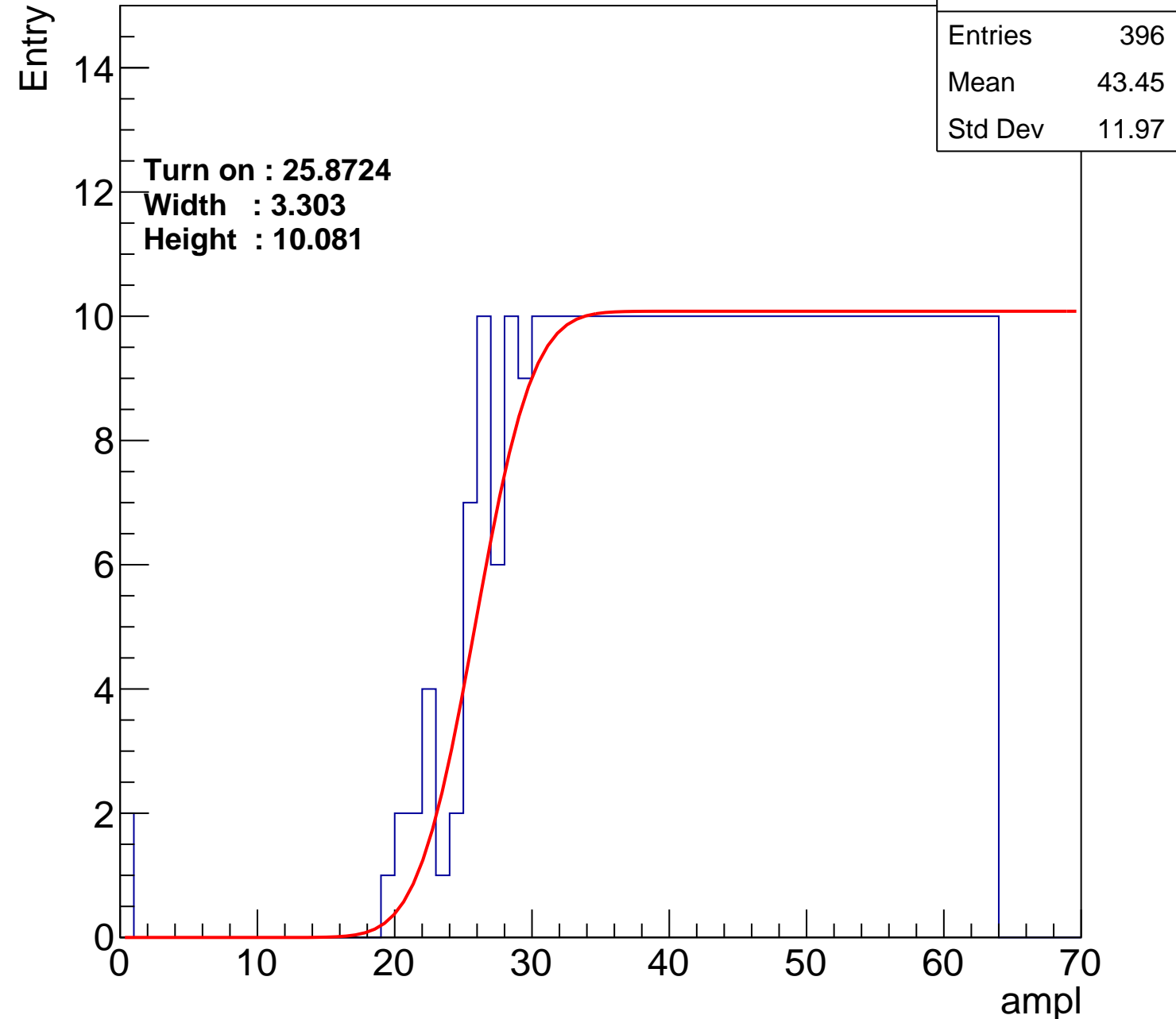
Width : 3.303

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch83

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 25.9444

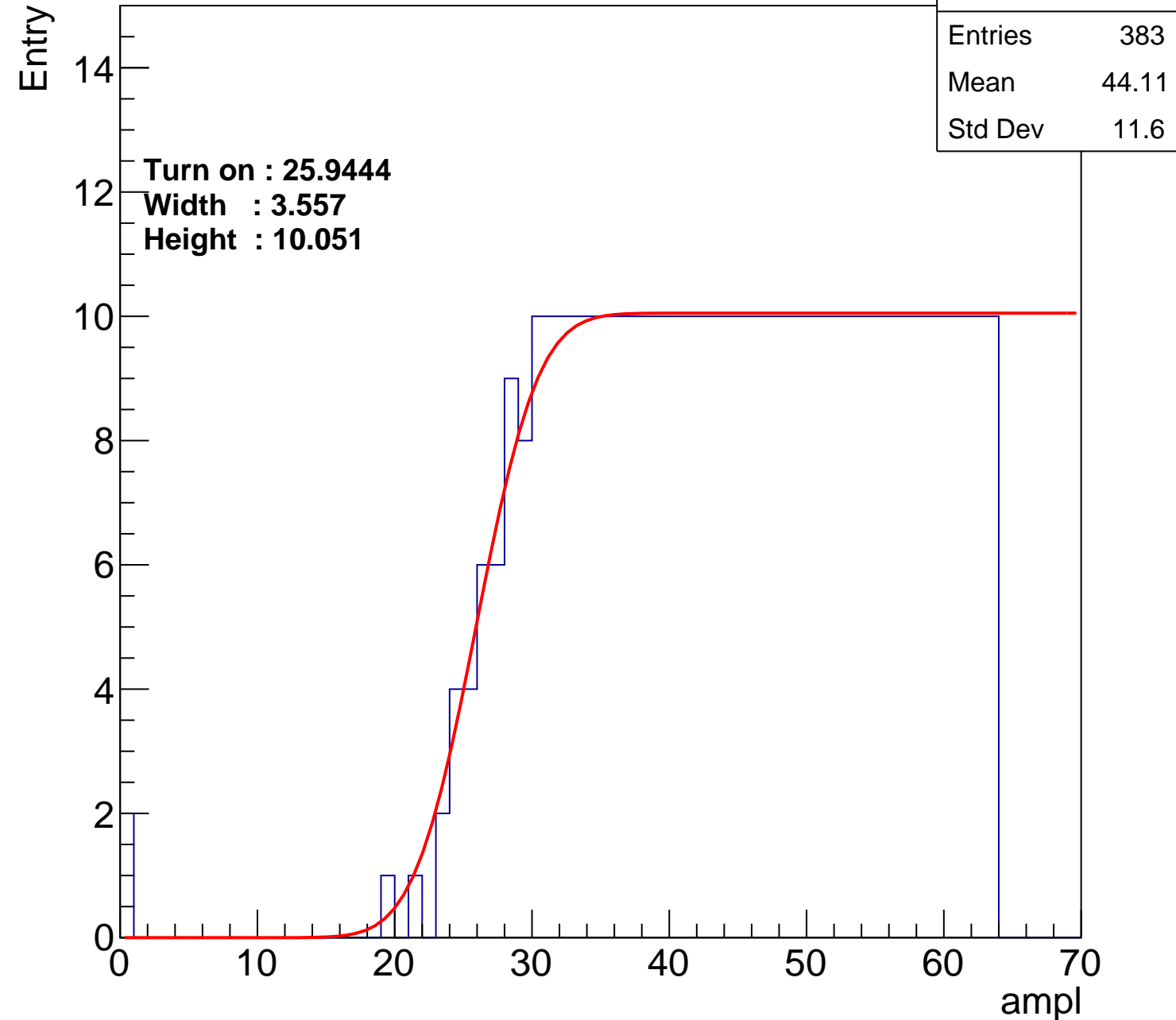
Width : 3.557

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch84

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.59
Std Dev	11.49

Turn on : 27.2537

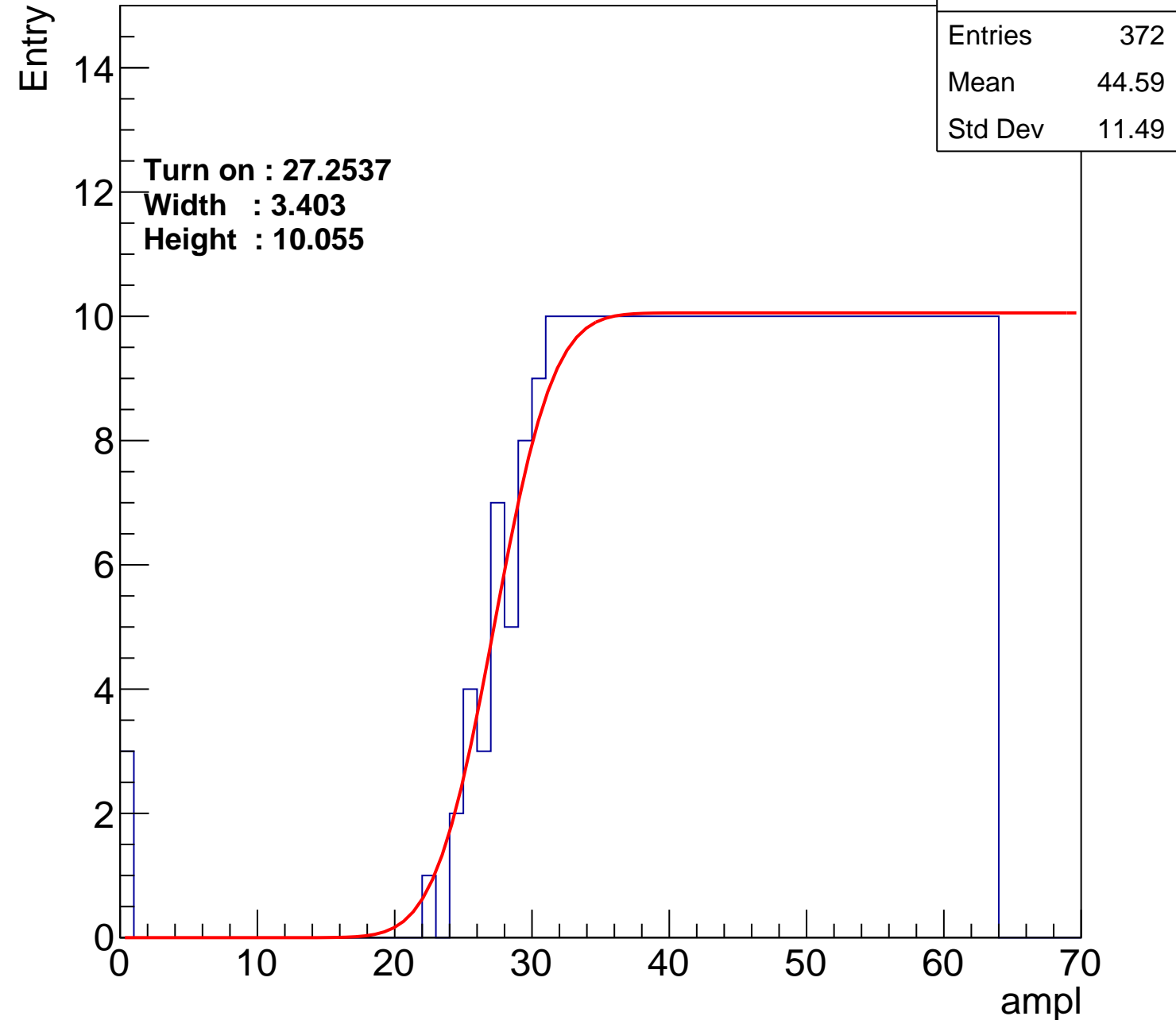
Width : 3.403

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch85

calib\_packv5\_042523\_0143.root, FC#5, port B1

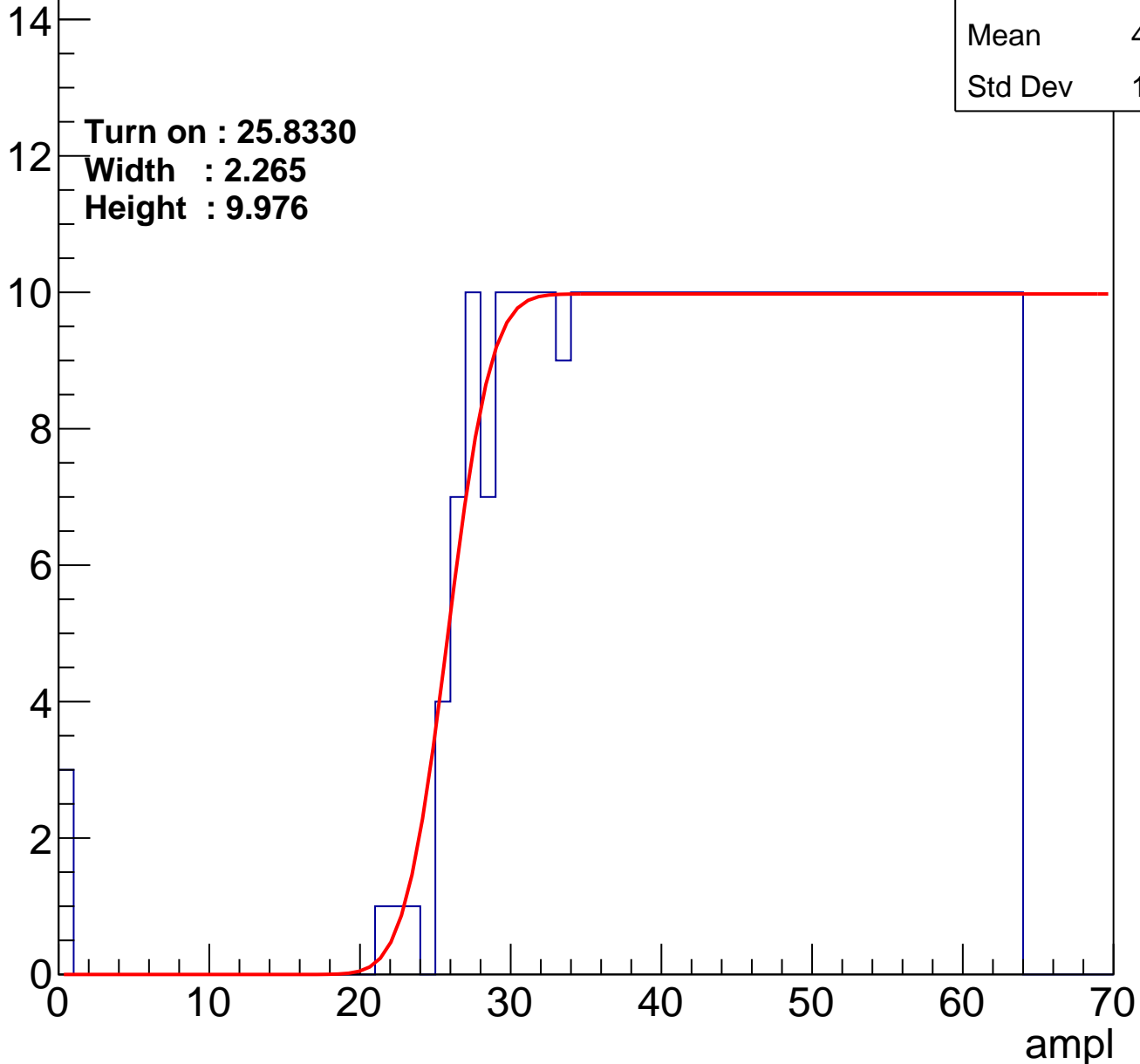
Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 25.8330

Width : 2.265

Height : 9.976

Entry



# B0L000S, U17-ch86

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.82
Std Dev	11.45

Turn on : 28.1345

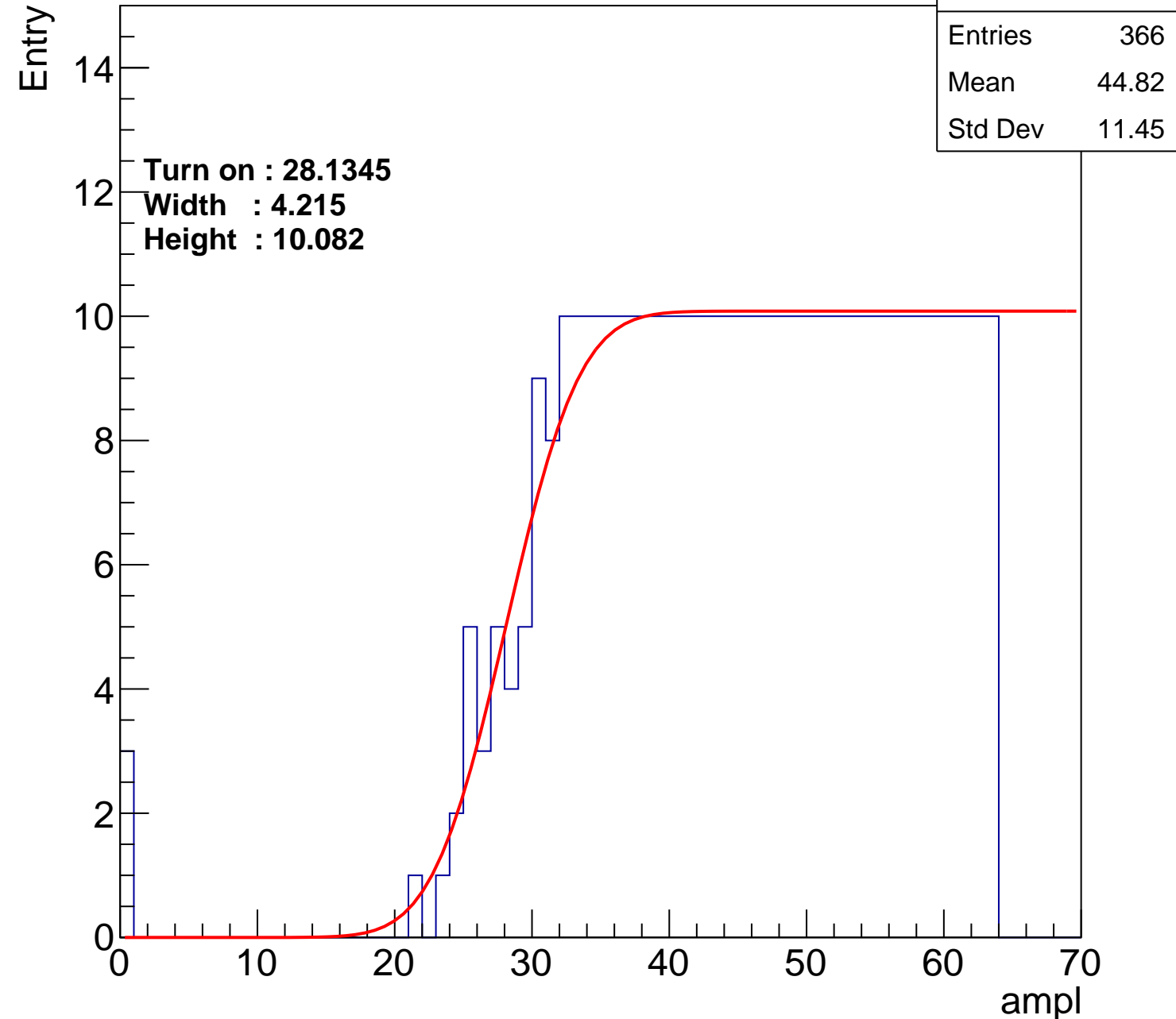
Width : 4.215

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch87

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.57
Std Dev	11.52

**Turn on : 26.4693**

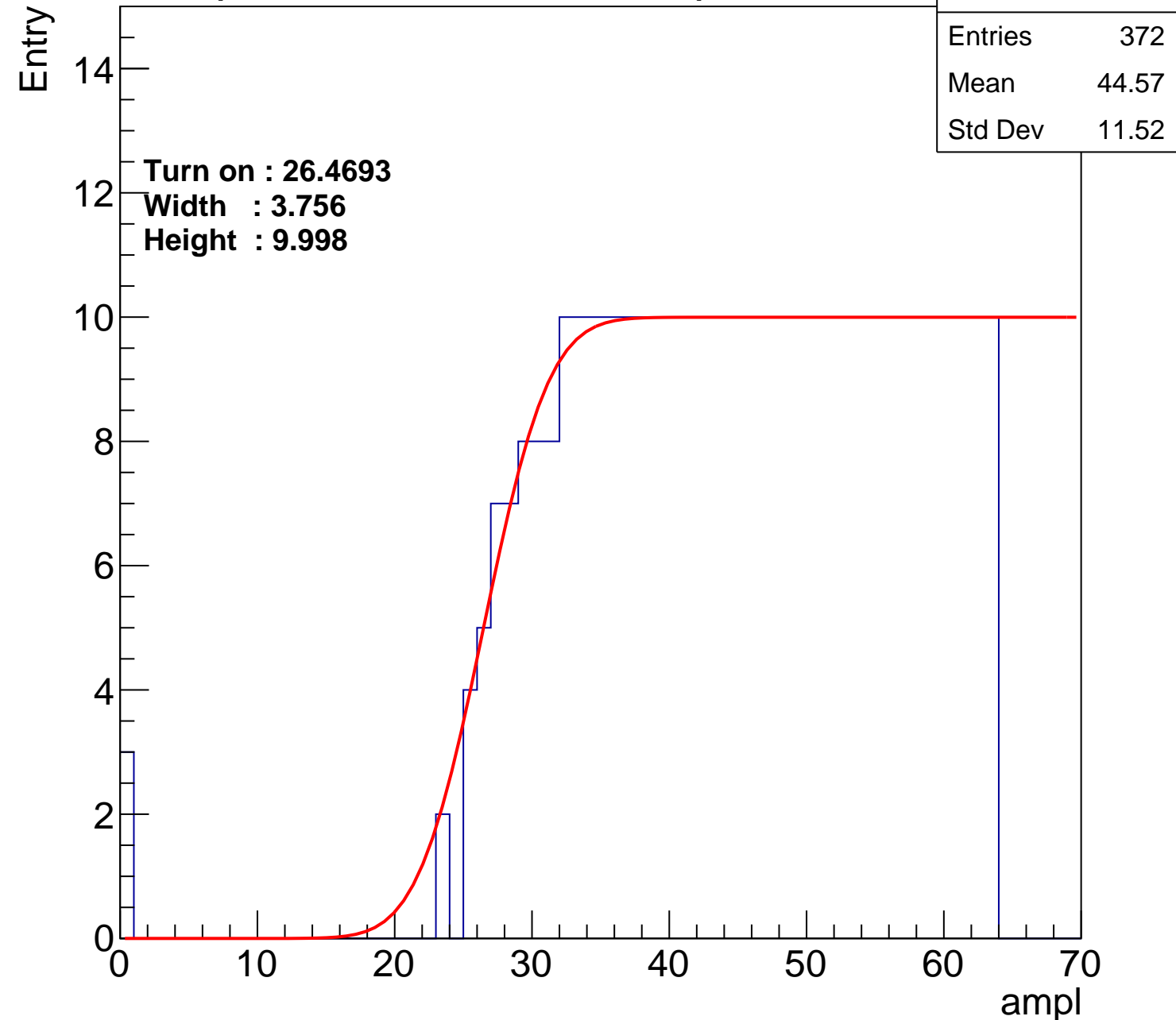
**Width : 3.756**

**Height : 9.998**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch88

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.13
Std Dev	11.43

Turn on : 25.8386

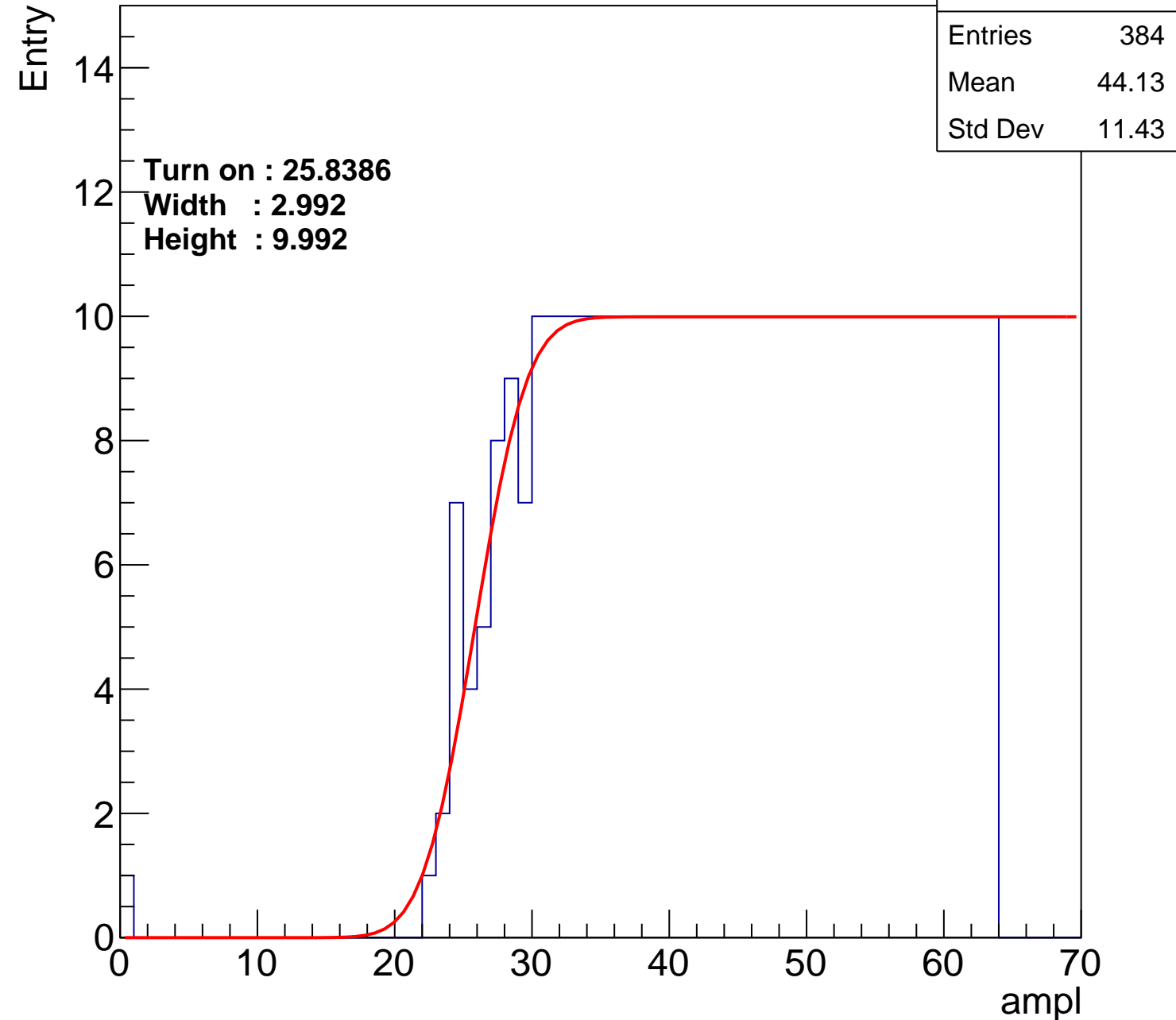
Width : 2.992

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch89

calib\_packv5\_042523\_0143.root, FC#5, port B1

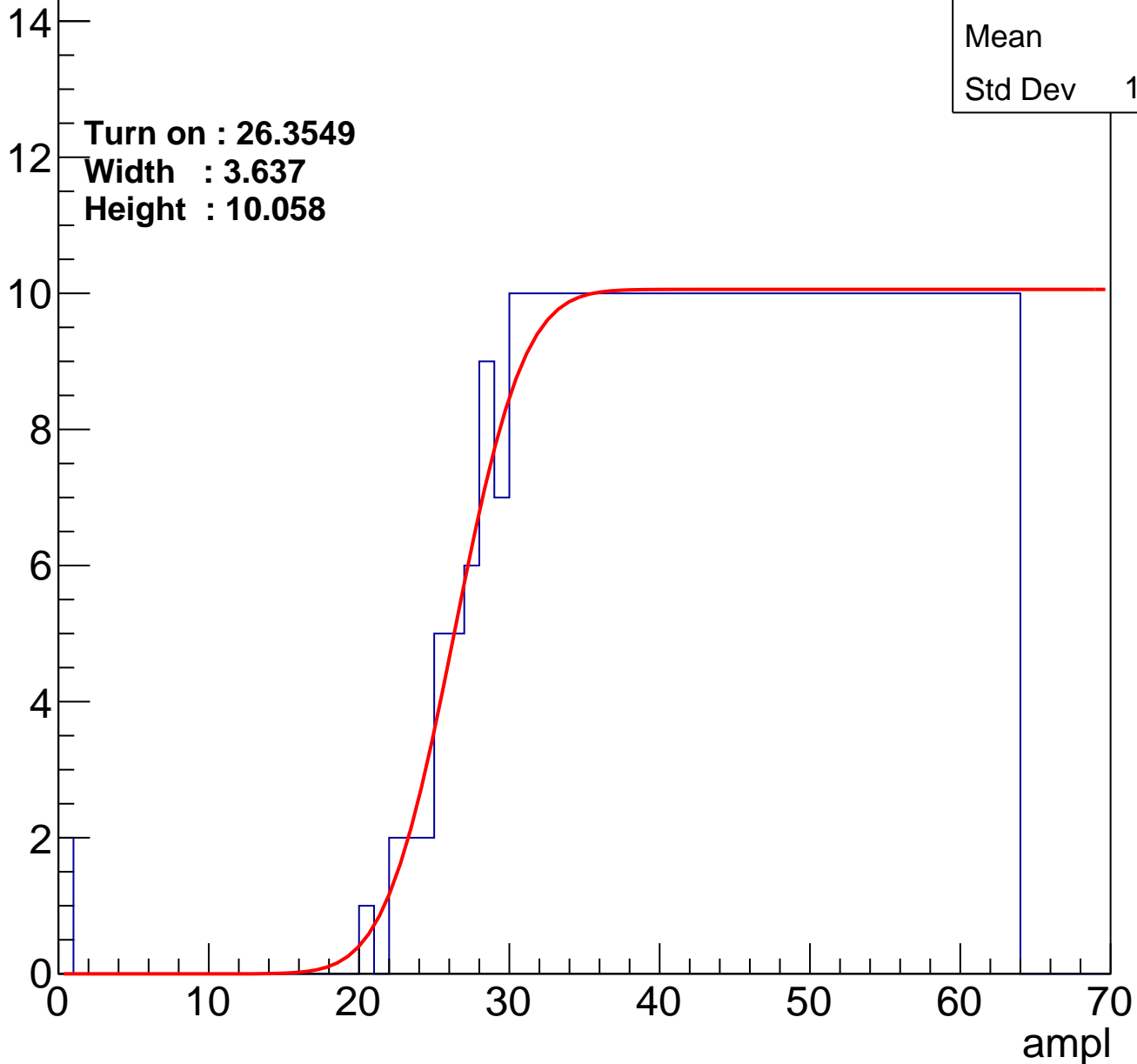
Entries	381
Mean	44.2
Std Dev	11.57

Turn on : 26.3549

Width : 3.637

Height : 10.058

Entry



# B0L000S, U17-ch90

calib\_packv5\_042523\_0143.root, FC#5, port B1

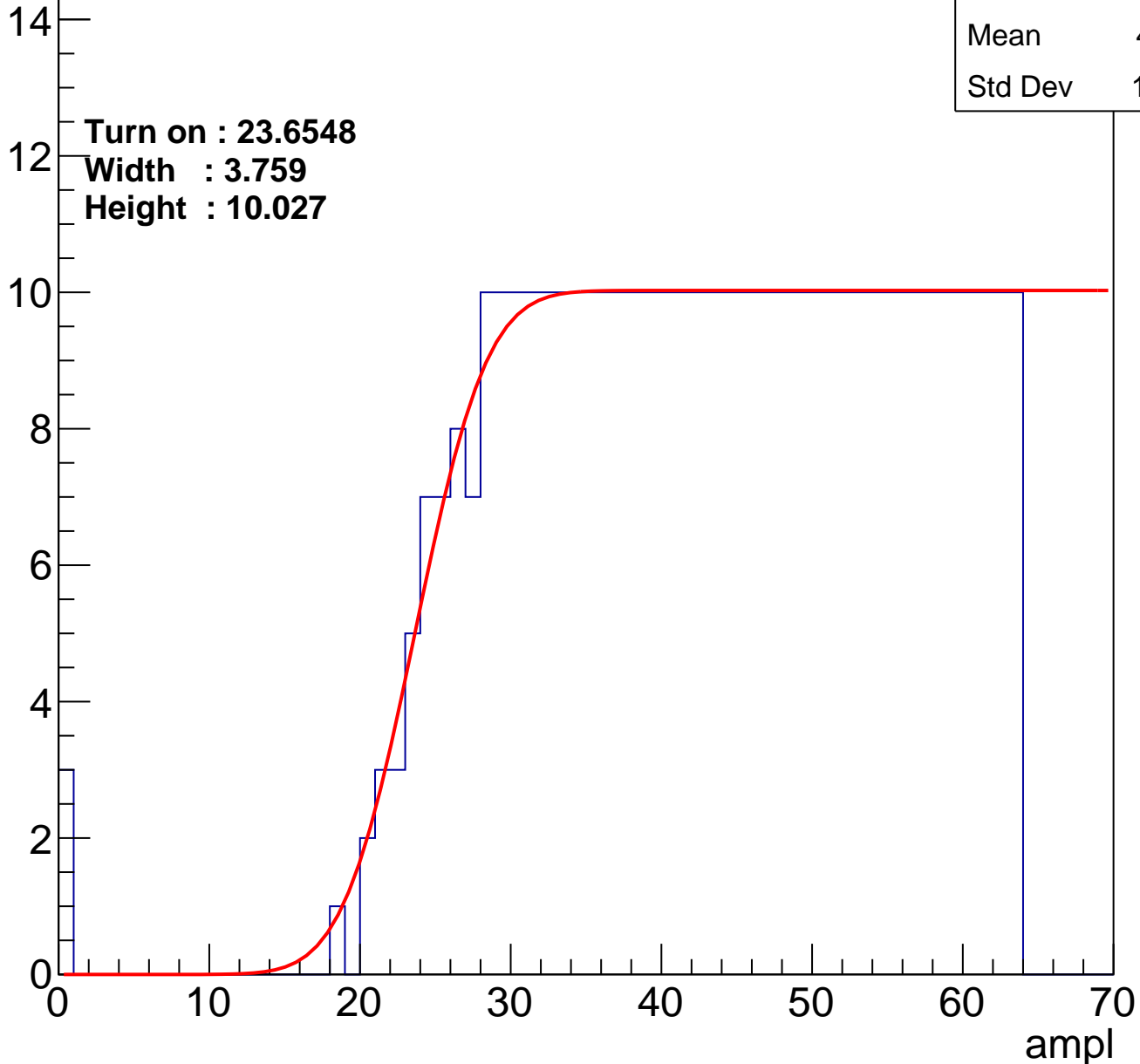
Entries	406
Mean	42.91
Std Dev	12.36

**Turn on : 23.6548**

**Width : 3.759**

**Height : 10.027**

Entry



# B0L000S, U17-ch91

calib\_packv5\_042523\_0143.root, FC#5, port B1

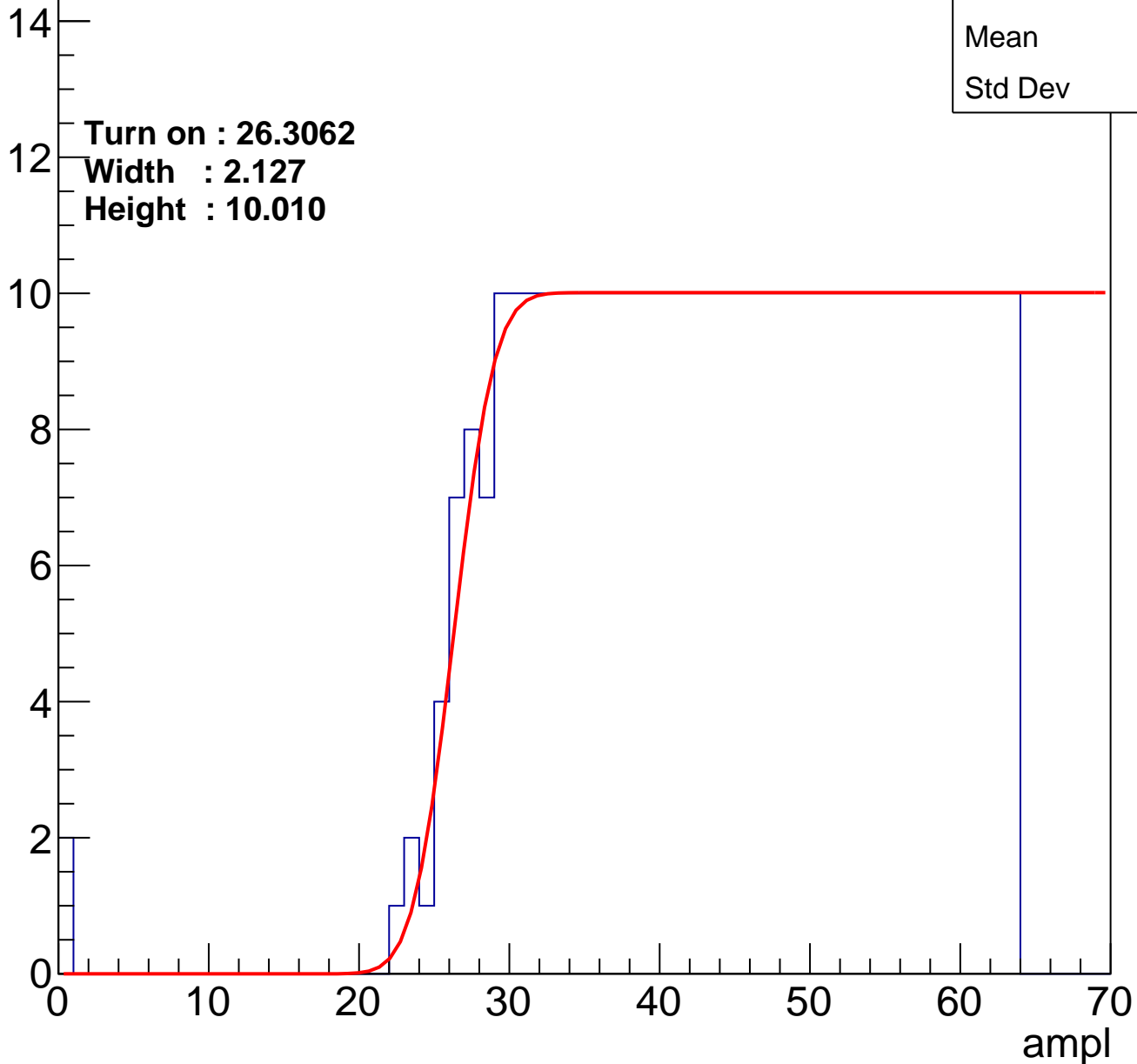
Entries	382
Mean	44.2
Std Dev	11.5

**Turn on : 26.3062**

**Width : 2.127**

**Height : 10.010**

Entry



# B0L000S, U17-ch92

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.55
Std Dev	11.52

Turn on : 27.5678

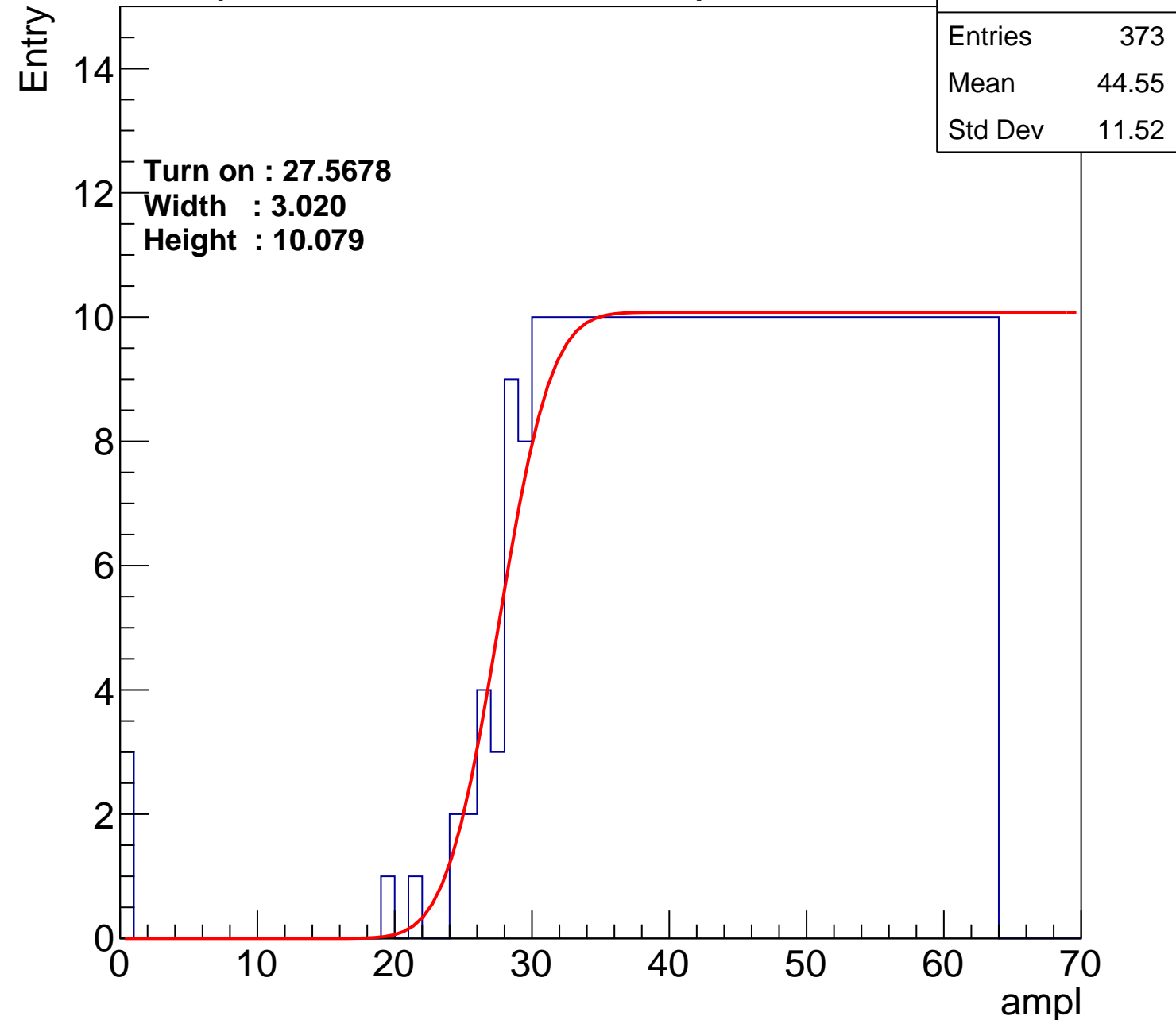
Width : 3.020

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch93

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.45
Std Dev	12.31

Turn on : 25.3361

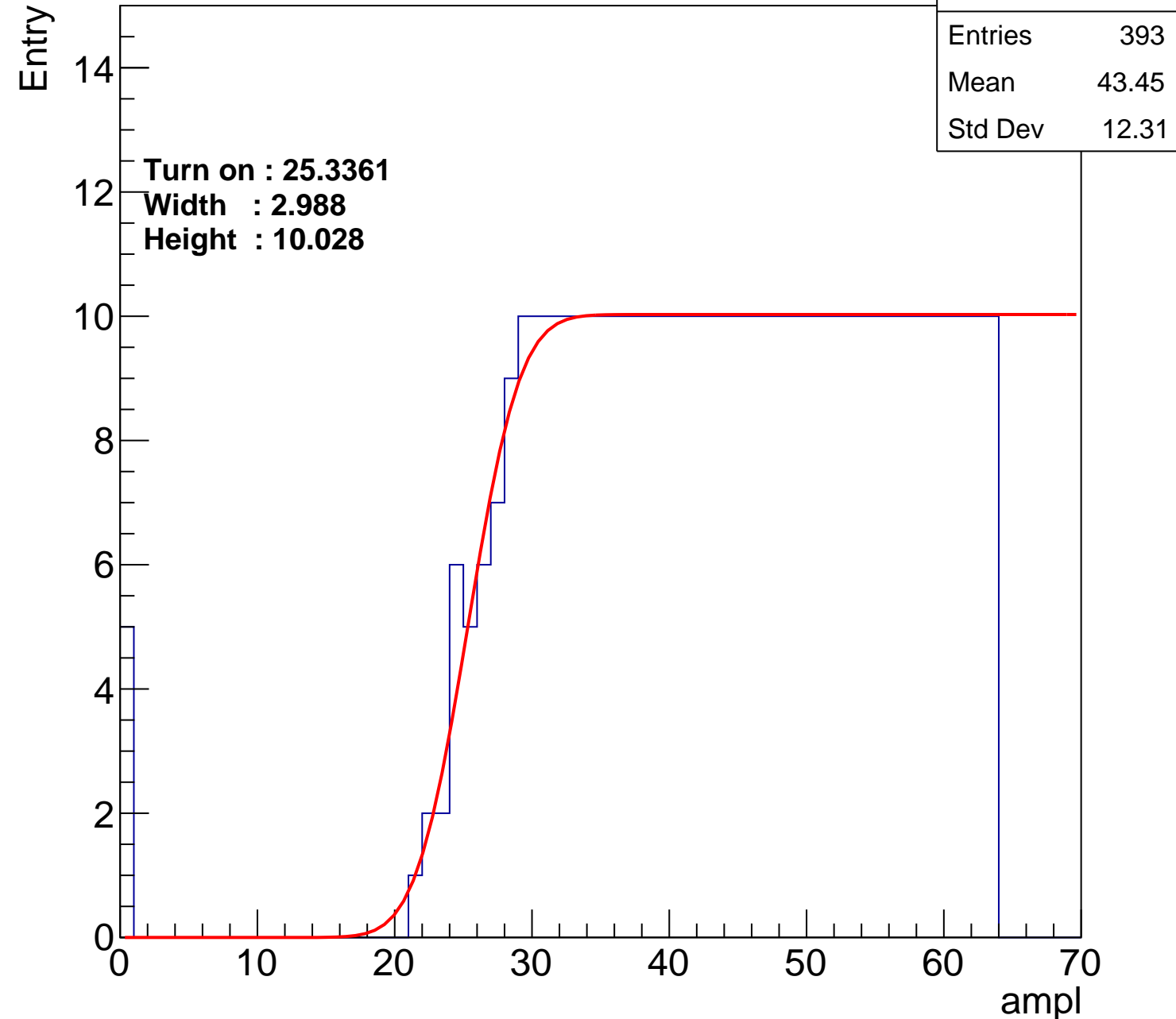
Width : 2.988

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch94

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.8037

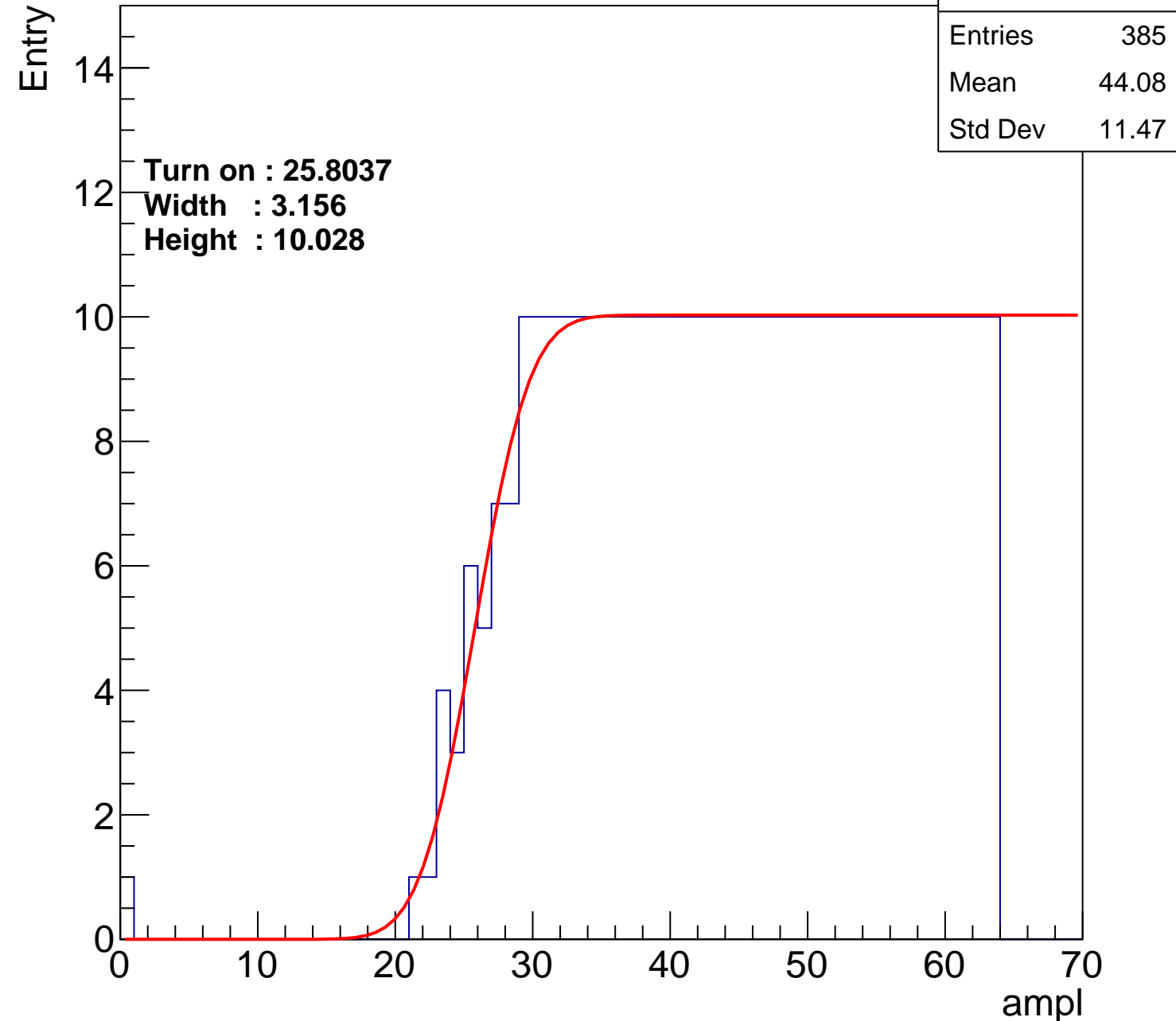
Width : 3.156

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch95

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.74
Std Dev	11.14

Turn on : 27.5012

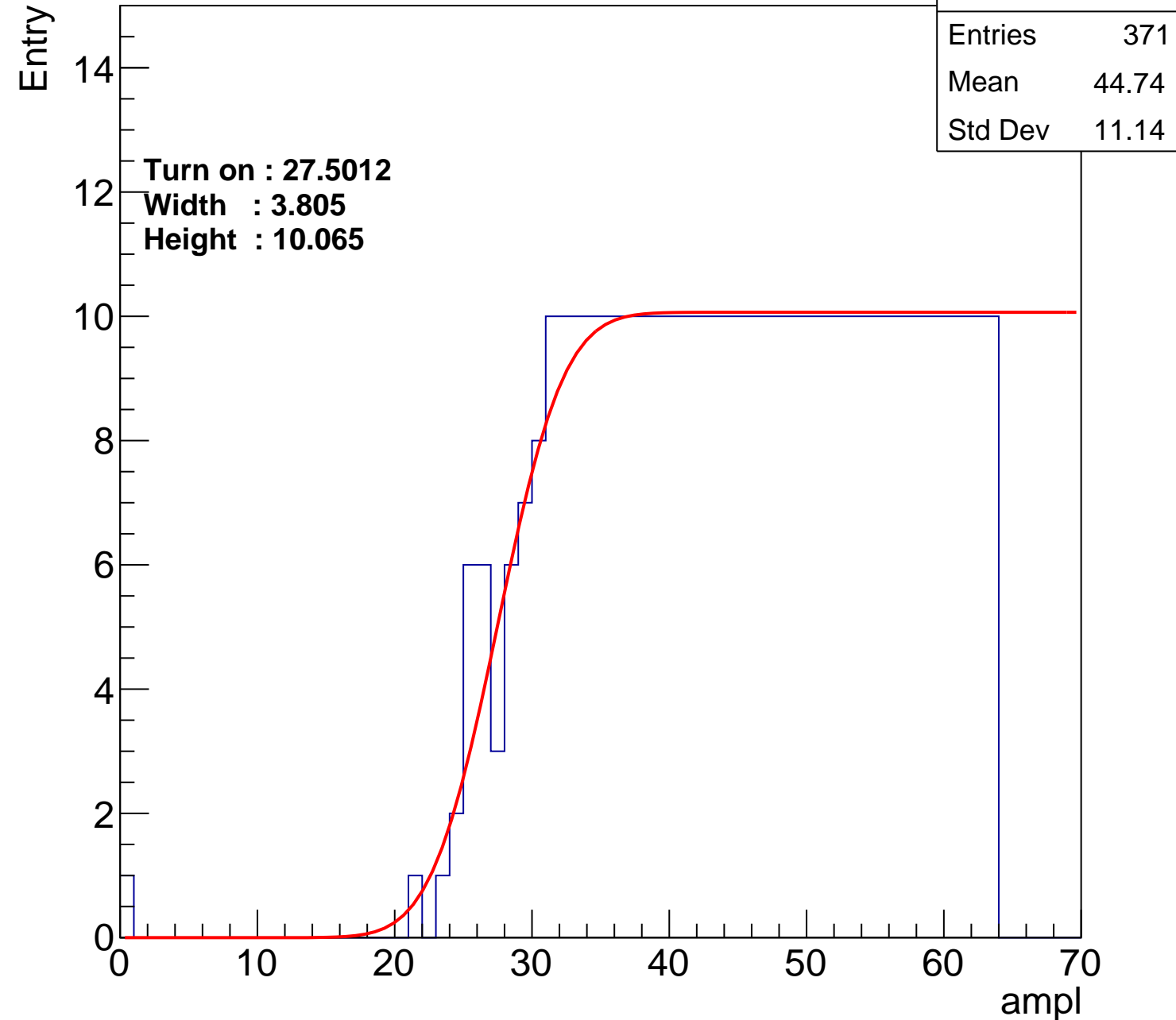
Width : 3.805

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 27.2891  
Width : 3.352  
Height : 10.047



# B0L000S, U17-ch97

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	398
Mean	43.41
Std Dev	11.92

Turn on : 25.2149

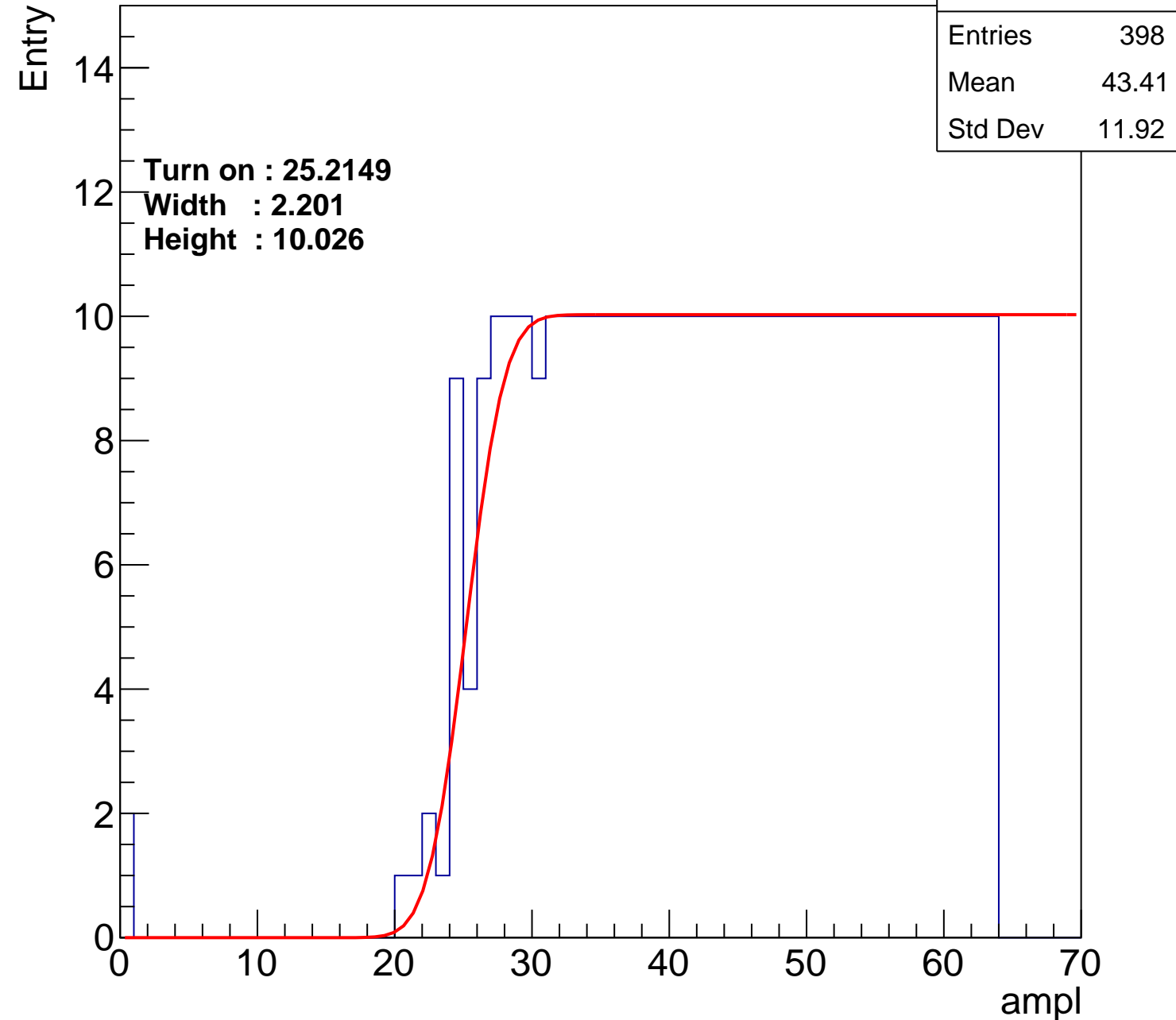
Width : 2.201

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch98

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.42
Std Dev	11.43

**Turn on : 26.6305**

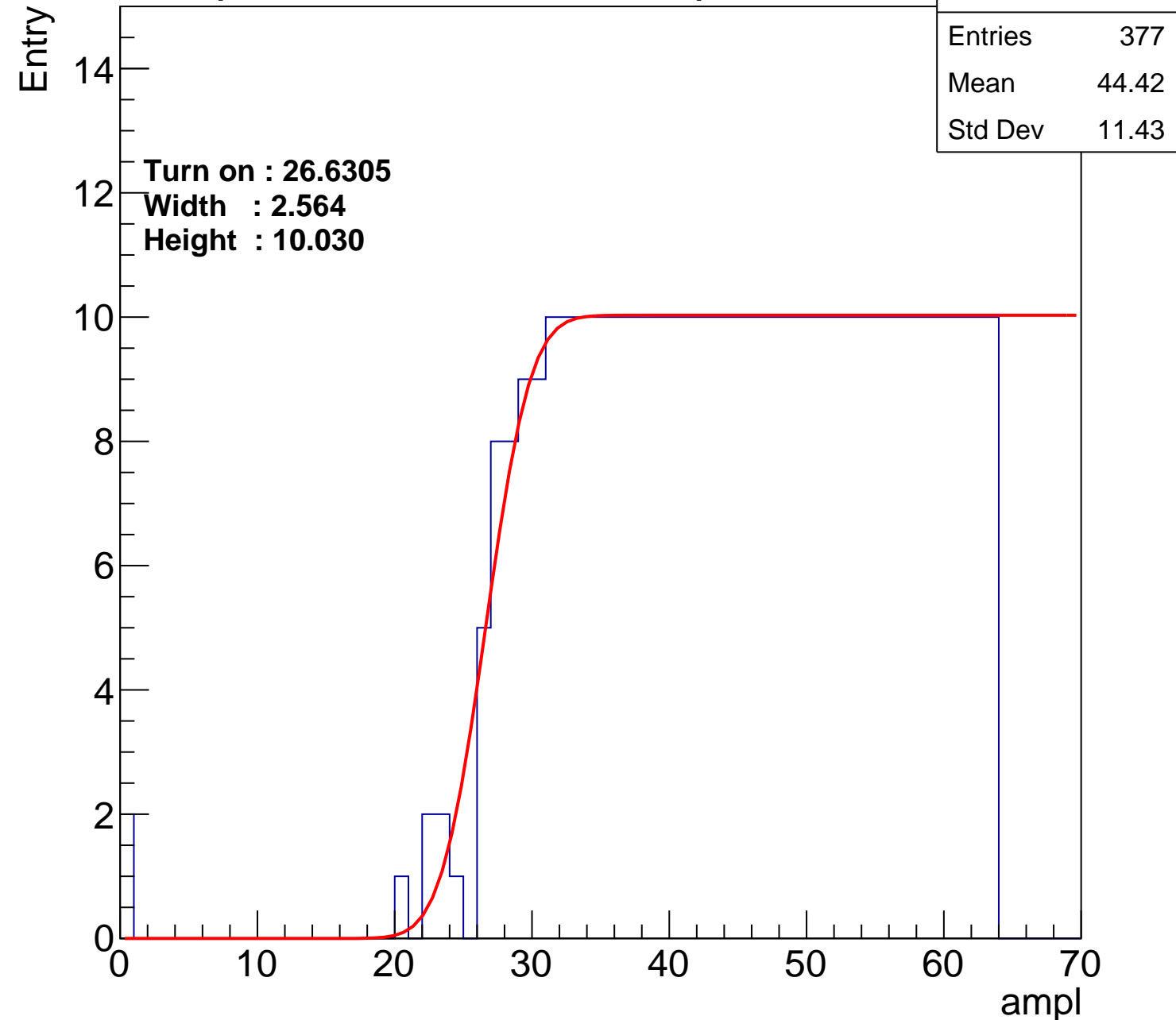
**Width : 2.564**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch99

calib\_packv5\_042523\_0143.root, FC#5, port B1

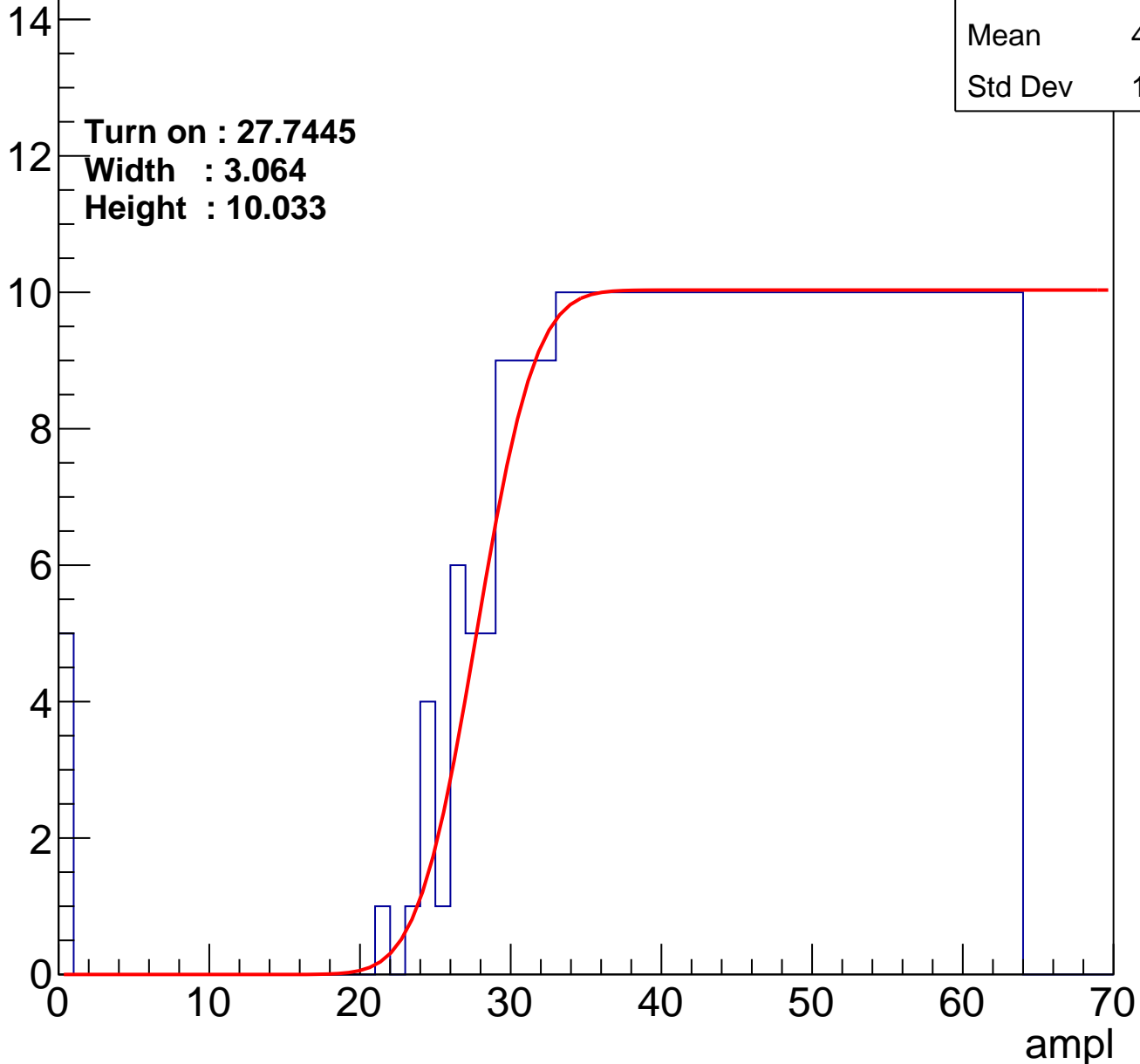
Entries	374
Mean	44.32
Std Dev	11.97

Turn on : 27.7445

Width : 3.064

Height : 10.033

Entry



# B0L000S, U17-ch100

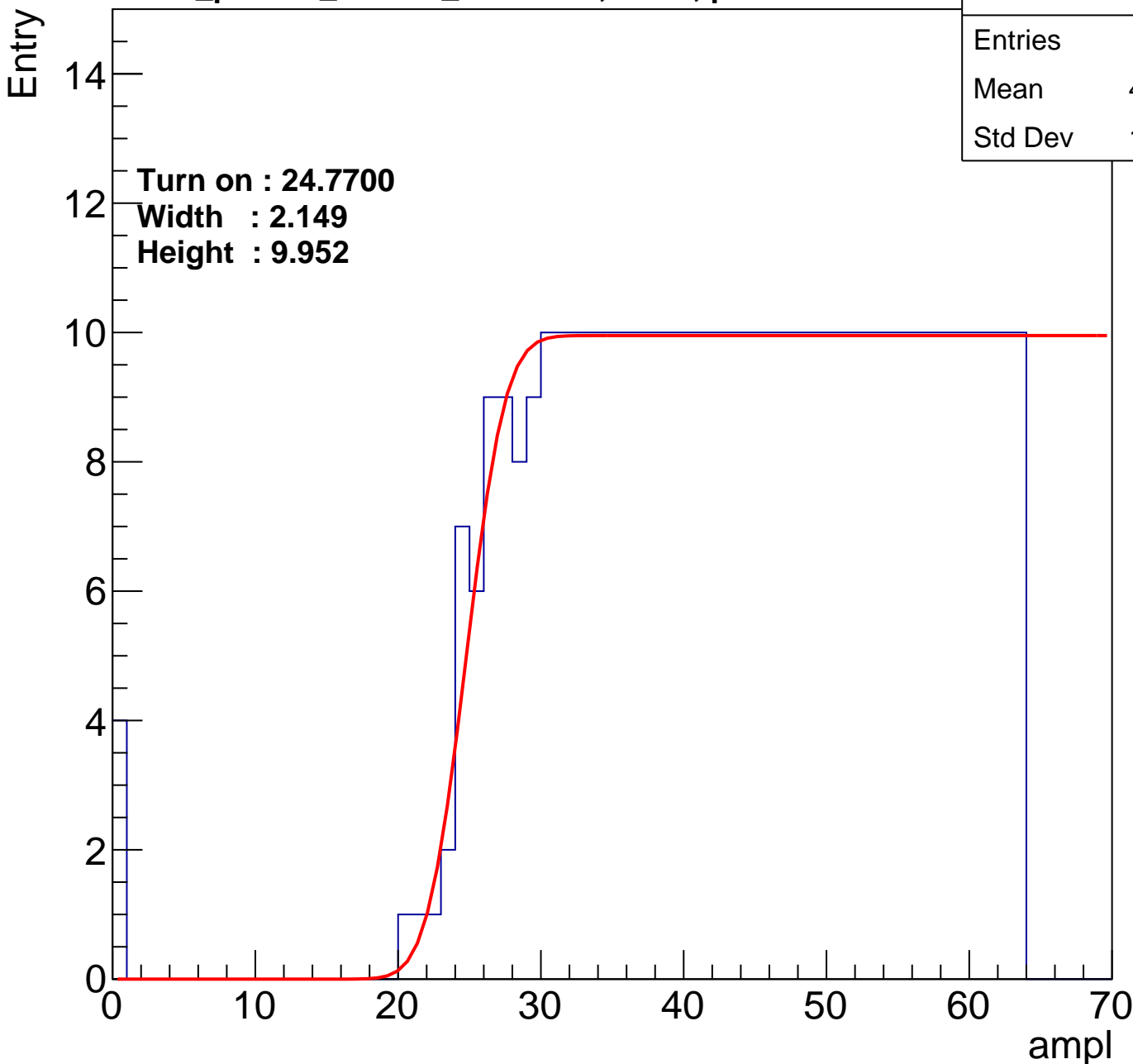
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	397
Mean	43.32
Std Dev	12.24

**Turn on : 24.7700**

**Width : 2.149**

**Height : 9.952**



# B0L000S, U17-ch101

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	398
Mean	43.25
Std Dev	12.3

Turn on : 24.7154

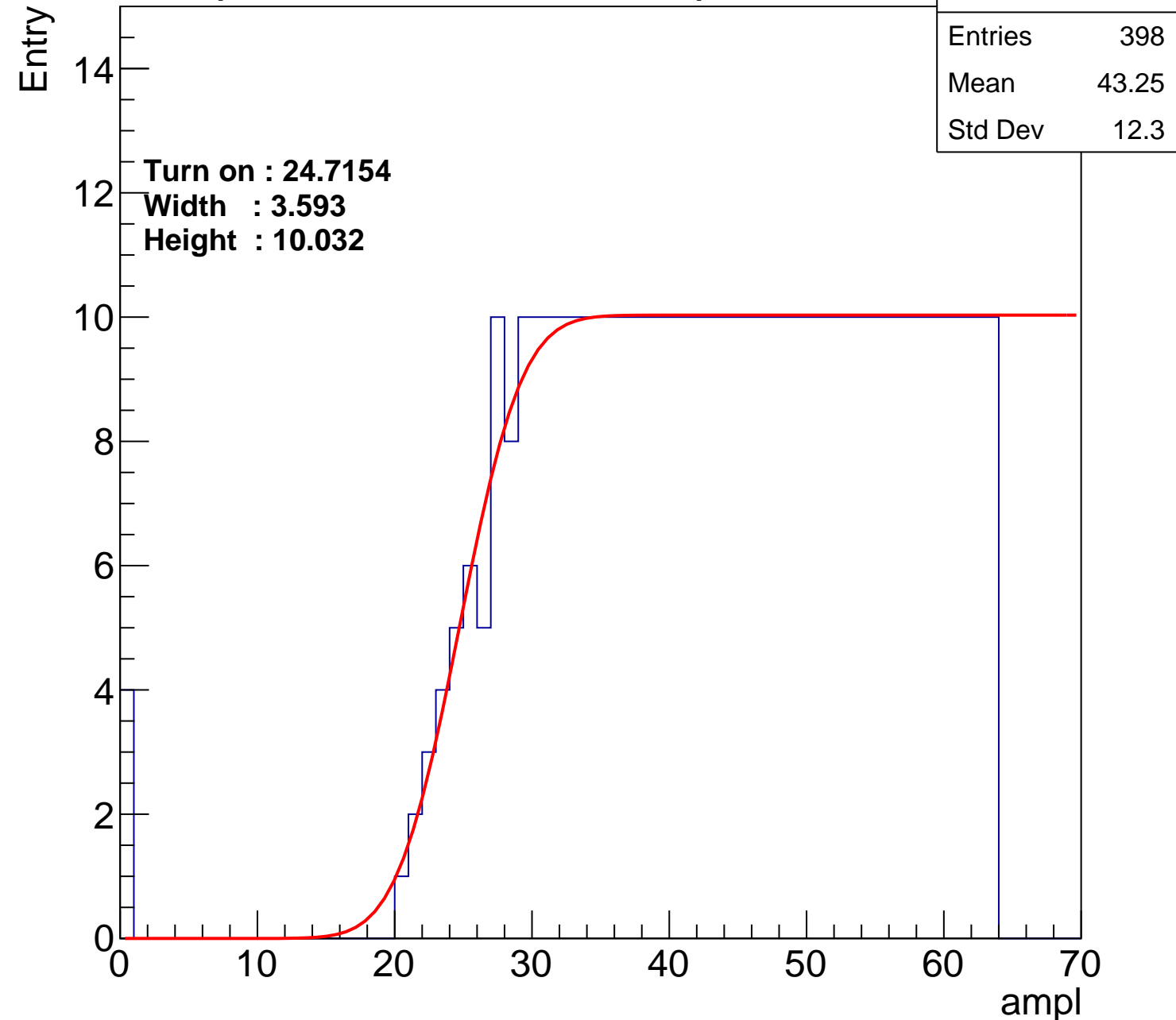
Width : 3.593

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch102

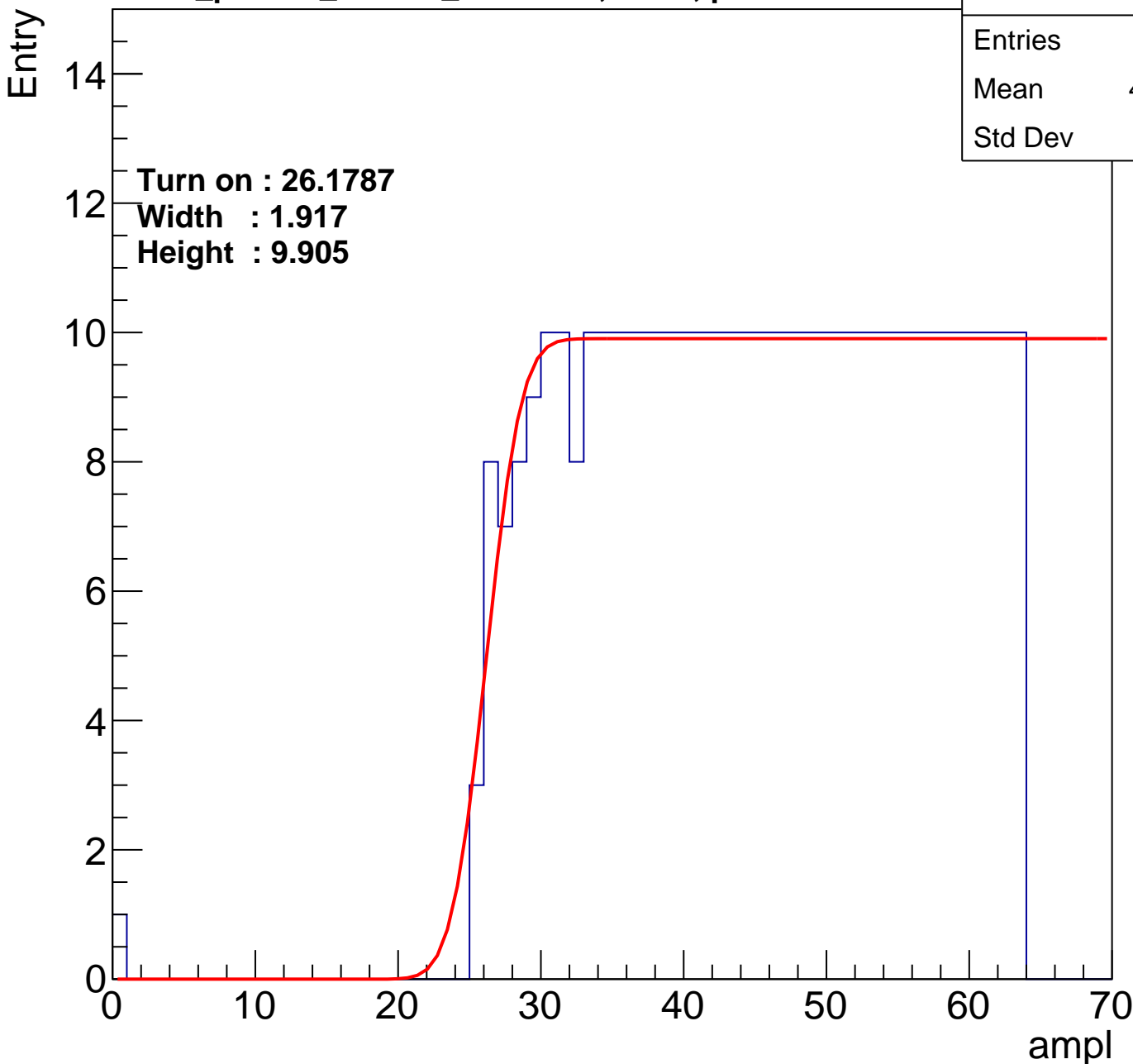
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	374
Mean	44.66
Std Dev	11.1

**Turn on : 26.1787**

**Width : 1.917**

**Height : 9.905**





# B0L000S, U17-ch103

calib\_packv5\_042523\_0143.root, FC#5, port B1

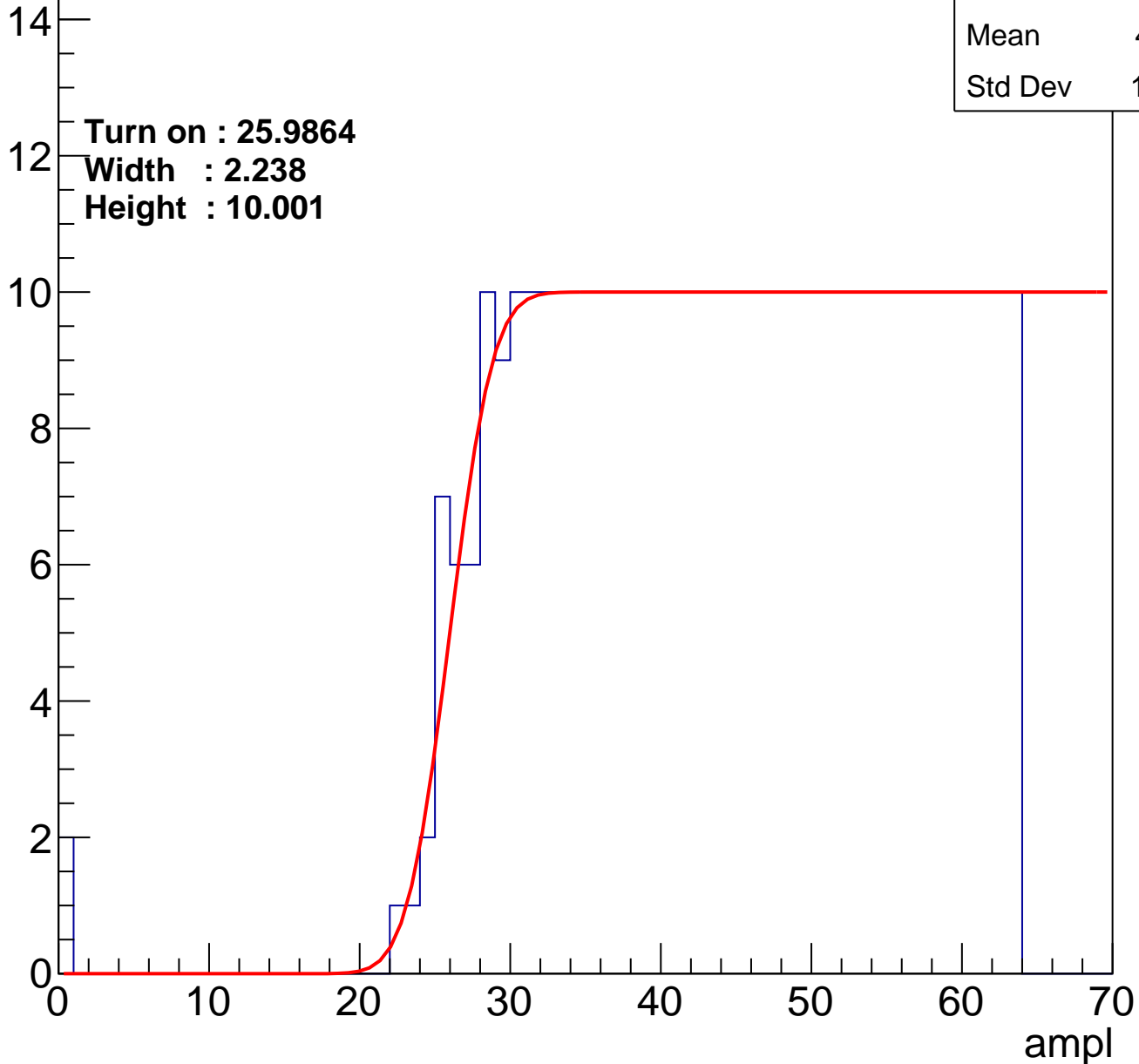
Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 25.9864

Width : 2.238

Height : 10.001

Entry



# B0L000S, U17-ch104

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.02
Std Dev	11.69

**Turn on : 26.0329**

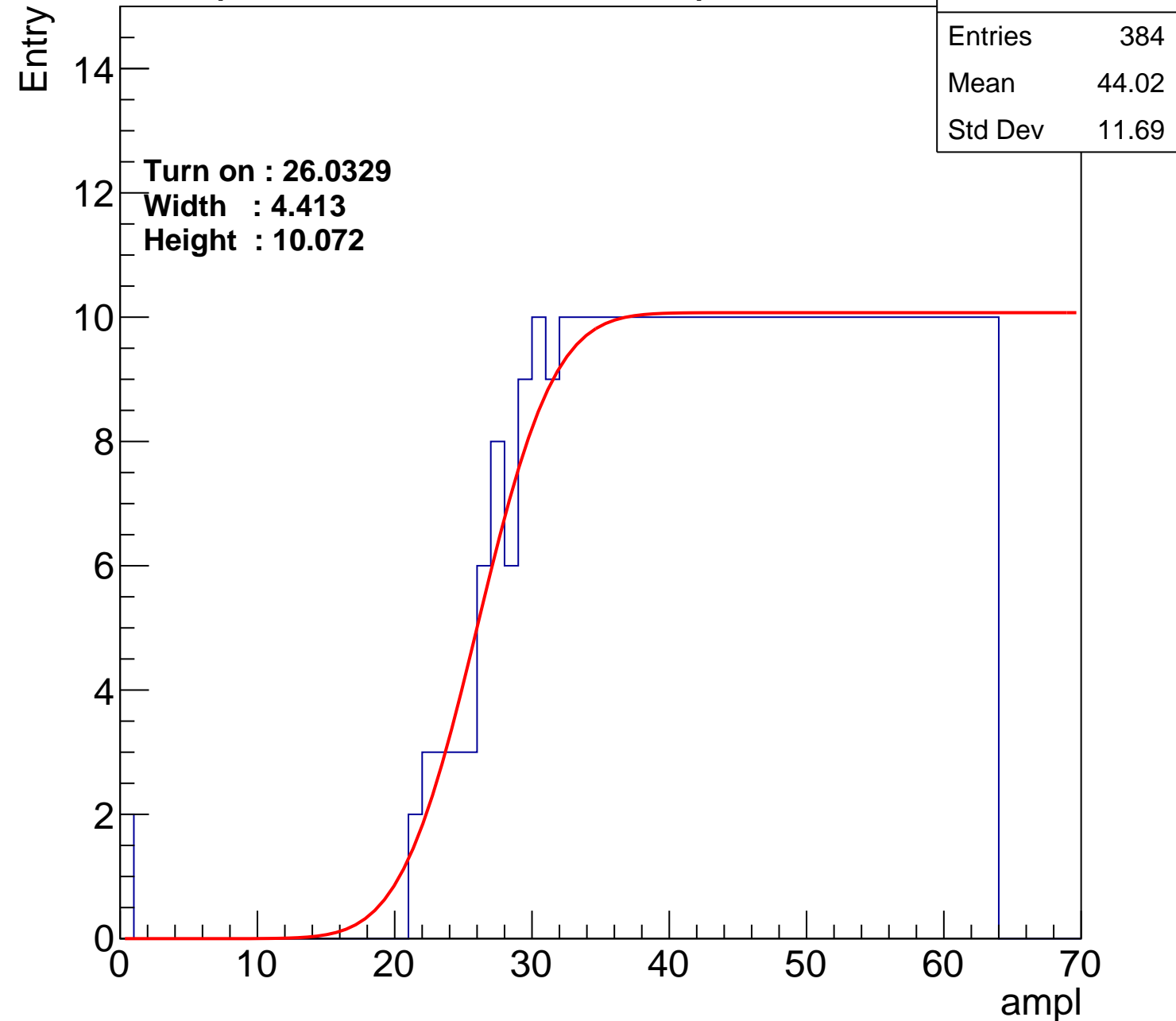
**Width : 4.413**

**Height : 10.072**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch105

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.65
Std Dev	11.34

Turn on : 27.7117

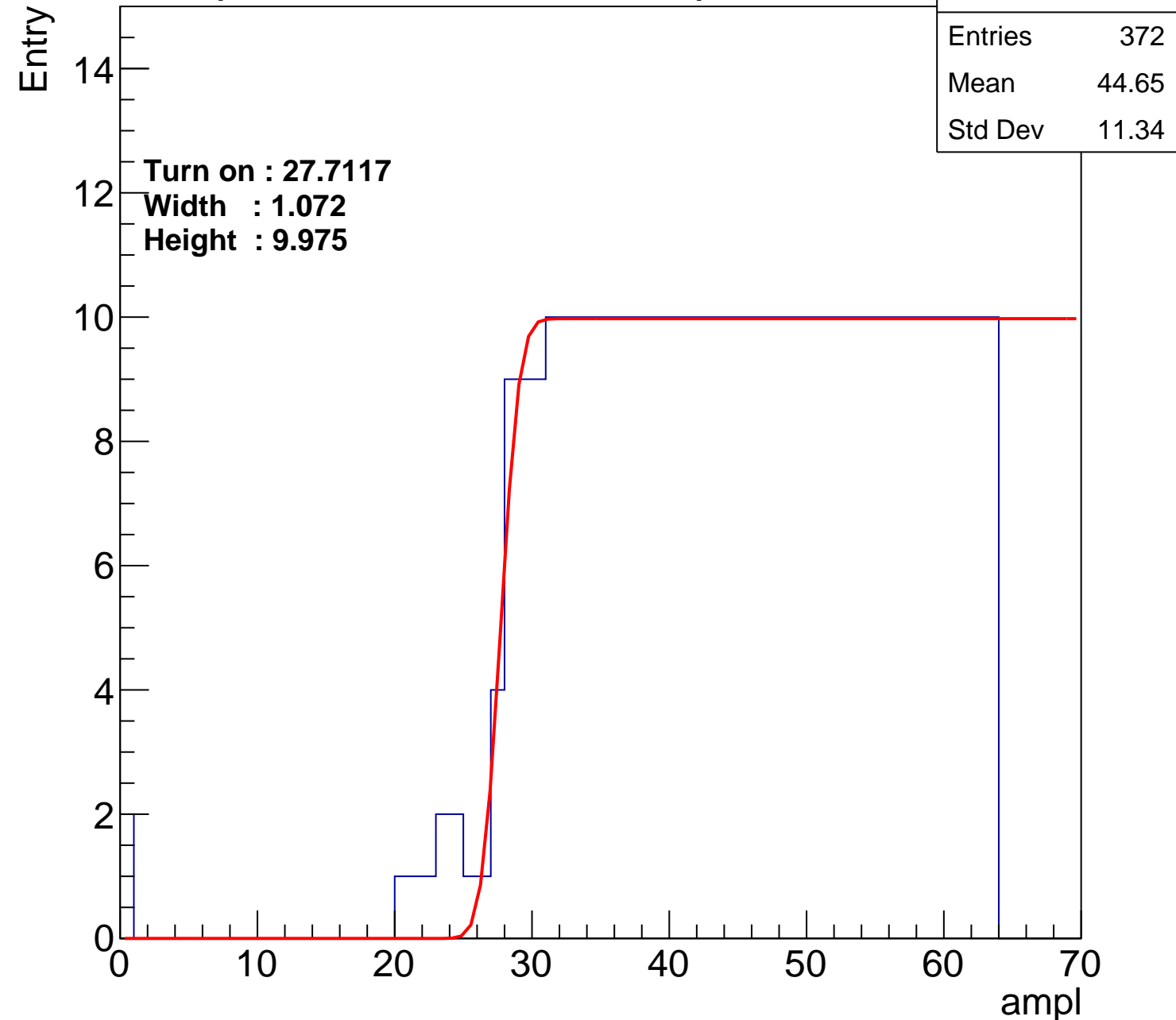
Width : 1.072

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch106

calib\_packv5\_042523\_0143.root, FC#5, port B1

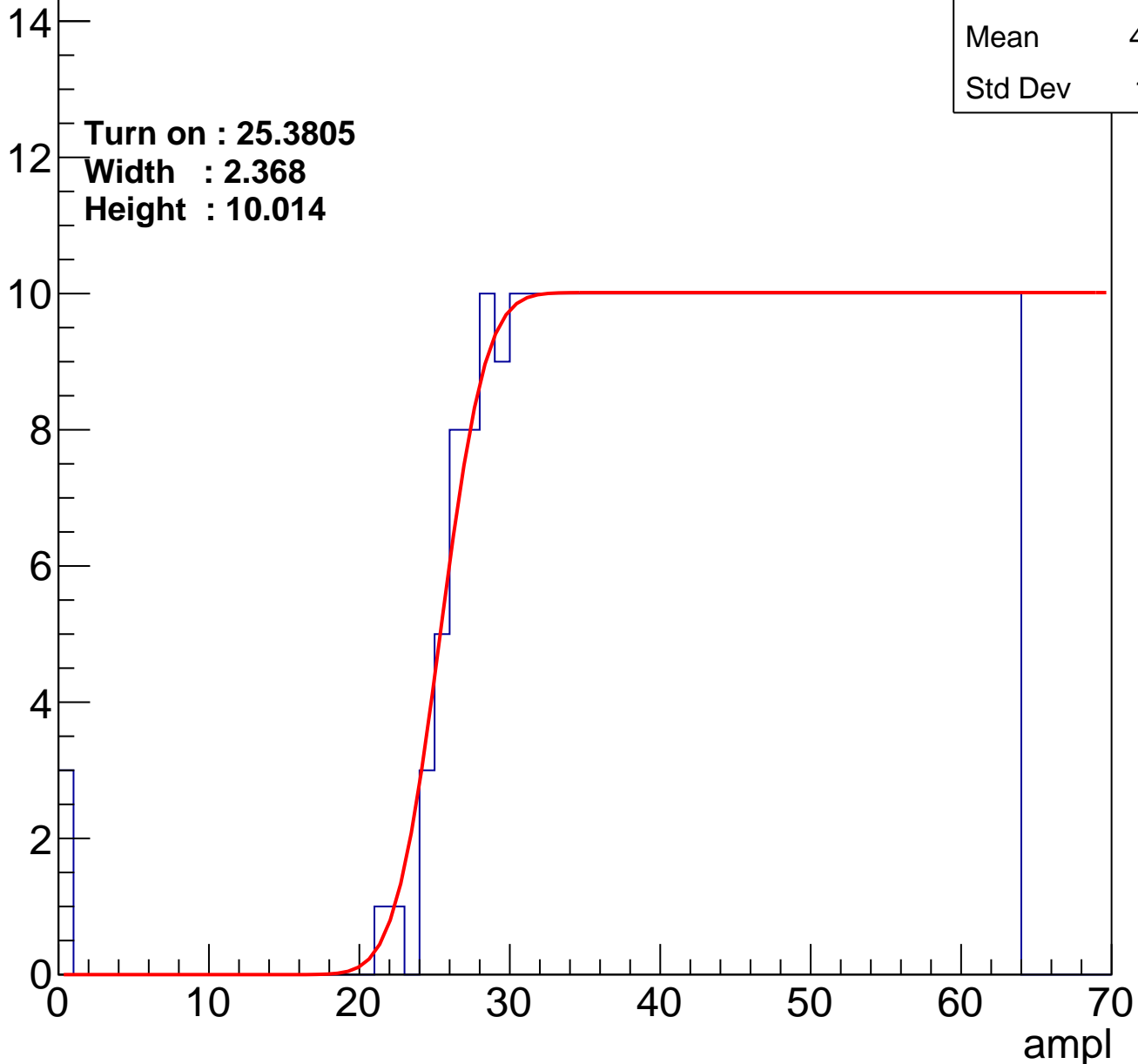
Entries	388
Mean	43.85
Std Dev	11.81

Turn on : 25.3805

Width : 2.368

Height : 10.014

Entry



# B0L000S, U17-ch107

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.74
Std Dev	11.76

**Turn on : 25.6229**

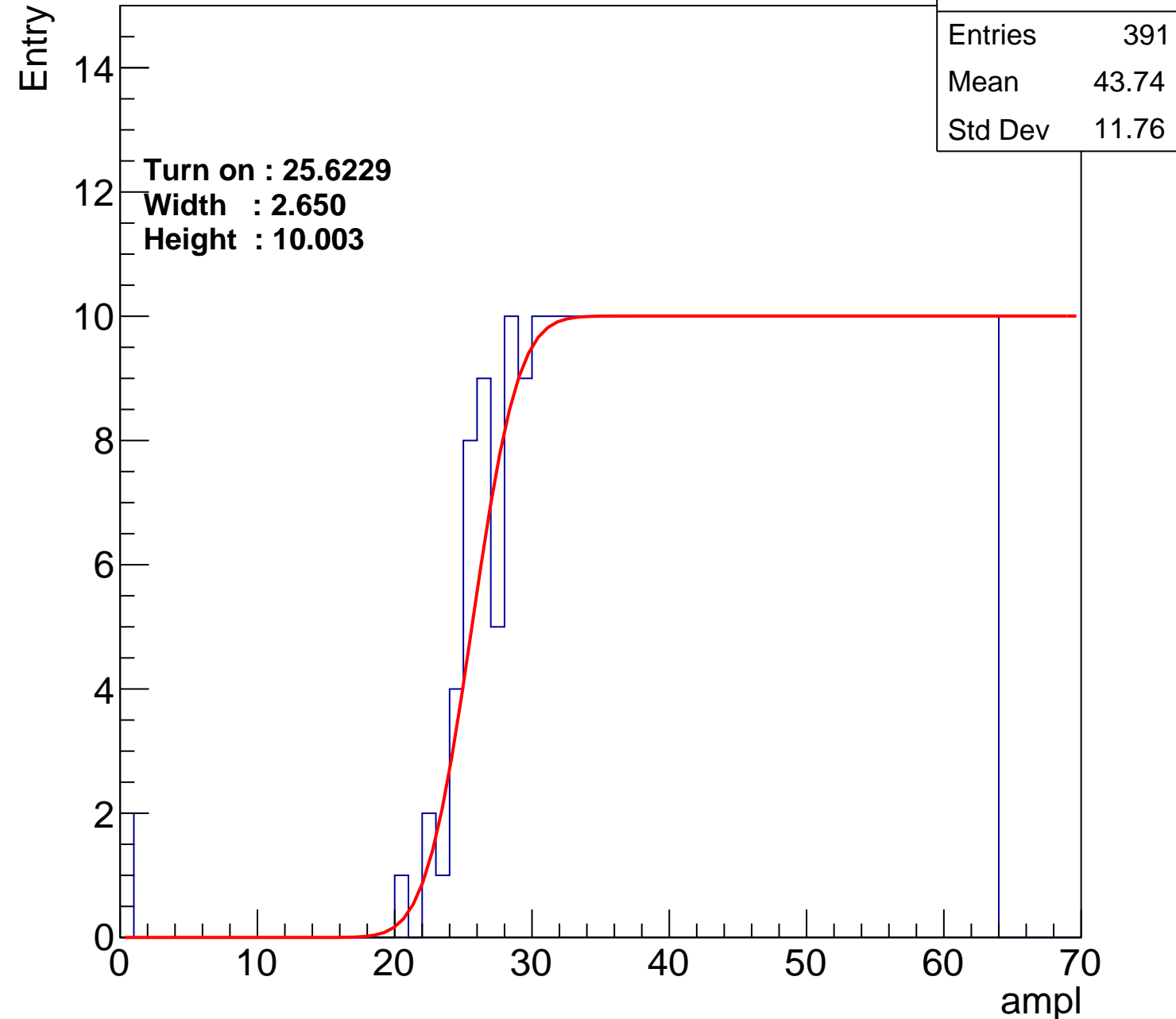
**Width : 2.650**

**Height : 10.003**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch108

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	378
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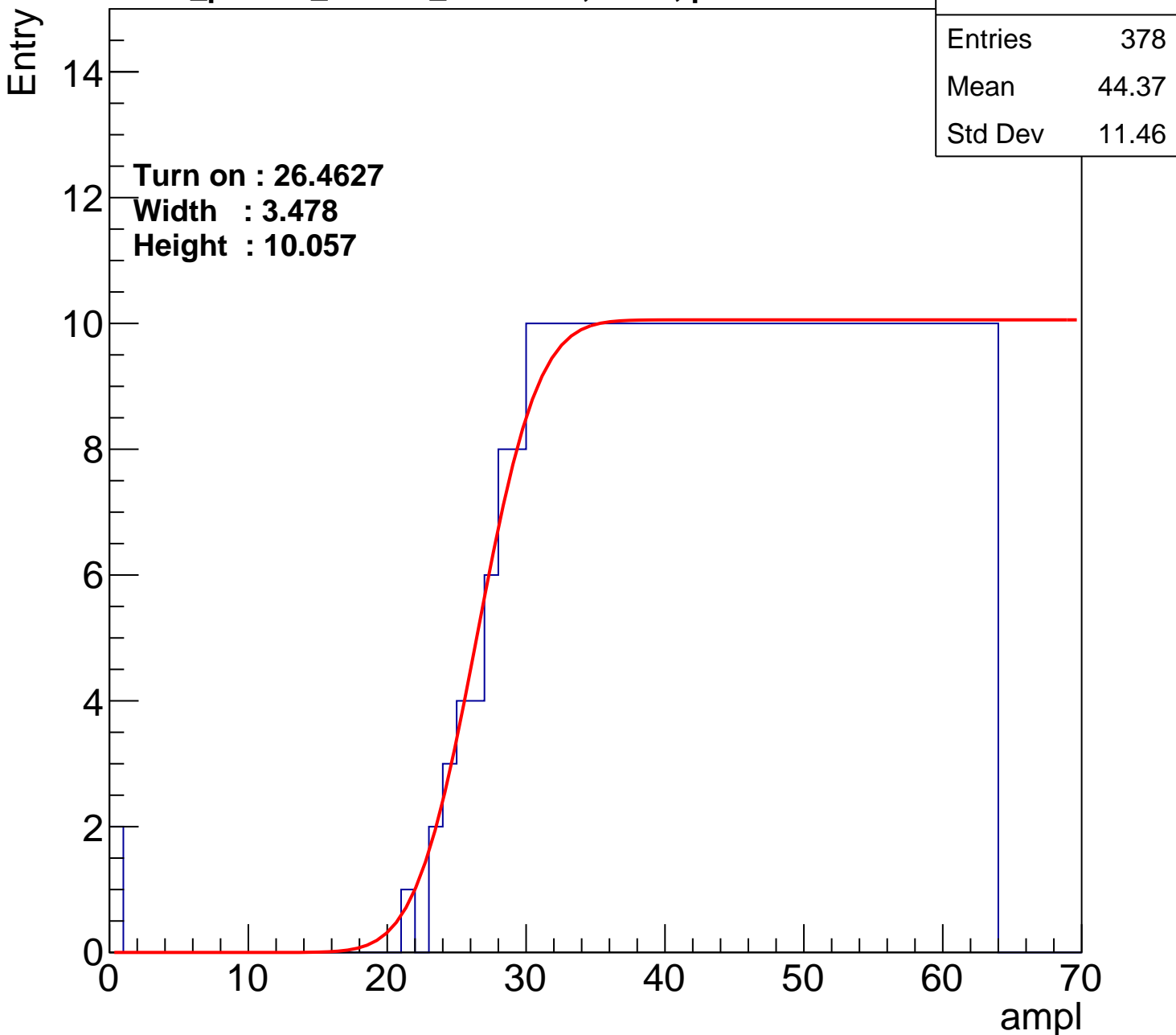
Mean	44.37
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Std Dev	11.46
---------	-------

**Turn on : 26.4627**

**Width : 3.478**

**Height : 10.057**



# B0L000S, U17-ch109

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	363
Mean	44.99
Std Dev	11.34

Turn on : 28.2766

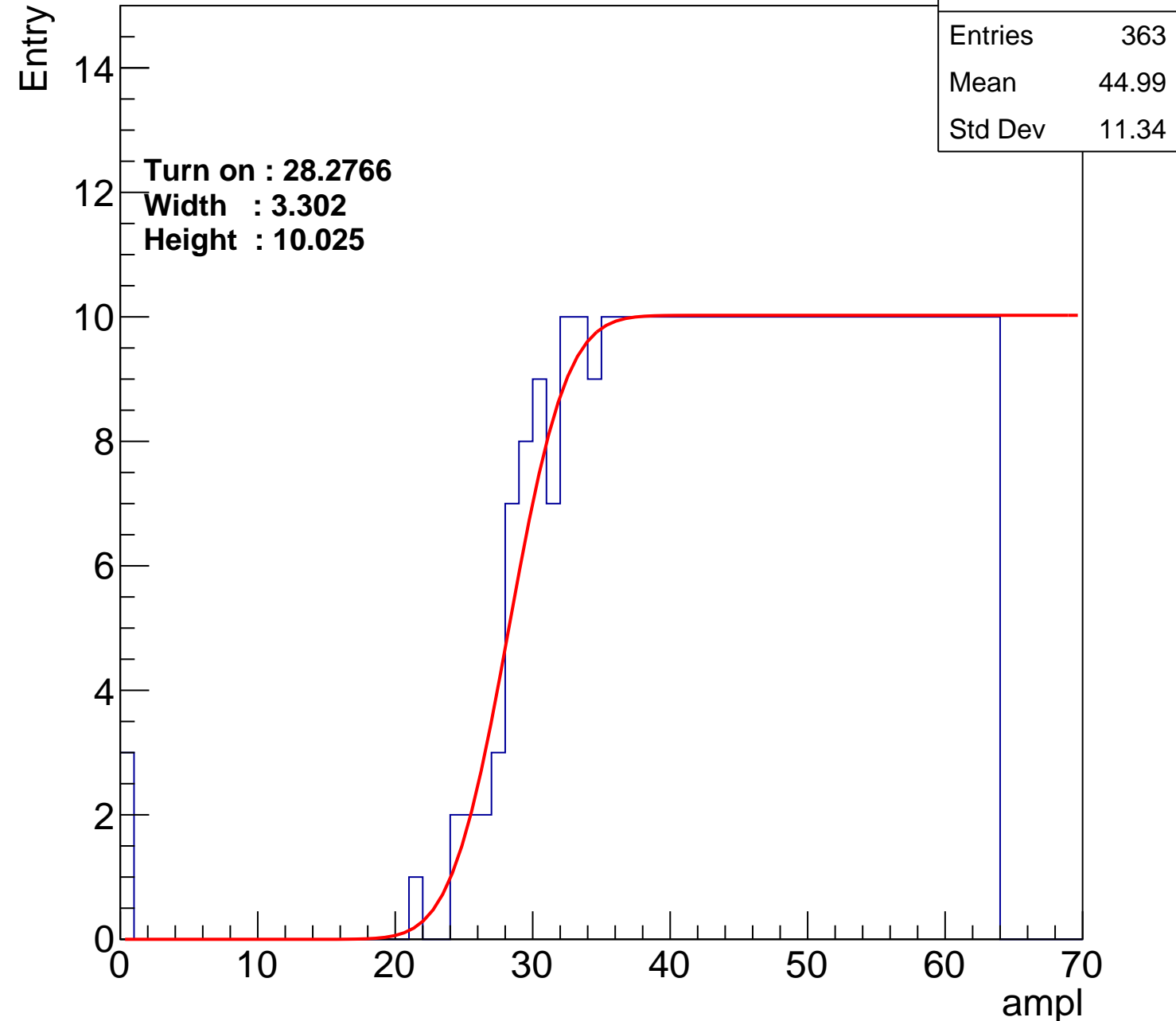
Width : 3.302

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch110

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.19
Std Dev	12

**Turn on : 26.9485**

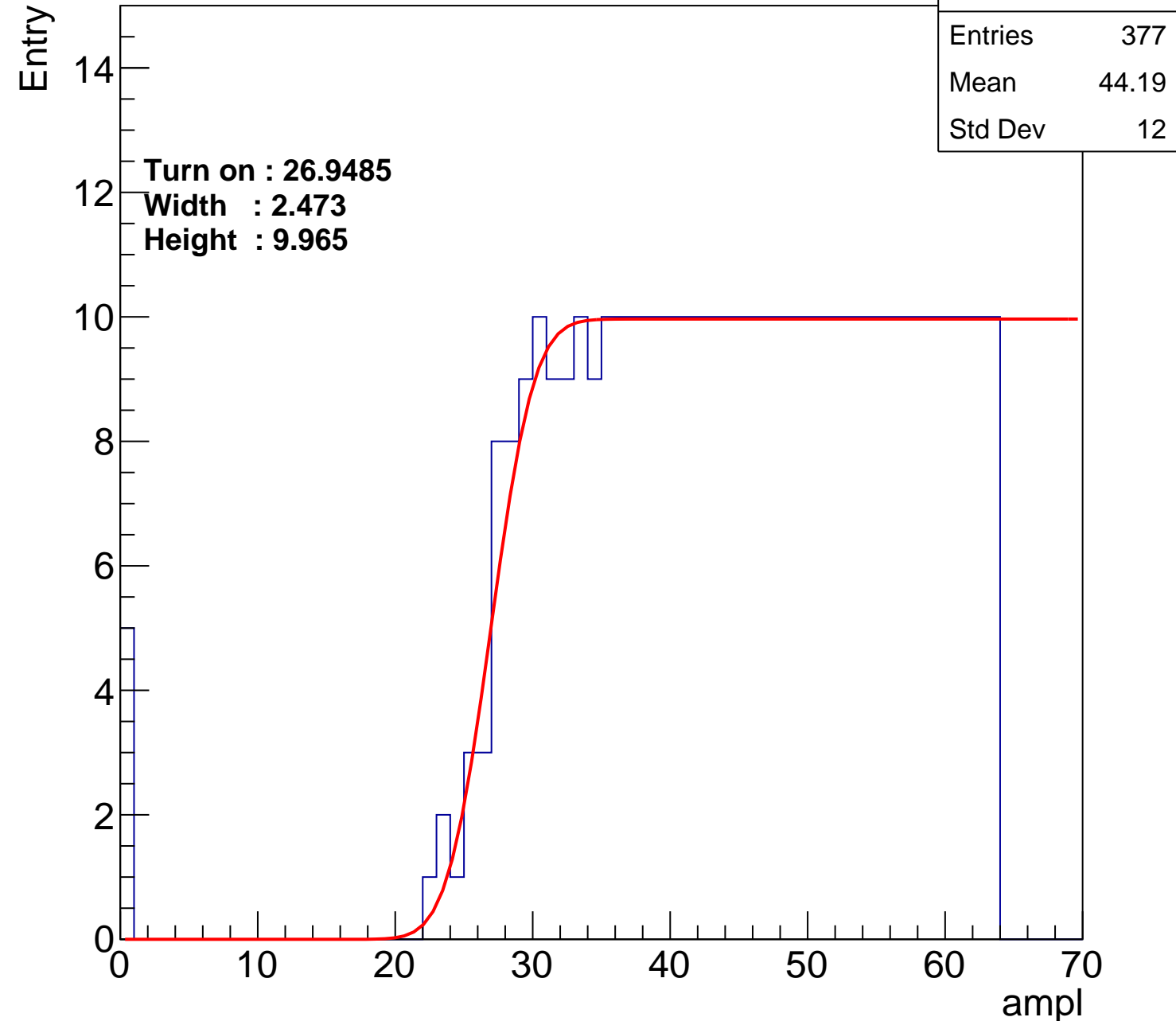
**Width : 2.473**

**Height : 9.965**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch111

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.68
Std Dev	11.86

Turn on : 25.0320

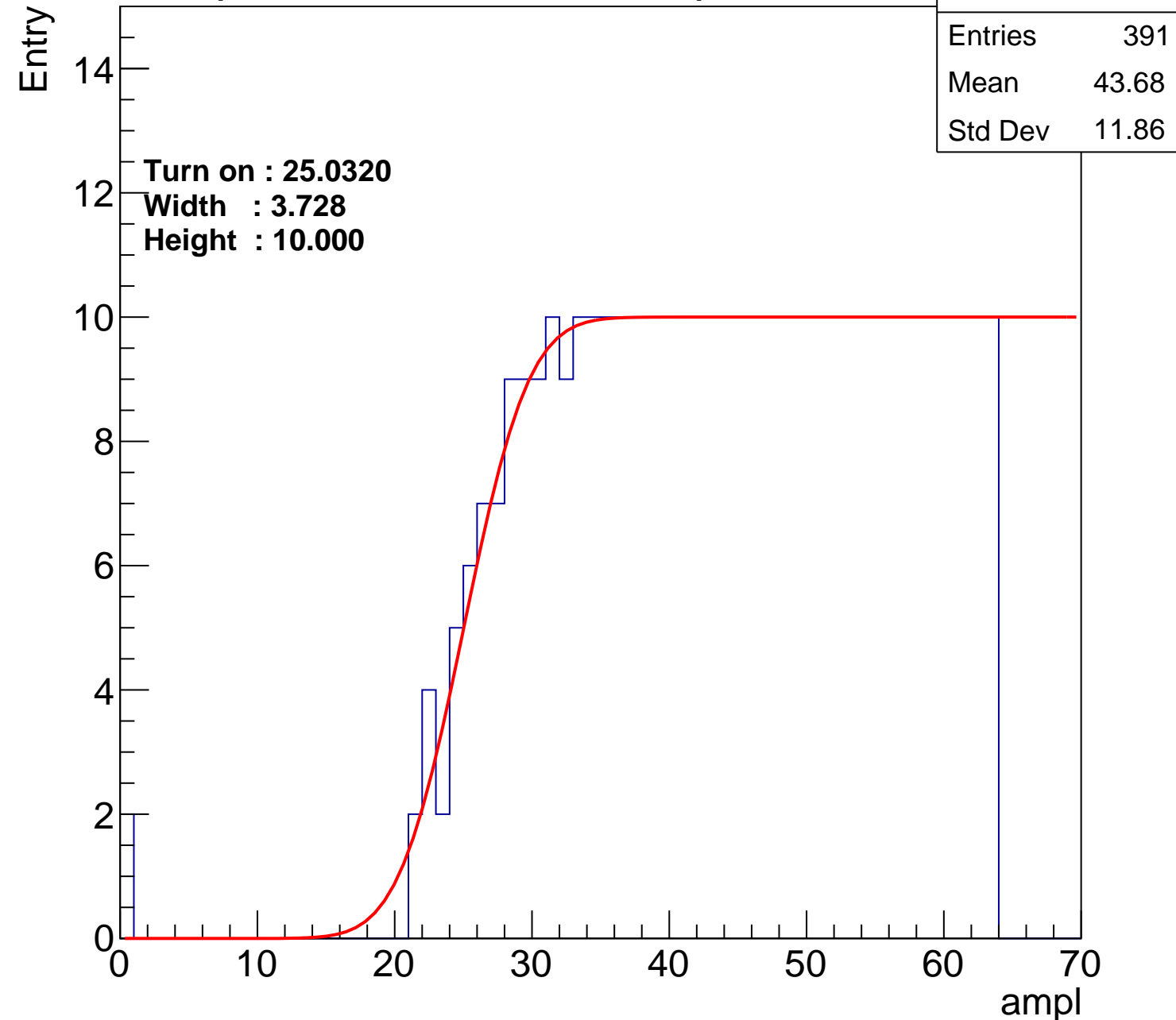
Width : 3.728

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch112

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.17
Std Dev	12.03

**Turn on : 26.5032**

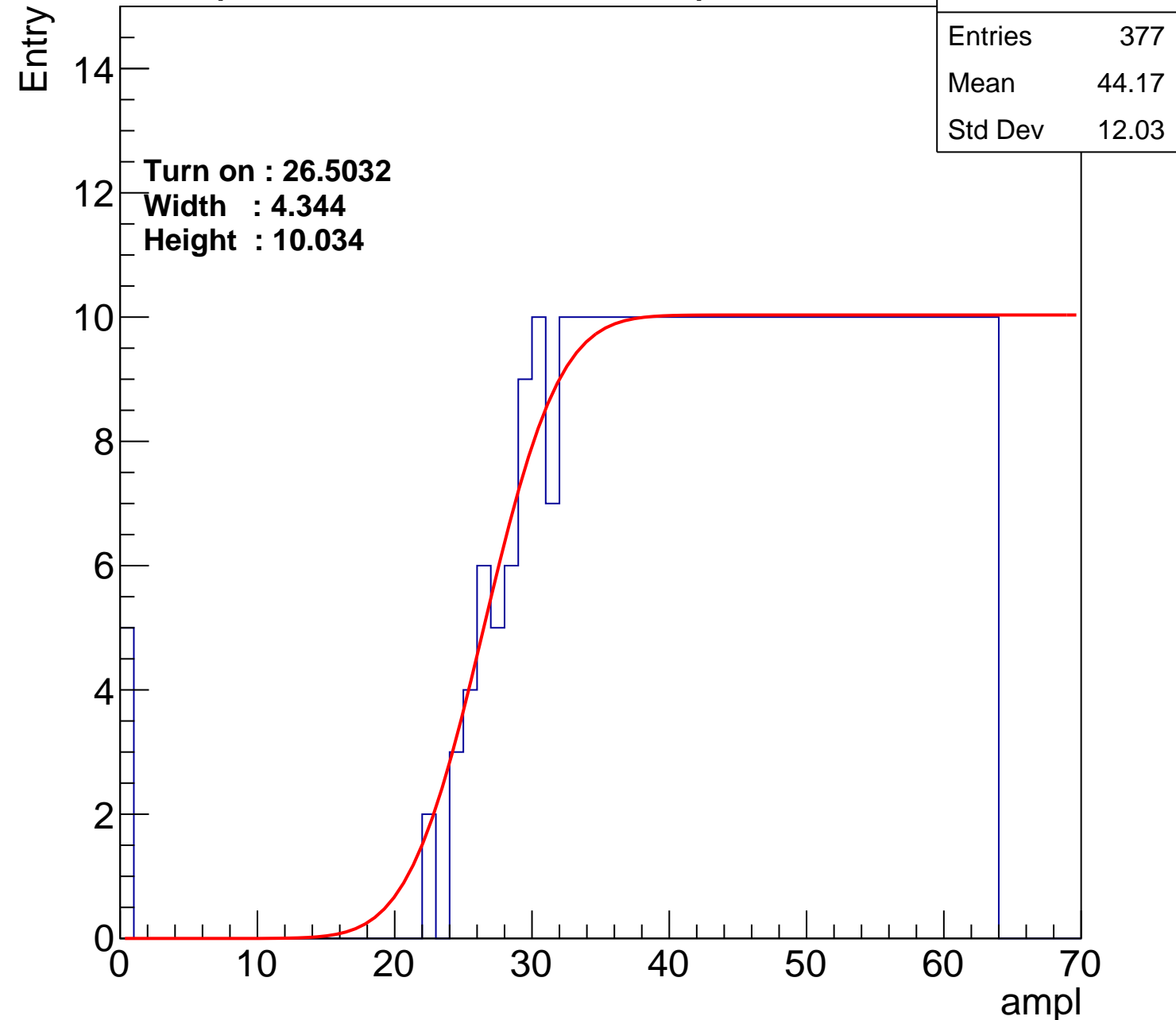
**Width : 4.344**

**Height : 10.034**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch113

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.63
Std Dev	11.73

Turn on : 25.0524

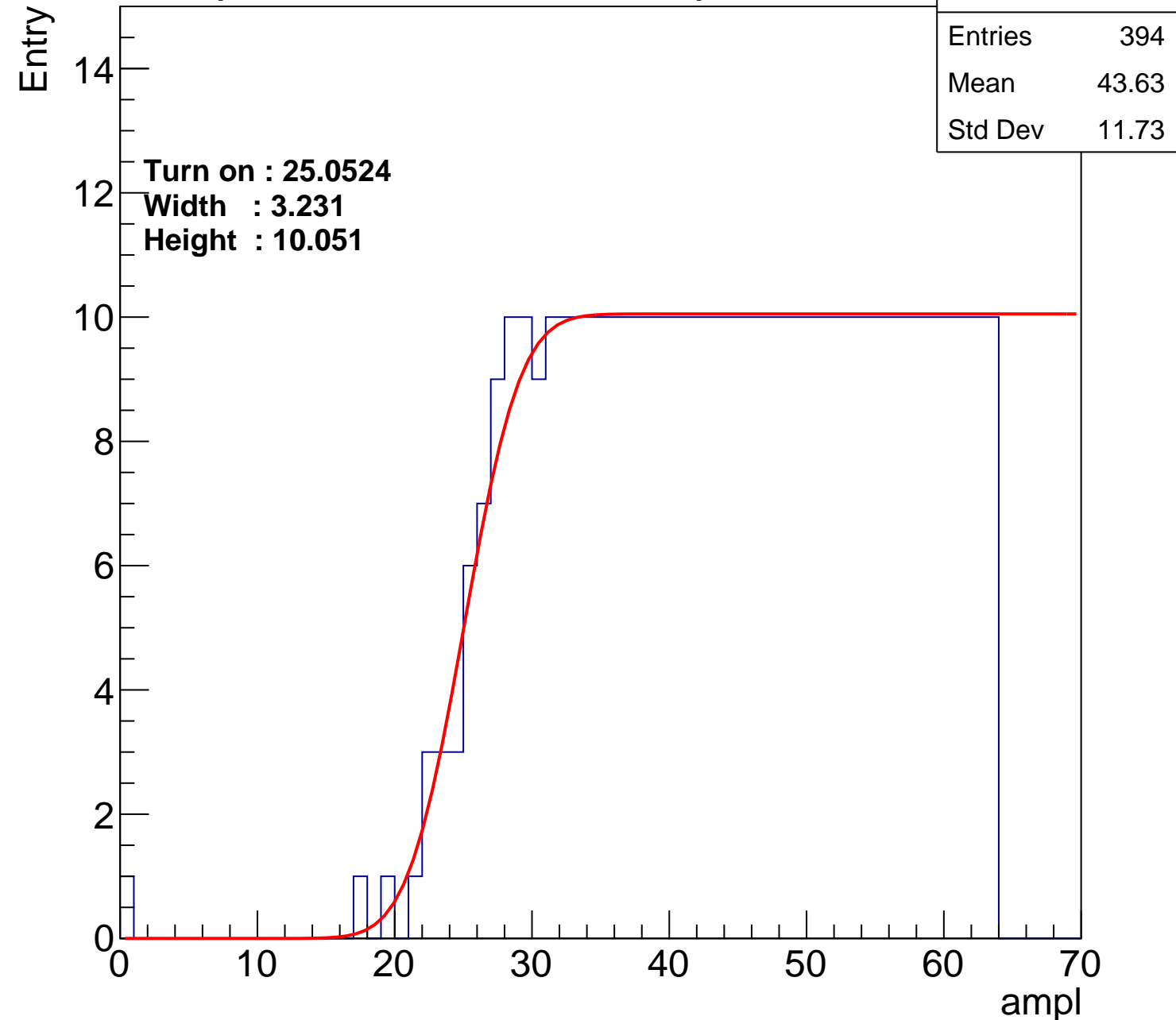
Width : 3.231

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch114

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.71
Std Dev	11.95

Turn on : 25.7036

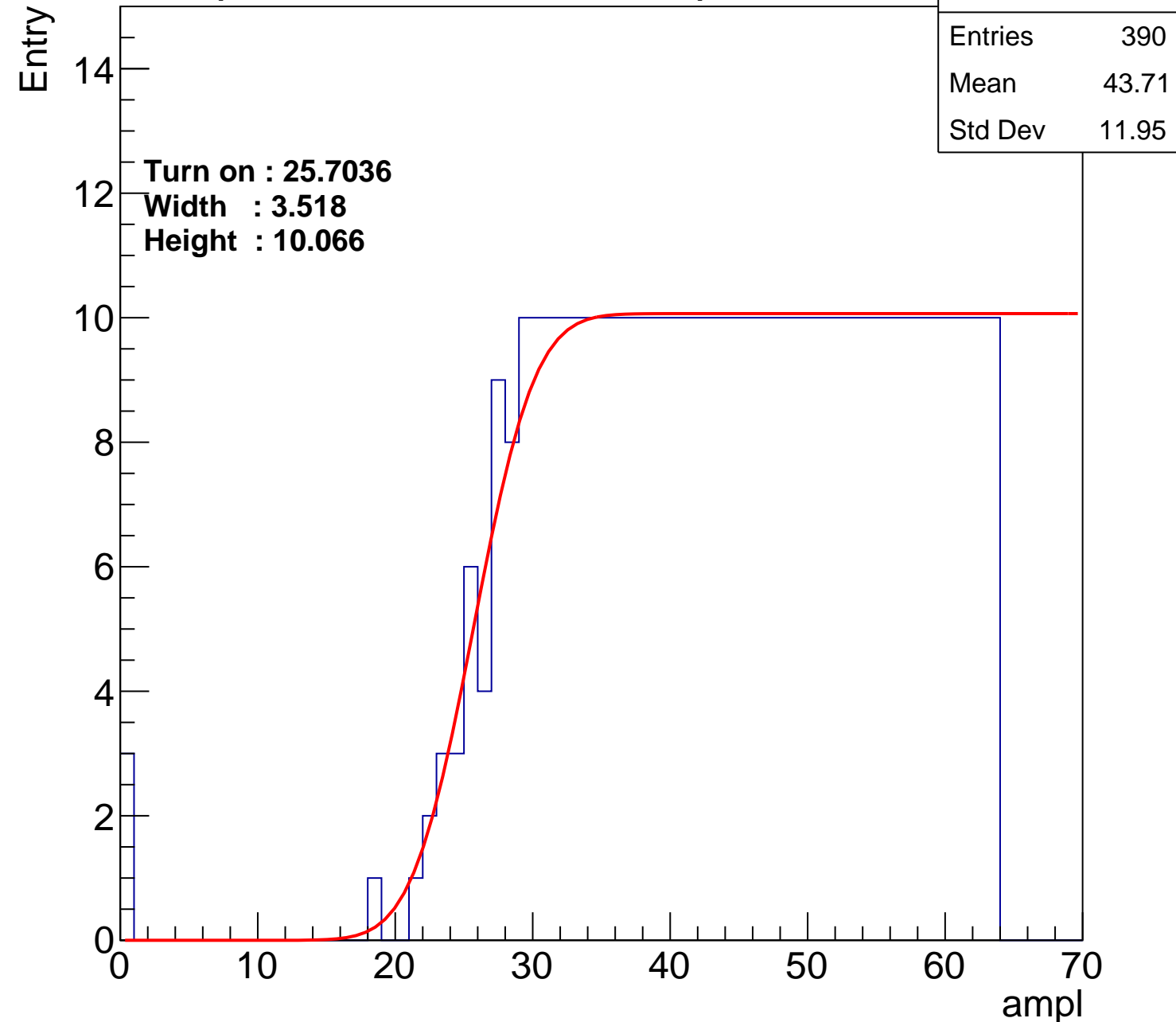
Width : 3.518

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch115

calib\_packv5\_042523\_0143.root, FC#5, port B1

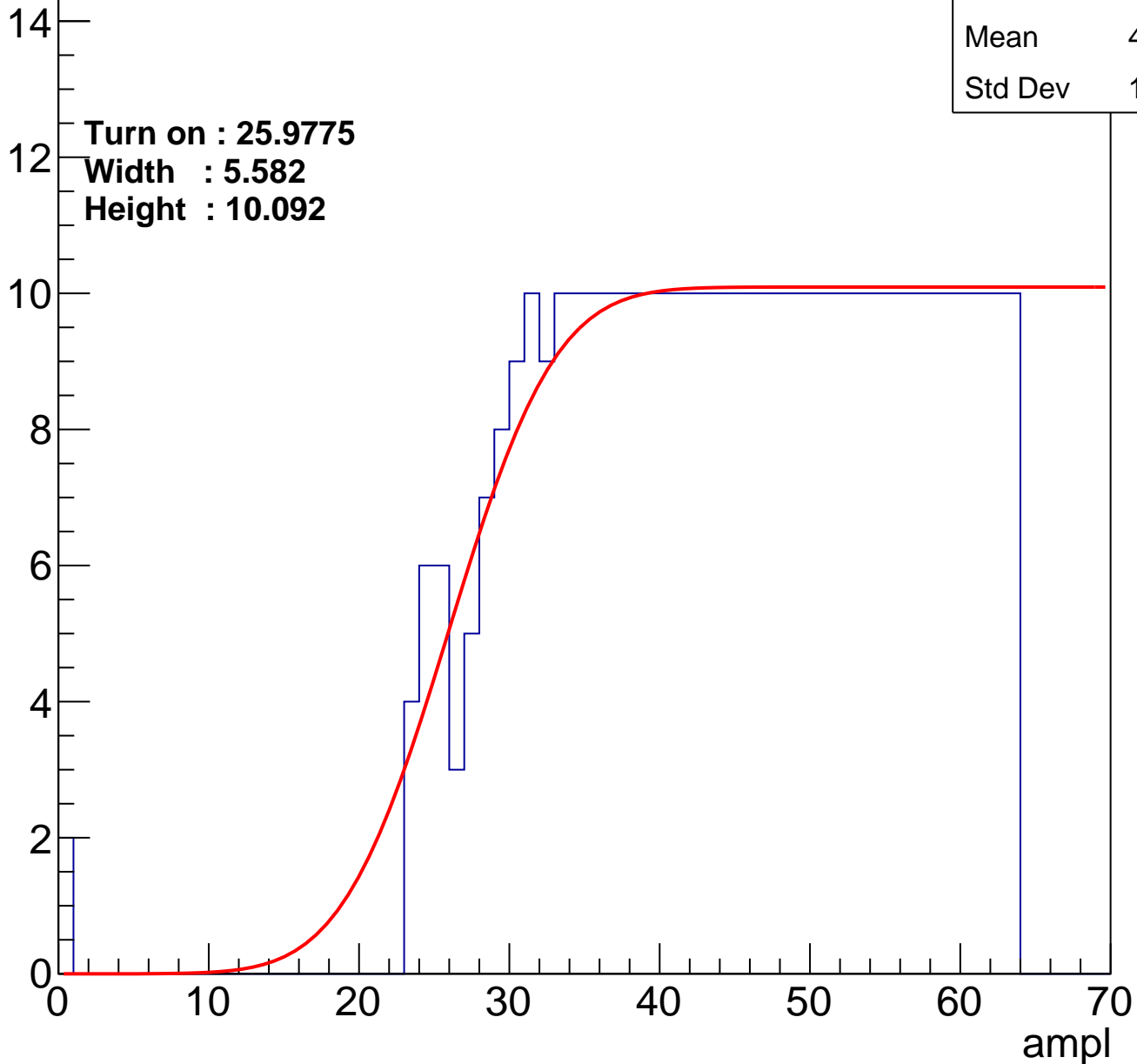
Entries	379
Mean	44.26
Std Dev	11.57

Turn on : 25.9775

Width : 5.582

Height : 10.092

Entry



# B0L000S, U17-ch116

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.73
Std Dev	11.78

**Turn on : 24.9895**

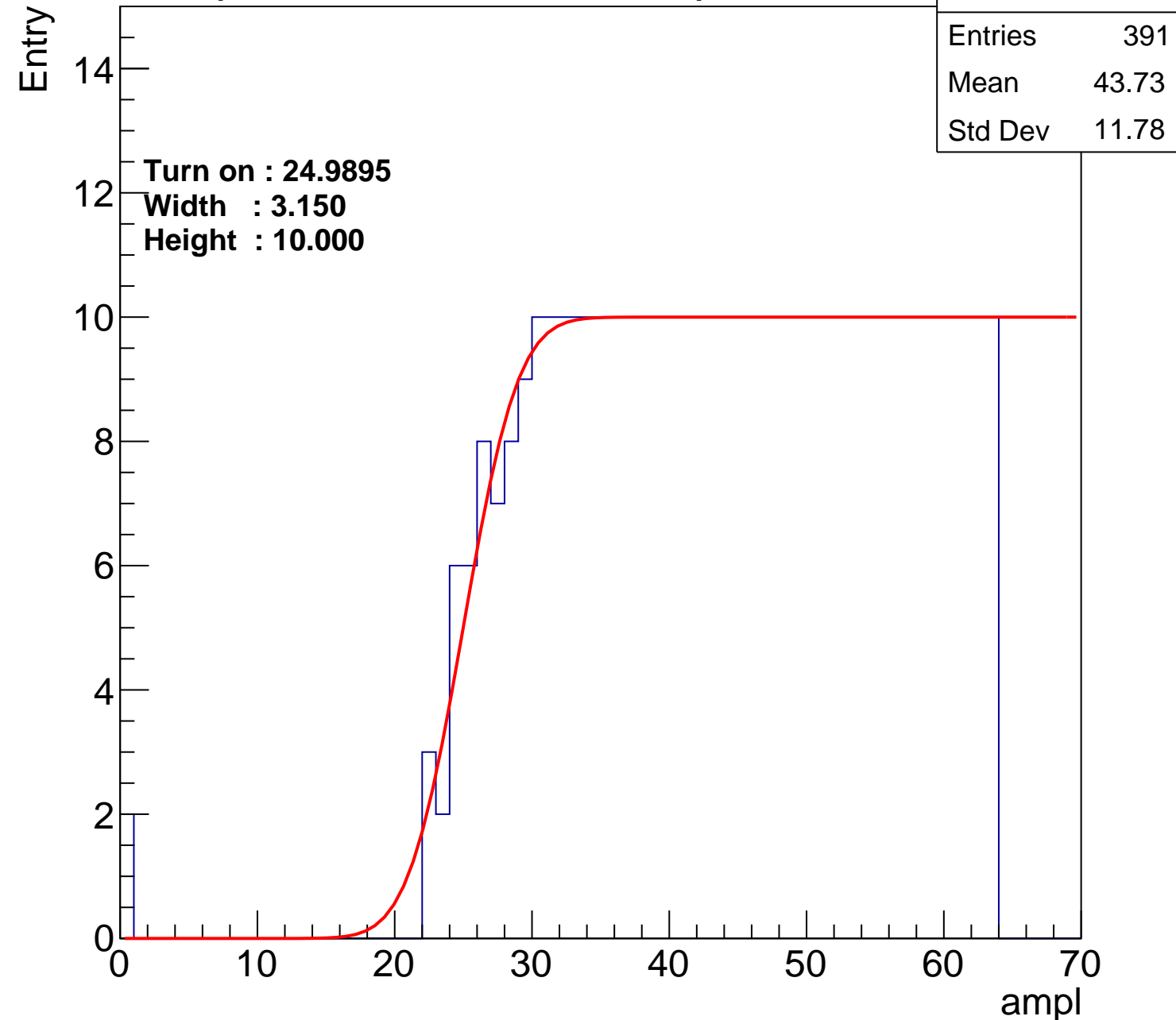
**Width : 3.150**

**Height : 10.000**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch117

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.8058

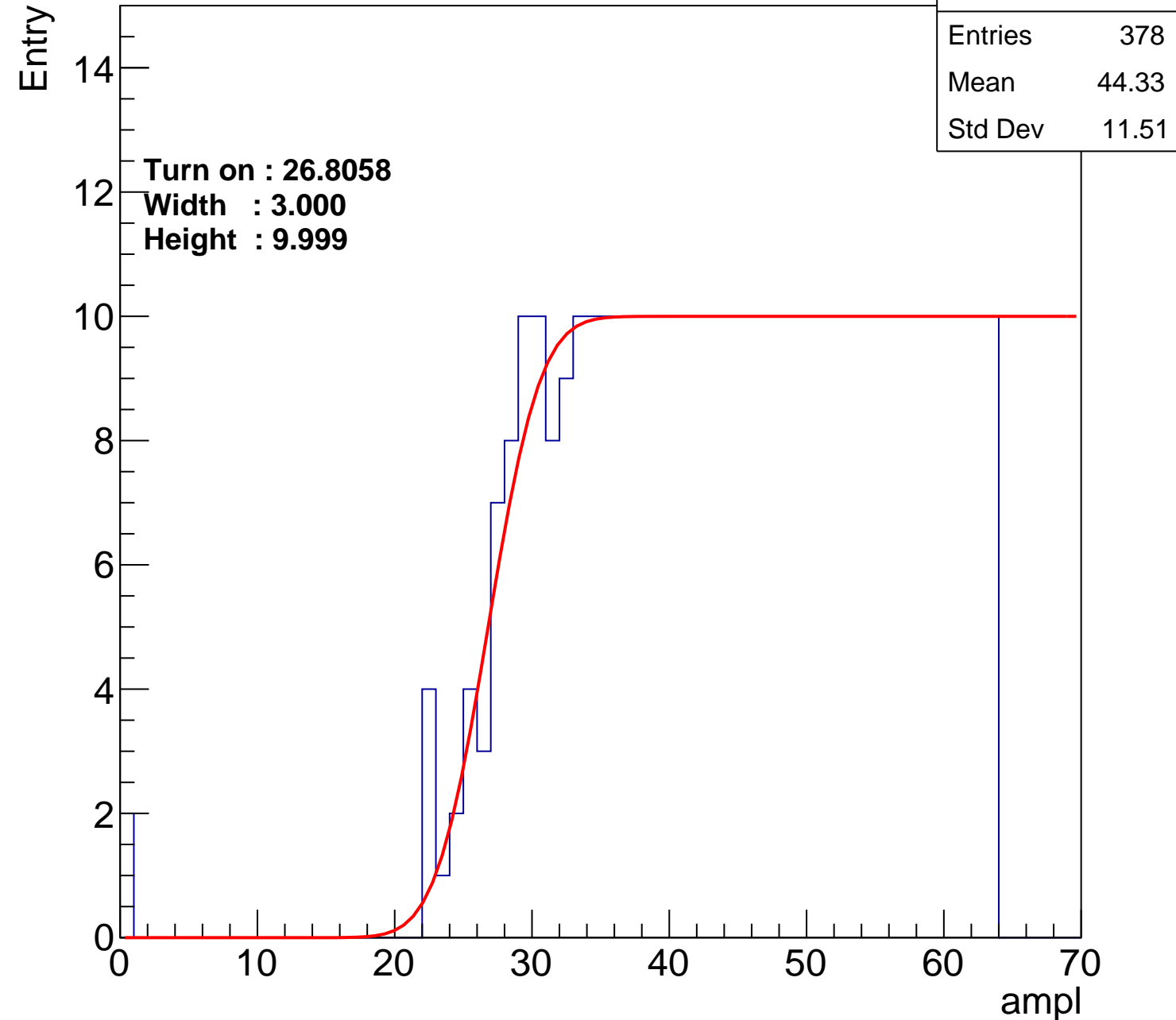
Width : 3.000

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch118

calib\_packv5\_042523\_0143.root, FC#5, port B1

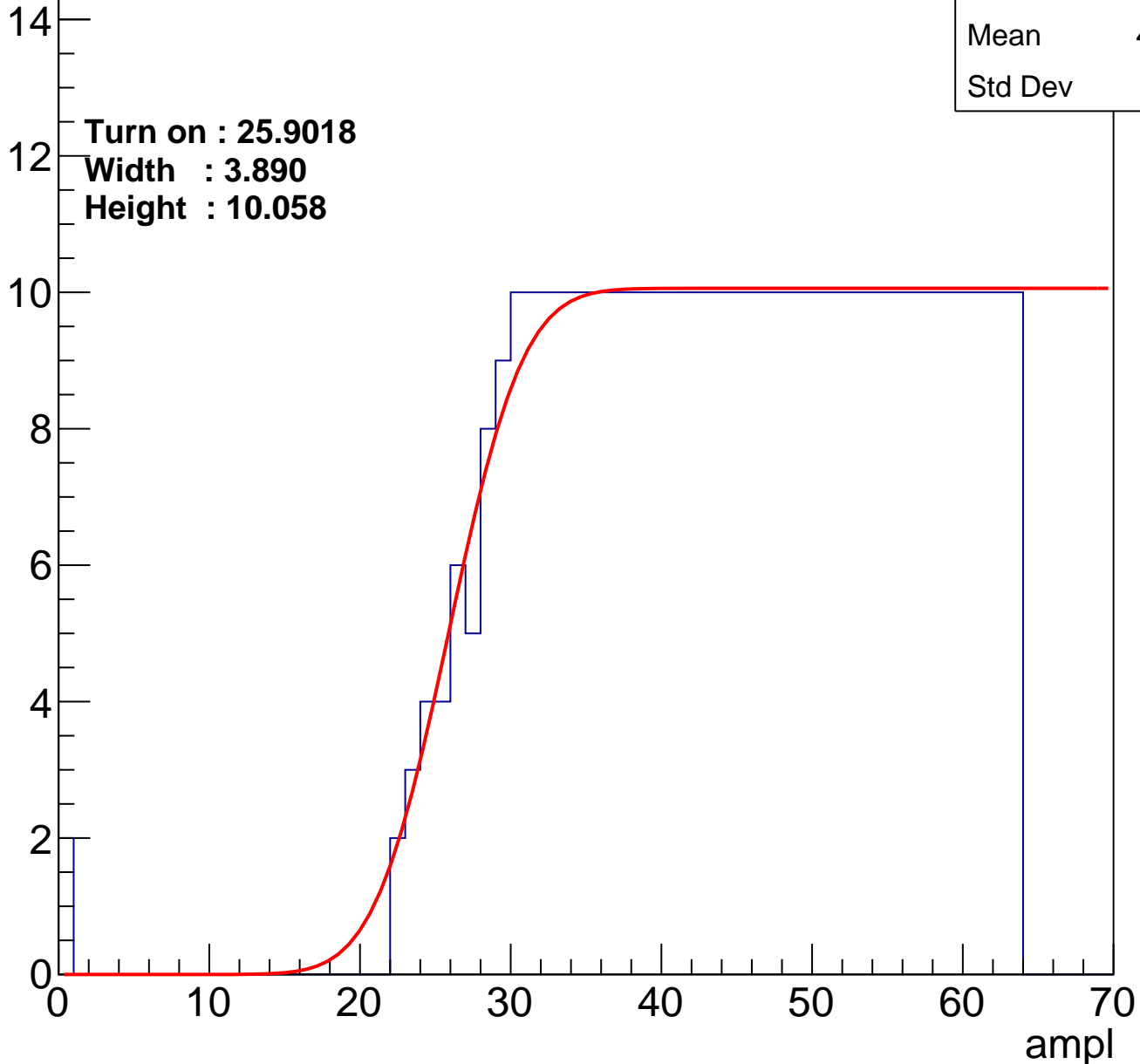
Entries	383
Mean	44.11
Std Dev	11.6

**Turn on : 25.9018**

**Width : 3.890**

**Height : 10.058**

Entry





# B0L000S, U17-ch119

calib\_packv5\_042523\_0143.root, FC#5, port B1

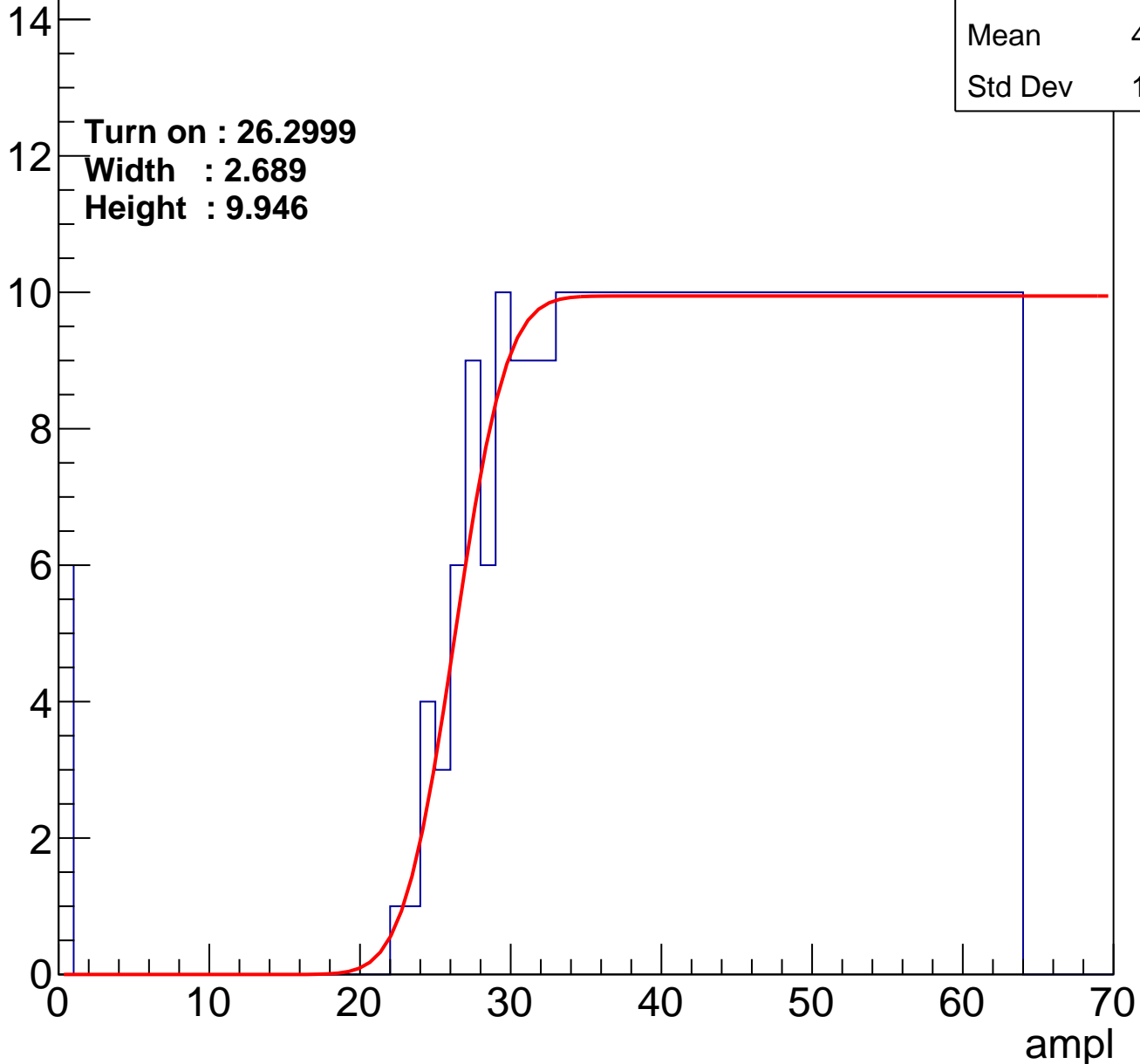
Entries	383
Mean	43.84
Std Dev	12.29

Turn on : 26.2999

Width : 2.689

Height : 9.946

Entry



# B0L000S, U17-ch120

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.5311

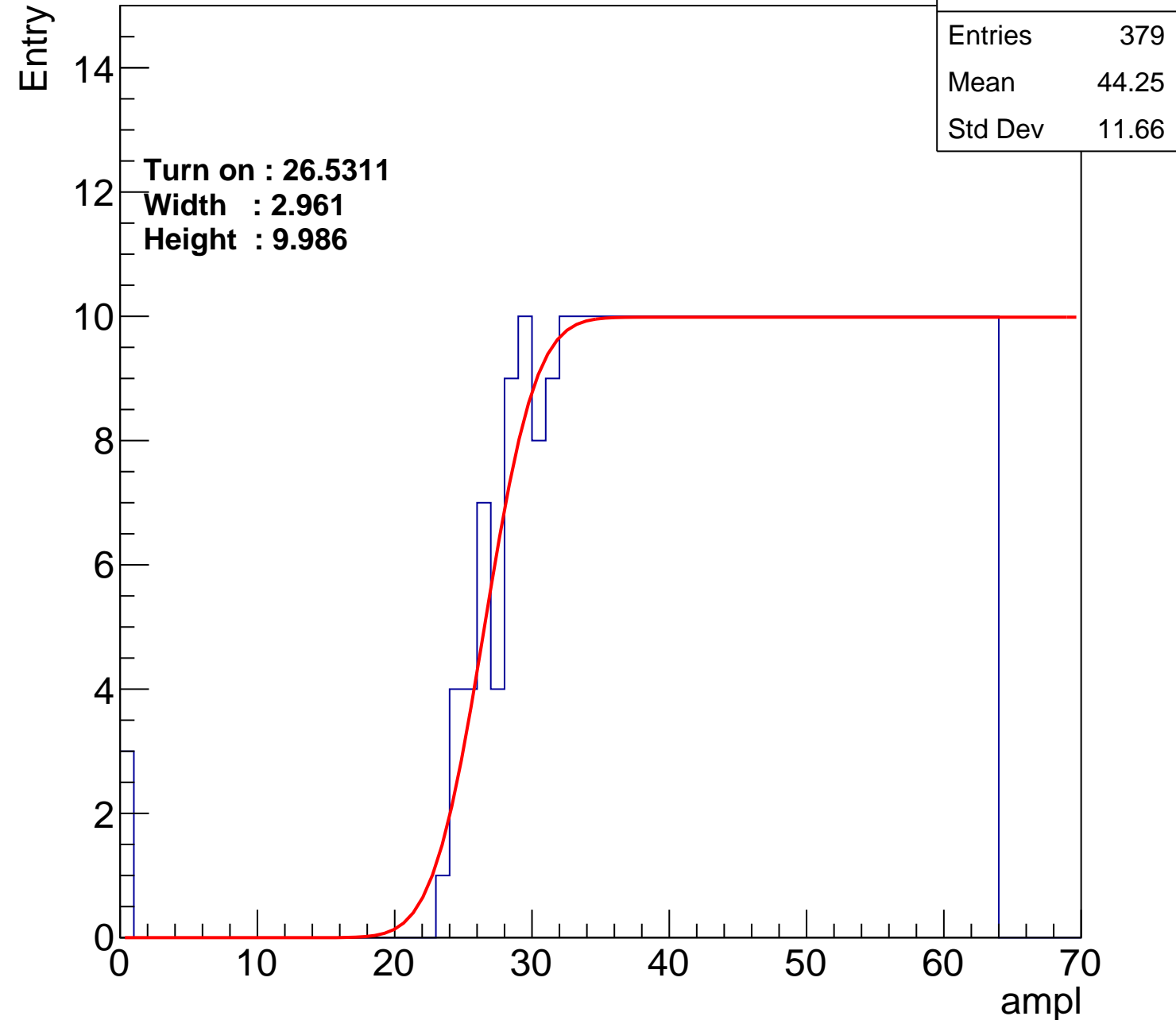
Width : 2.961

Height : 9.986

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch121

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.01
Std Dev	11.77

**Turn on : 25.8808**

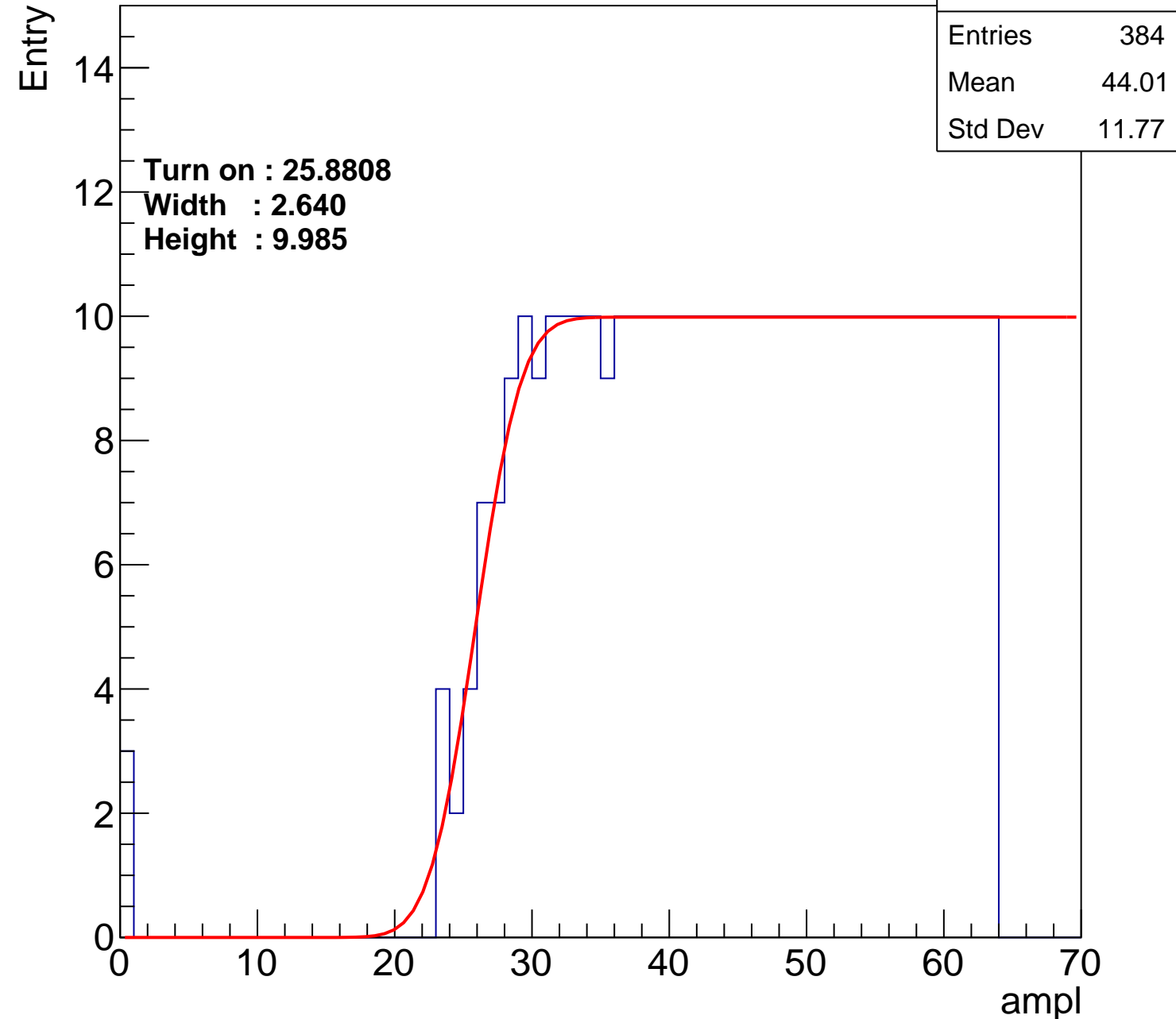
**Width : 2.640**

**Height : 9.985**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch122

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.73
Std Dev	12.19

**Turn on : 26.4356**

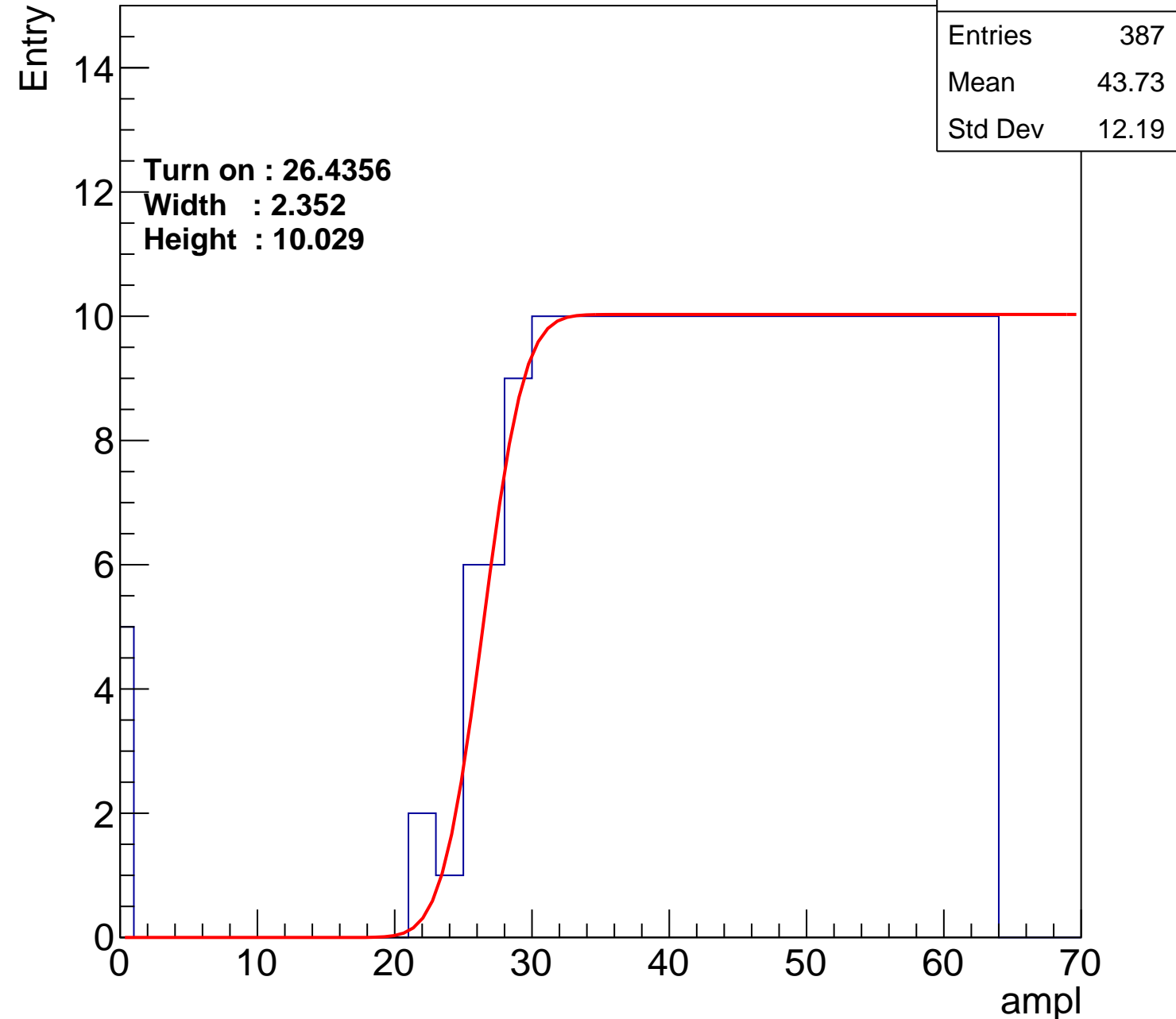
**Width : 2.352**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch123

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	43.78
Std Dev	12.33

**Turn on : 26.2353**

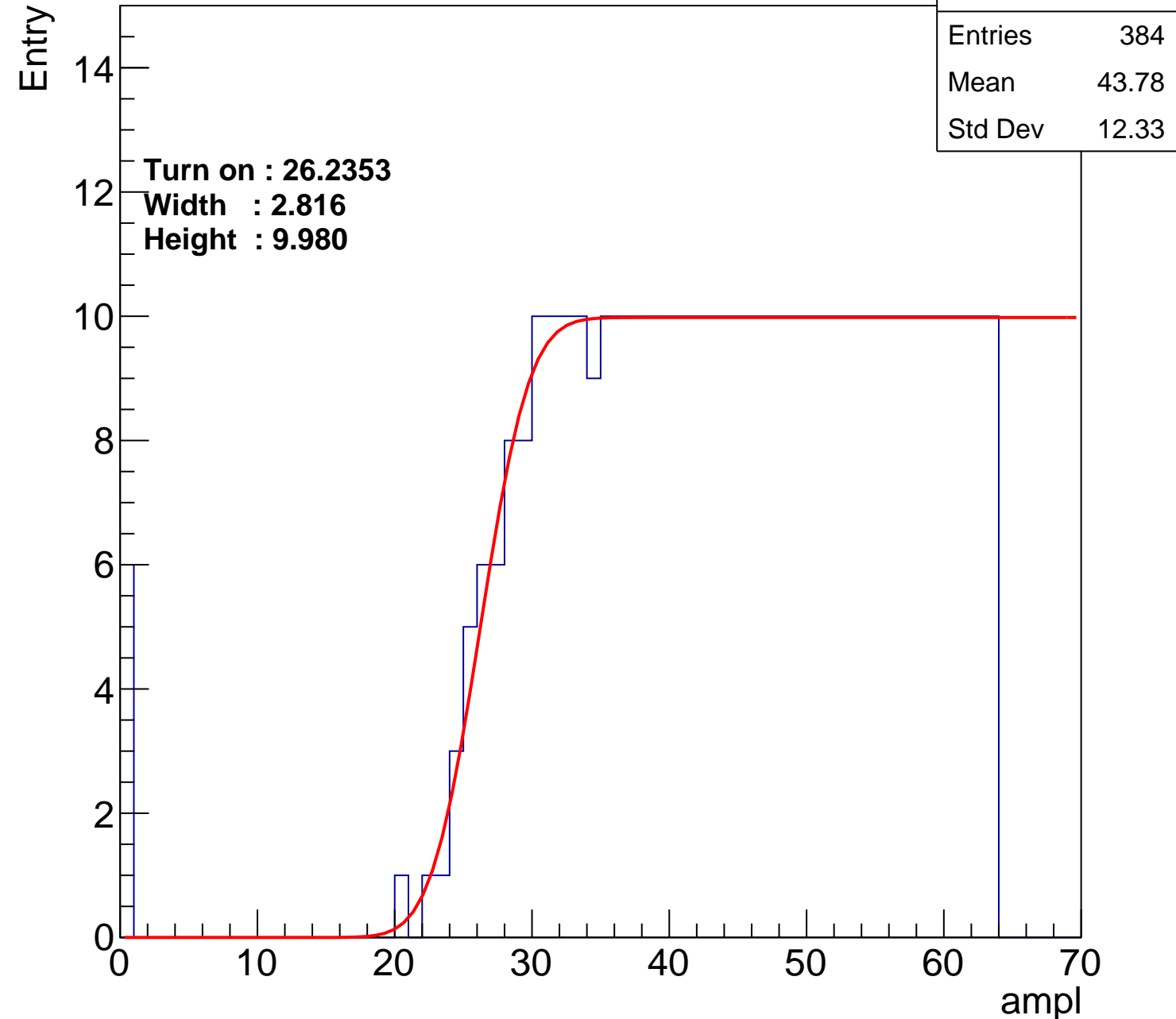
**Width : 2.816**

**Height : 9.980**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch124

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	398
Mean	43.35
Std Dev	12.02

**Turn on : 24.7864**

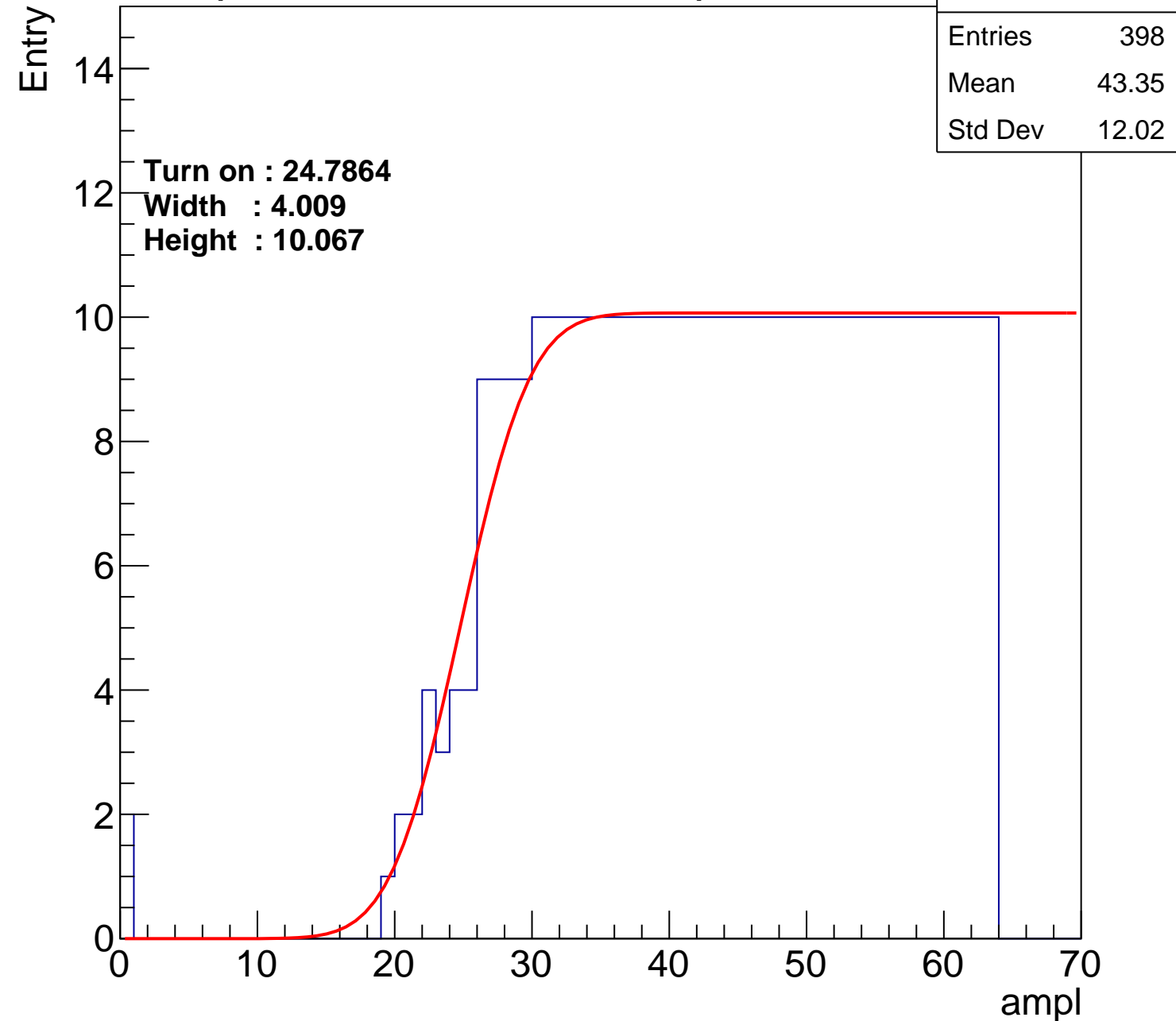
**Width : 4.009**

**Height : 10.067**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch125

calib\_packv5\_042523\_0143.root, FC#5, port B1

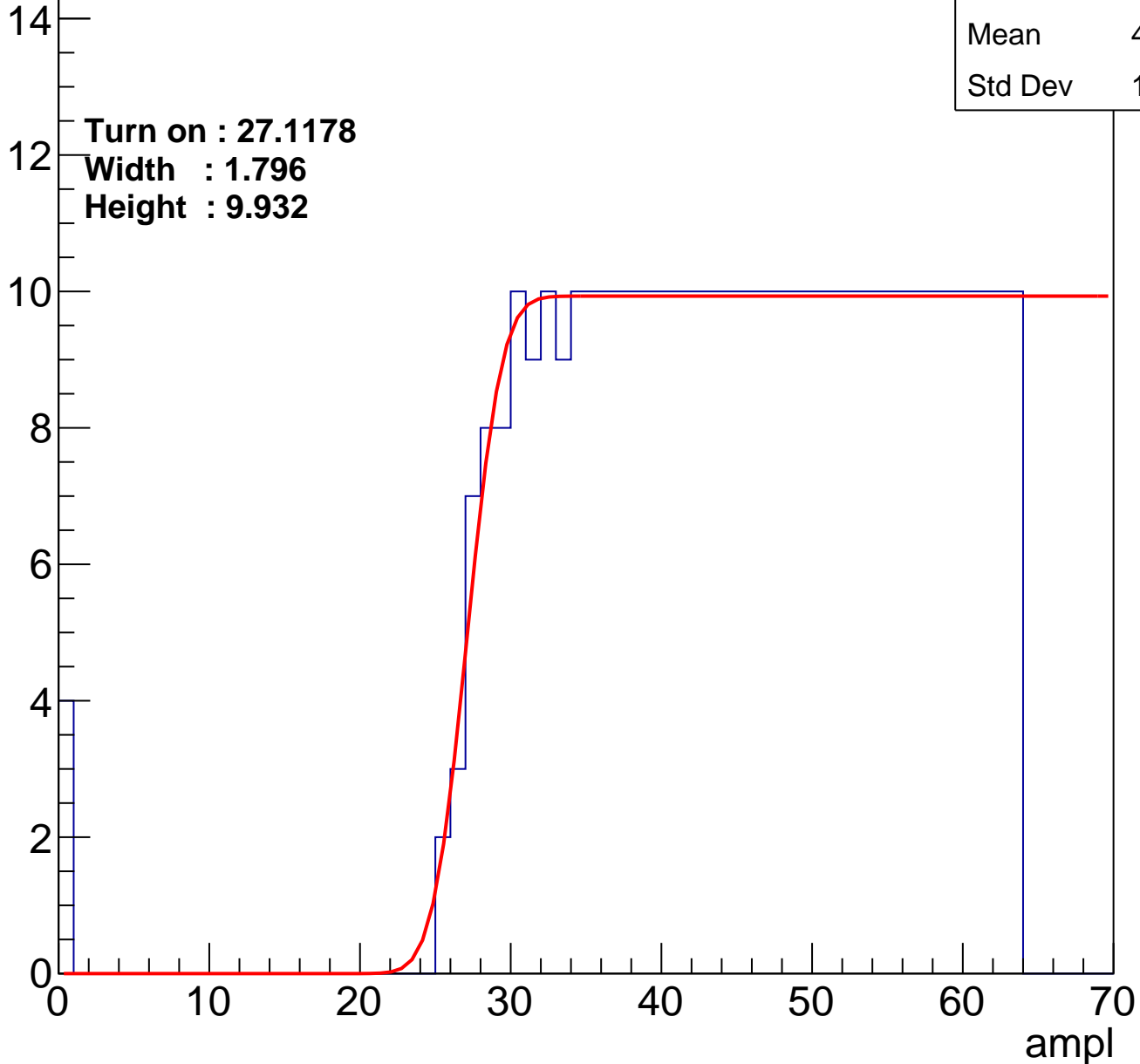
Entries	370
Mean	44.65
Std Dev	11.59

**Turn on : 27.1178**

**Width : 1.796**

**Height : 9.932**

Entry



# B0L000S, U17-ch126

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.64
Std Dev	11.39

Turn on : 26.7411

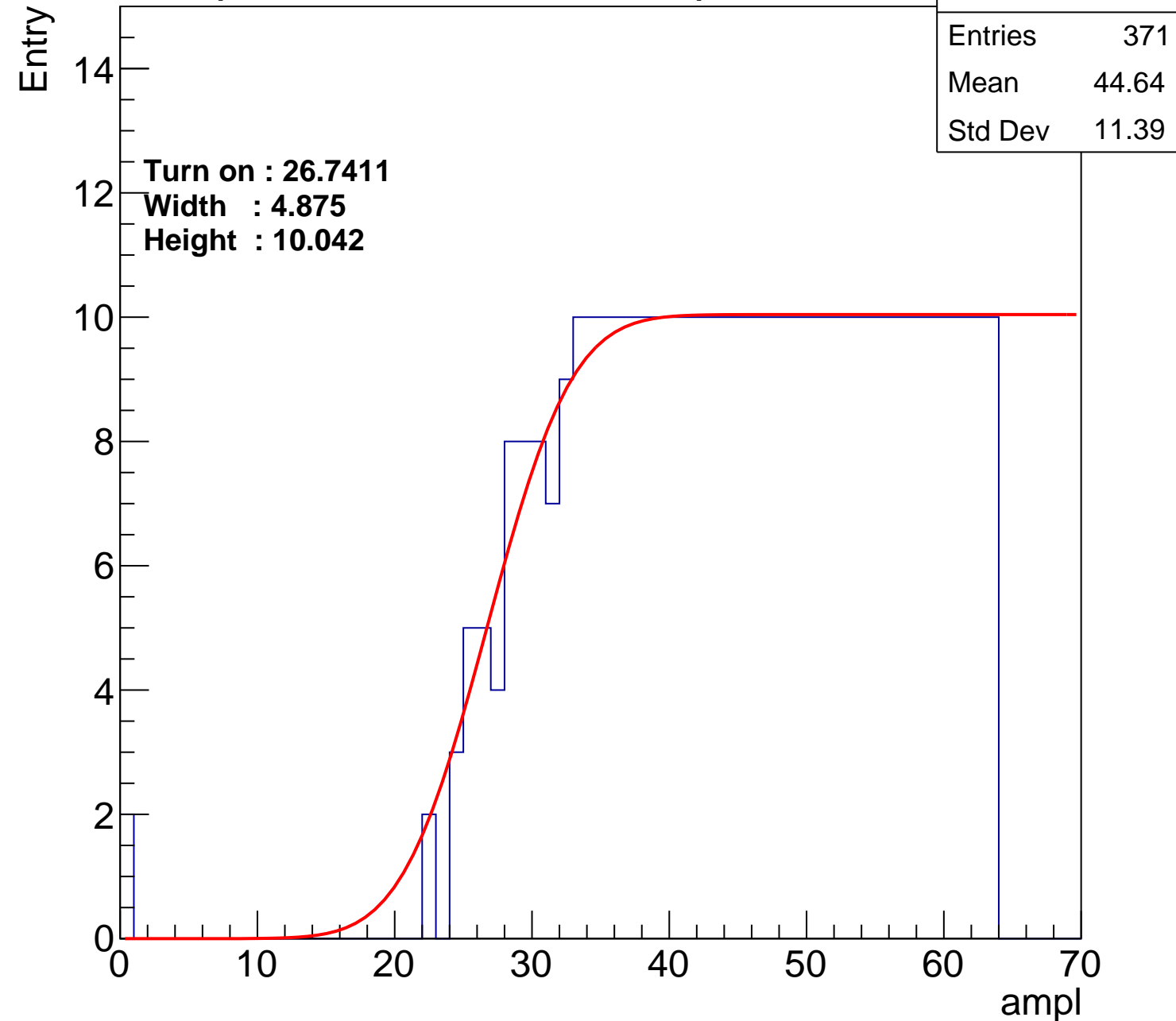
Width : 4.875

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U17-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	43.79
Std Dev	12.33

Turn on : 26.7663

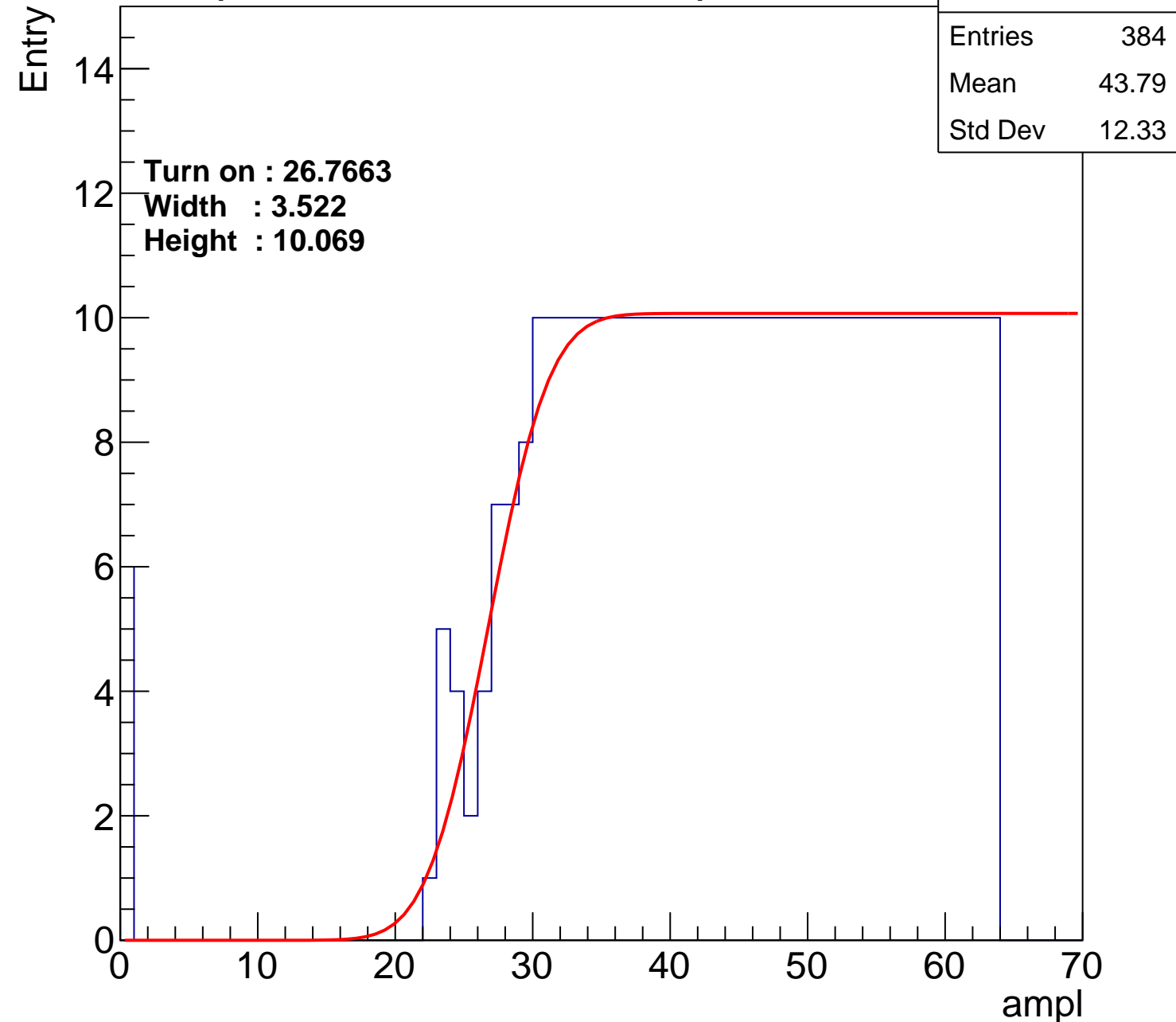
Width : 3.522

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U17-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	43.79
Std Dev	12.33

Turn on : 26.7663

Width : 3.522

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

