

B0L001S, U25-ch0, adc0

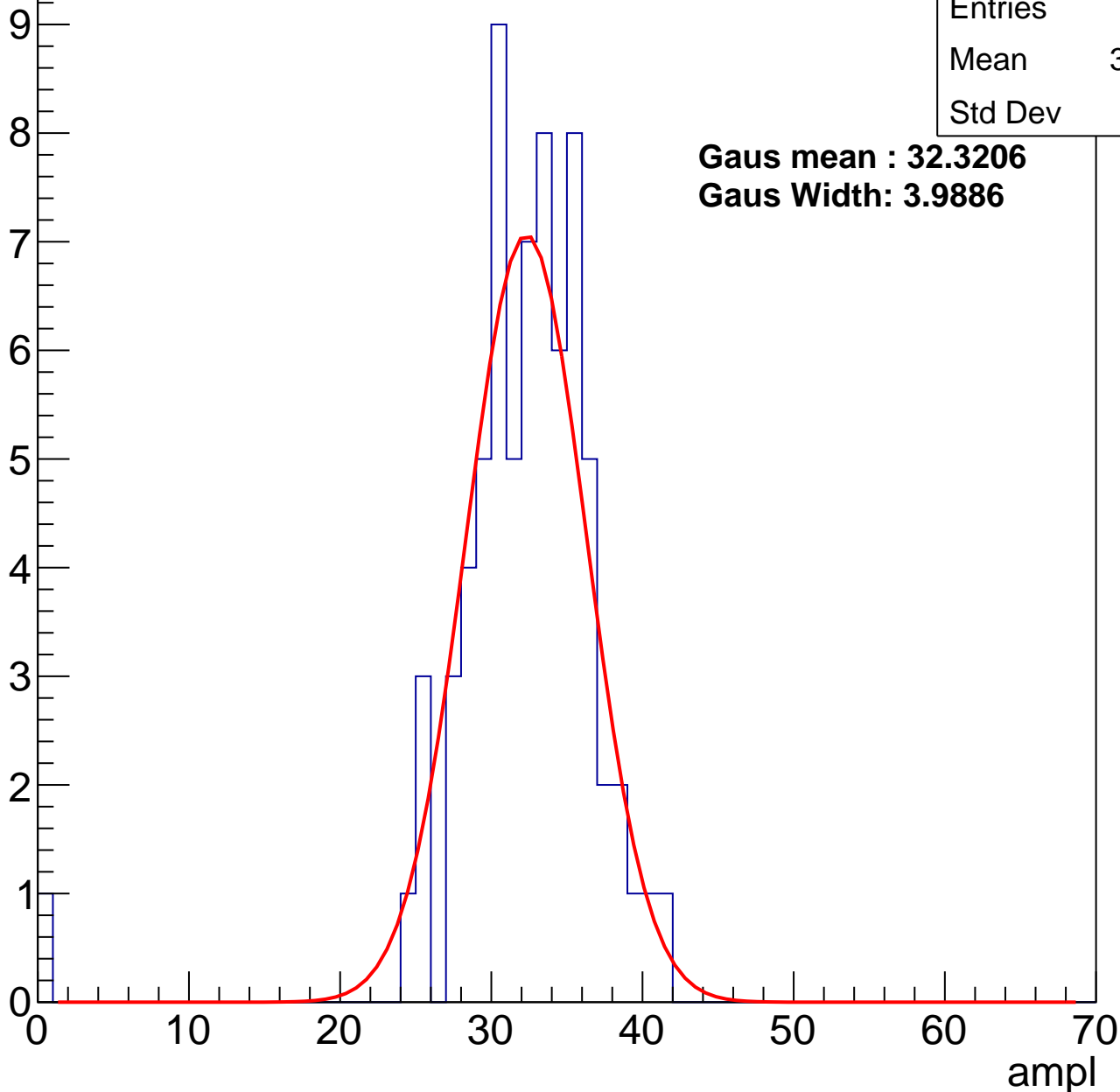
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	31.72
Std Dev	5.21

Gaus mean : 32.3206

Gaus Width: 3.9886



B0L001S, U25-ch0, adc1

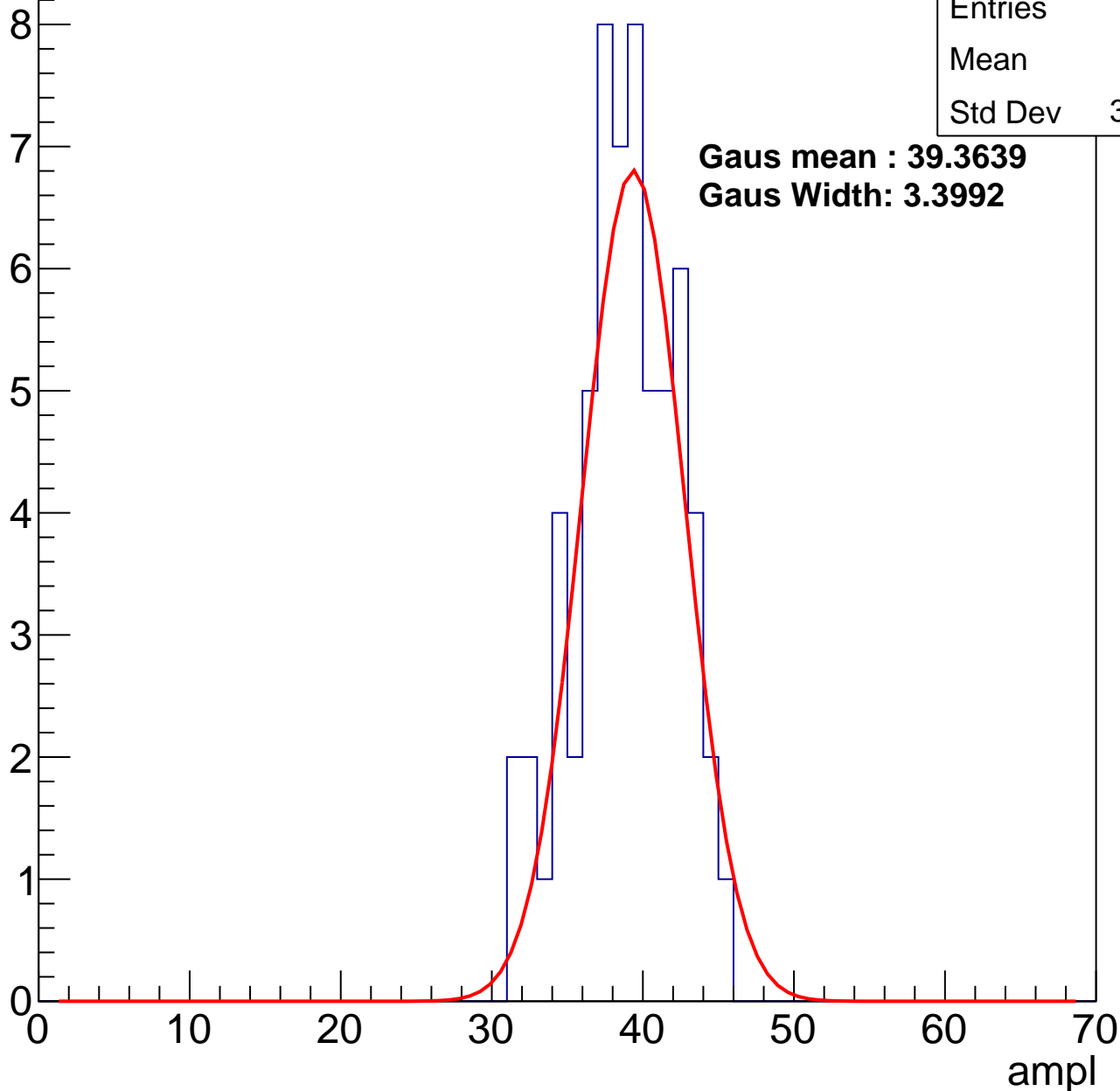
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	38.4
Std Dev	3.324

Gaus mean : 39.3639

Gaus Width: 3.3992



B0L001S, U25-ch0, adc2

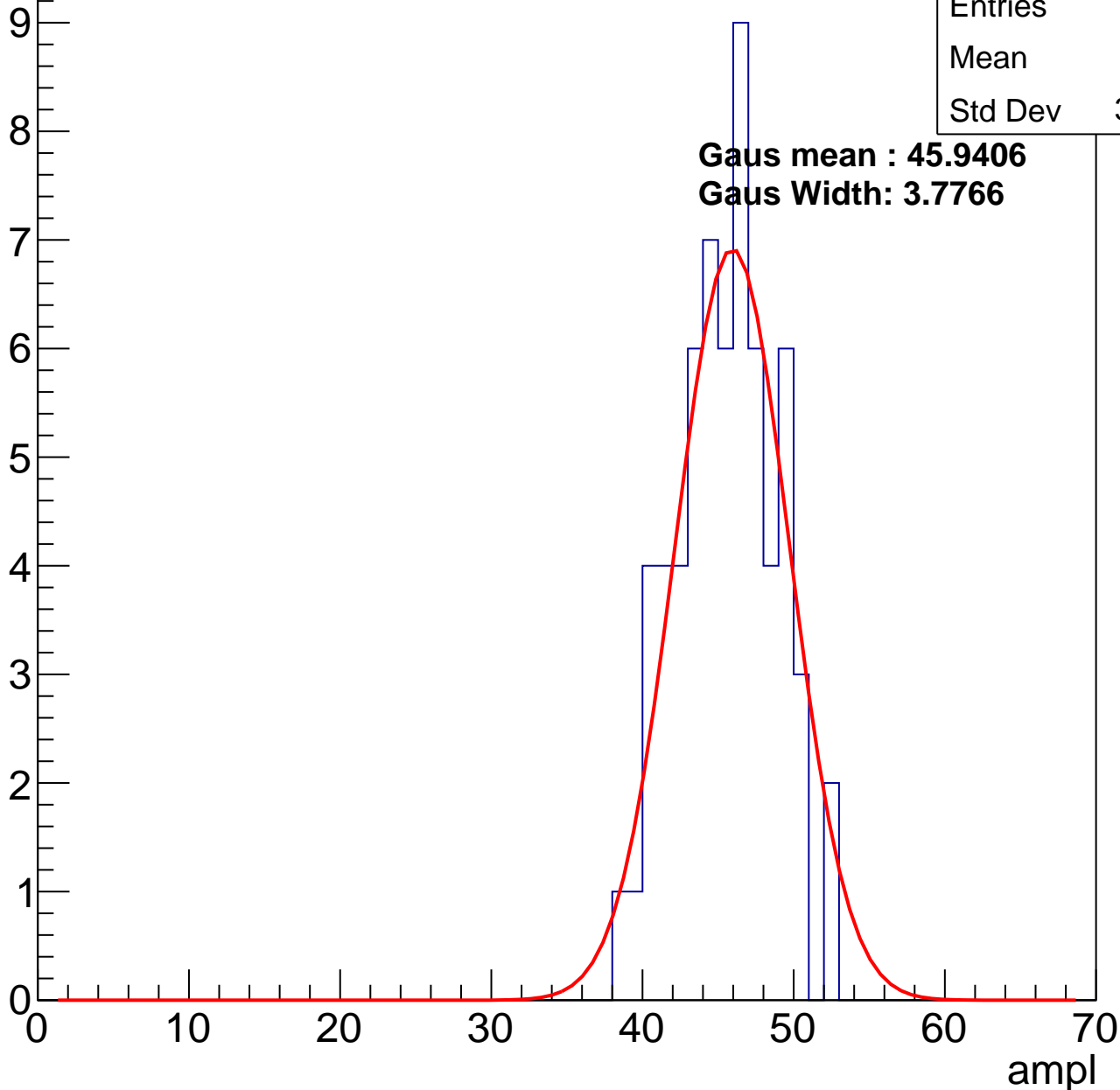
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	45.1
Std Dev	3.221

Gaus mean : 45.9406

Gaus Width: 3.7766



B0L001S, U25-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

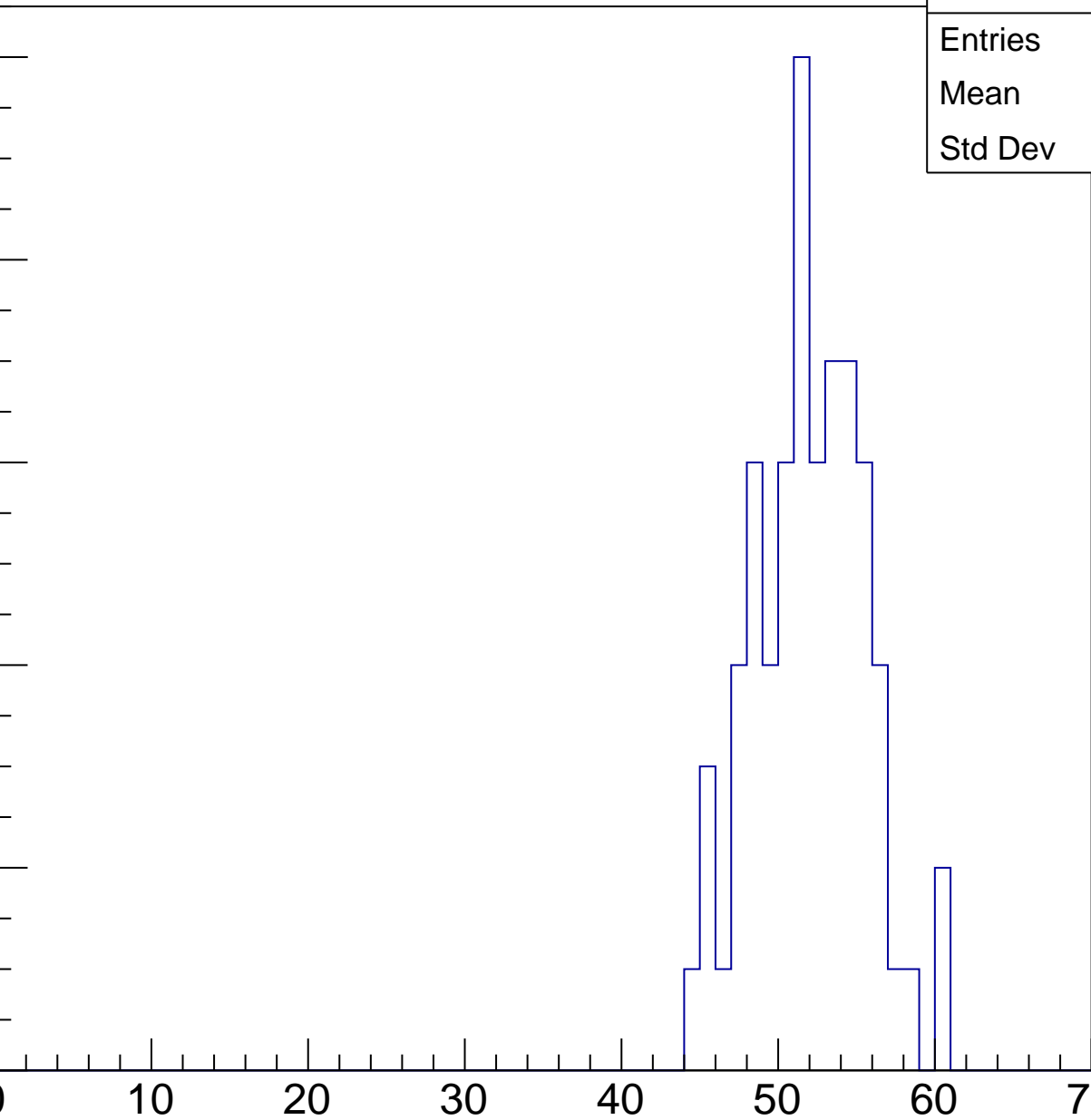
Entries	69
Mean	51.55
Std Dev	3.491

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

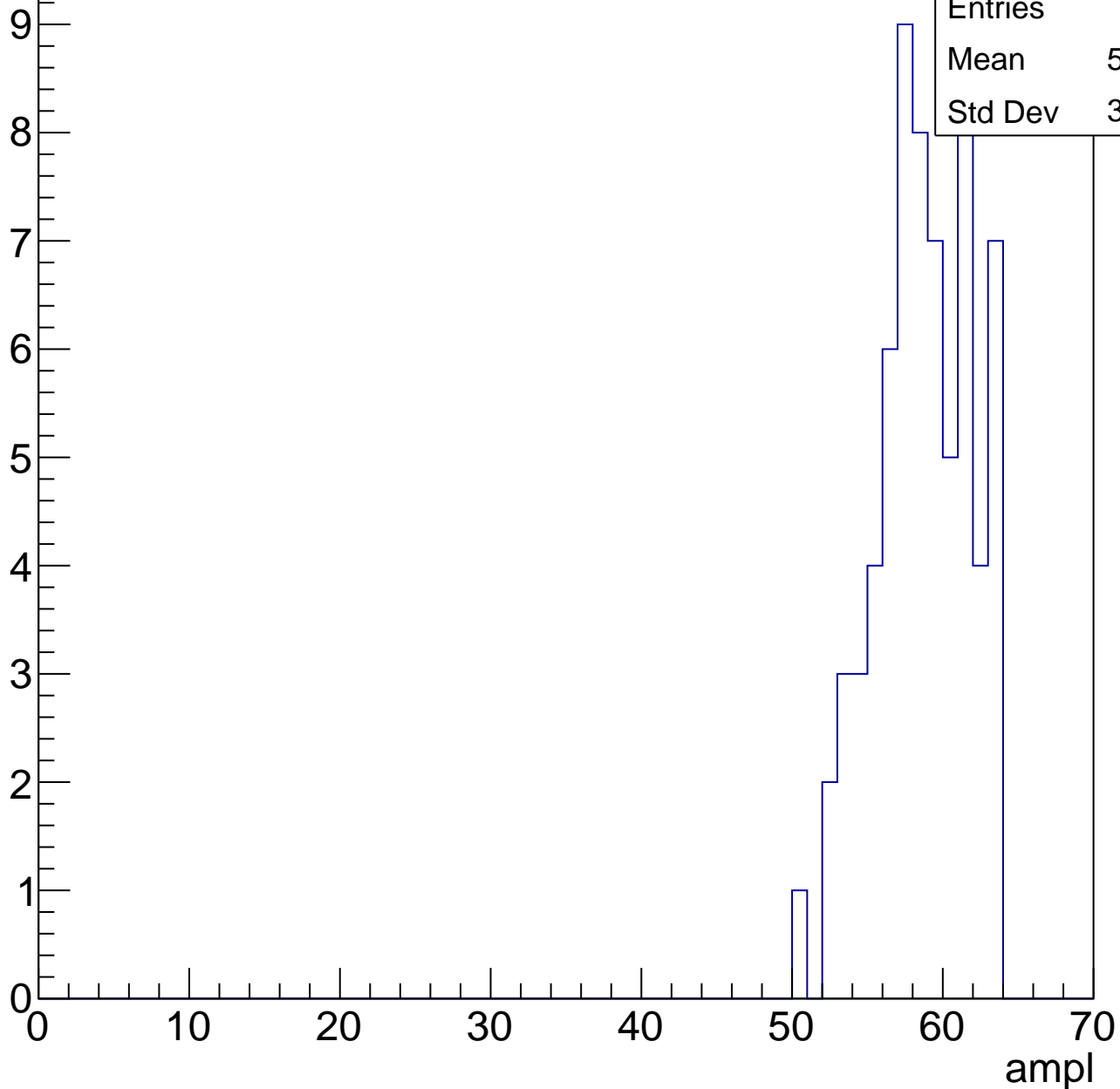
ampl



B0L001S, U25-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

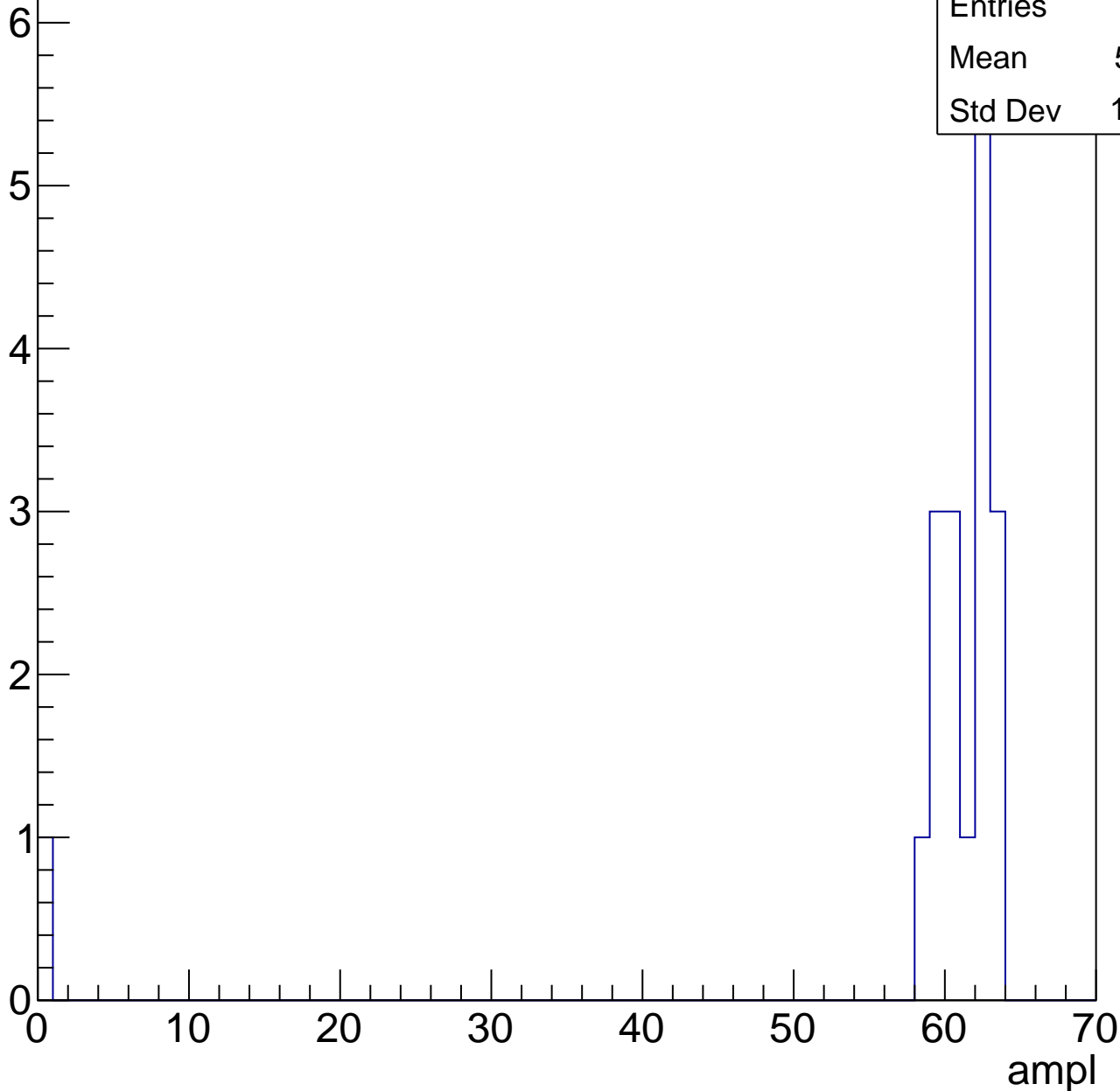


B0L001S, U25-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	57.61
Std Dev	14.06



B0L001S, U25-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch1, adc0

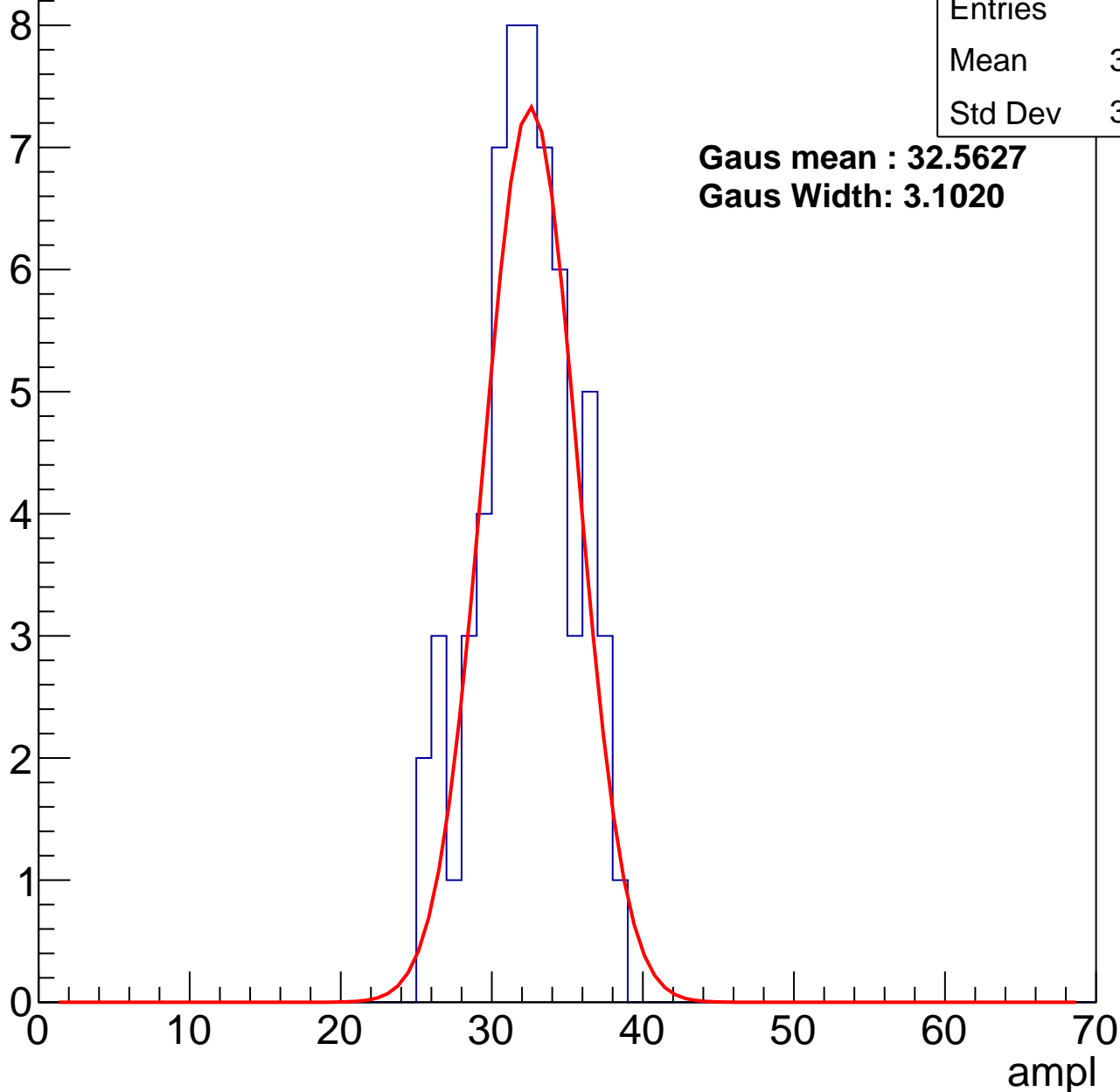
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.77
Std Dev	3.128

Gaus mean : 32.5627

Gaus Width: 3.1020



B0L001S, U25-ch1, adc1

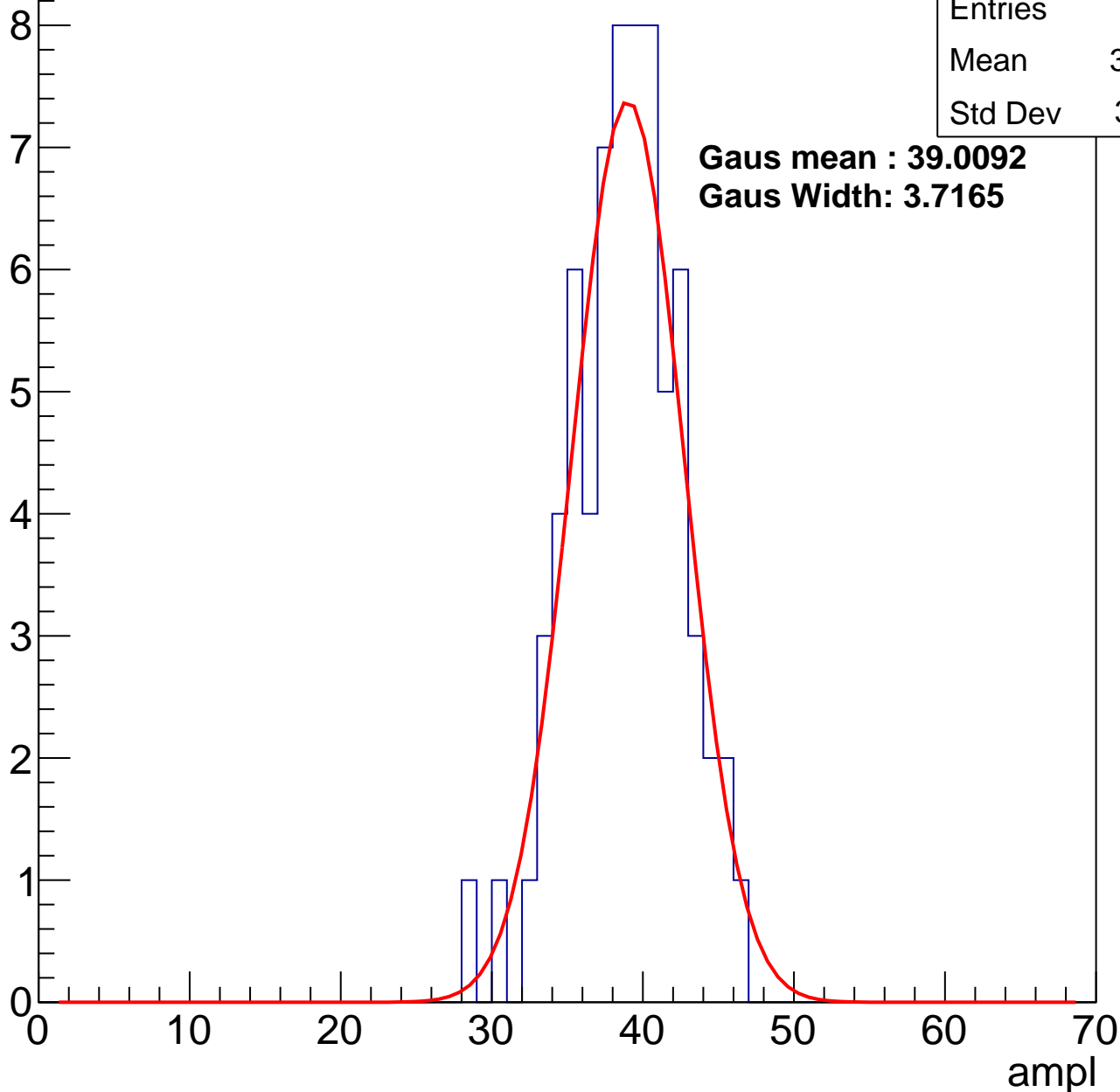
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.34
Std Dev	3.581

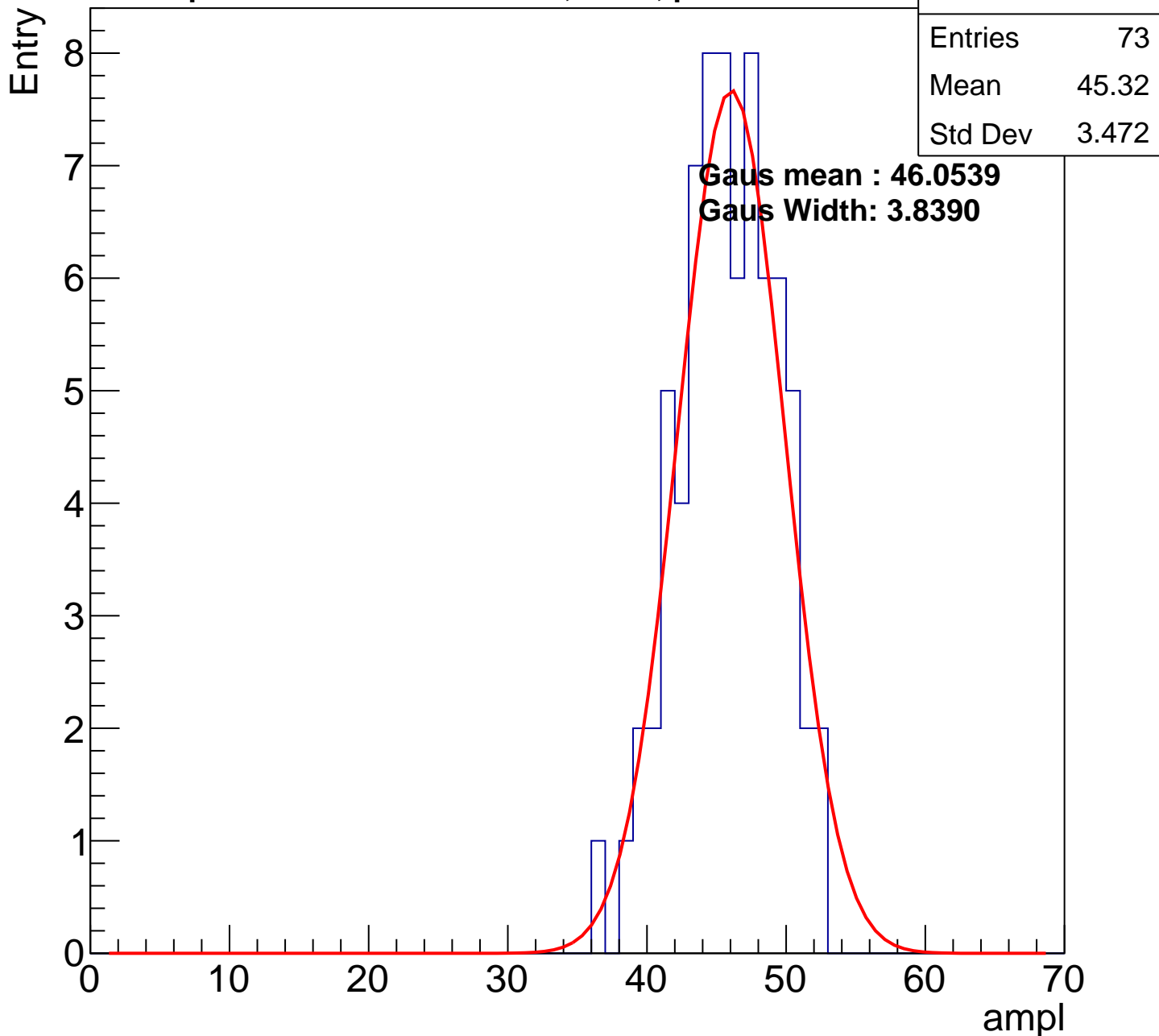
Gaus mean : 39.0092

Gaus Width: 3.7165



B0L001S, U25-ch1, adc2

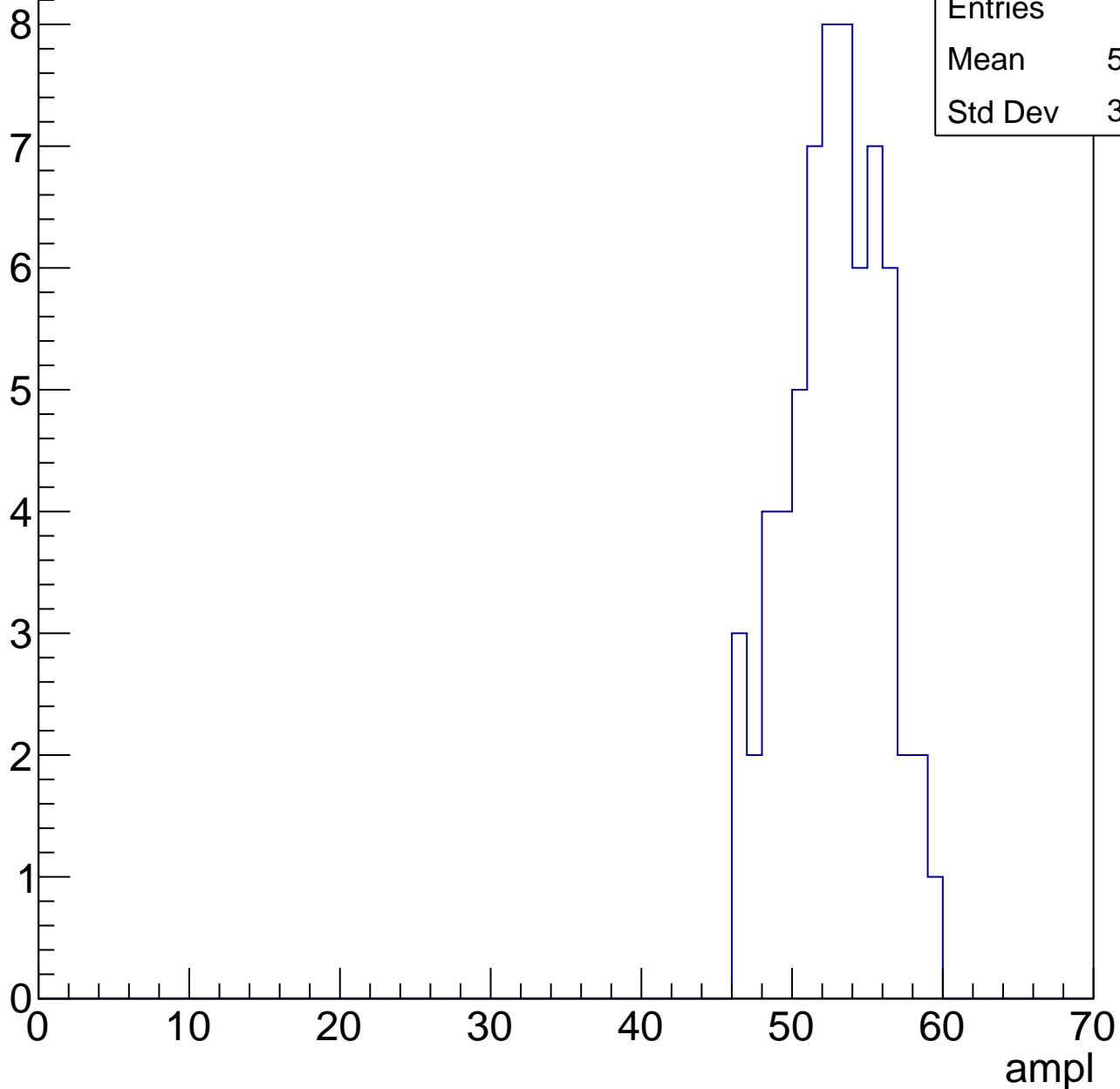
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

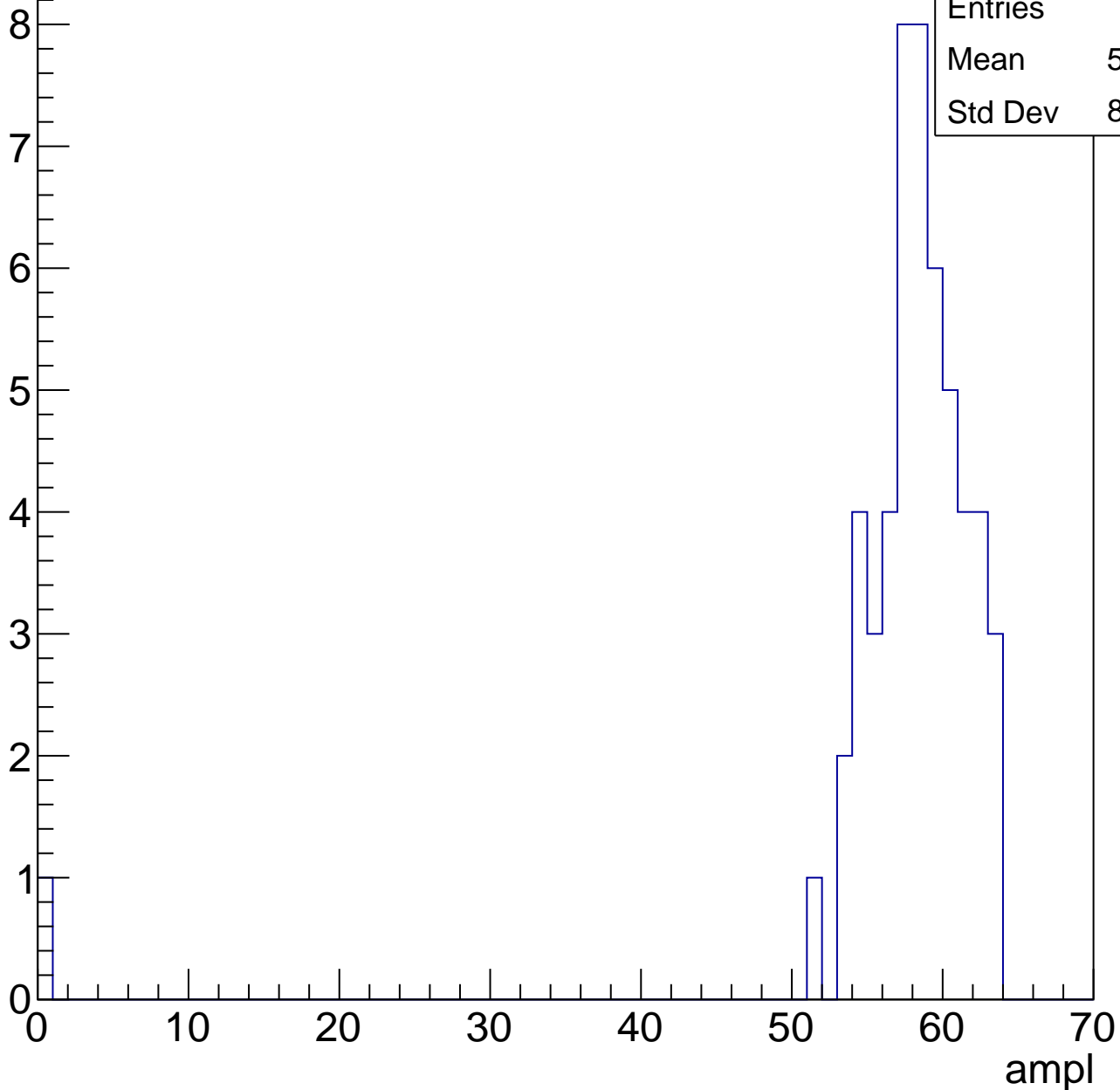


B0L001S, U25-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

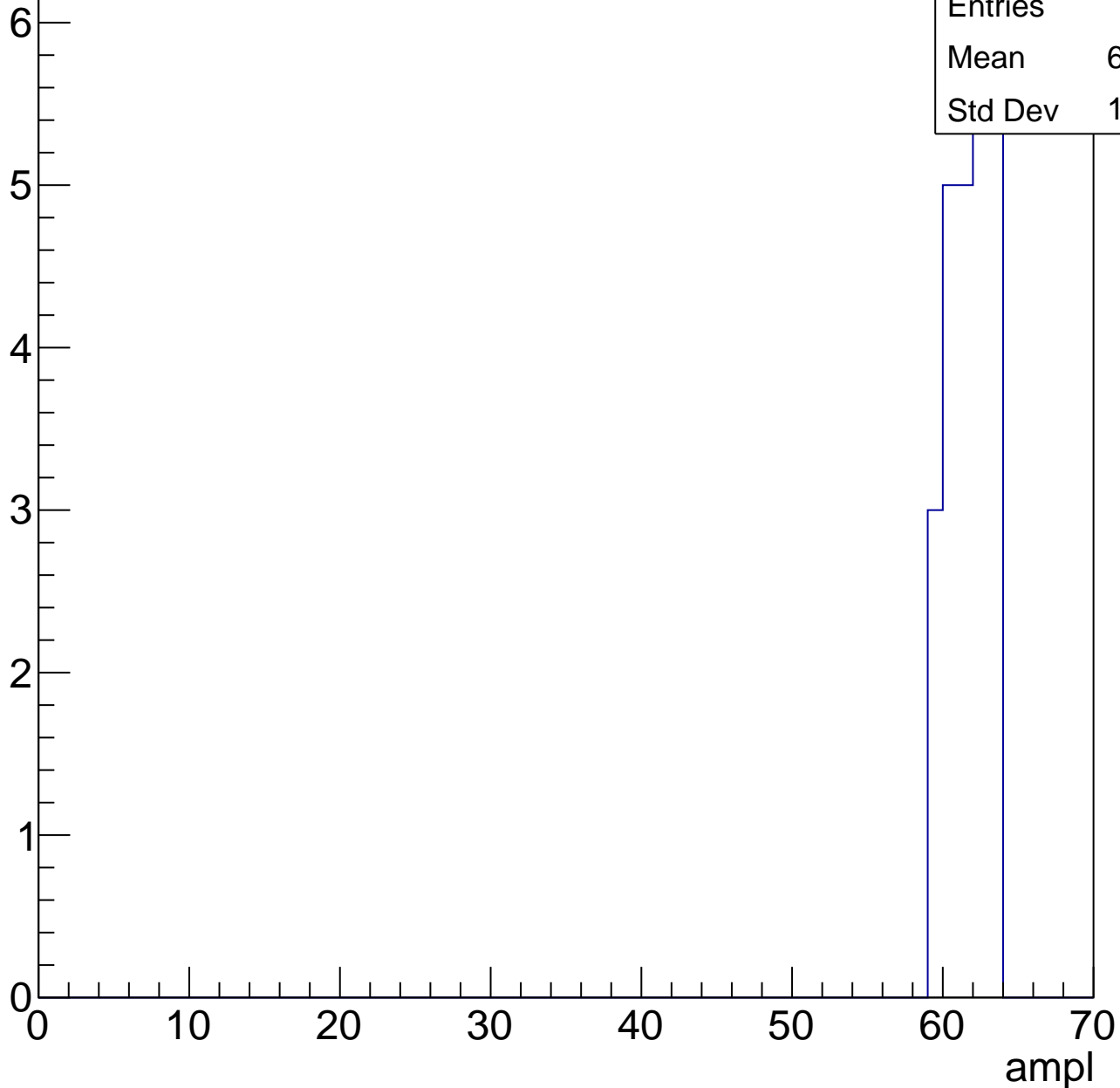
Entries	53
Mean	56.92
Std Dev	8.375



B0L001S, U25-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

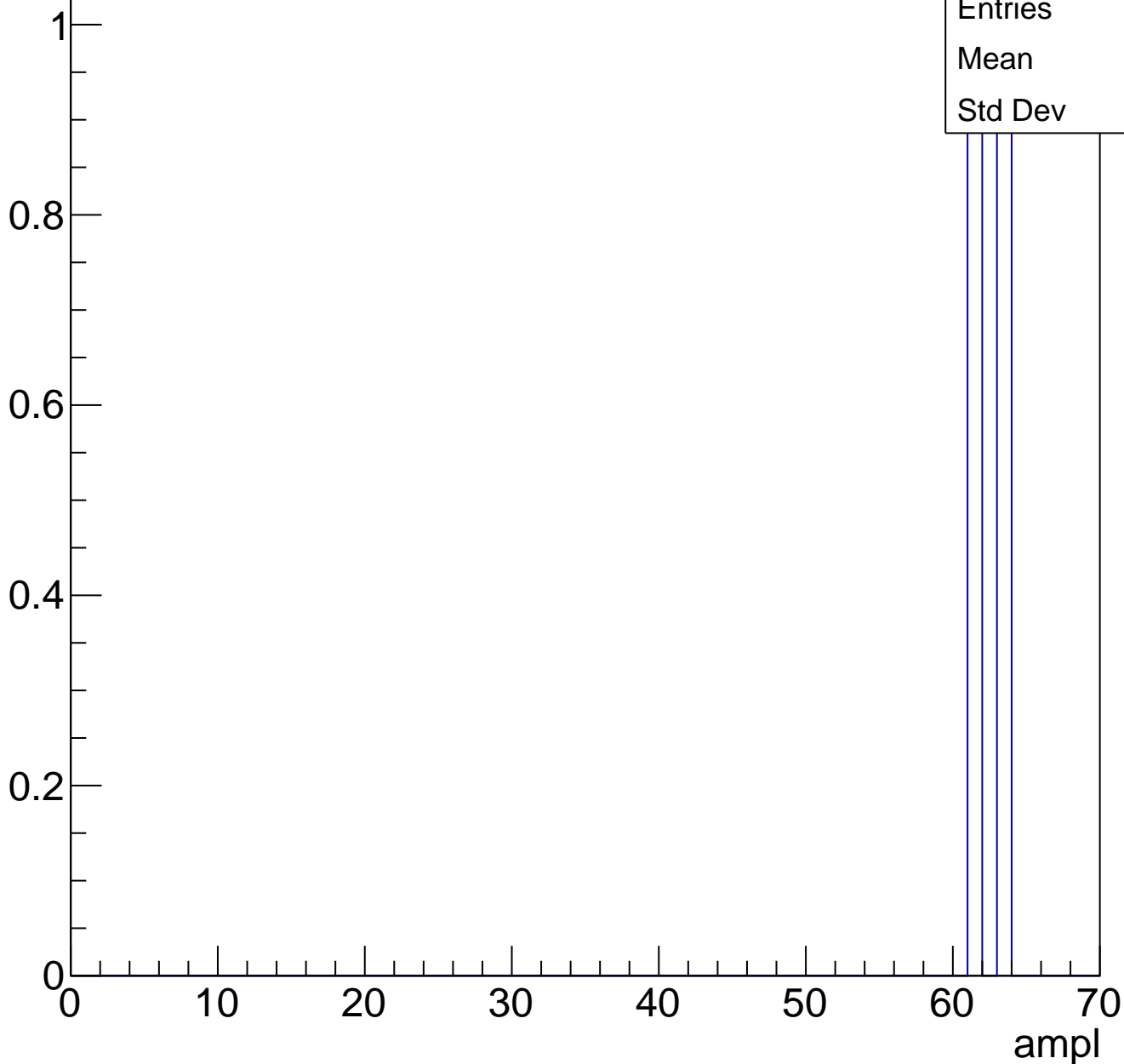
Entry



B0L001S, U25-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch2, adc0

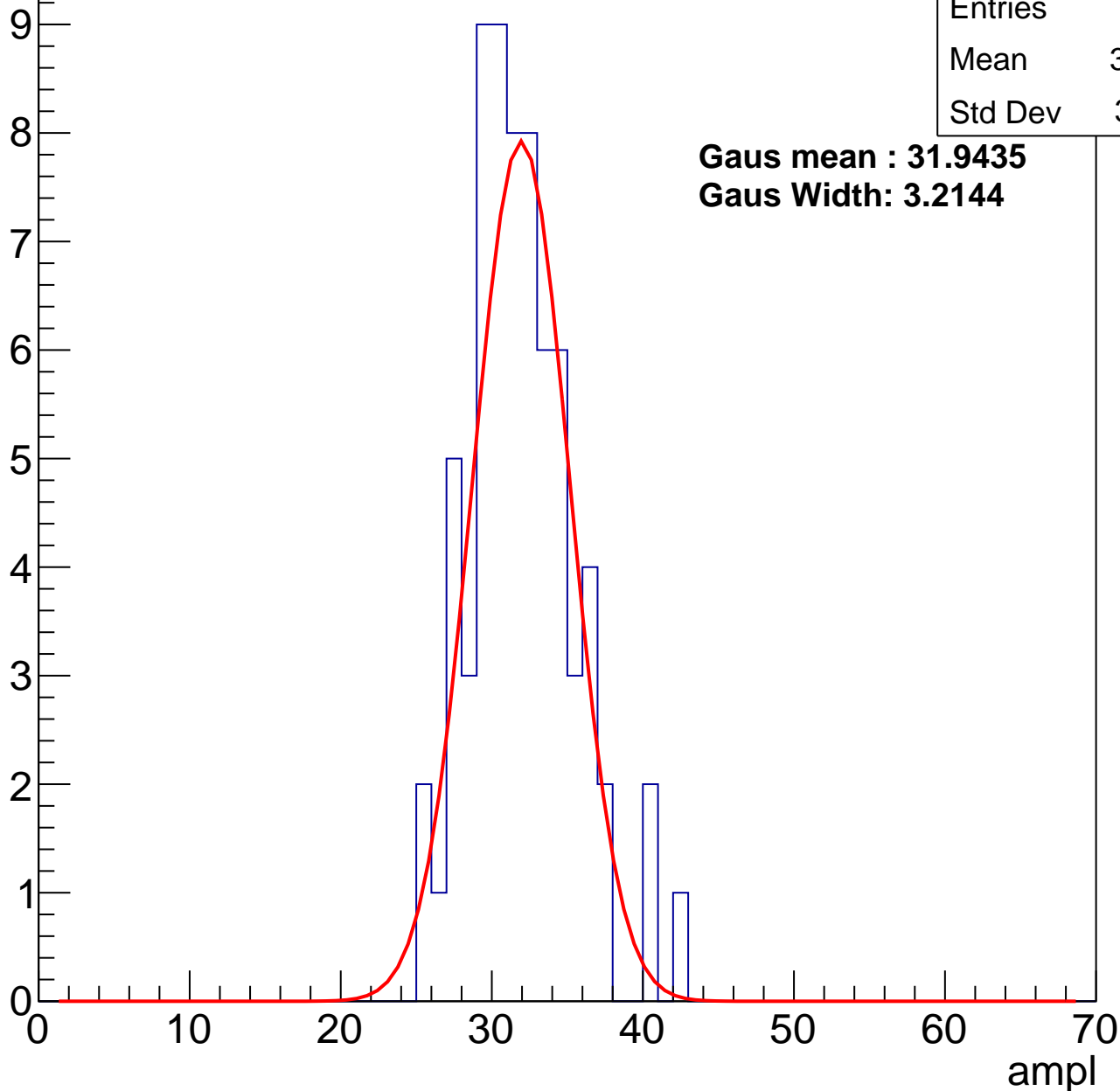
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.55
Std Dev	3.441

Gaus mean : 31.9435

Gaus Width: 3.2144



B0L001S, U25-ch2, adc1

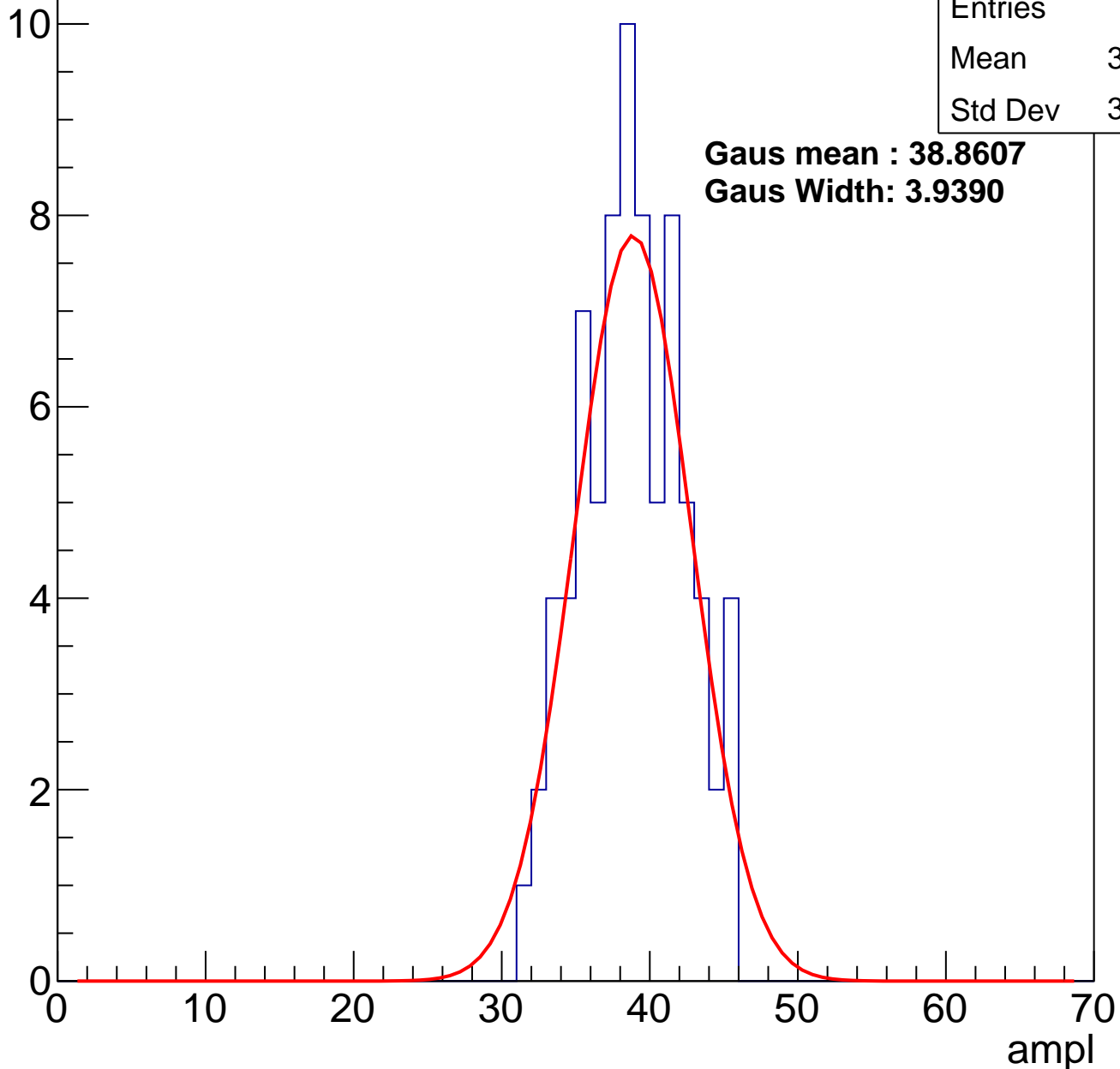
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	38.36
Std Dev	3.445

Gaus mean : 38.8607

Gaus Width: 3.9390

Entry



B0L001S, U25-ch2, adc2

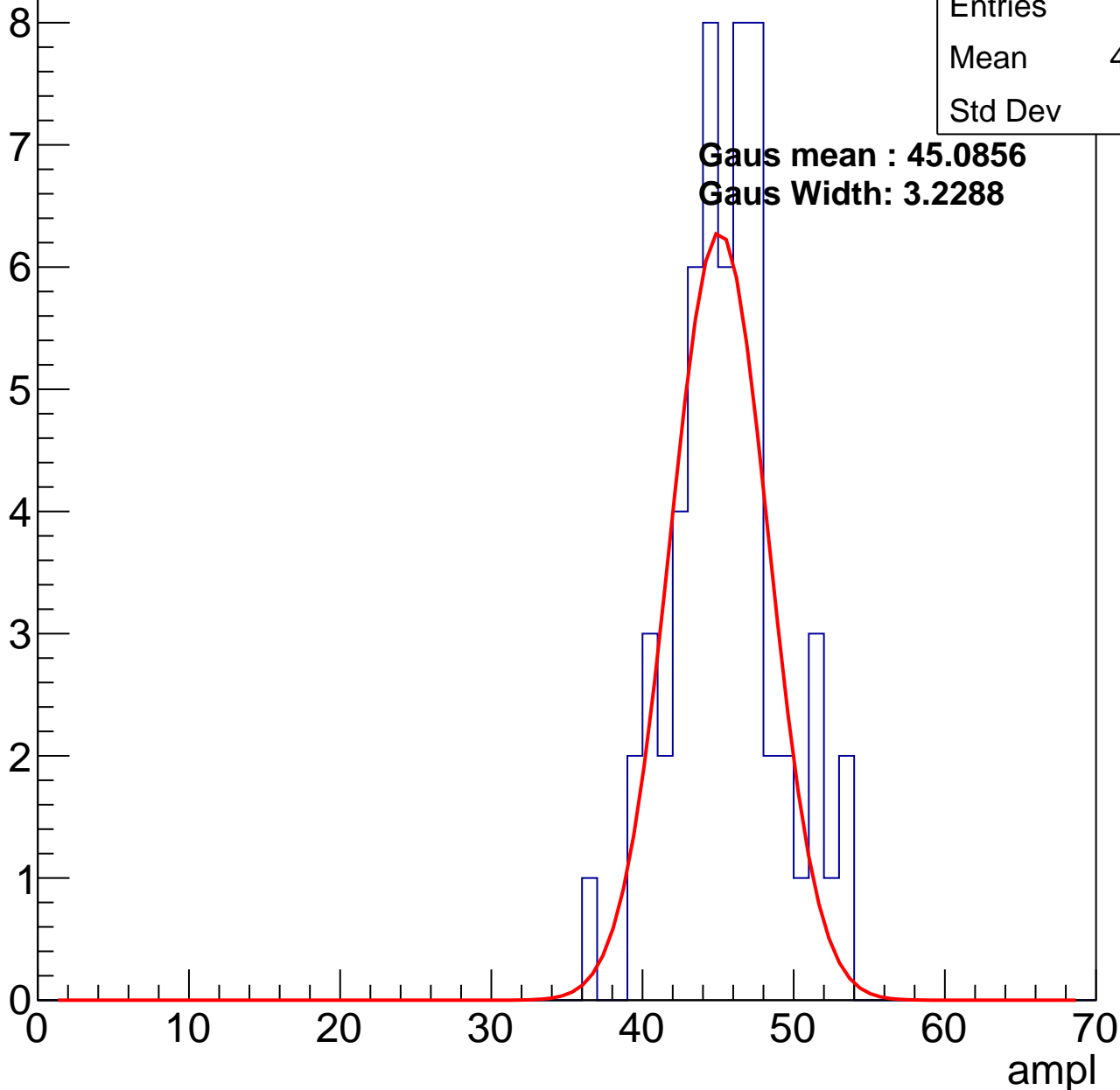
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	45.14
Std Dev	3.51

Gaus mean : 45.0856

Gaus Width: 3.2288

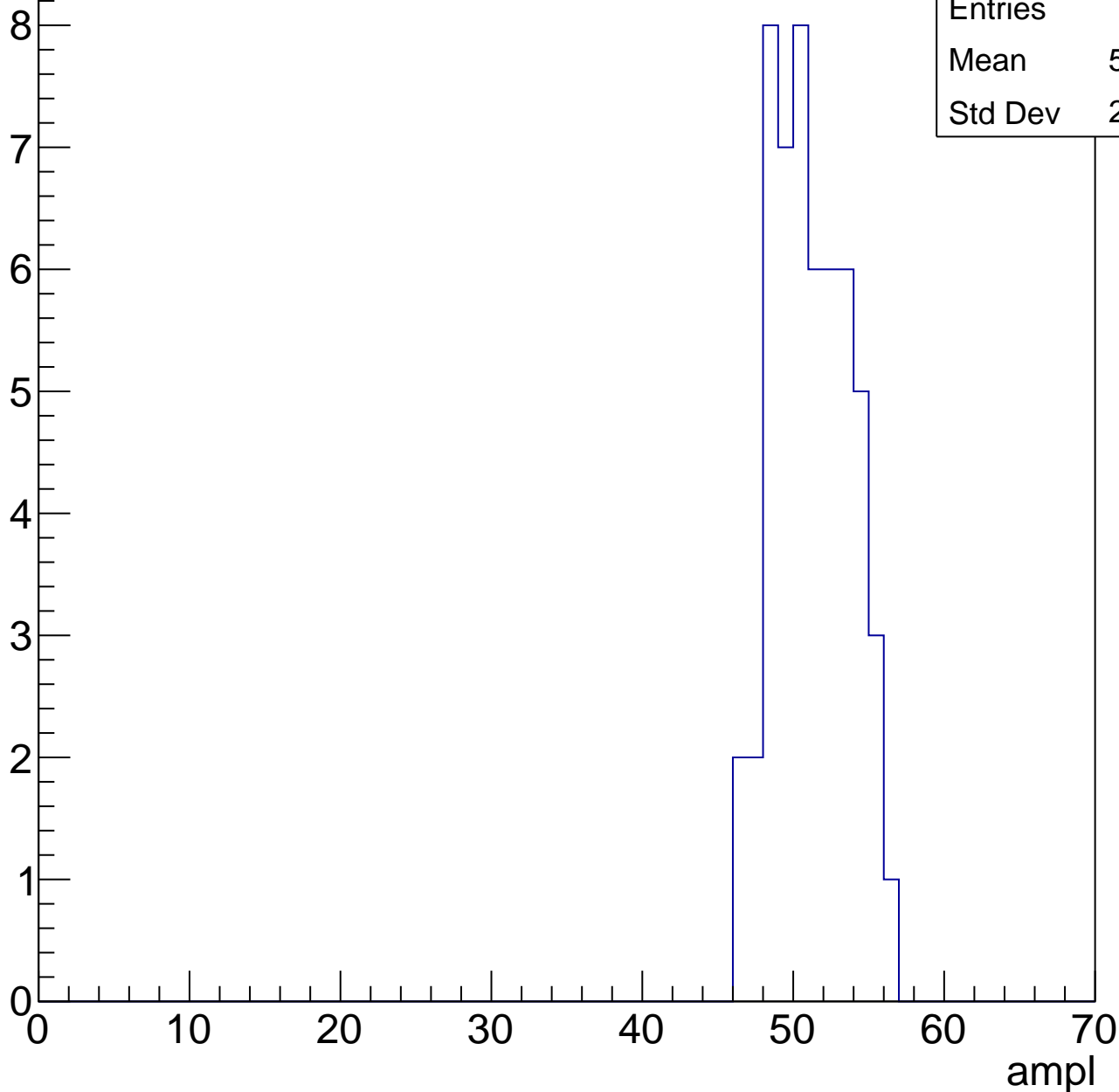


B0L001S, U25-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

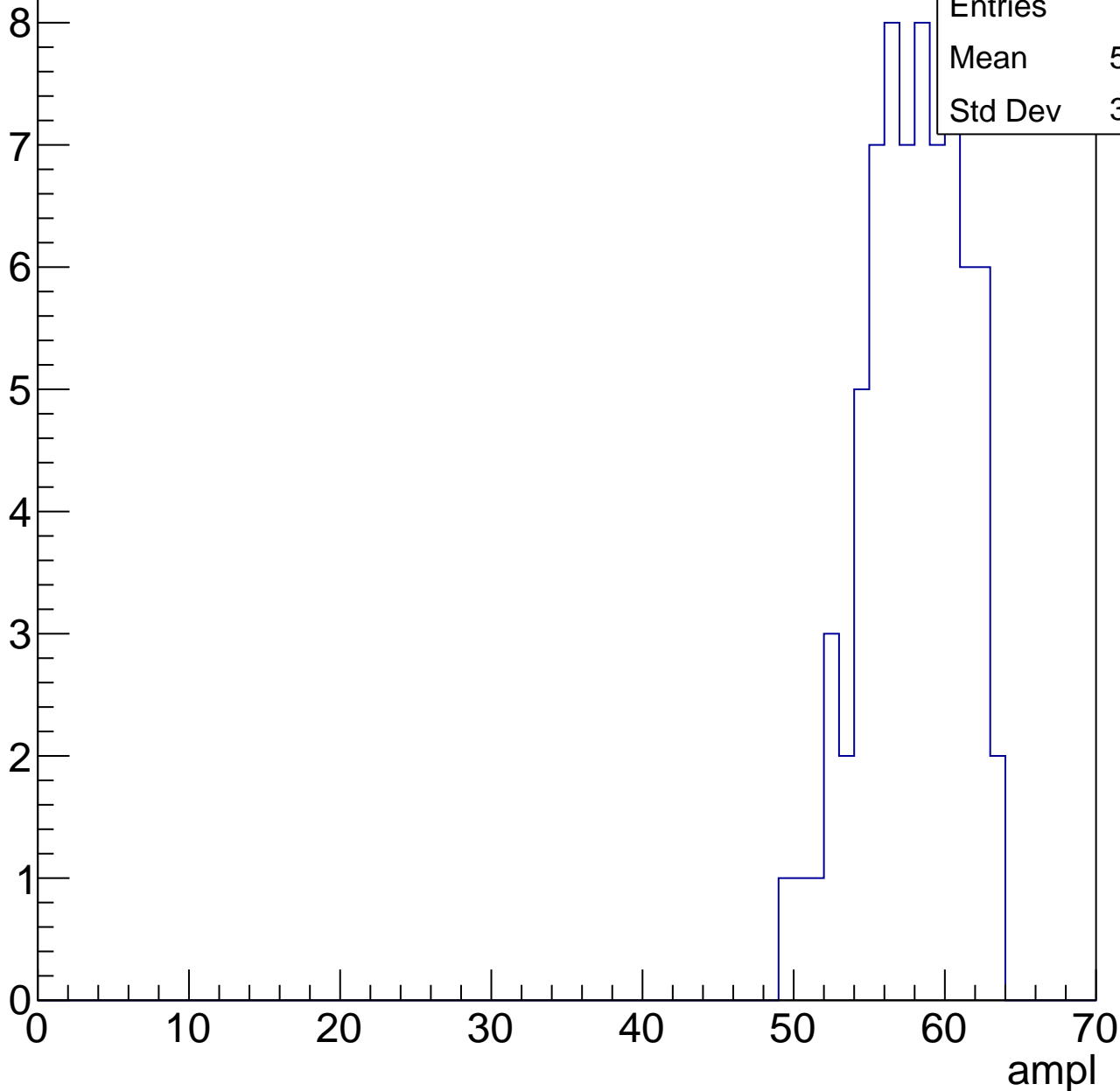
Entries	54
Mean	50.74
Std Dev	2.488



B0L001S, U25-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

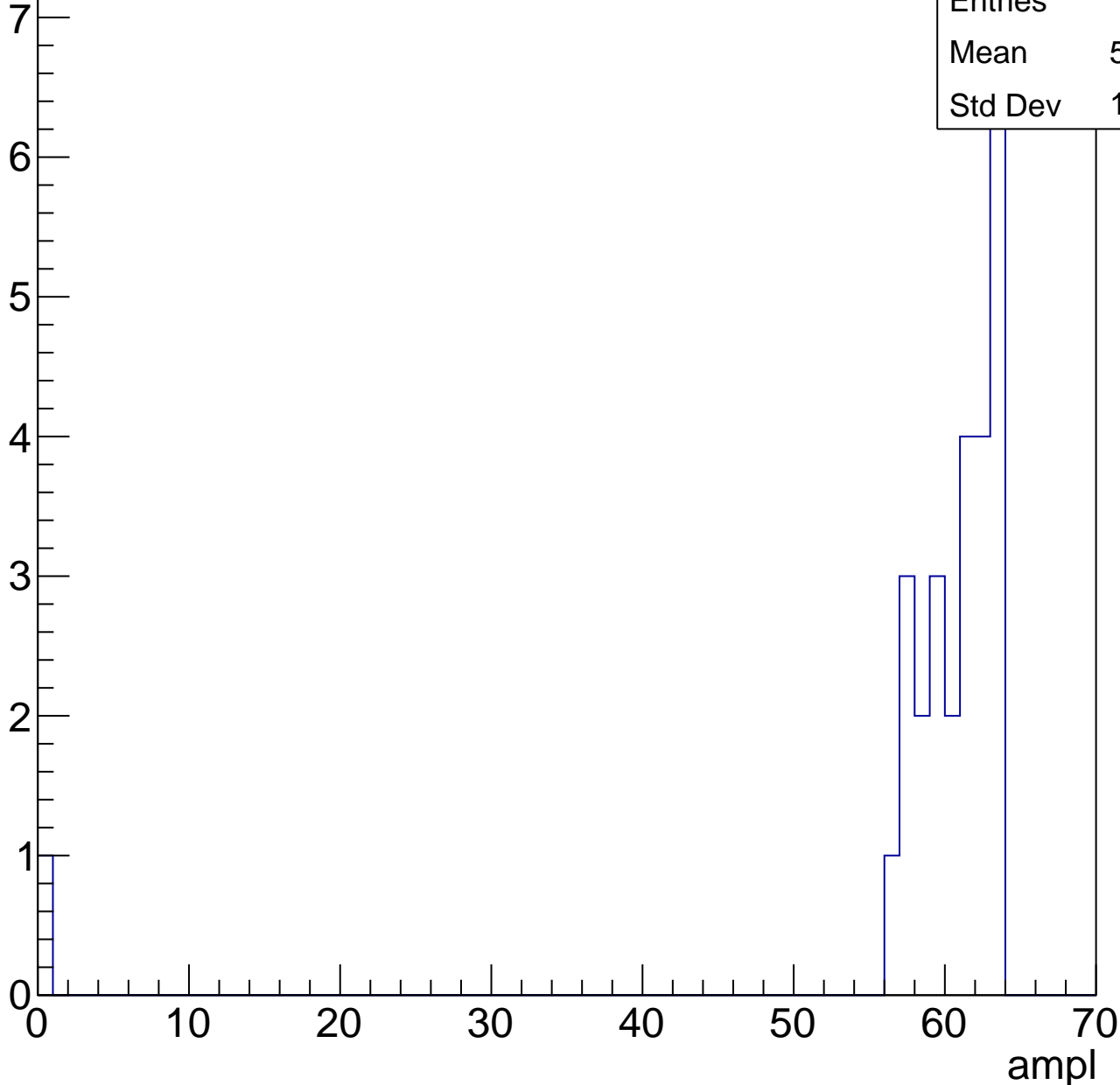


B0L001S, U25-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.26
Std Dev	11.64



B0L001S, U25-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch3, adc0

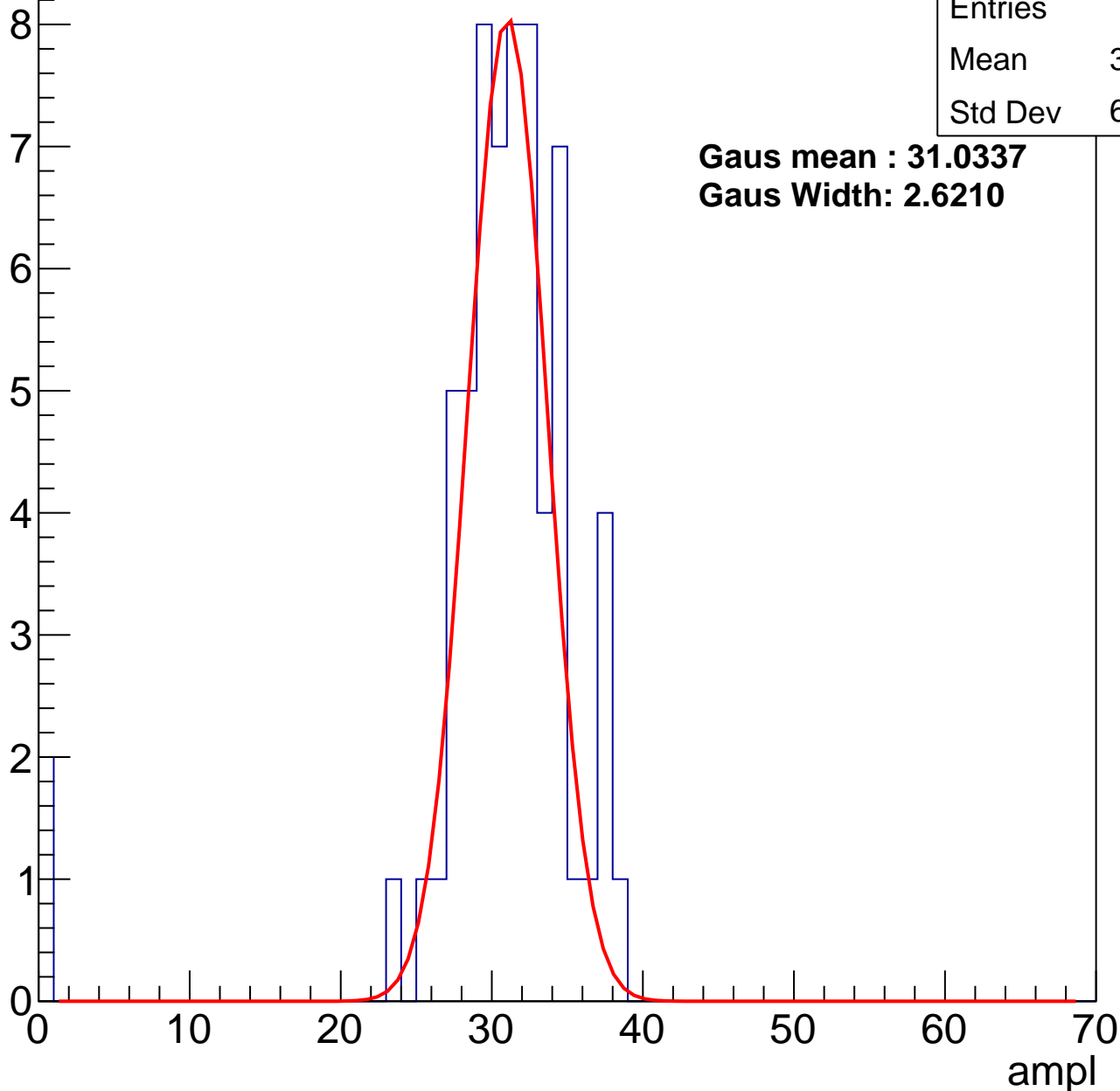
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.03
Std Dev	6.215

Gaus mean : 31.0337

Gaus Width: 2.6210



B0L001S, U25-ch3, adc1

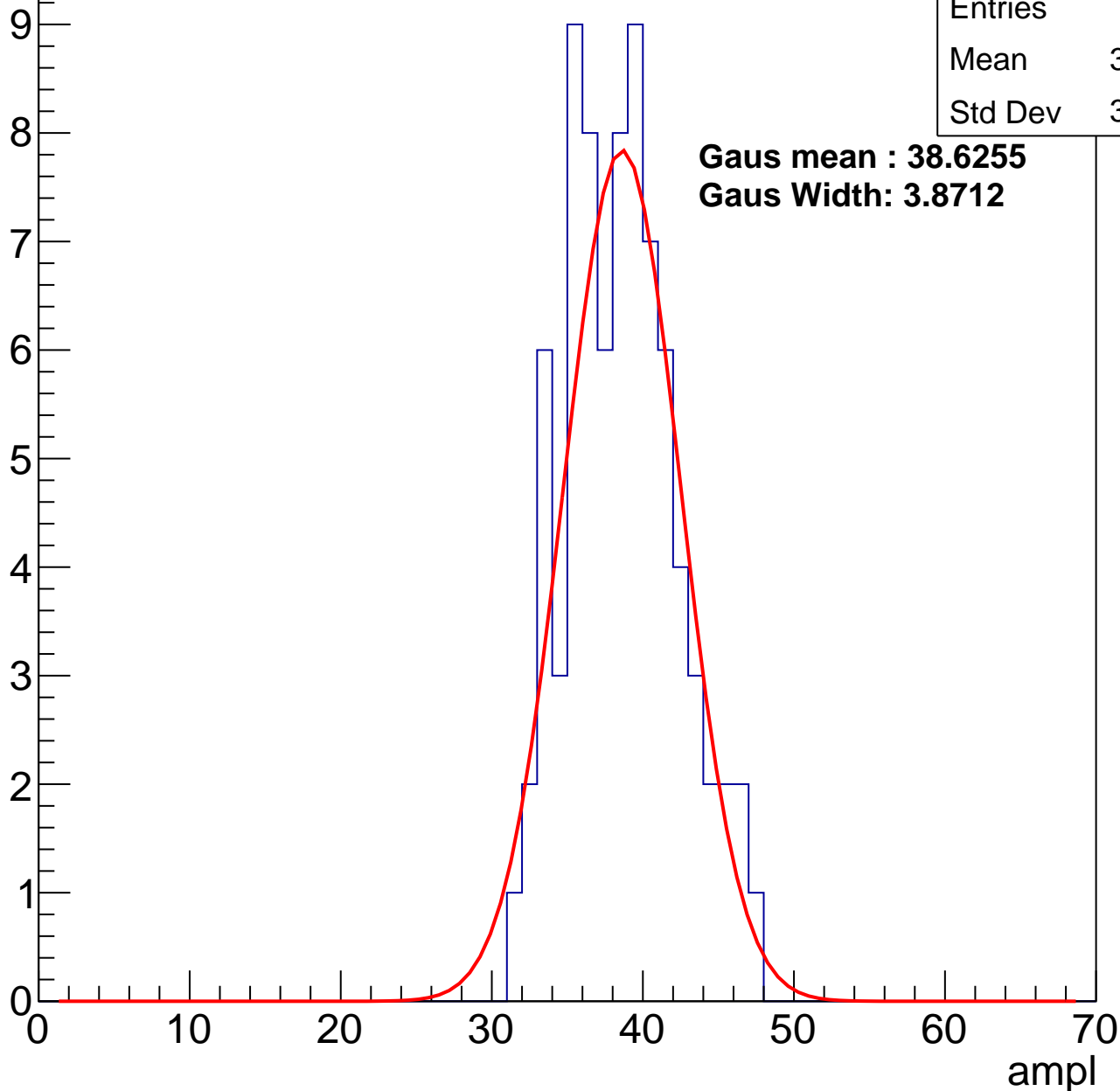
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	38.16
Std Dev	3.637

Gaus mean : 38.6255

Gaus Width: 3.8712



B0L001S, U25-ch3, adc2

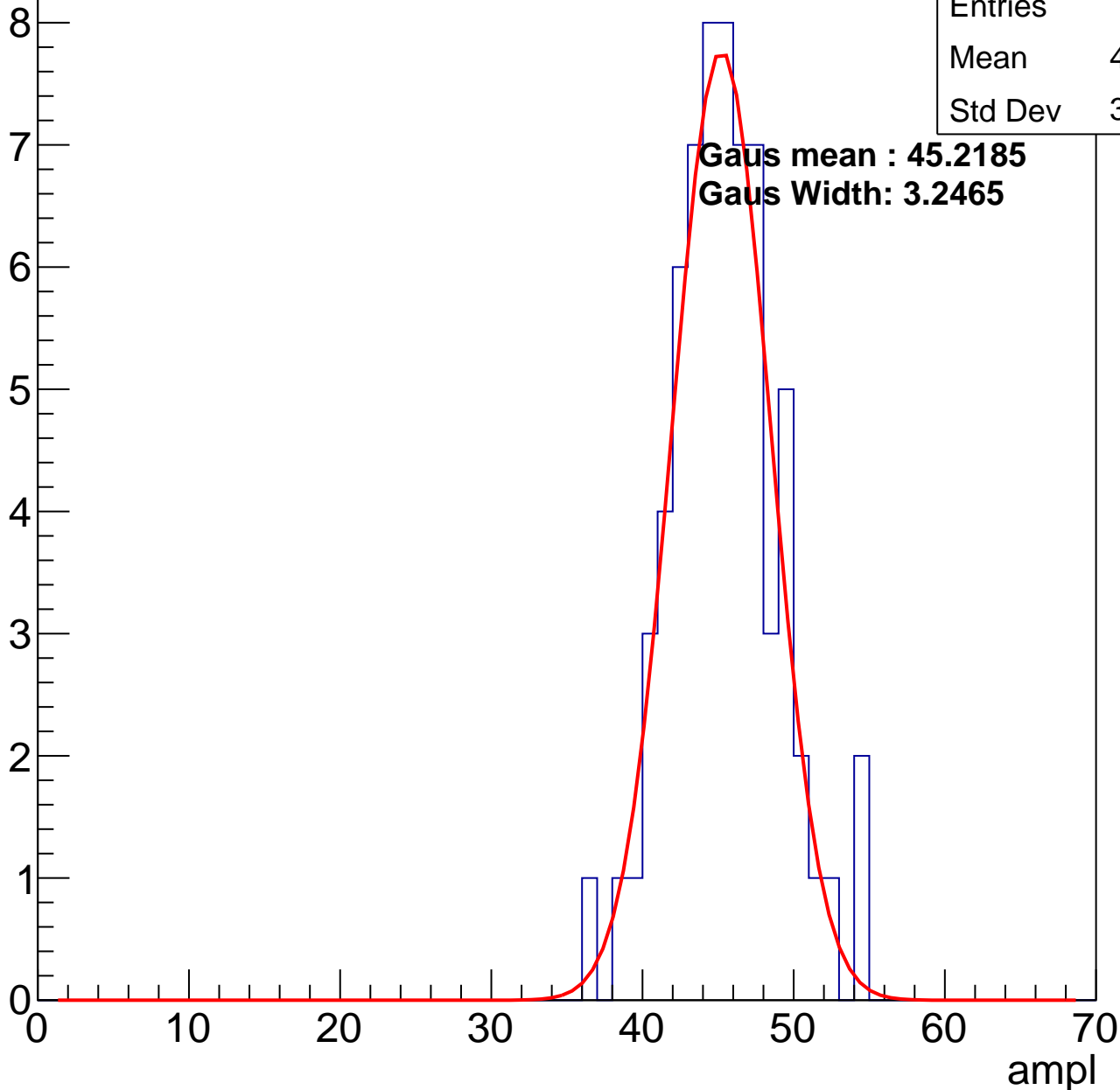
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.97
Std Dev	3.528

Gaus mean : 45.2185

Gaus Width: 3.2465

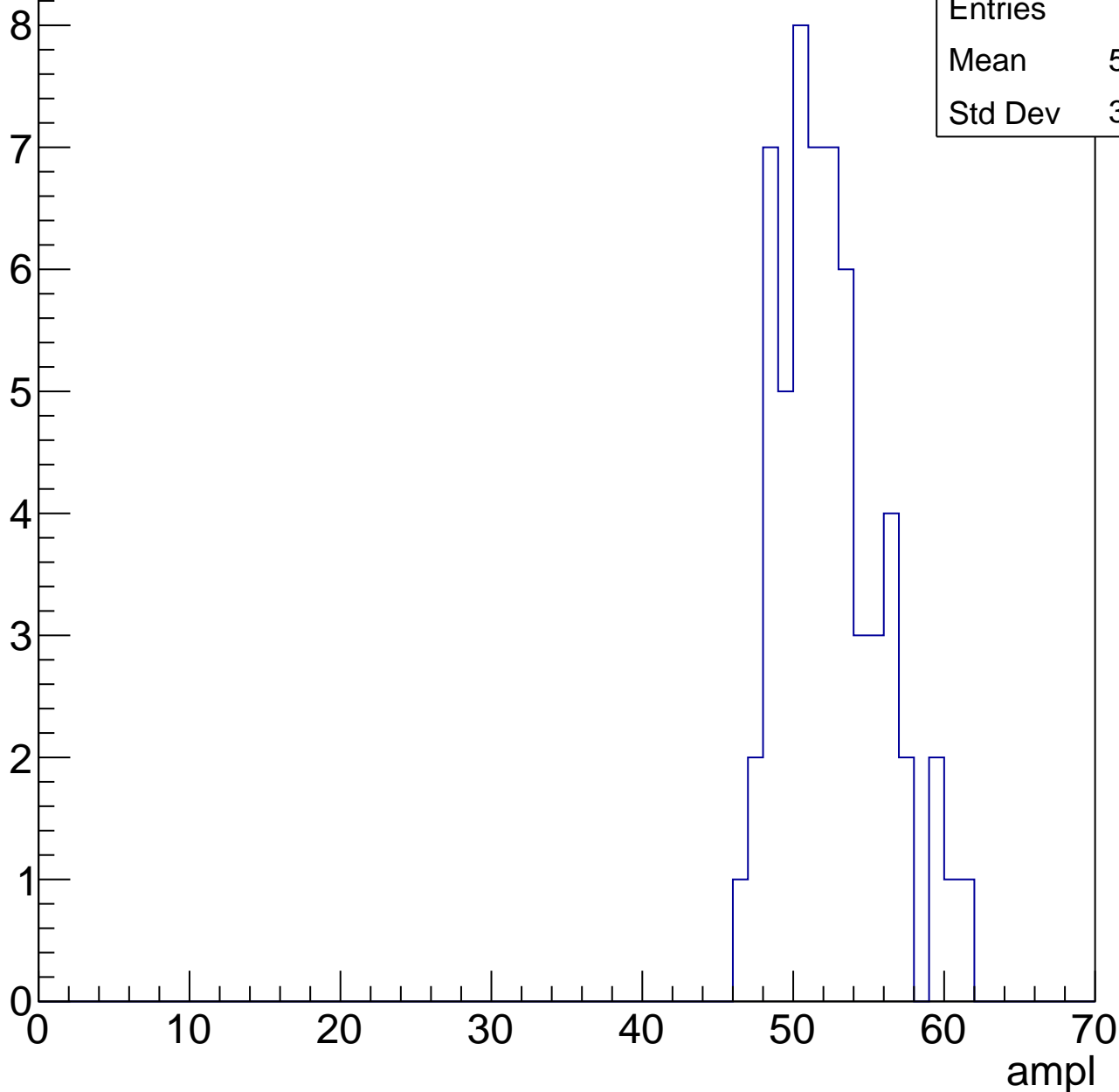


B0L001S, U25-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	51.93
Std Dev	3.404

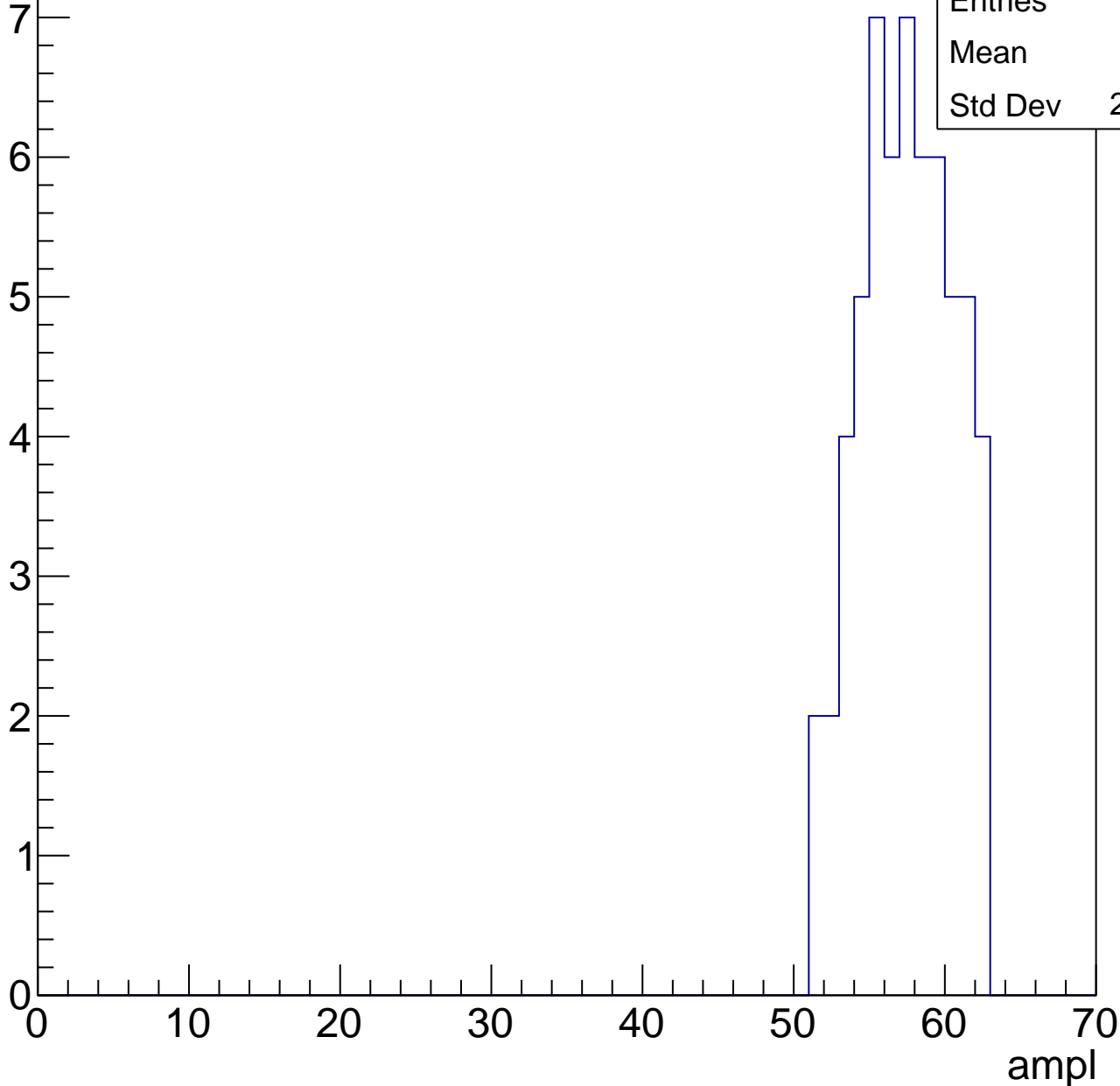


B0L001S, U25-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

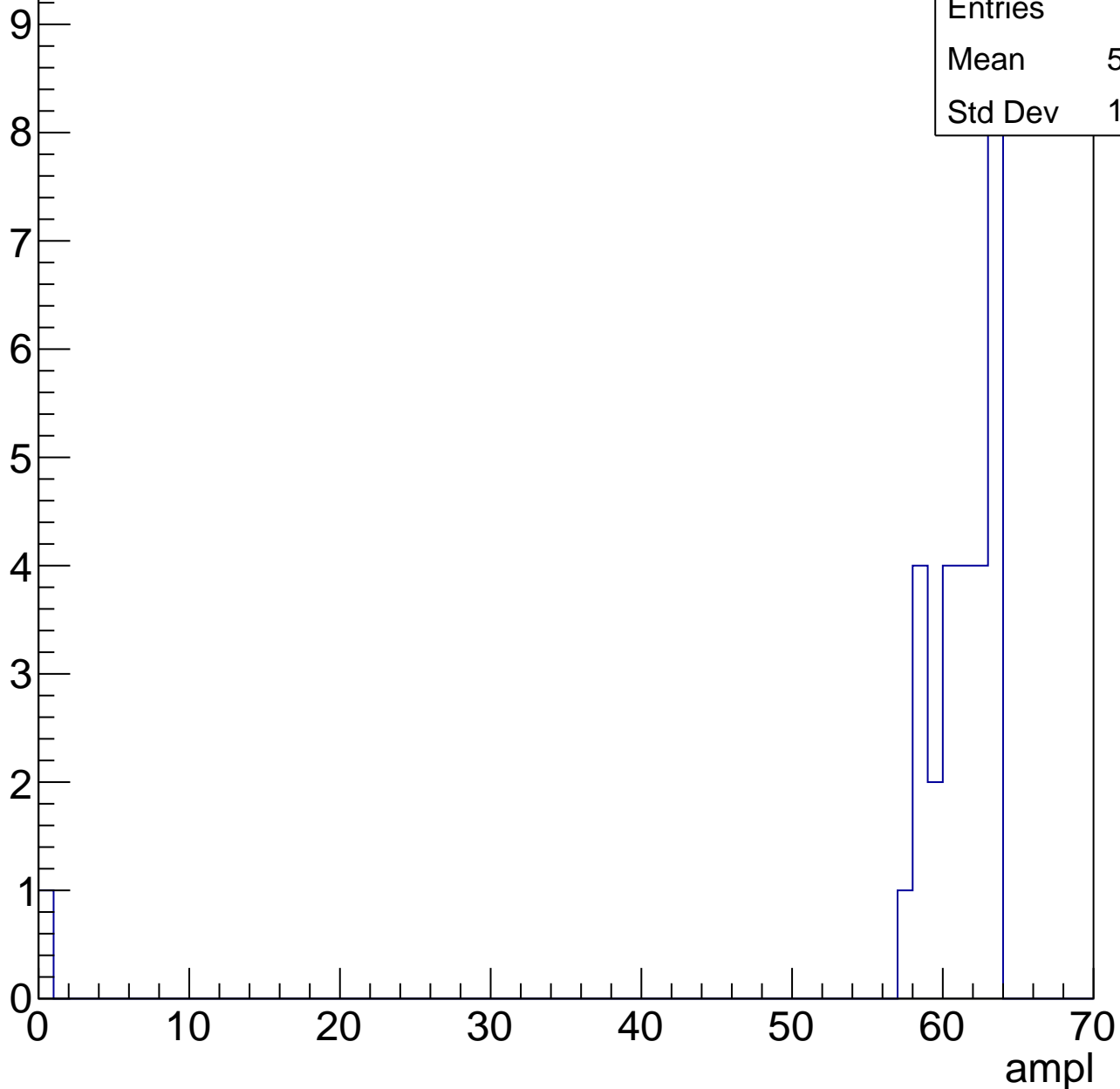
Entries	59
Mean	57
Std Dev	2.969



B0L001S, U25-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch4, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.59
Std Dev	4.805

Gaus mean : 31.6376

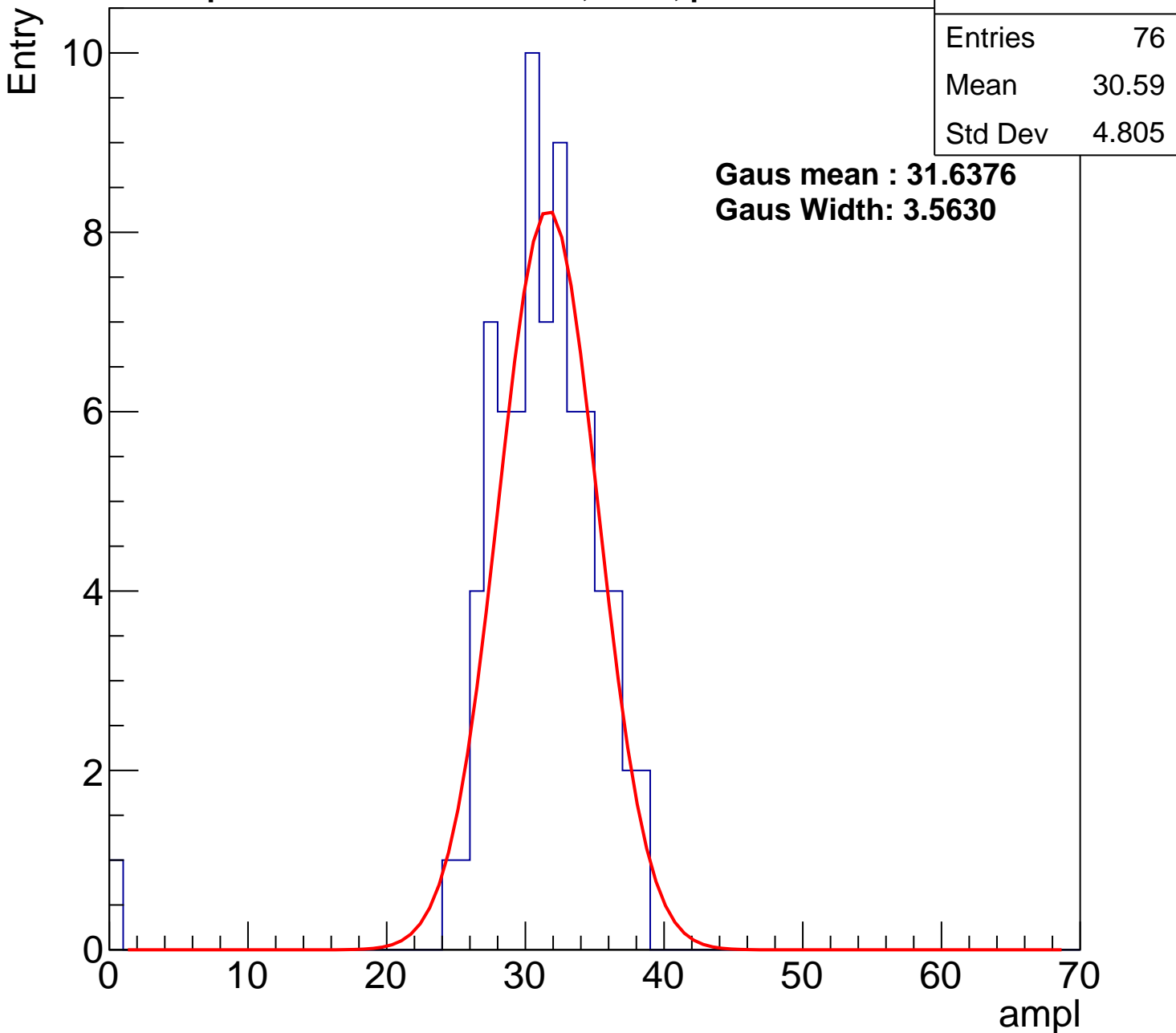
Gaus Width: 3.5630

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch4, adc1

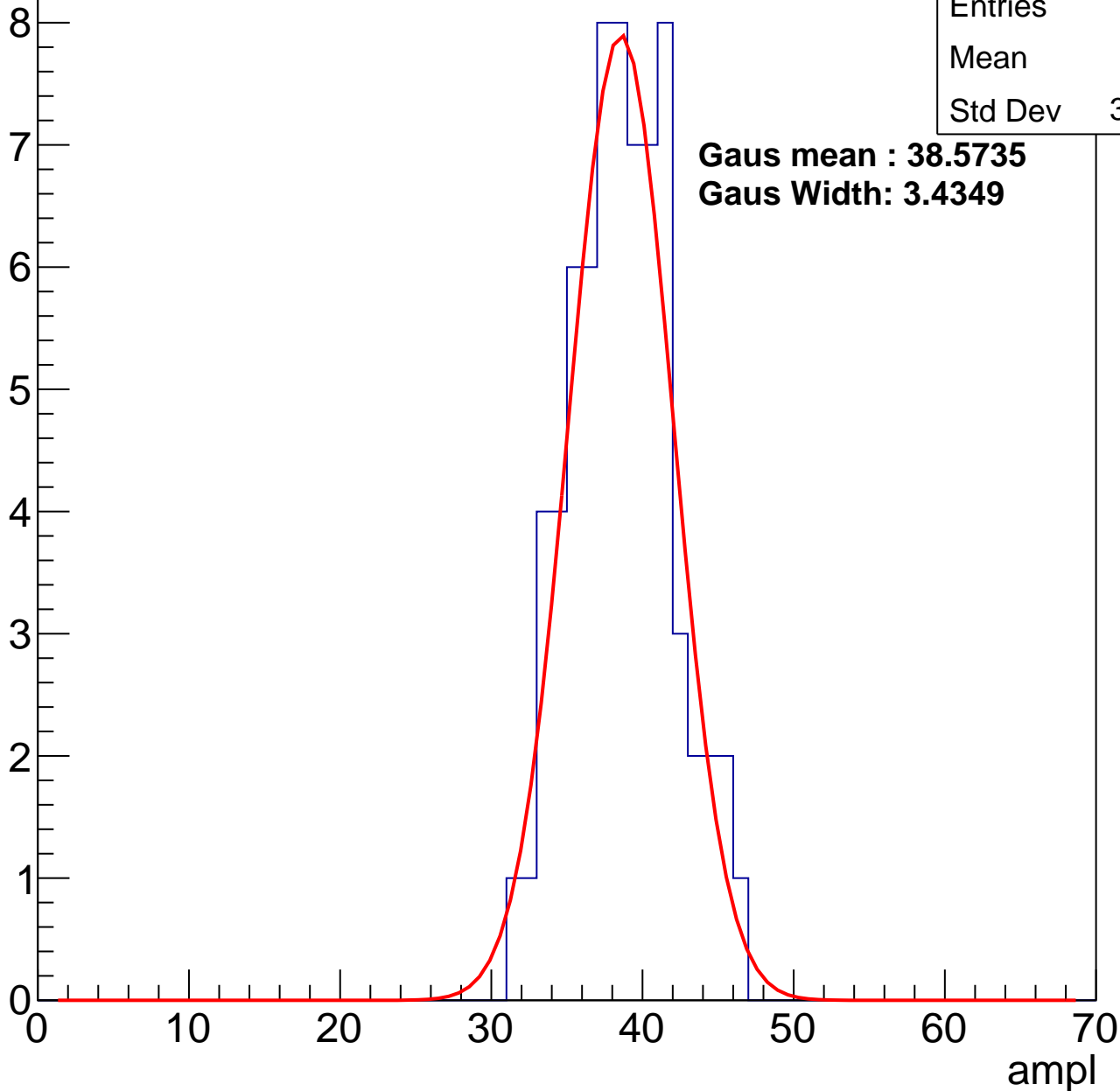
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.2
Std Dev	3.319

Gaus mean : 38.5735

Gaus Width: 3.4349



B0L001S, U25-ch4, adc2

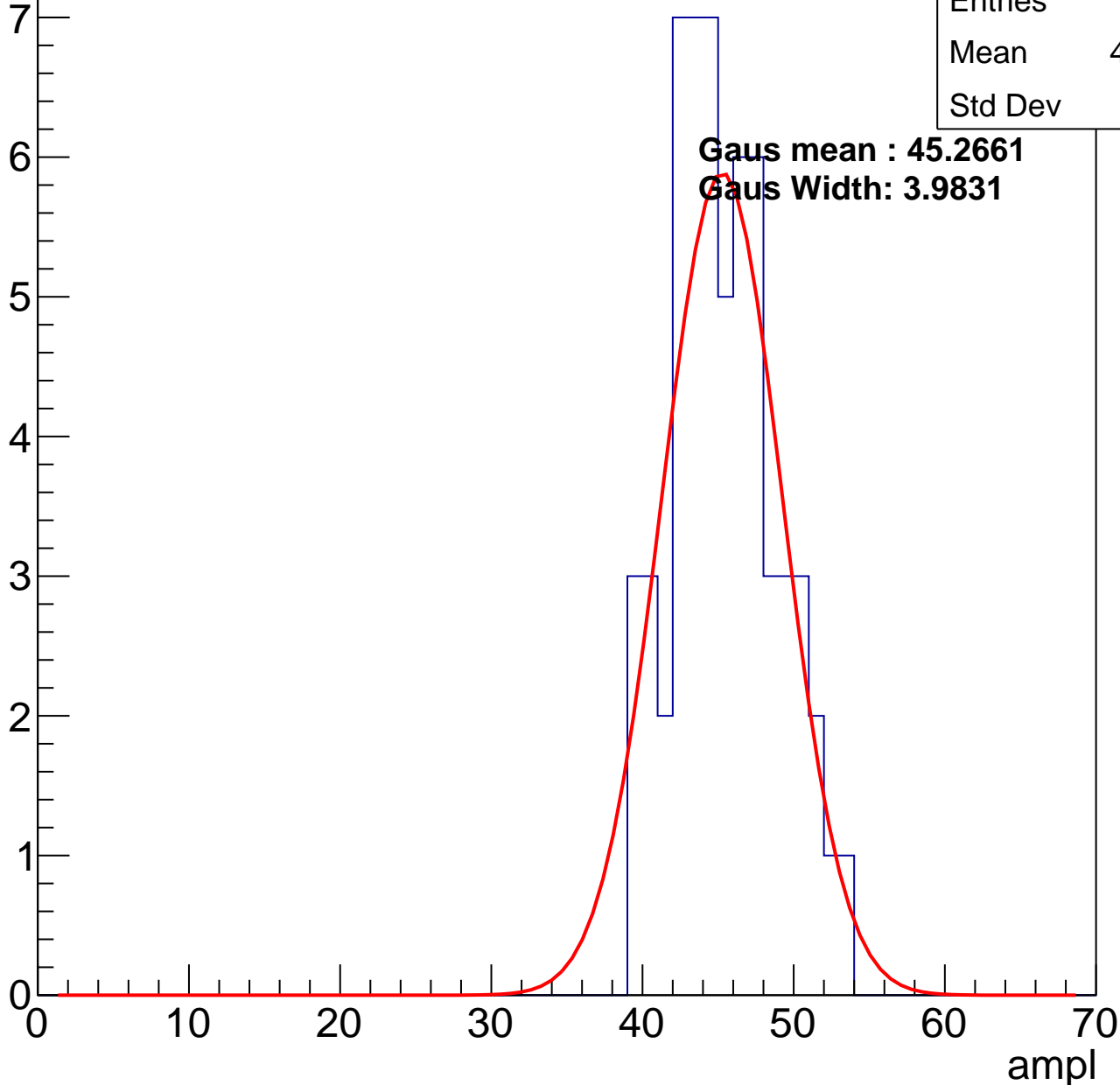
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.97
Std Dev	3.39

Gaus mean : 45.2661

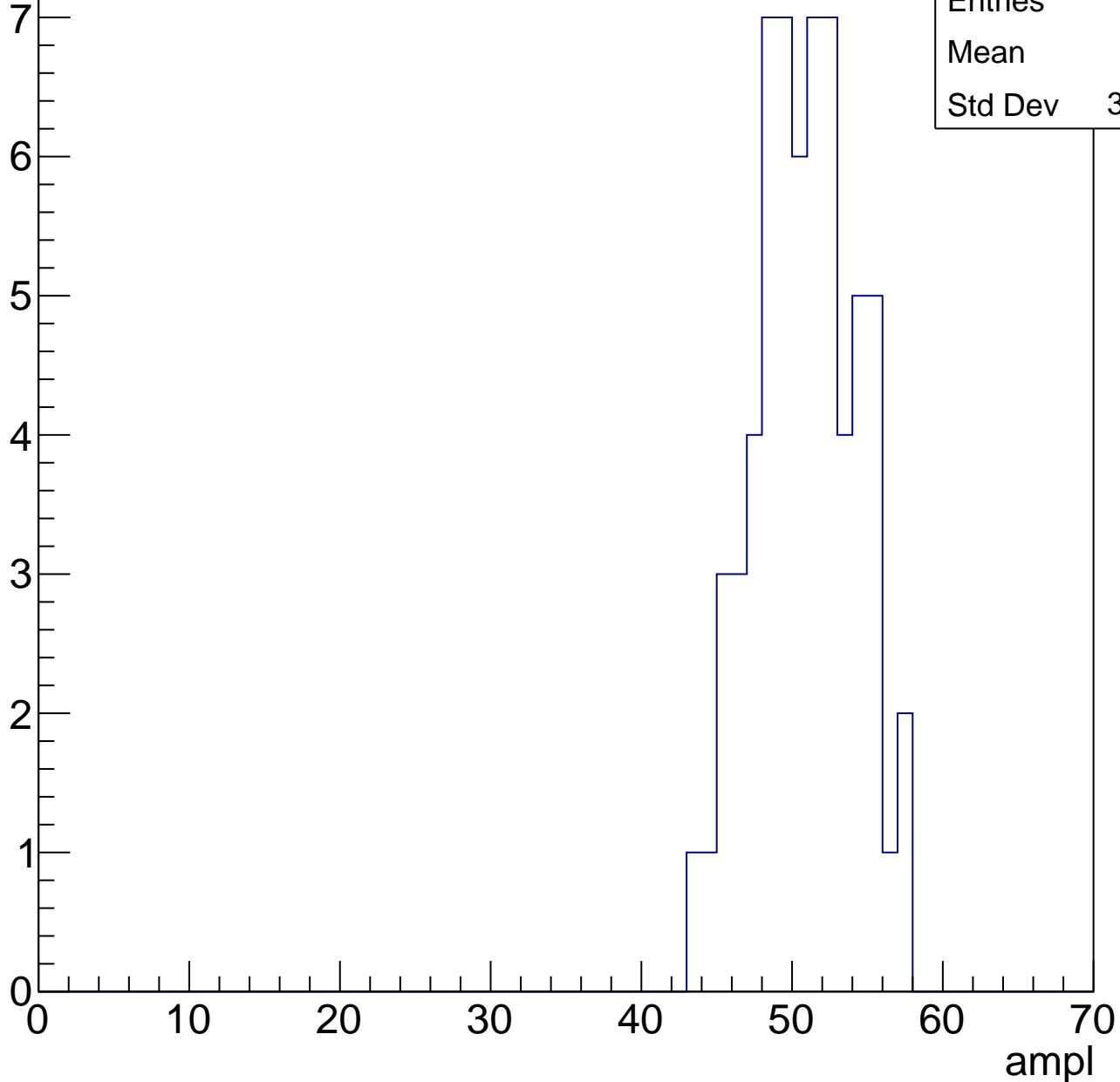
Gaus Width: 3.9831



B0L001S, U25-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

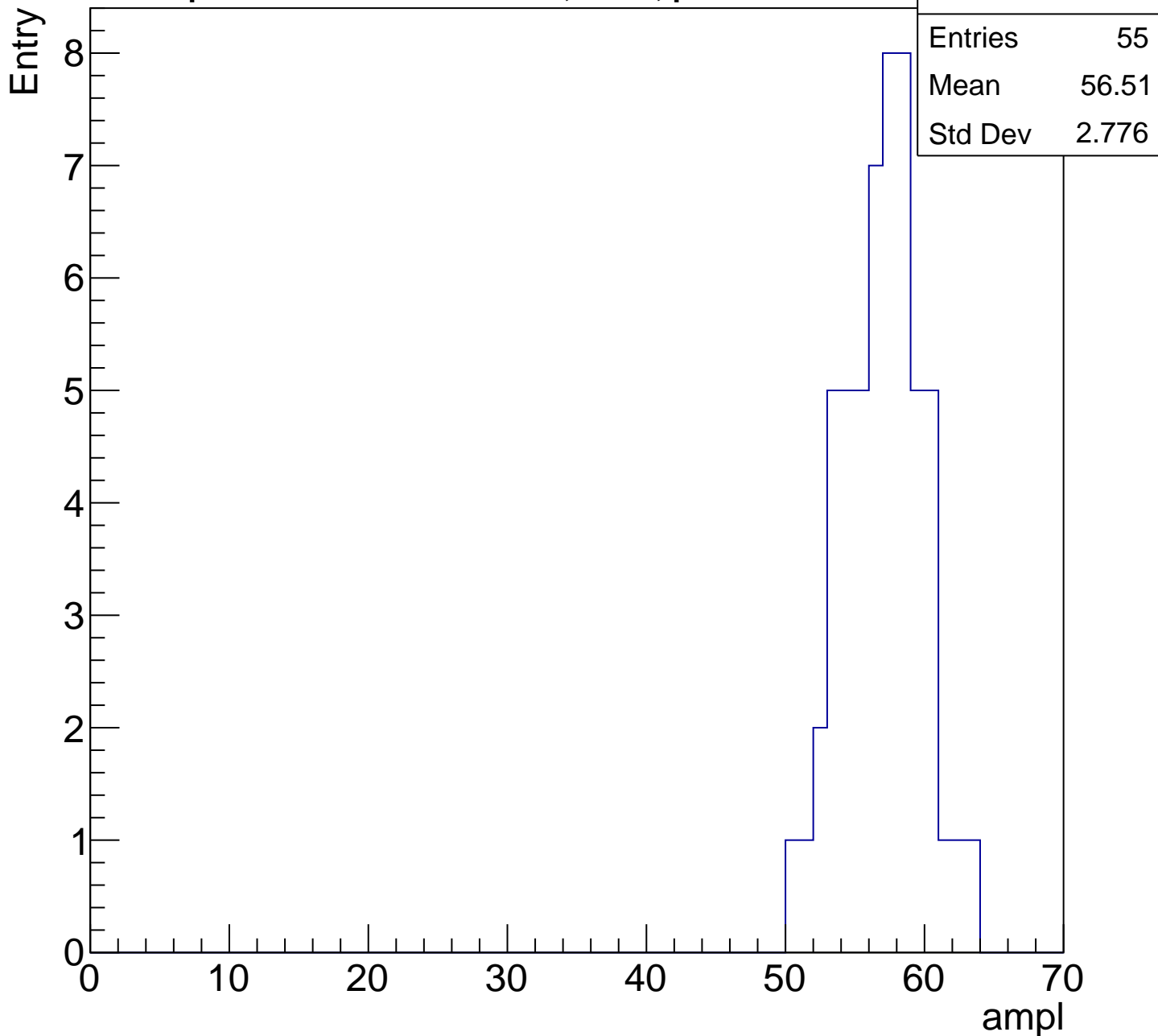
Entry



Entries	63
Mean	50.4
Std Dev	3.283

B0L001S, U25-ch4, adc4

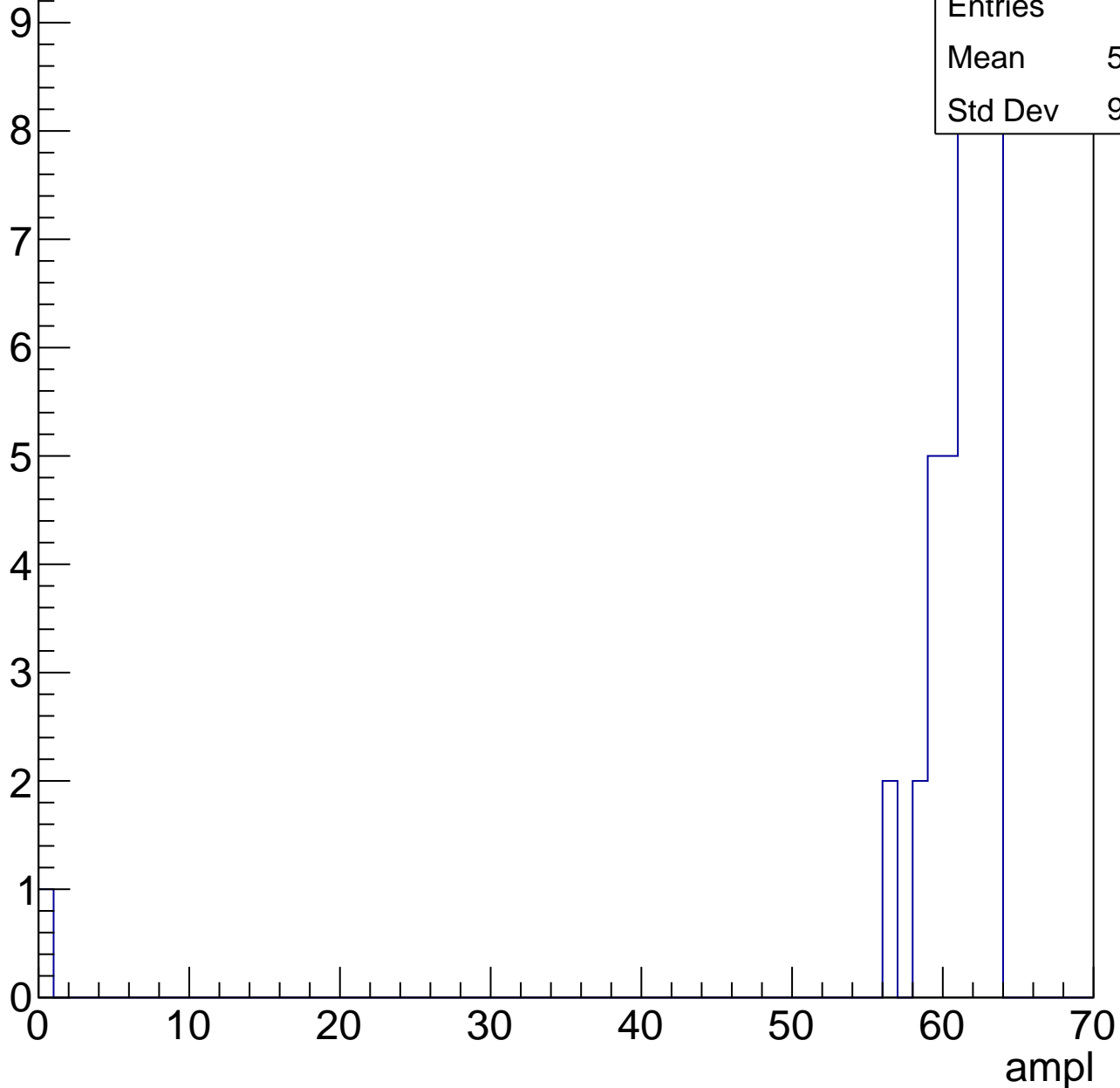
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch5, adc0

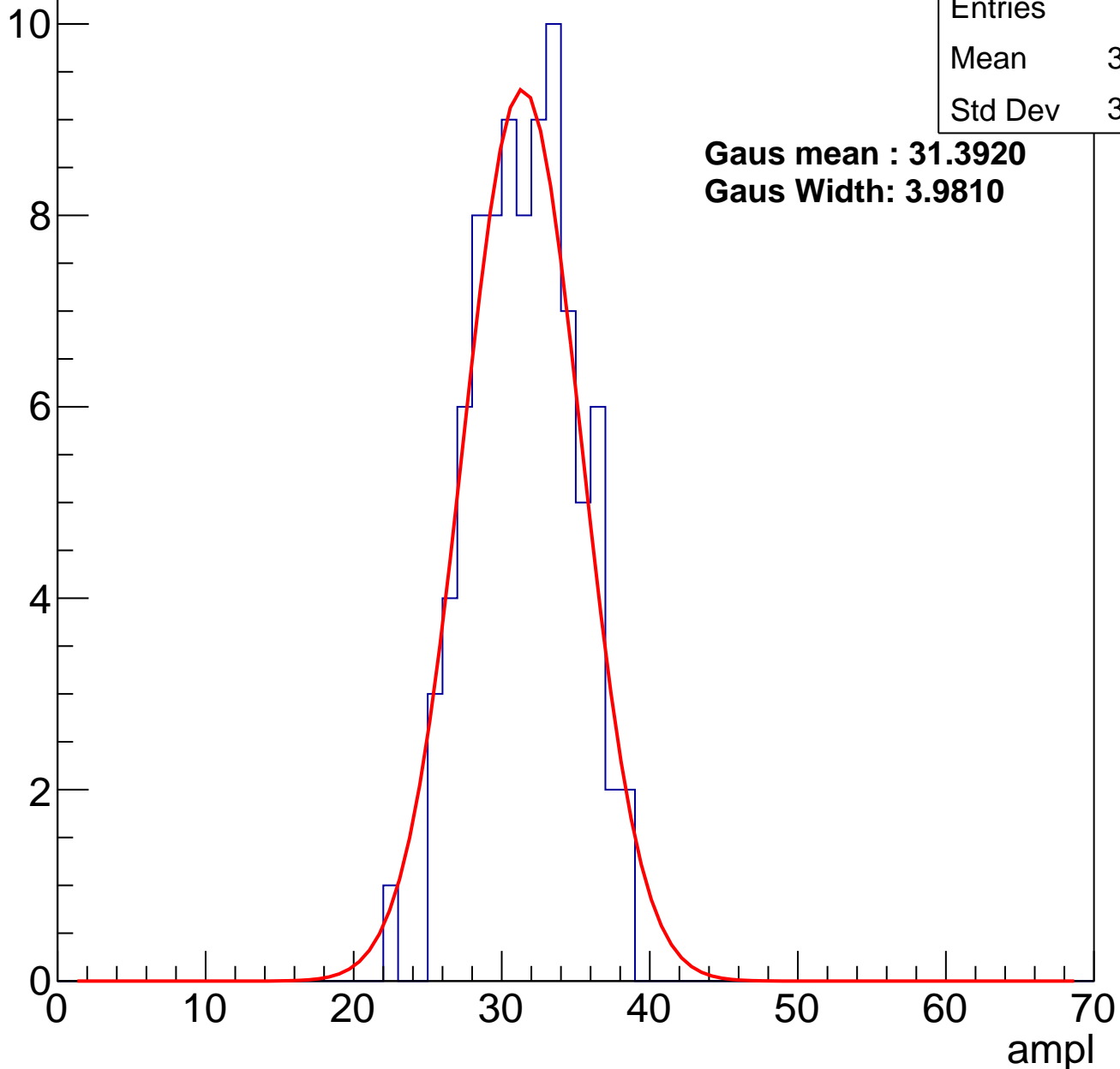
calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	31.07
Std Dev	3.387

Gaus mean : 31.3920

Gaus Width: 3.9810

Entry



B0L001S, U25-ch5, adc1

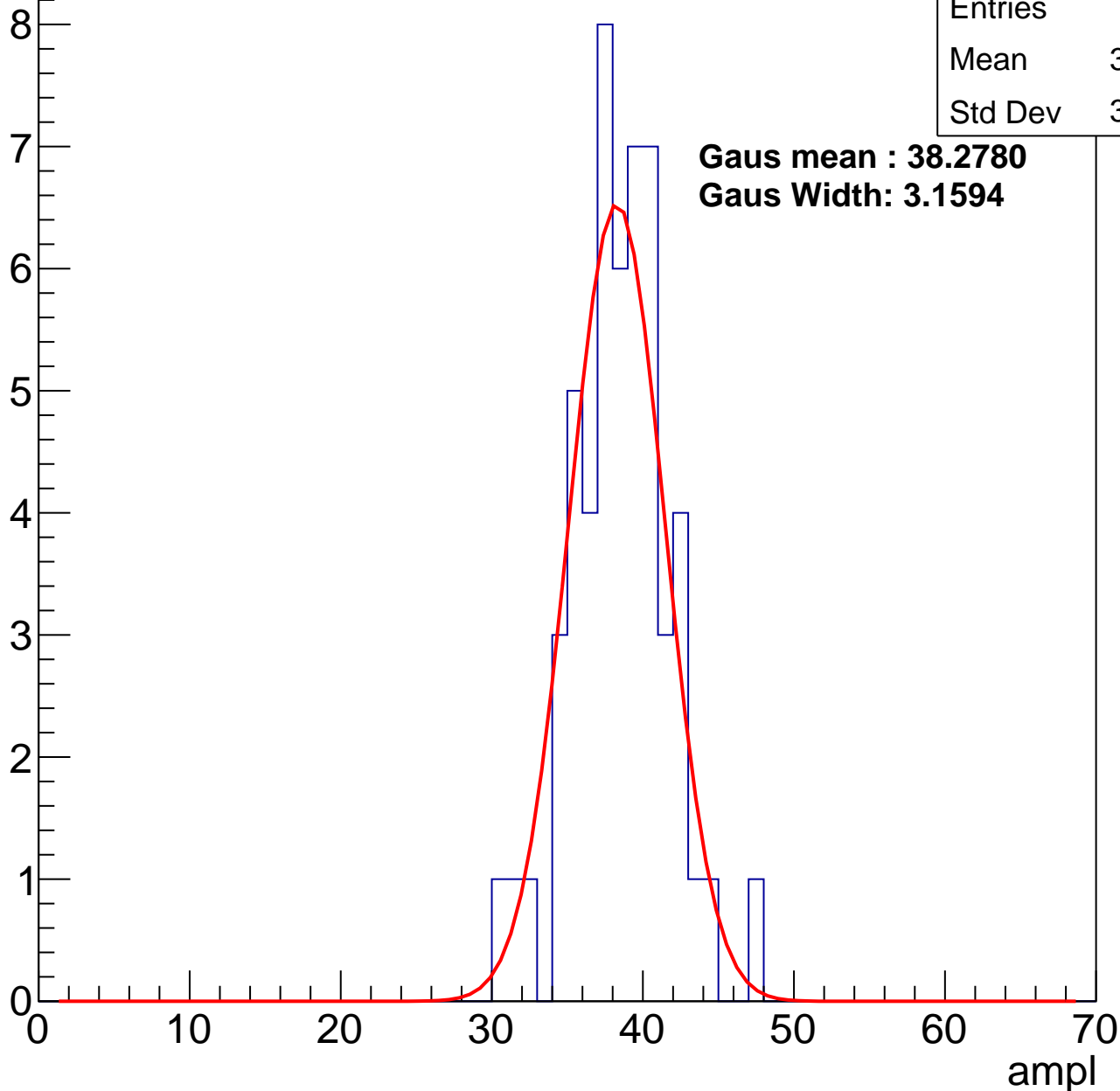
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	38.04
Std Dev	3.174

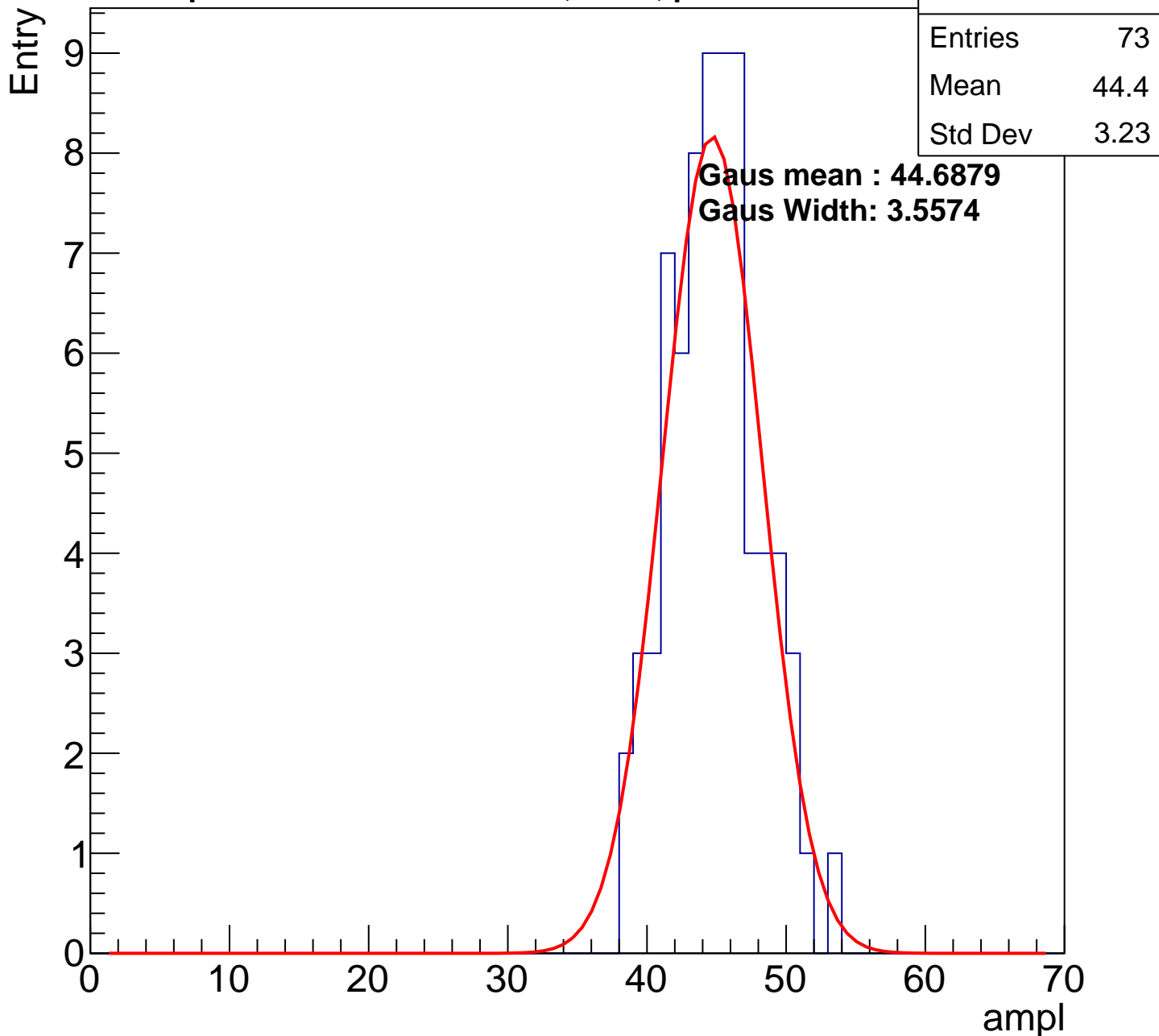
Gaus mean : 38.2780

Gaus Width: 3.1594



B0L001S, U25-ch5, adc2

calib_packv5_042523_0143.root, FC#9, port A1

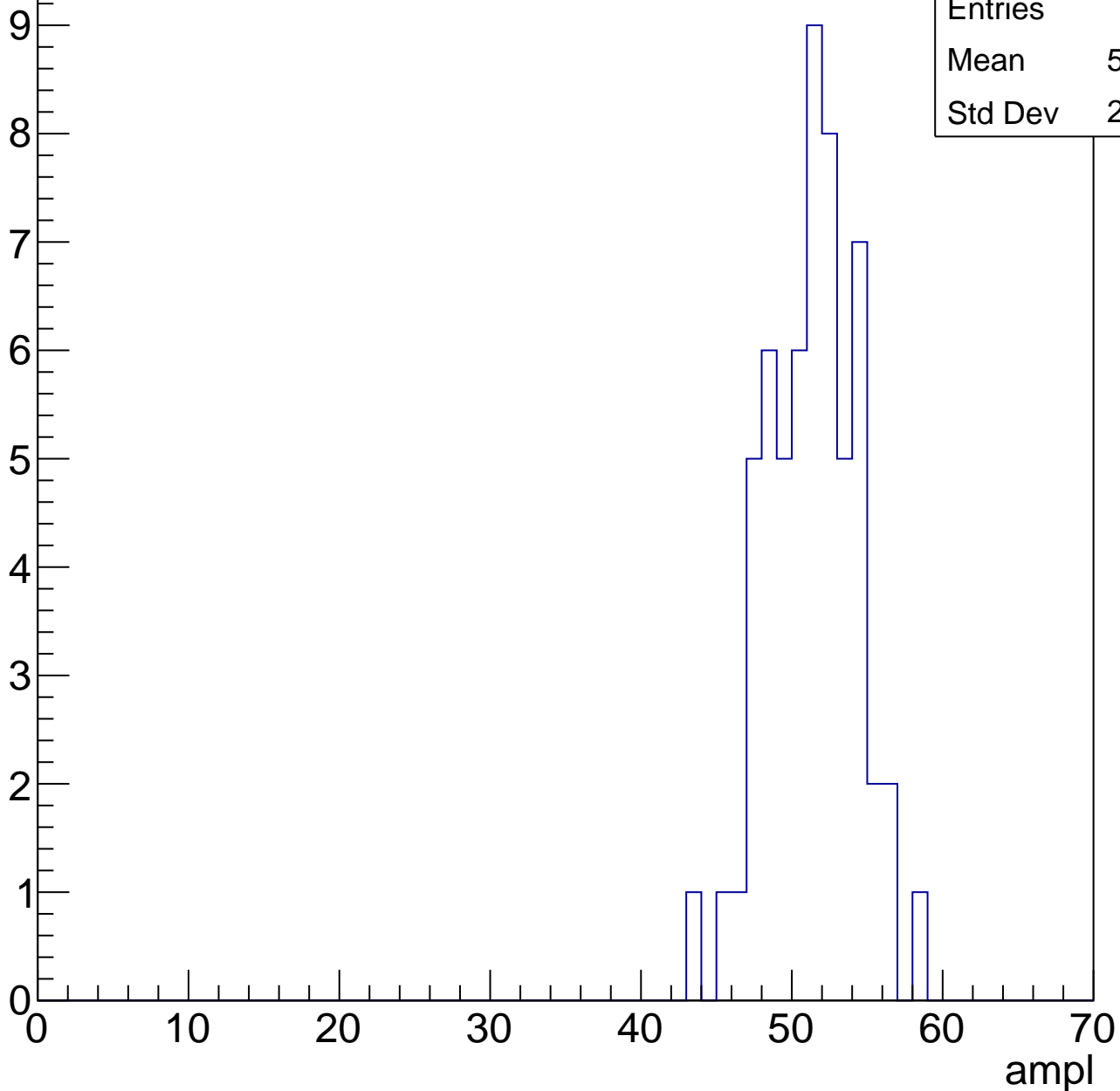


B0L001S, U25-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.85
Std Dev	2.927

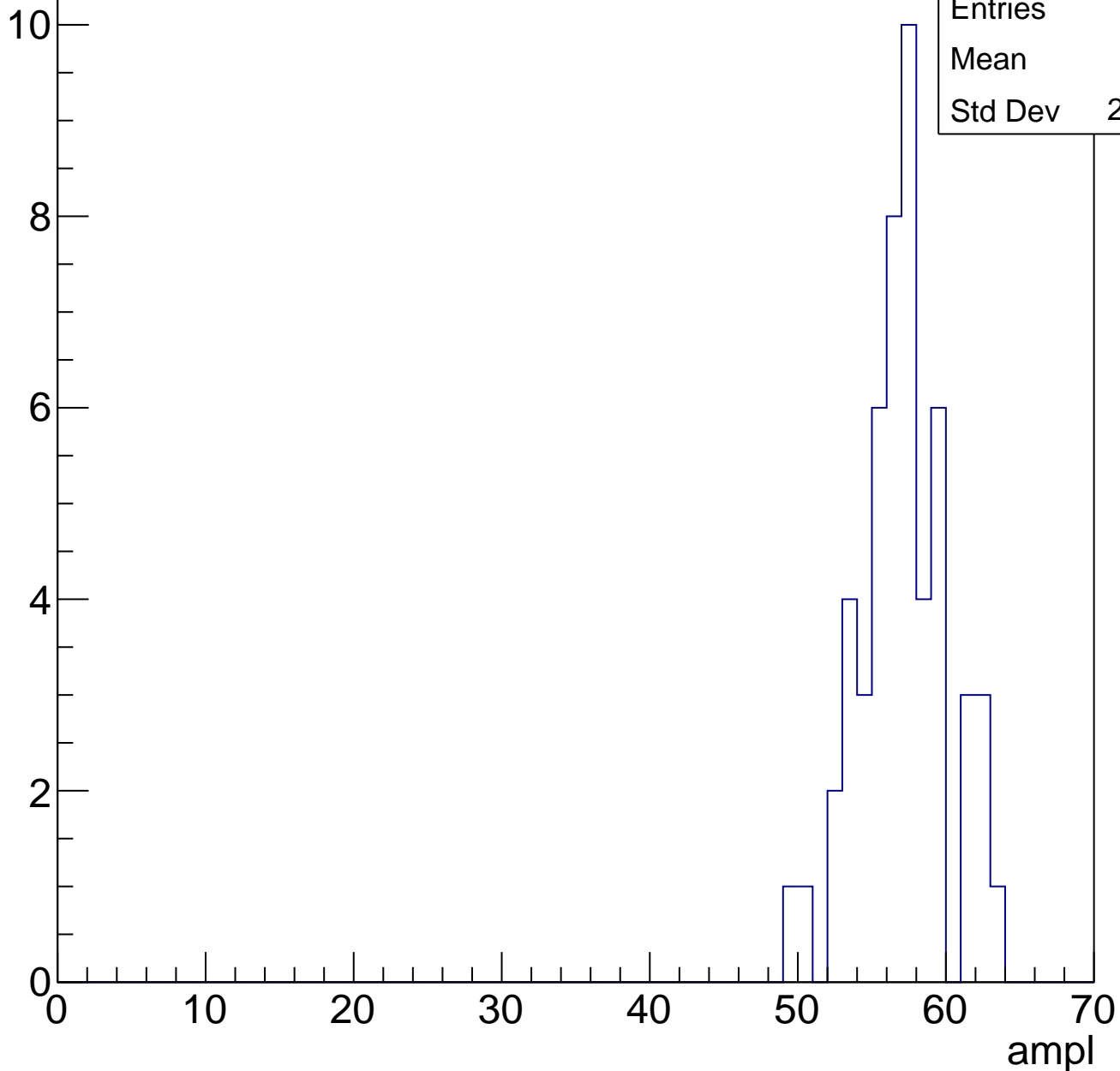


B0L001S, U25-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

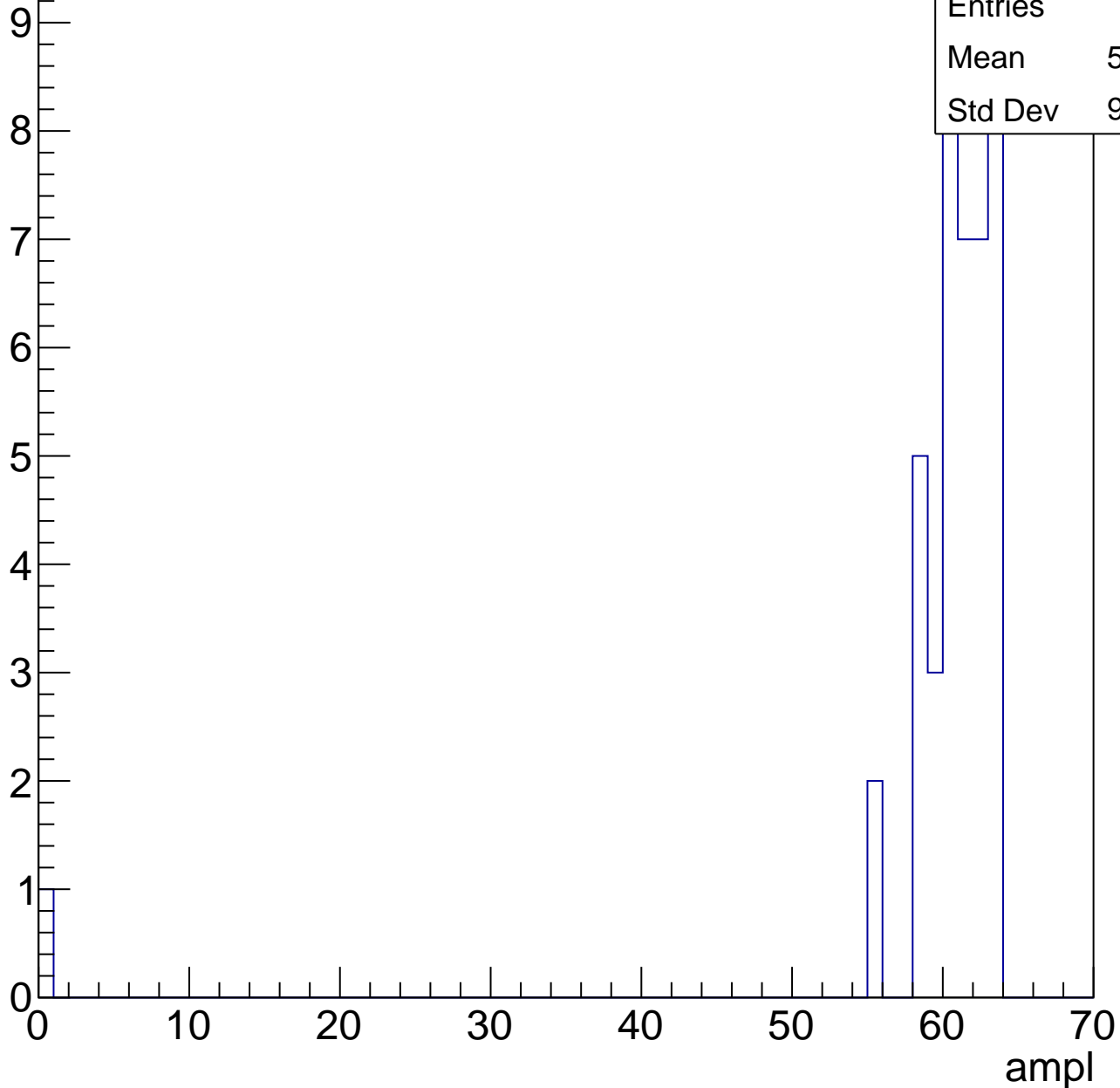
Entries	52
Mean	56.6
Std Dev	2.989



B0L001S, U25-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

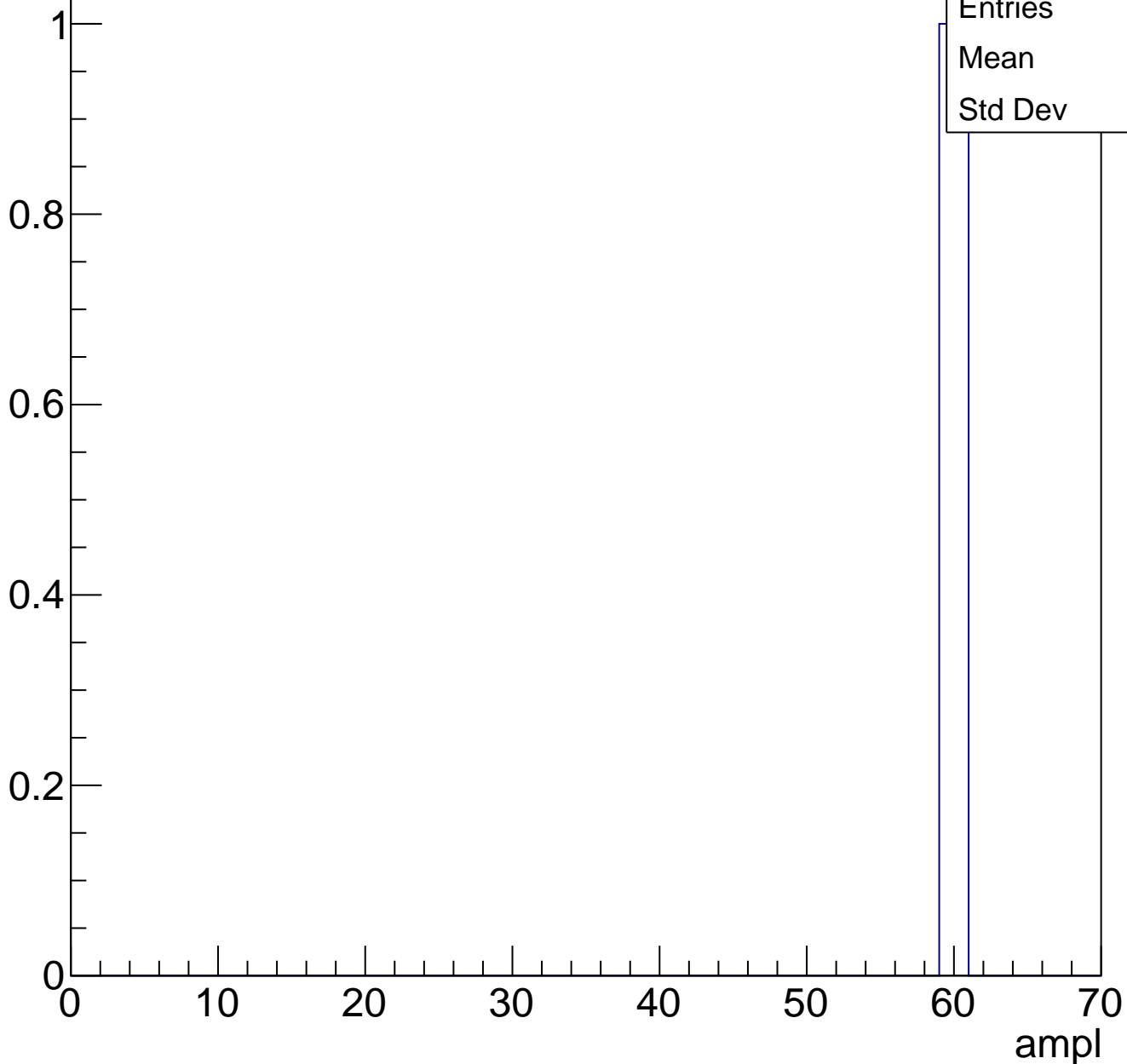
Entry



B0L001S, U25-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch6, adc0

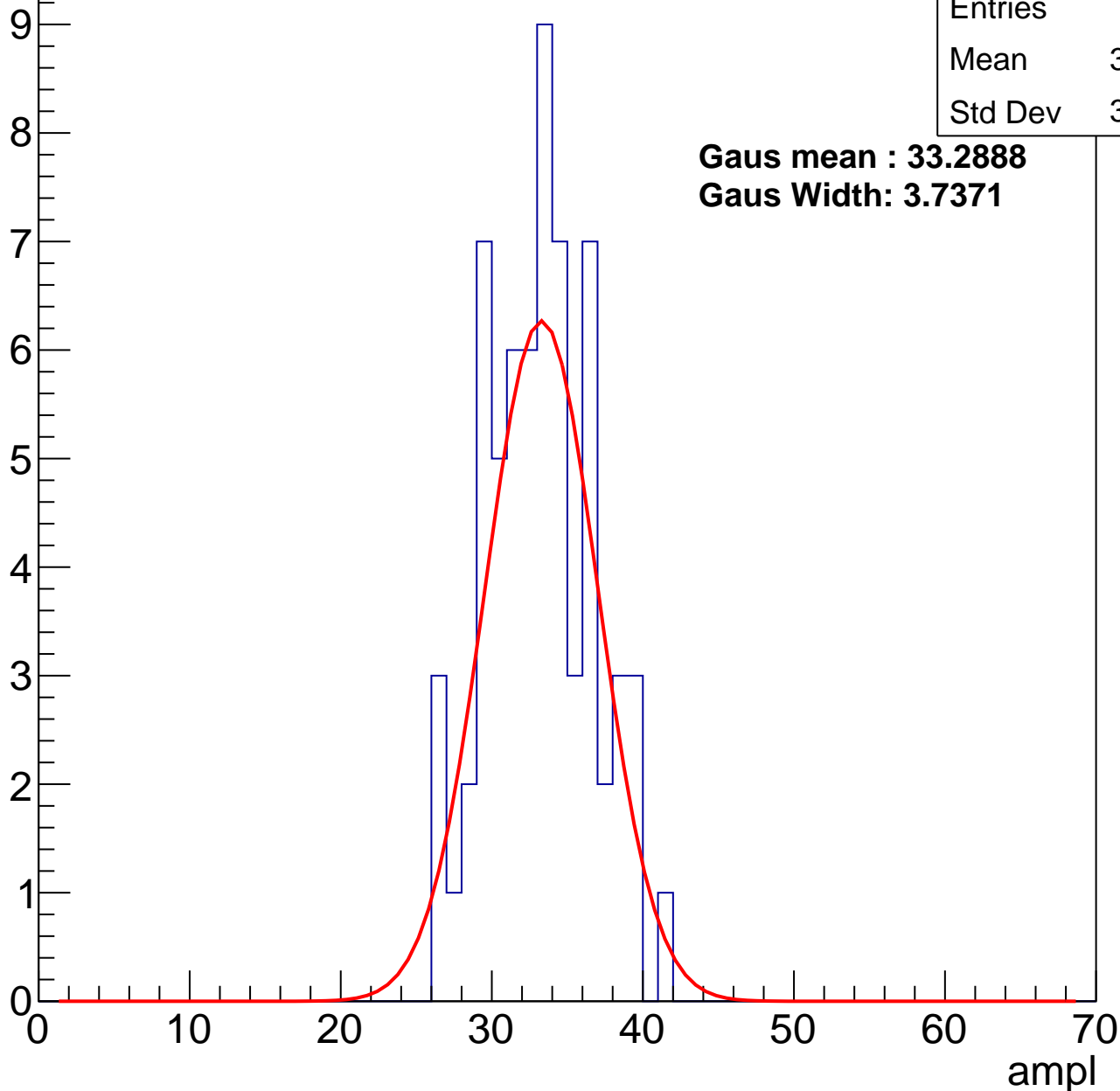
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.77
Std Dev	3.459

Gaus mean : 33.2888

Gaus Width: 3.7371



B0L001S, U25-ch6, adc1

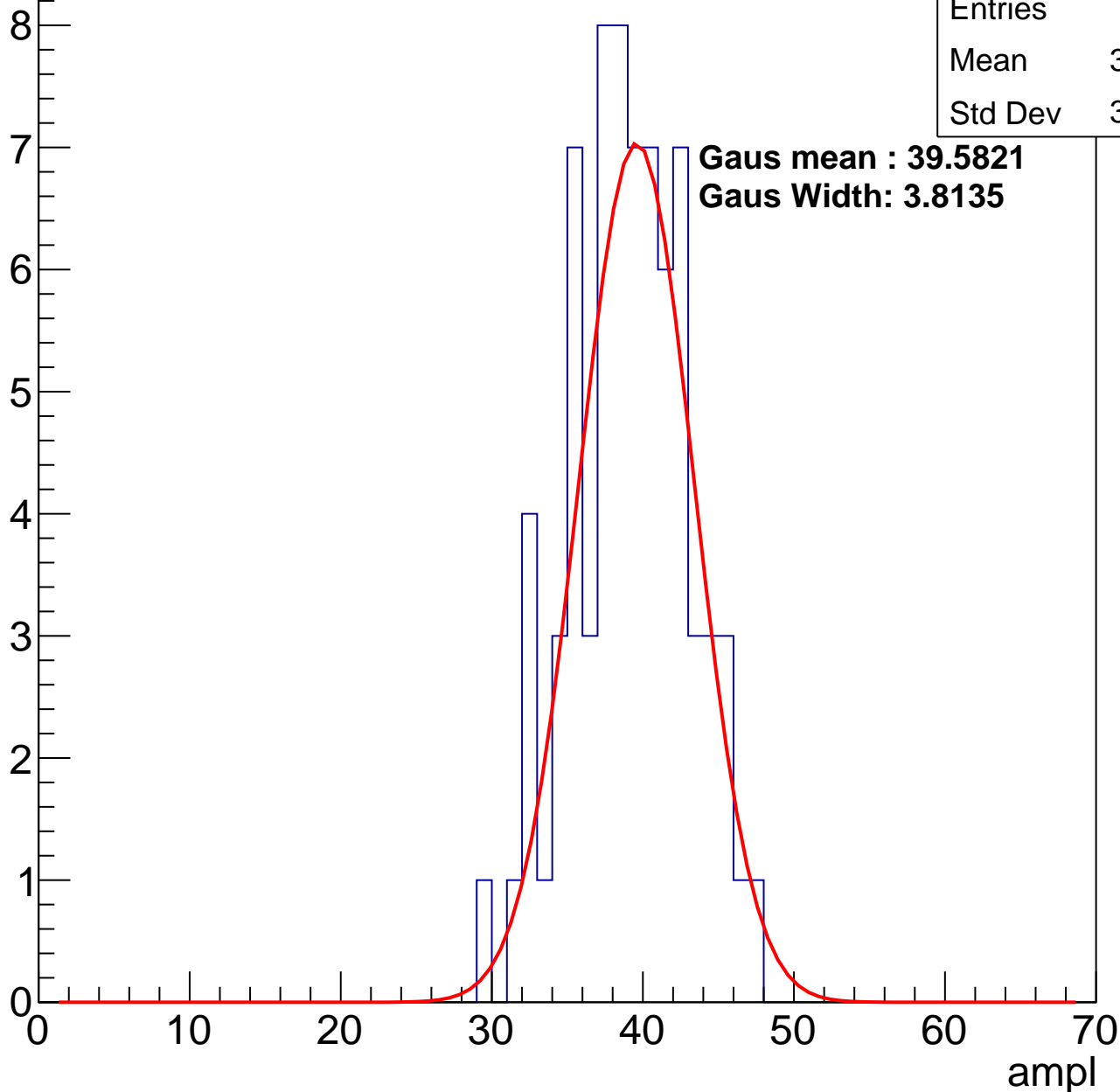
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.62
Std Dev	3.816

Gaus mean : 39.5821

Gaus Width: 3.8135



B0L001S, U25-ch6, adc2

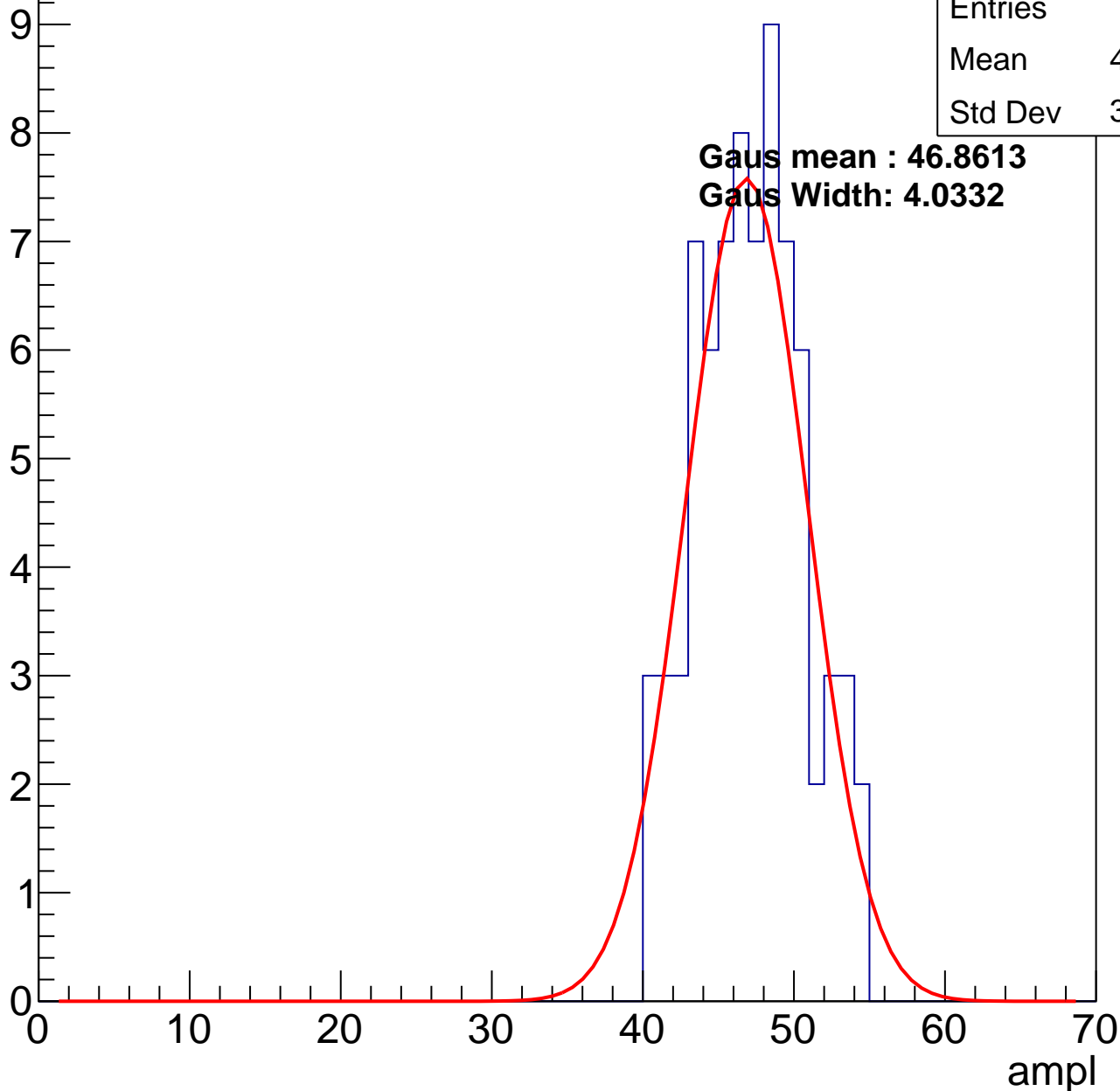
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	46.66
Std Dev	3.493

Gaus mean : 46.8613

Gaus Width: 4.0332

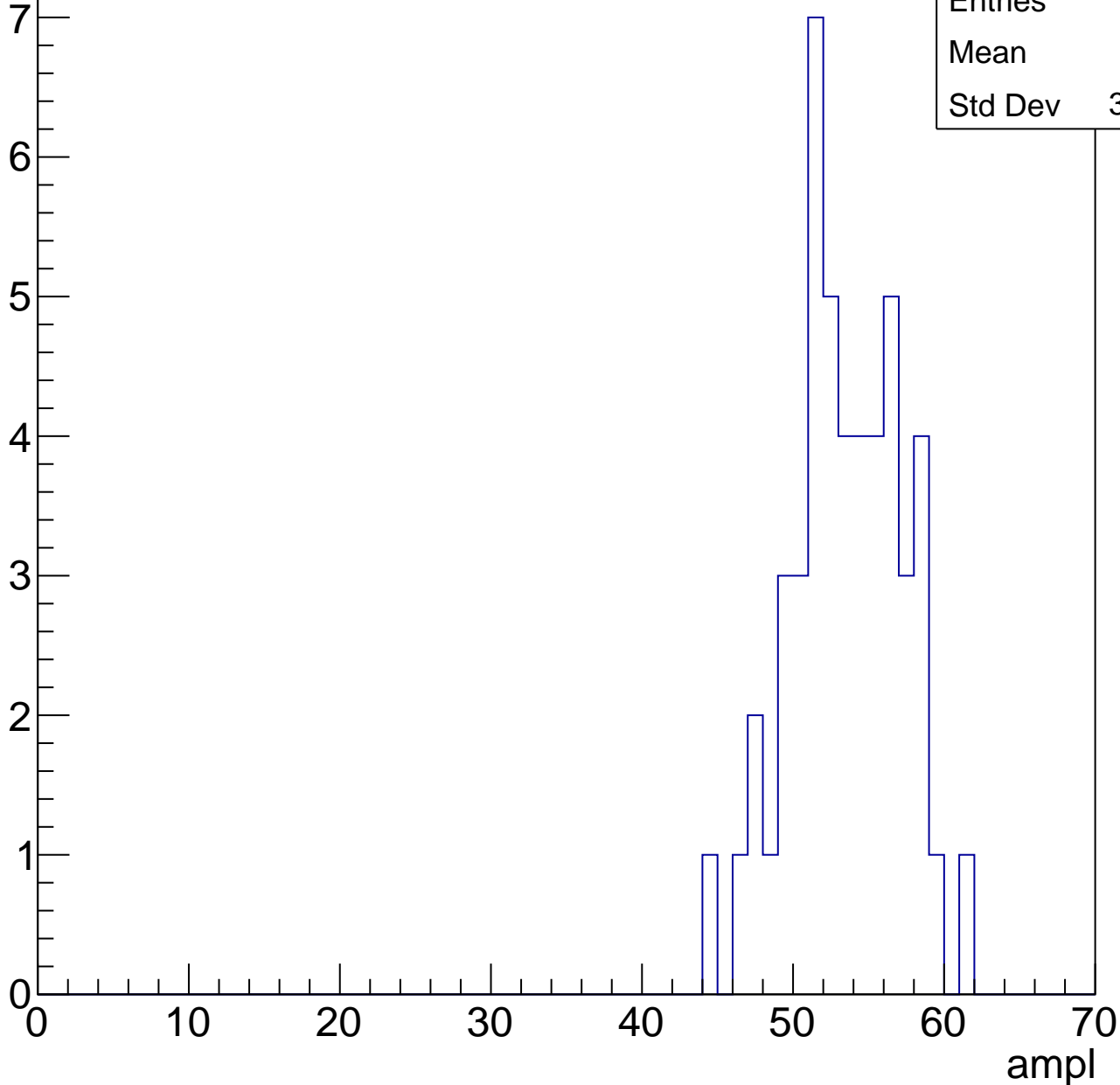


B0L001S, U25-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	53
Std Dev	3.637

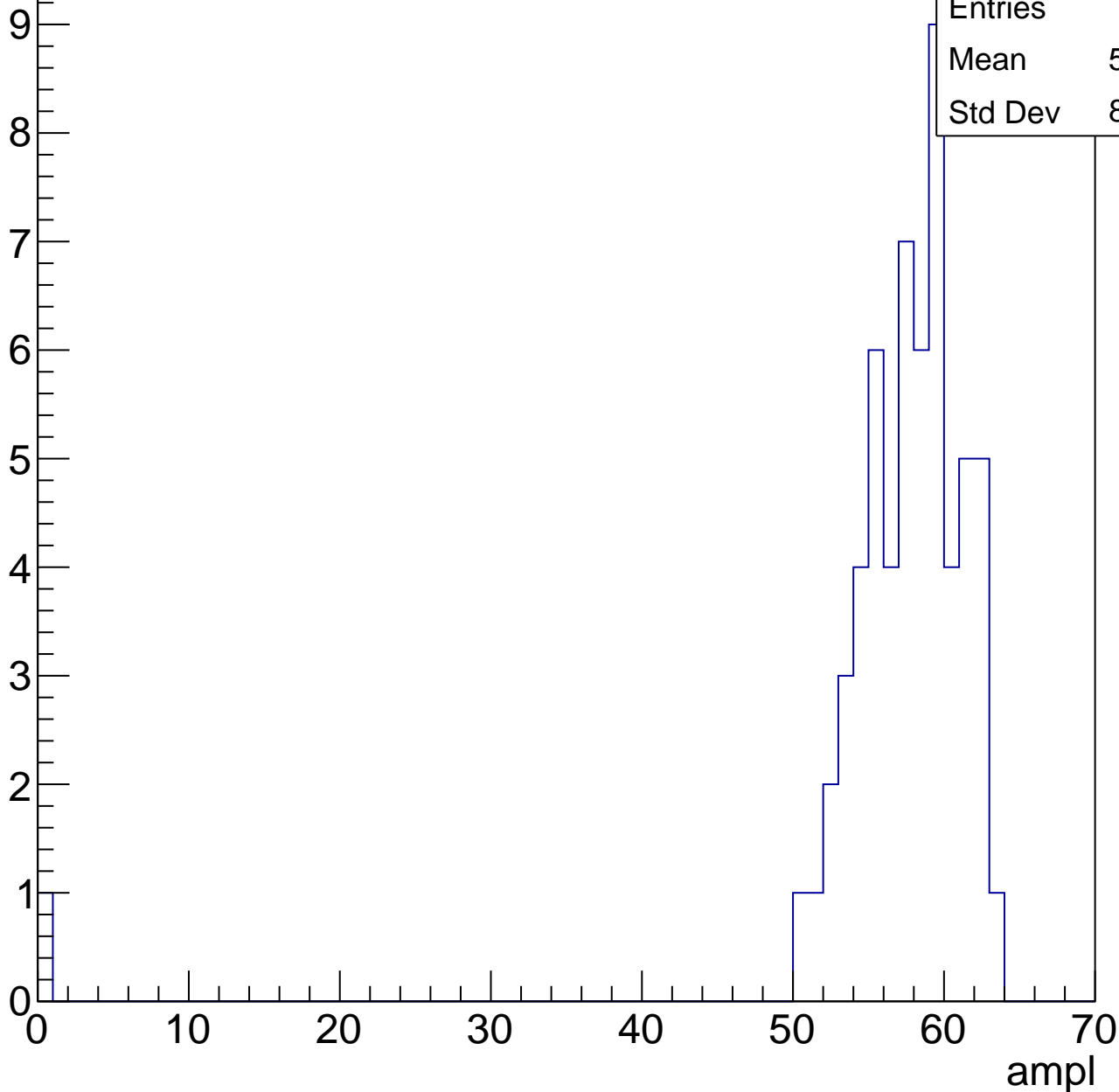


B0L001S, U25-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.44
Std Dev	8.022

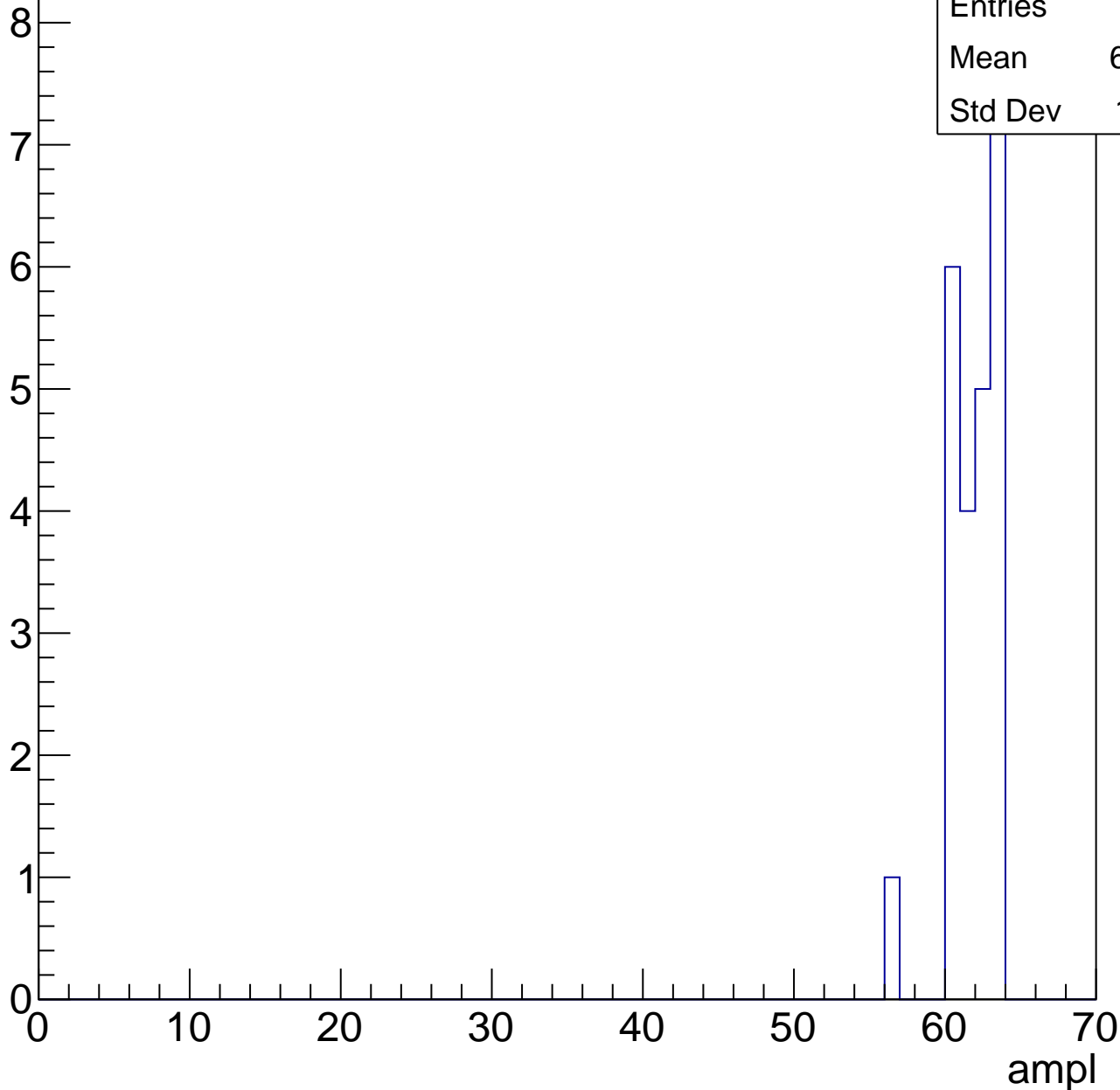


B0L001S, U25-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	61.42
Std Dev	1.631



B0L001S, U25-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B0L001S, U25-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch7, adc0

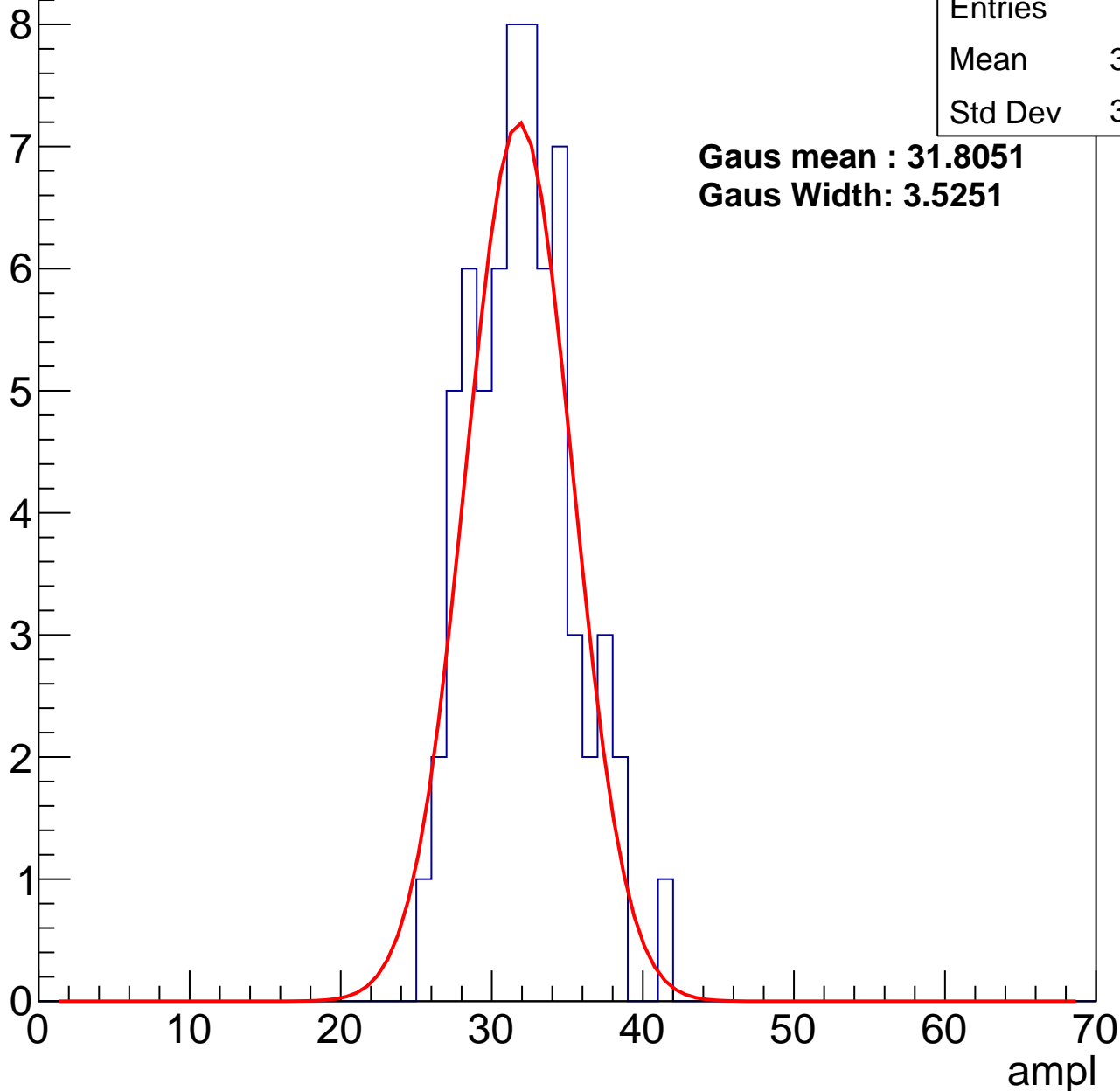
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.54
Std Dev	3.342

Gaus mean : 31.8051

Gaus Width: 3.5251



B0L001S, U25-ch7, adc1

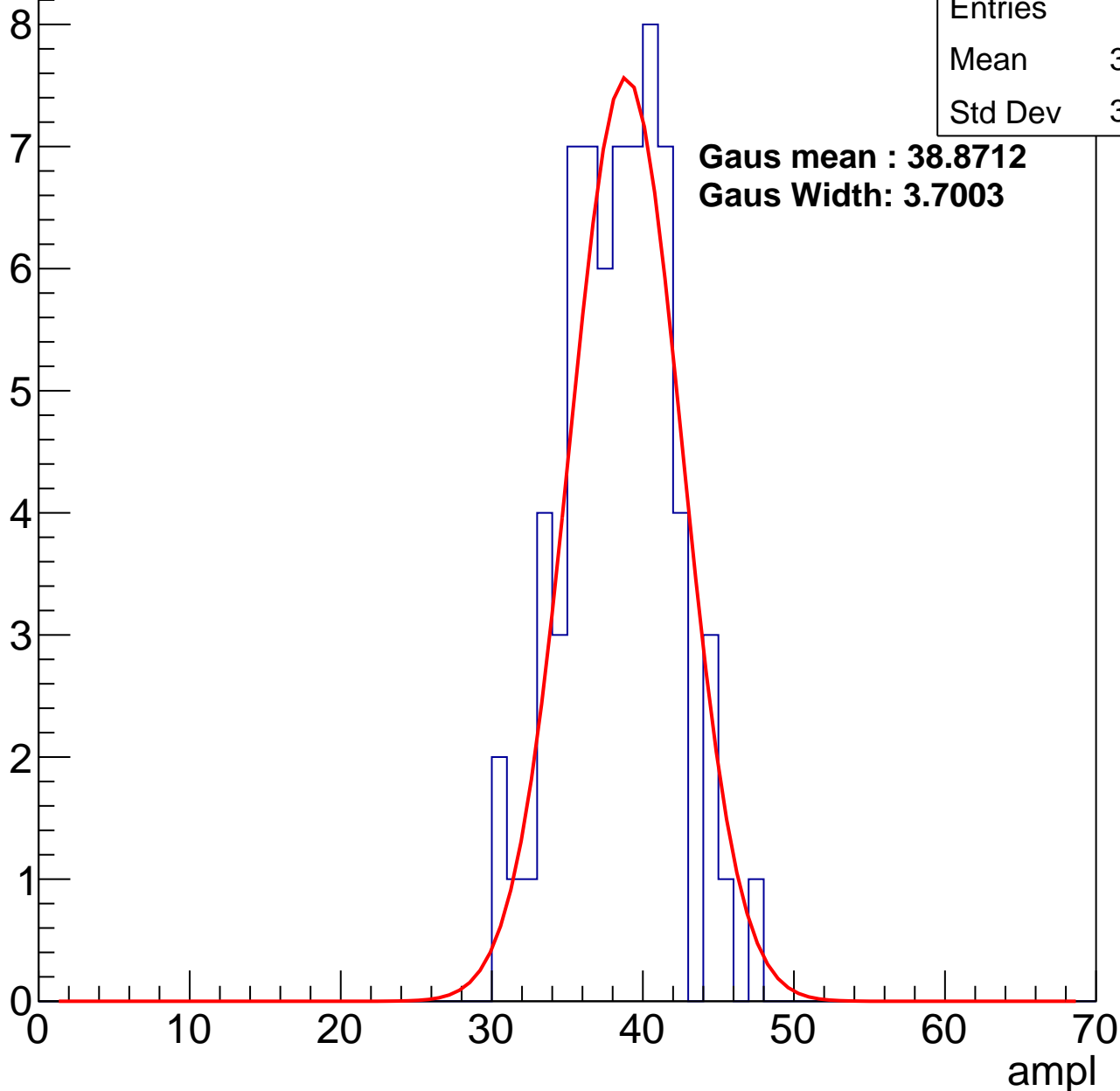
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.88
Std Dev	3.533

Gaus mean : 38.8712

Gaus Width: 3.7003



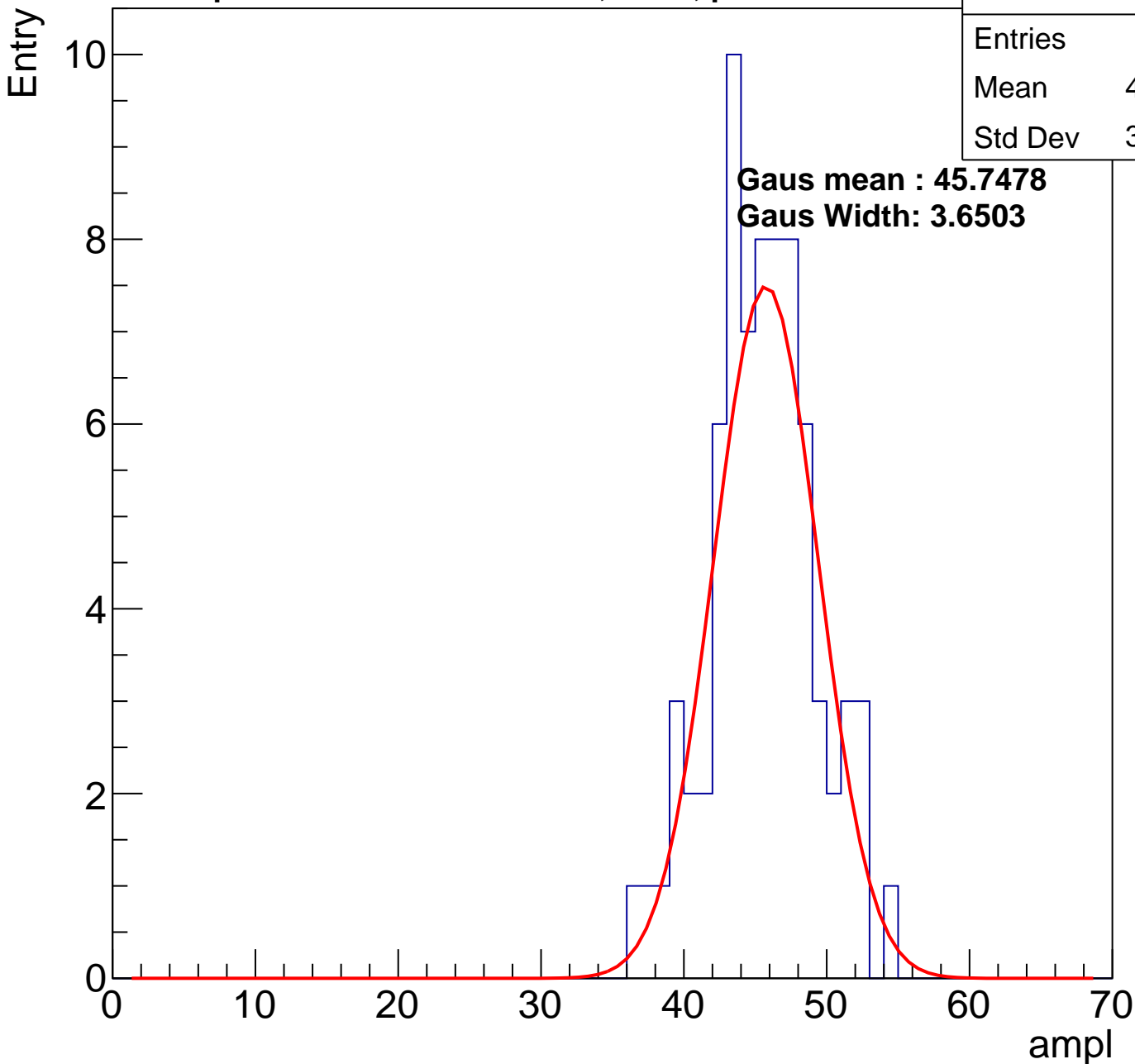
B0L001S, U25-ch7, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	45.09
Std Dev	3.674

Gaus mean : 45.7478

Gaus Width: 3.6503

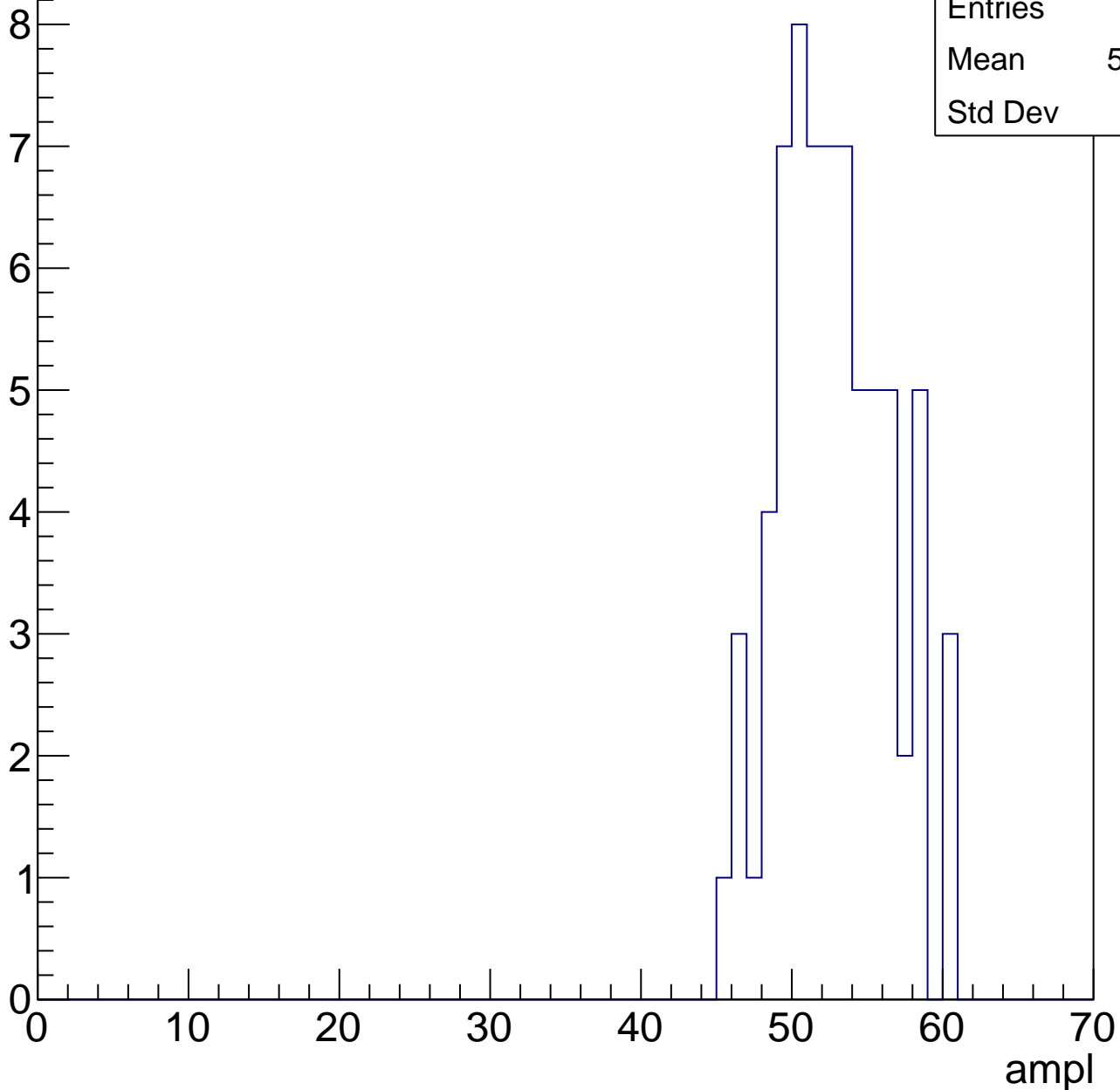


B0L001S, U25-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.37
Std Dev	3.61

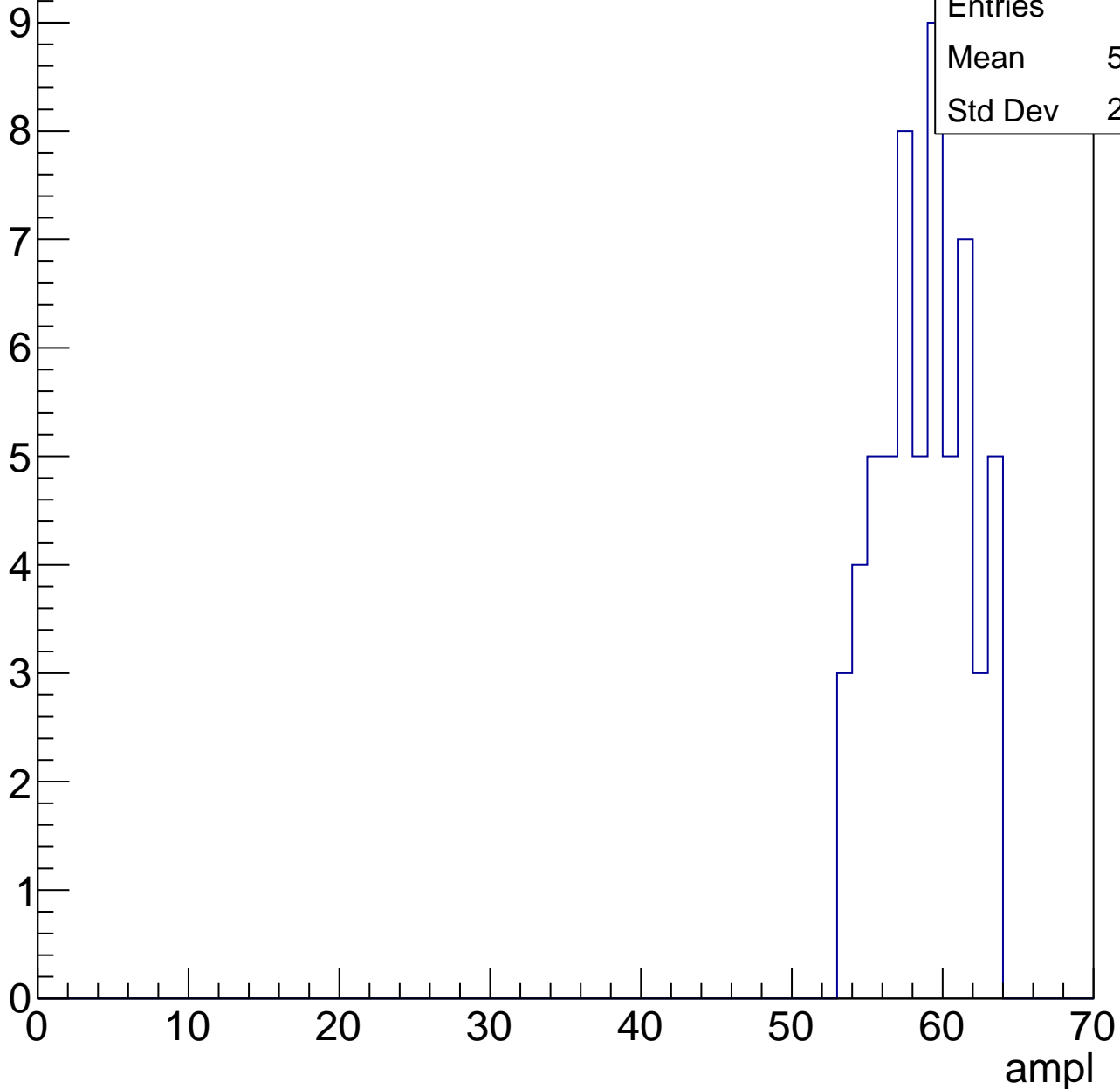


B0L001S, U25-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	58.22
Std Dev	2.835

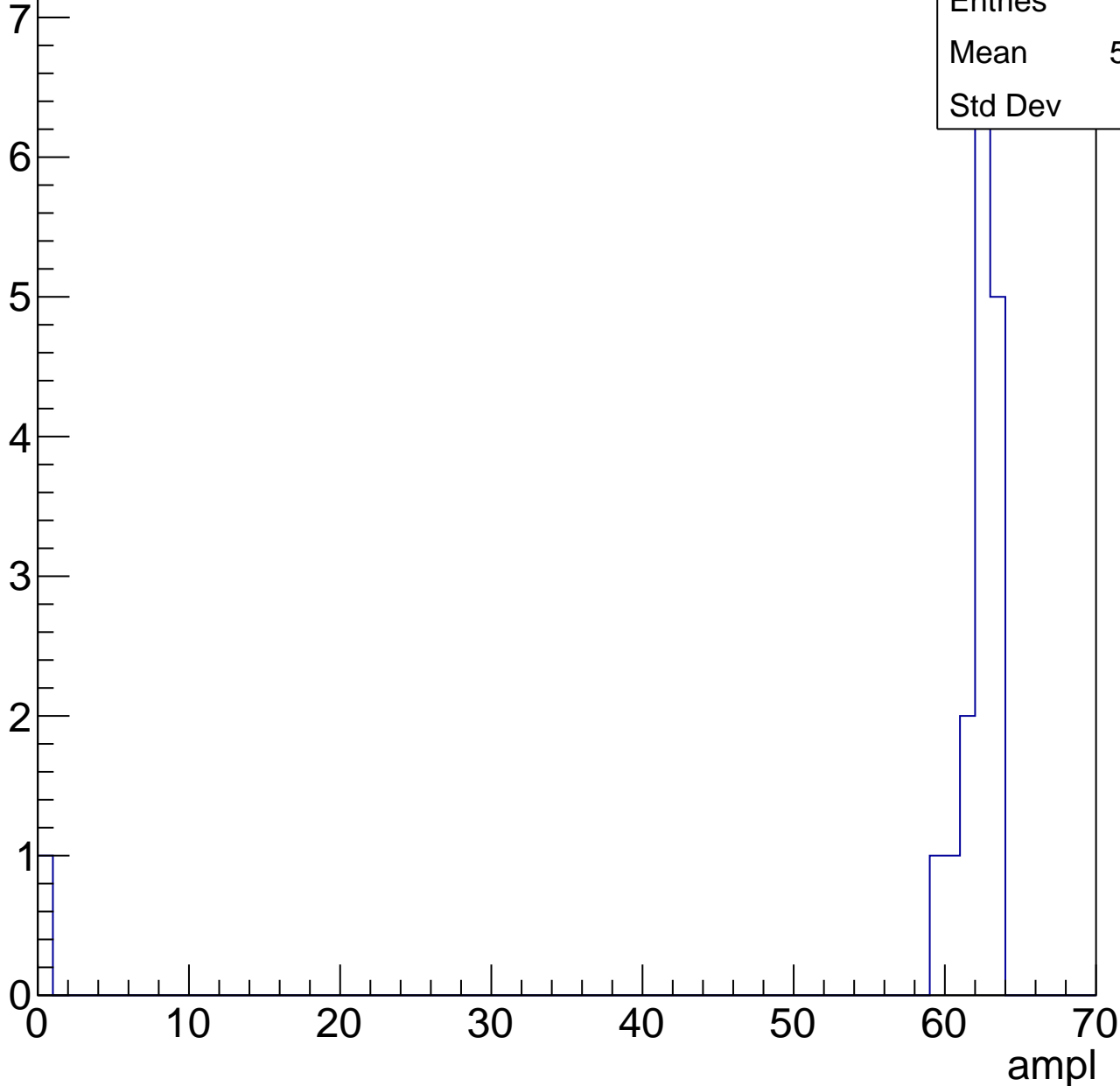


B0L001S, U25-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

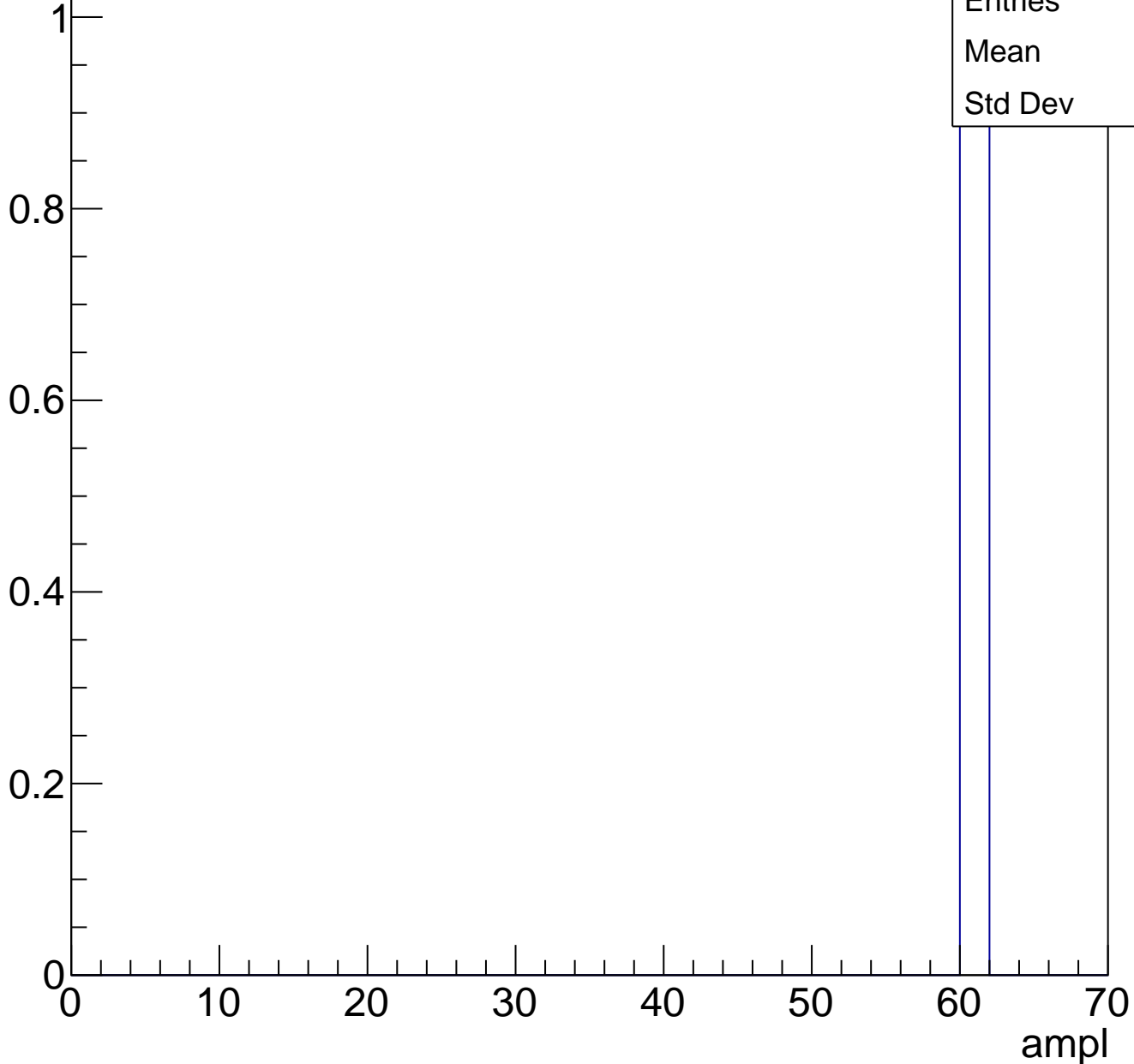
Entries	17
Mean	58.24
Std Dev	14.6



B0L001S, U25-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch8, adc0

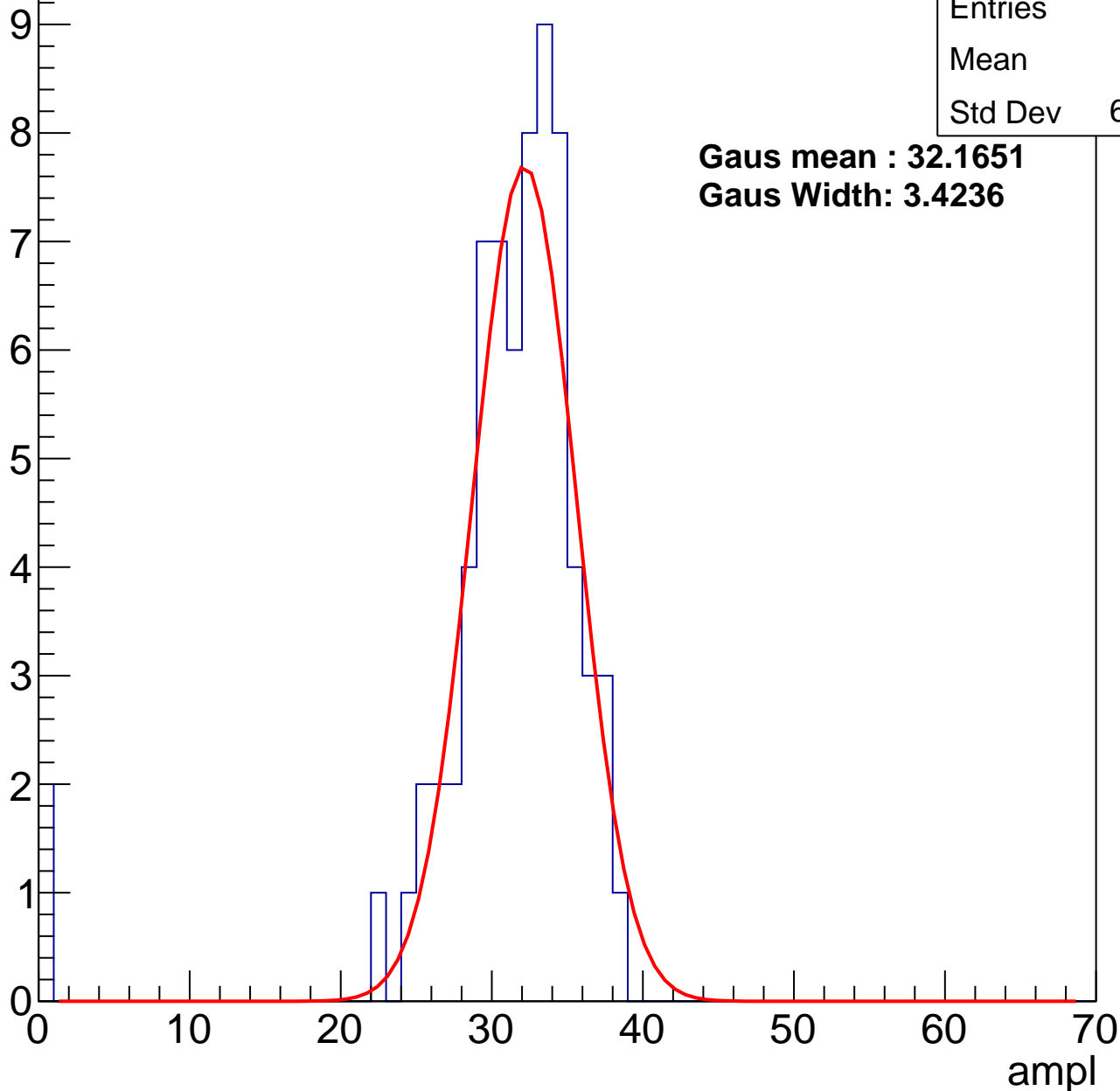
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.5
Std Dev	6.178

Gaus mean : 32.1651

Gaus Width: 3.4236



B0L001S, U25-ch8, adc1

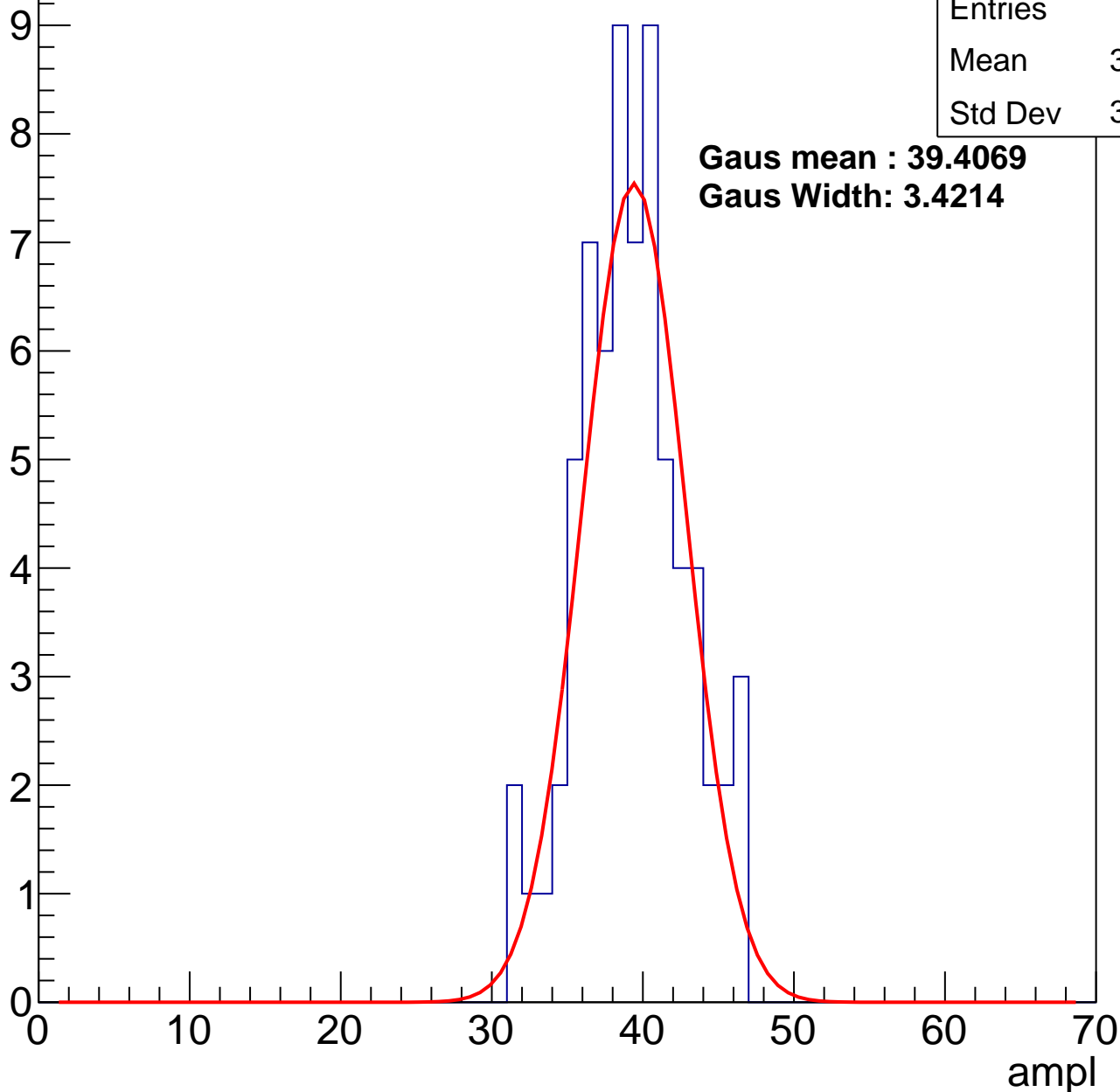
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	38.84
Std Dev	3.479

Gaus mean : 39.4069

Gaus Width: 3.4214



B0L001S, U25-ch8, adc2

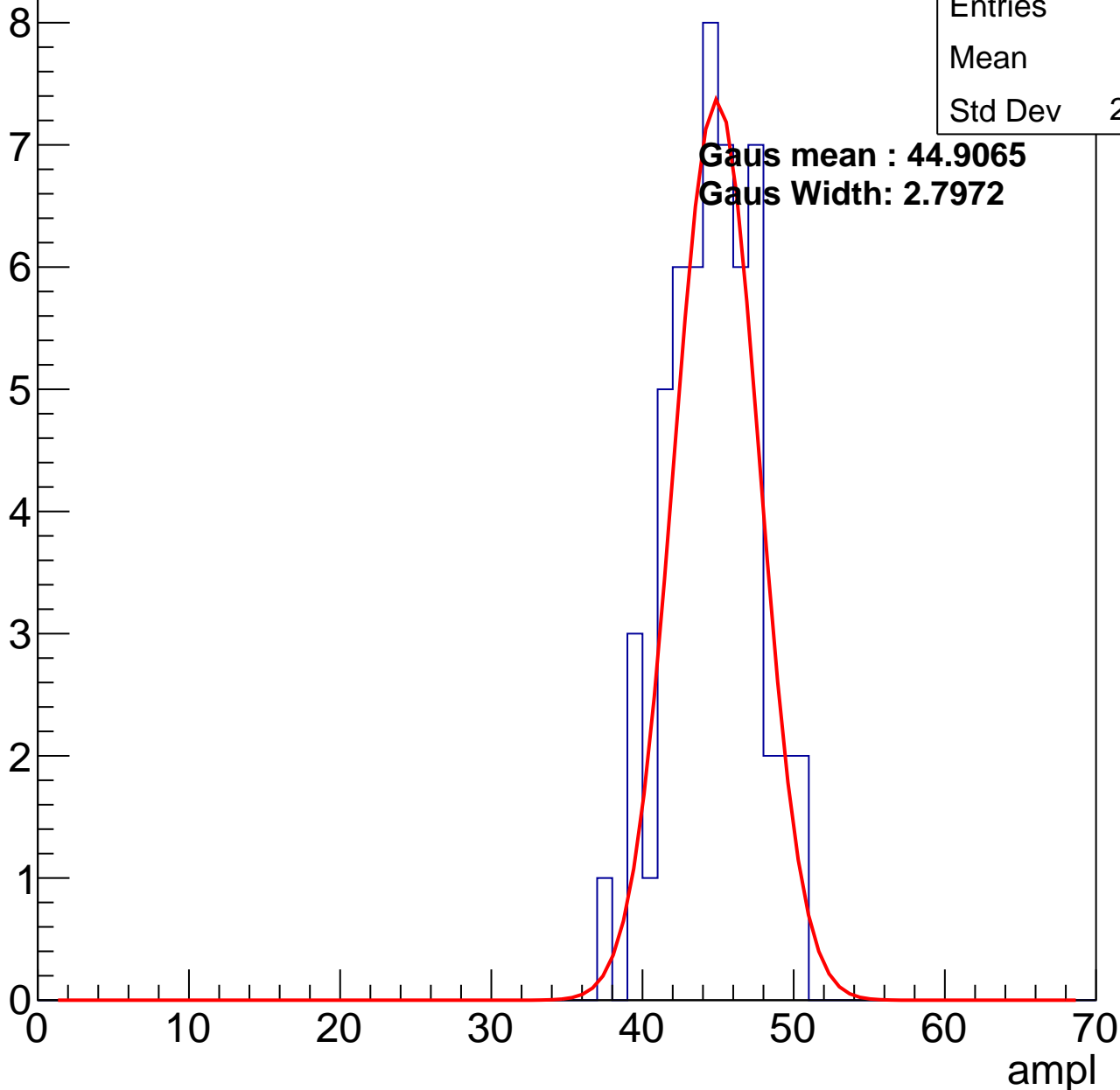
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.2
Std Dev	2.869

Gaus mean : 44.9065

Gaus Width: 2.7972



B0L001S, U25-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

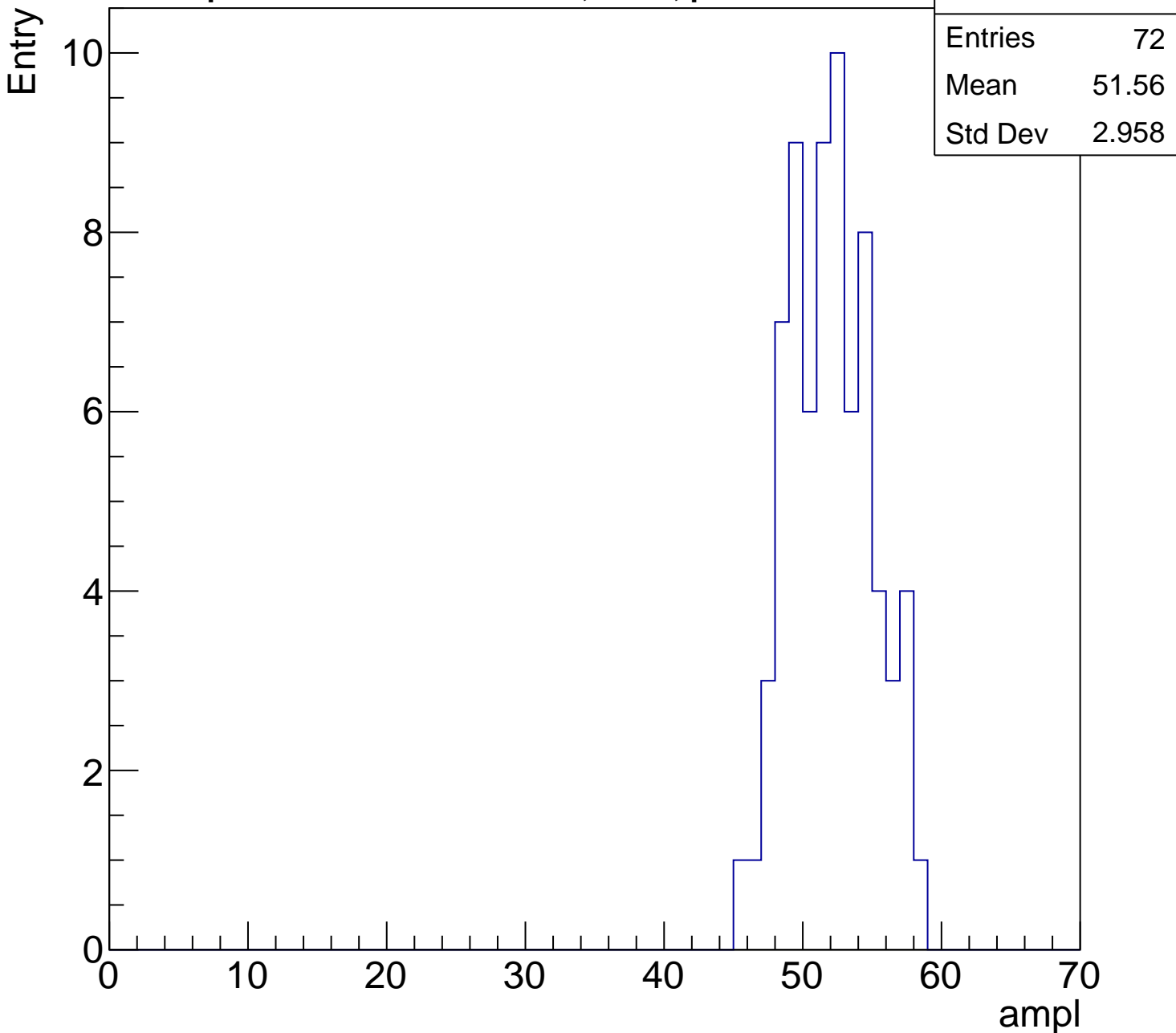
Entries	72
Mean	51.56
Std Dev	2.958

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

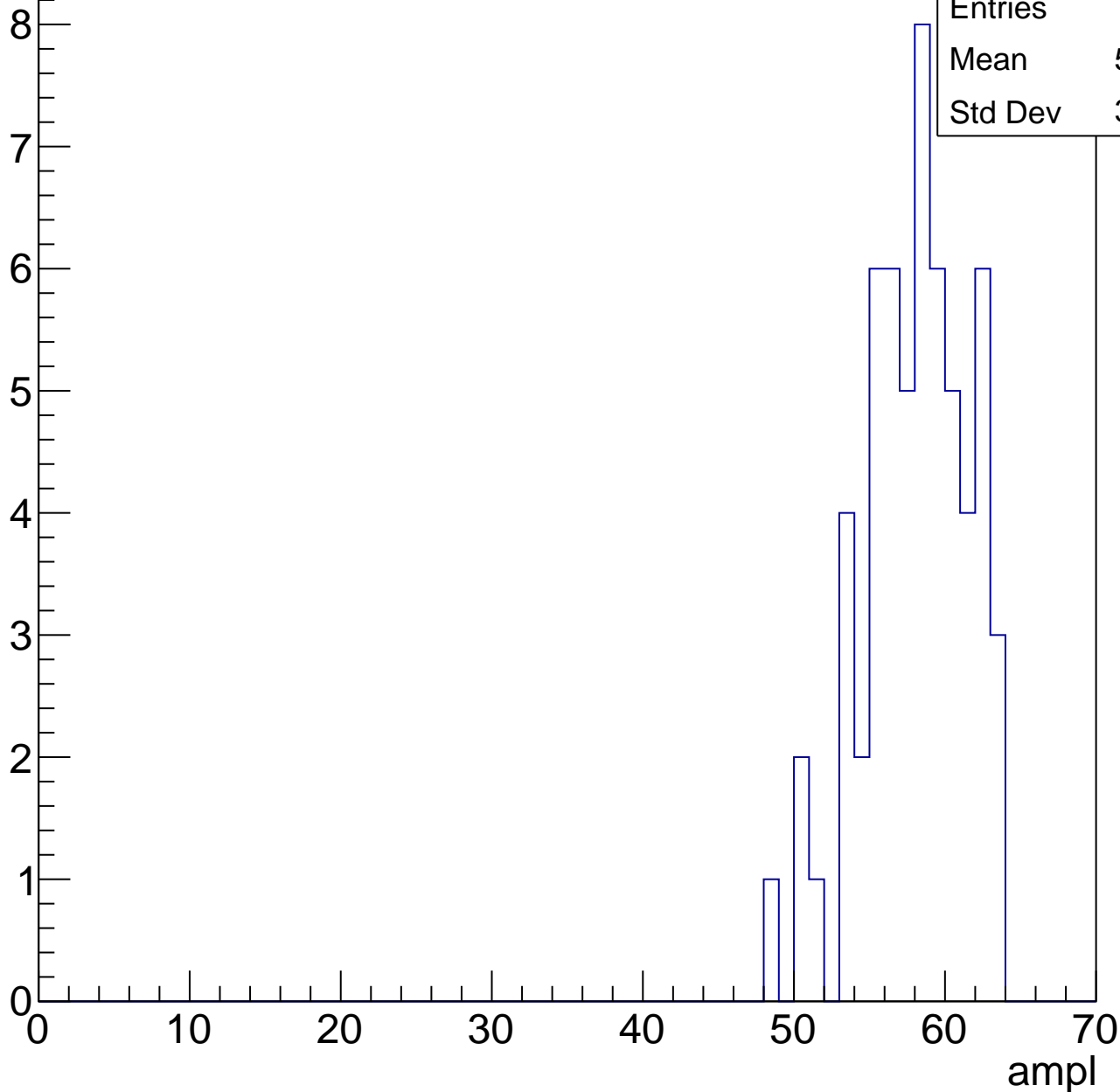


B0L001S, U25-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.51
Std Dev	3.471



B0L001S, U25-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries

29

Mean

60.76

Std Dev

1.869

0

ampl

0

10

20

30

40

50

60

70

B0L001S, U25-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch9, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	30.53
Std Dev	3.925

Gaus mean : 30.9189

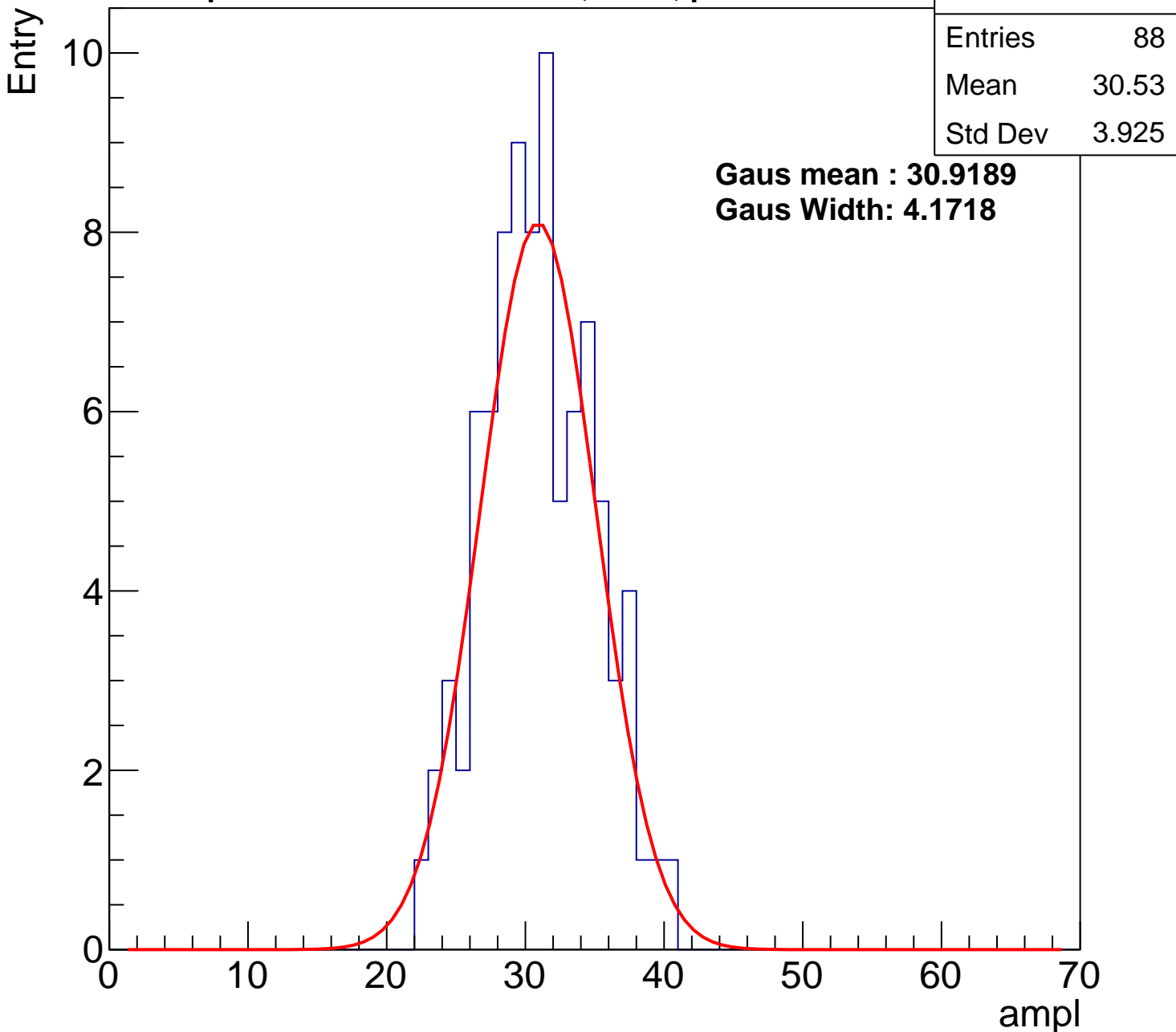
Gaus Width: 4.1718

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch9, adc1

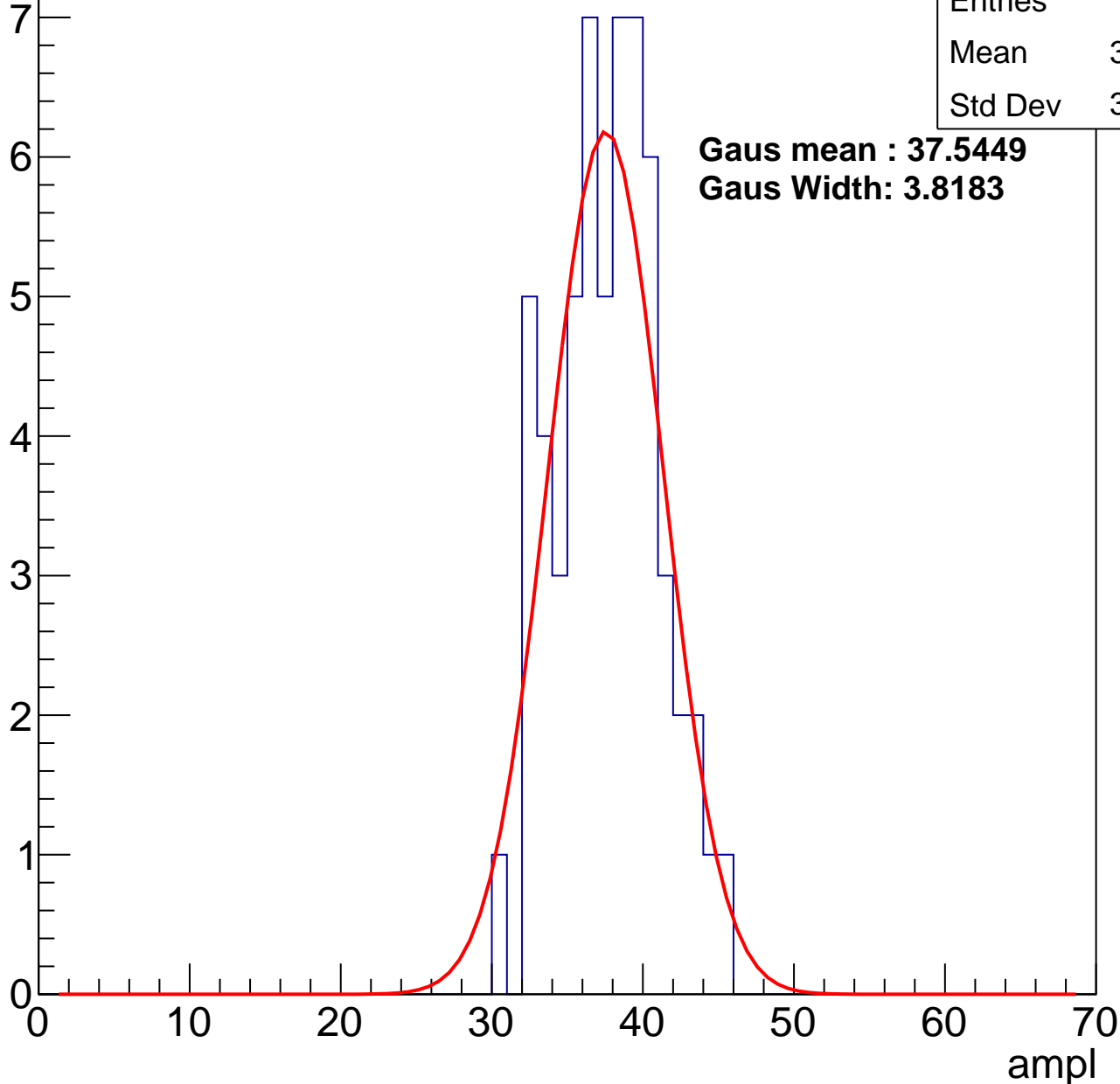
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.24
Std Dev	3.346

Gaus mean : 37.5449

Gaus Width: 3.8183



B0L001S, U25-ch9, adc2

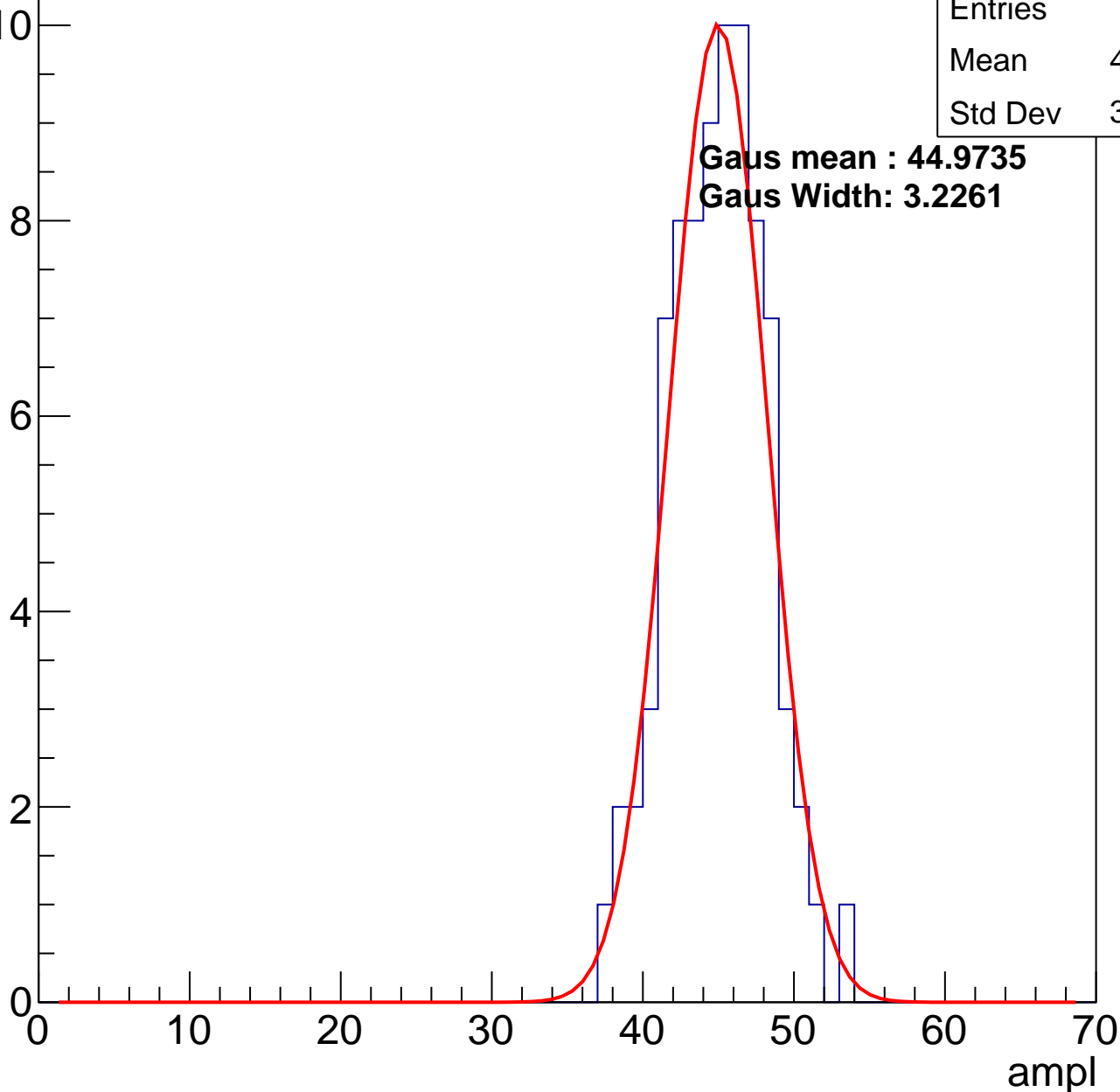
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	44.48
Std Dev	3.152

Gaus mean : 44.9735

Gaus Width: 3.2261

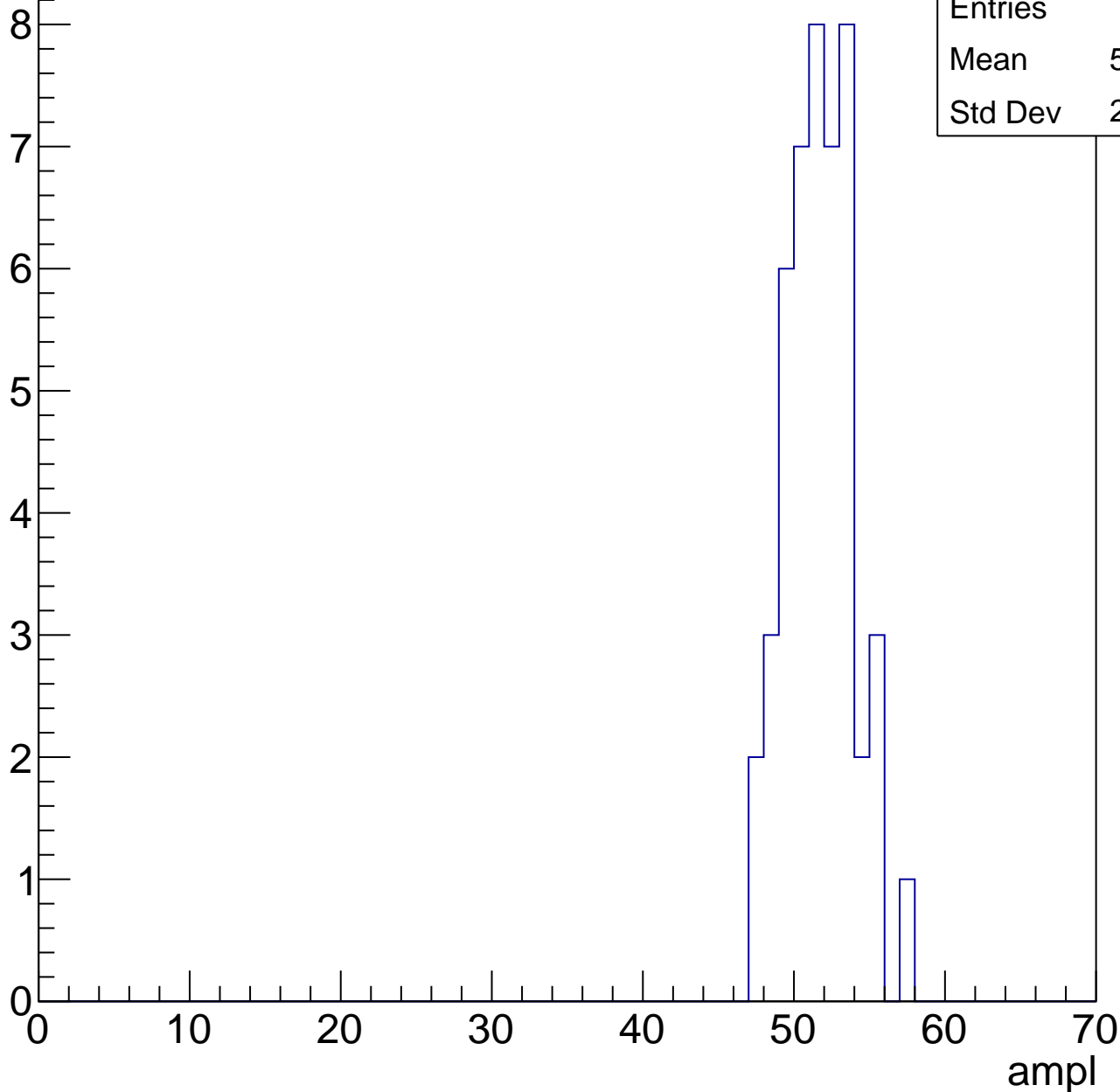


B0L001S, U25-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	51.23
Std Dev	2.205

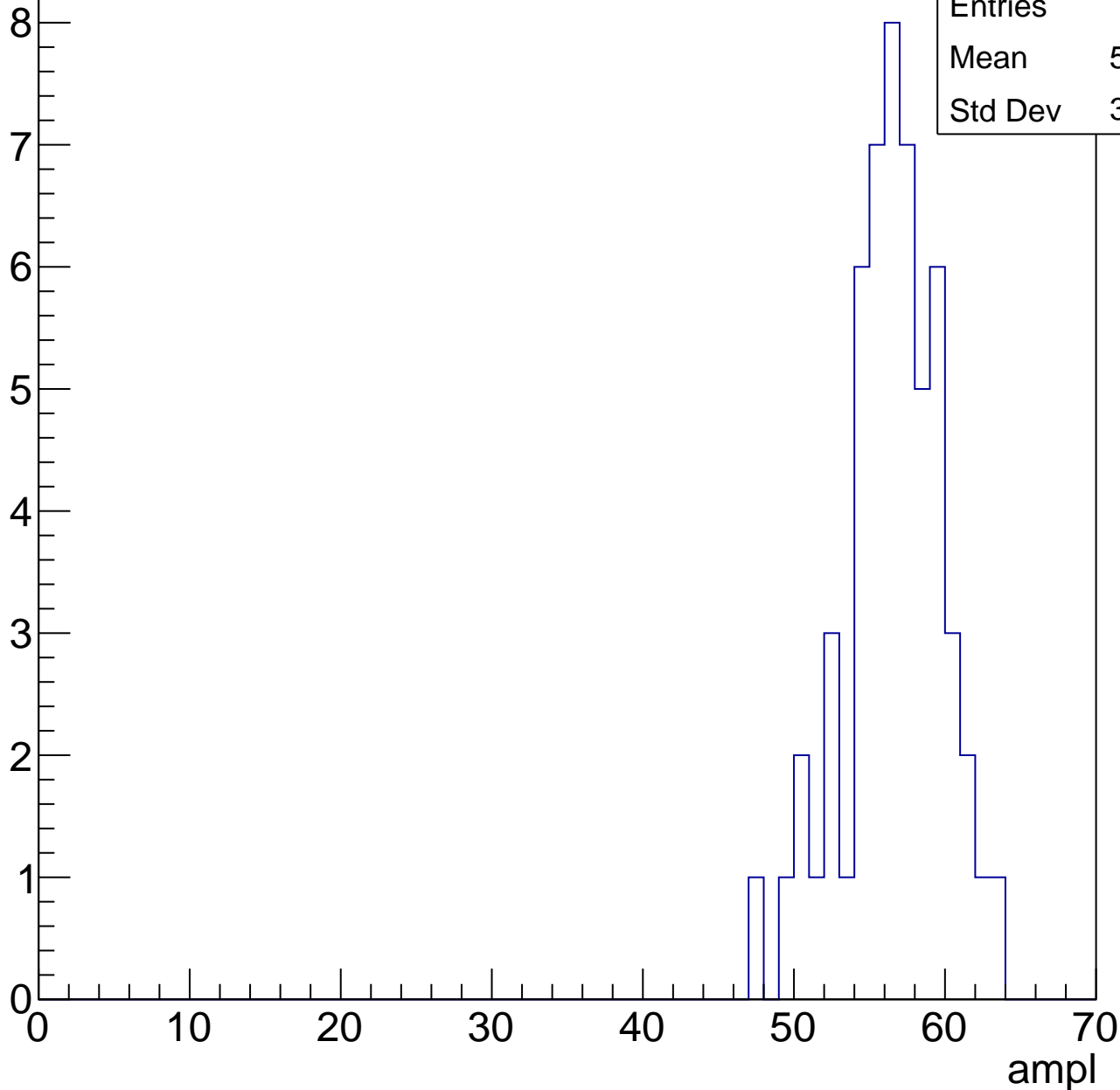


B0L001S, U25-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

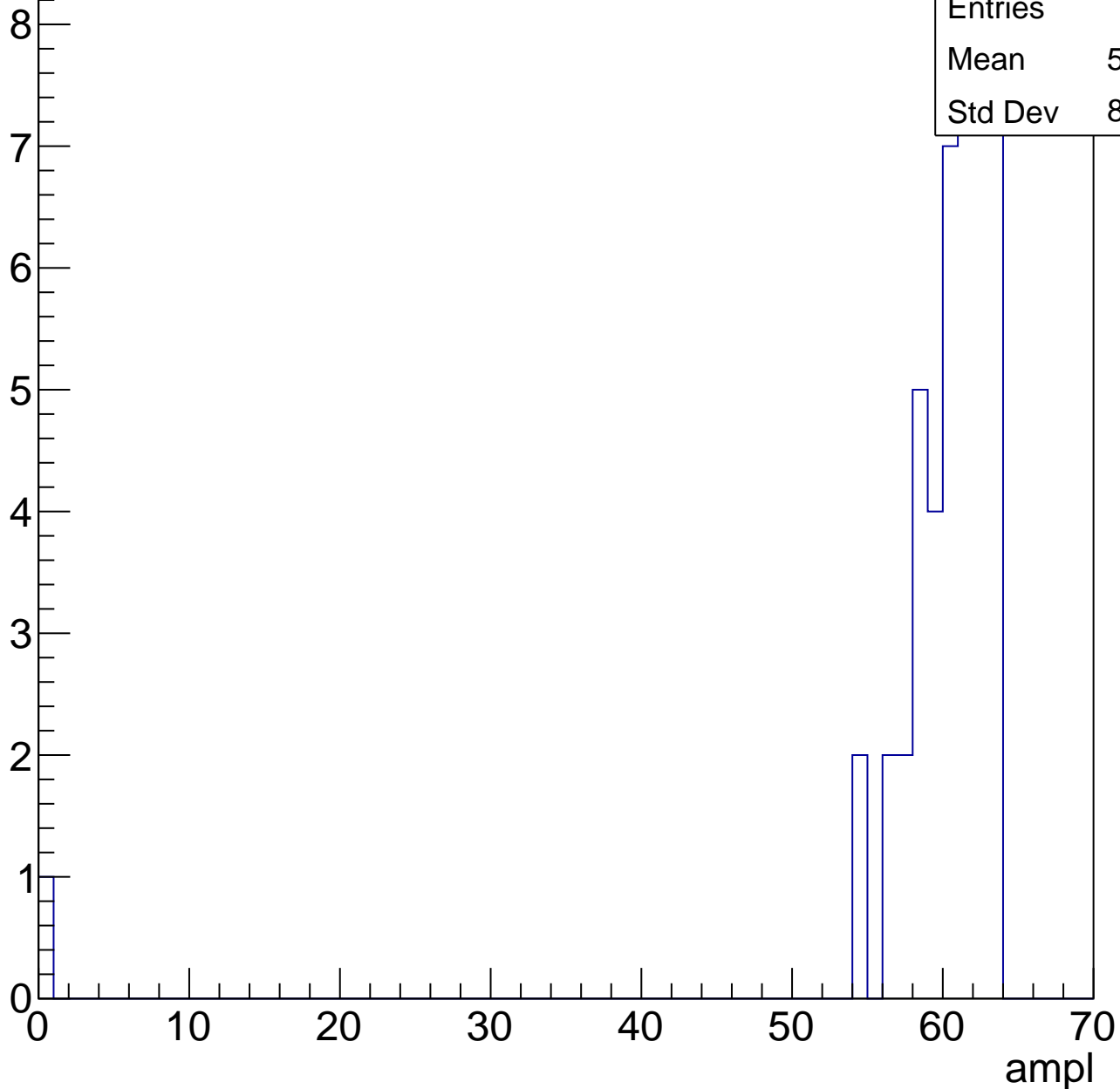
Entries	55
Mean	56.05
Std Dev	3.244



B0L001S, U25-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch10, adc0

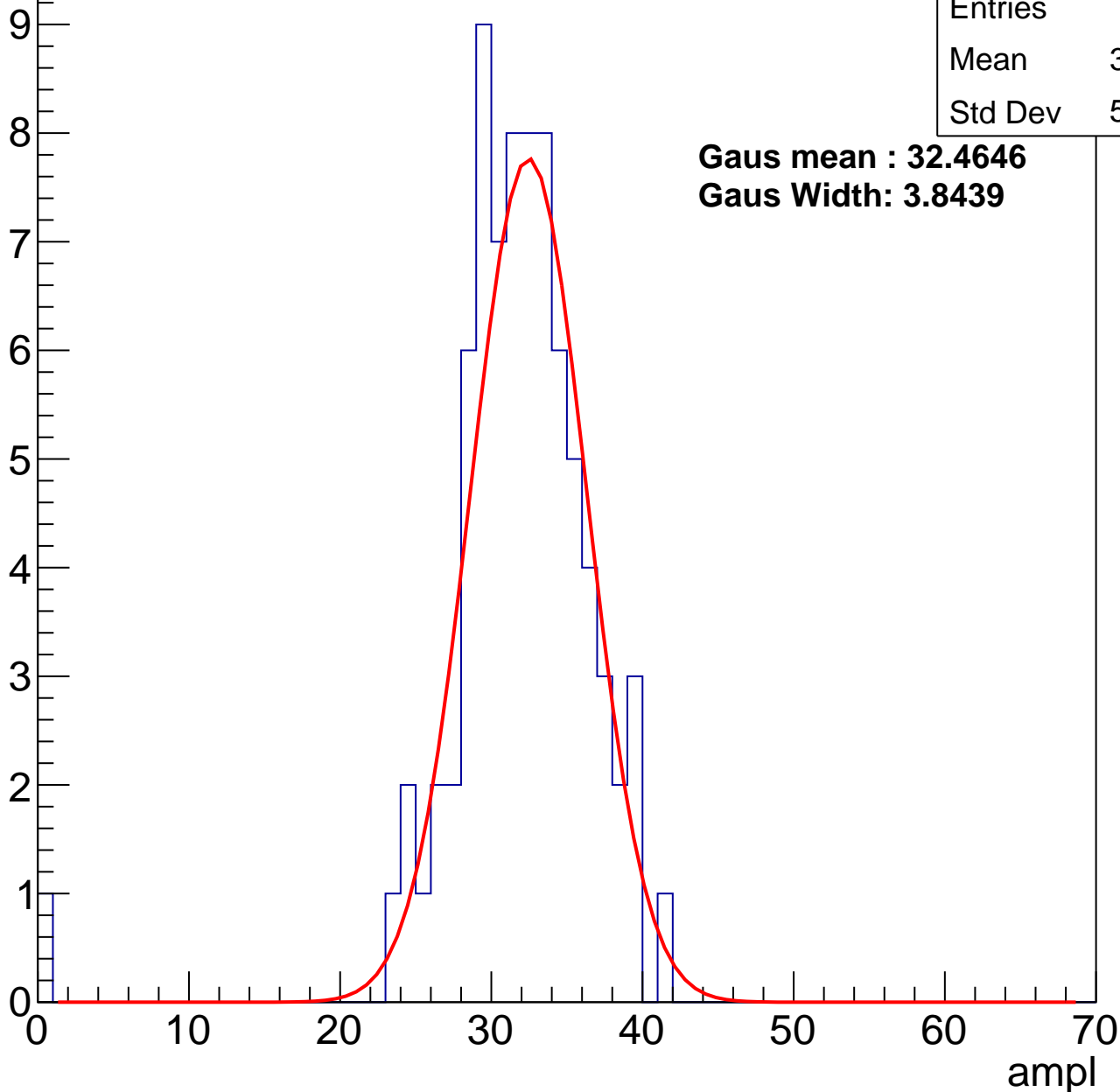
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	31.35
Std Dev	5.156

Gaus mean : 32.4646

Gaus Width: 3.8439



B0L001S, U25-ch10, adc1

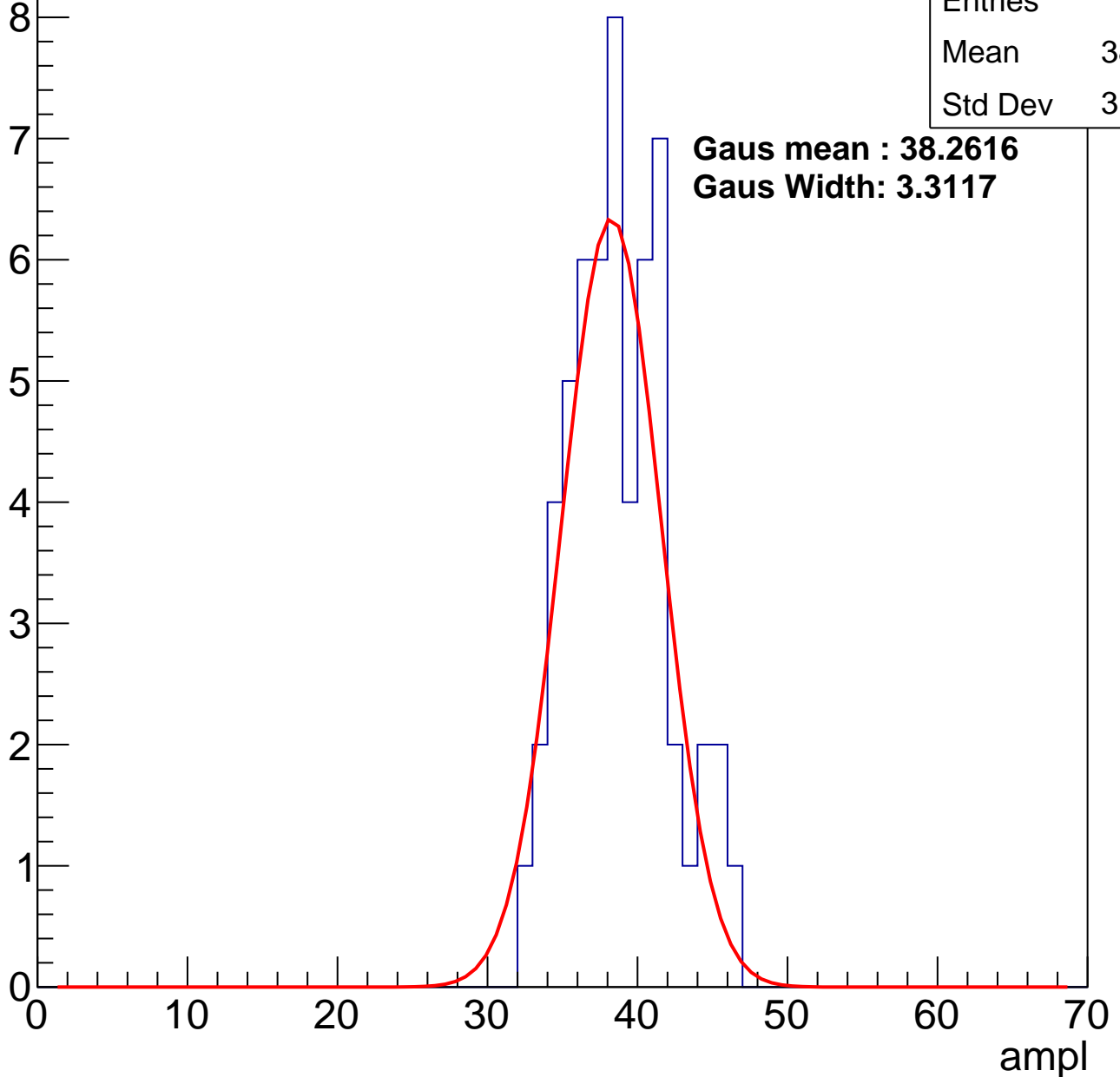
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	38.33
Std Dev	3.246

Gaus mean : 38.2616

Gaus Width: 3.3117



B0L001S, U25-ch10, adc2

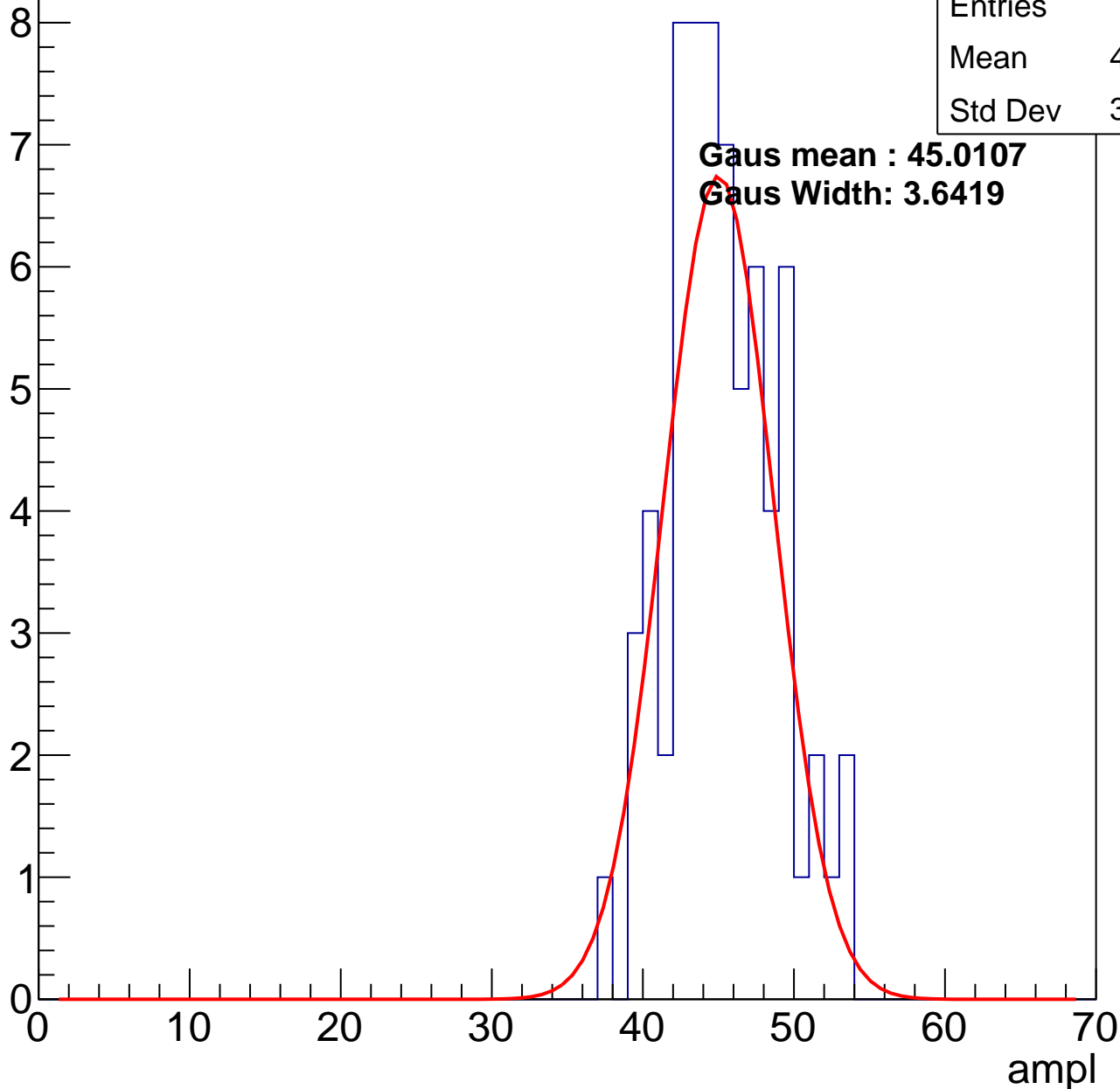
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.87
Std Dev	3.535

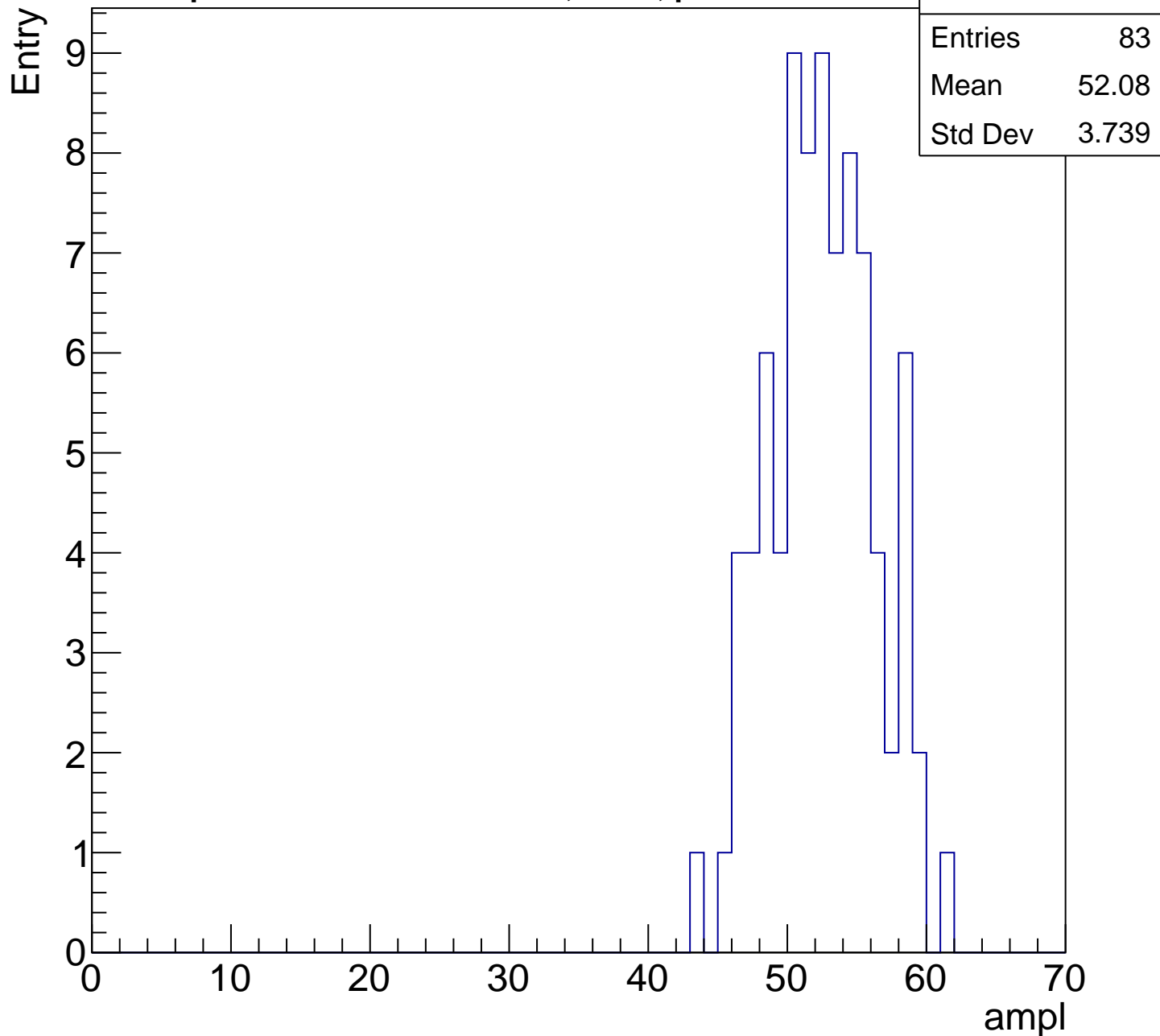
Gaus mean : 45.0107

Gaus Width: 3.6419



B0L001S, U25-ch10, adc3

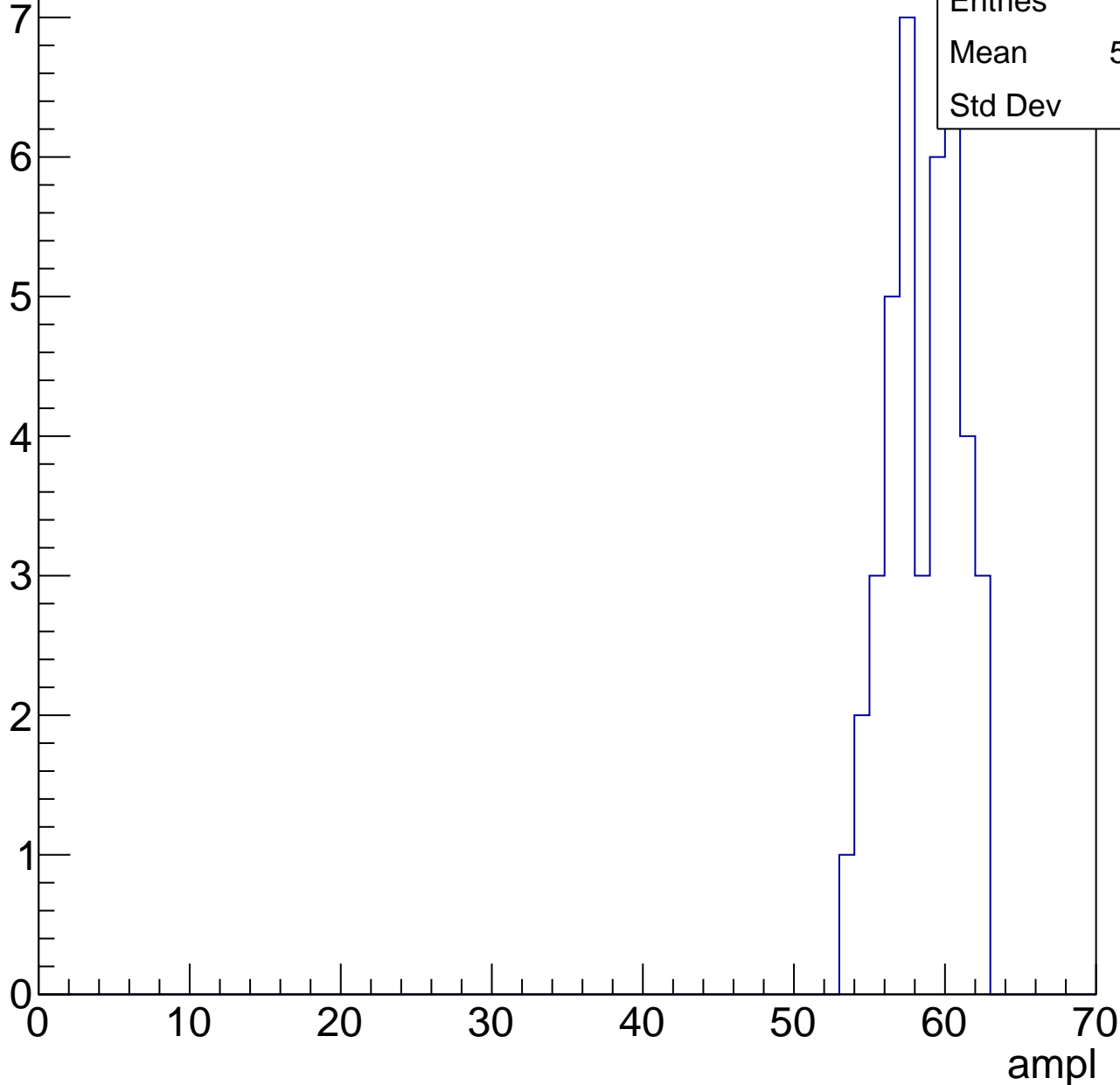
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

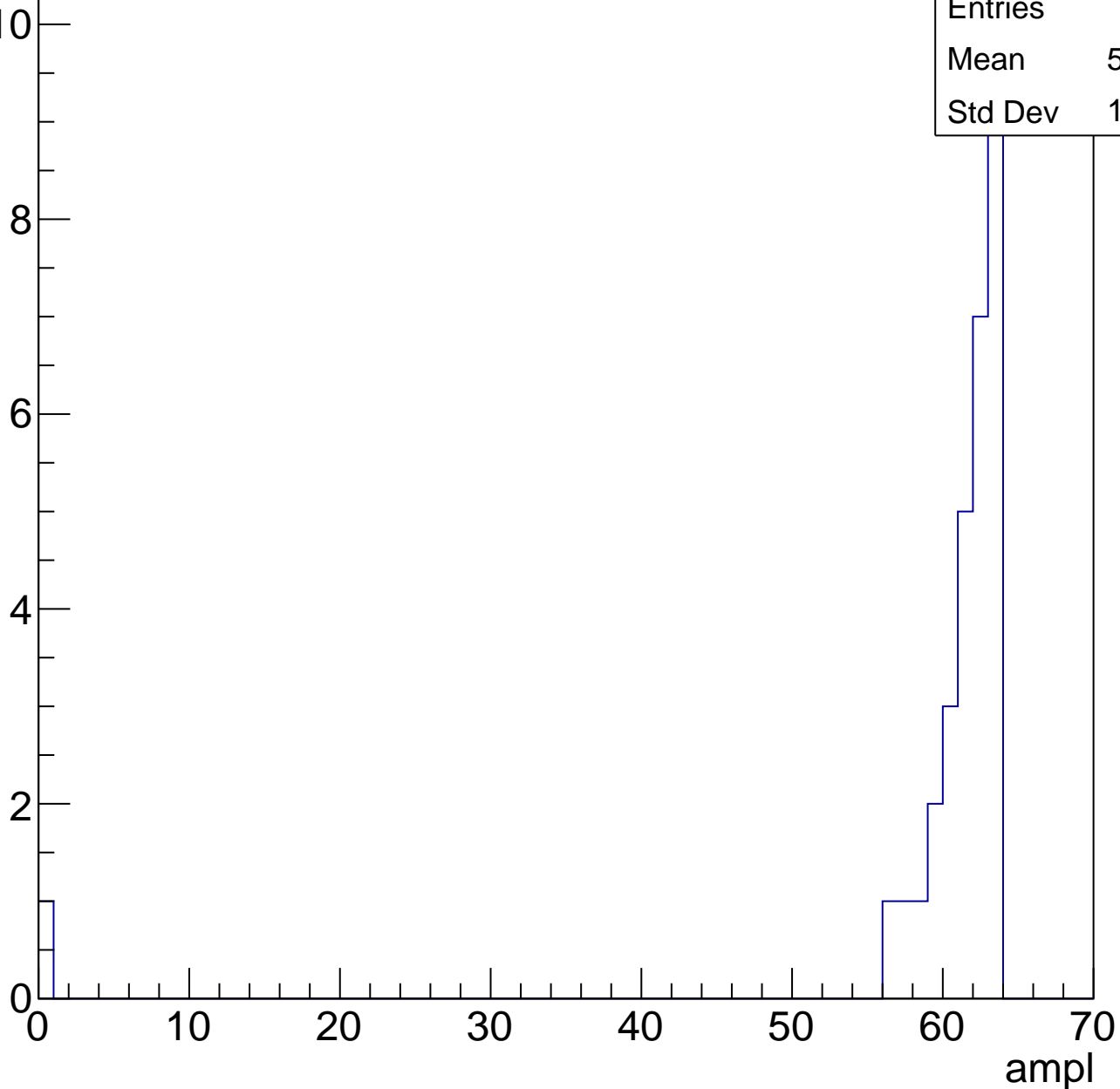


B0L001S, U25-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.29
Std Dev	10.98



B0L001S, U25-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch11, adc0

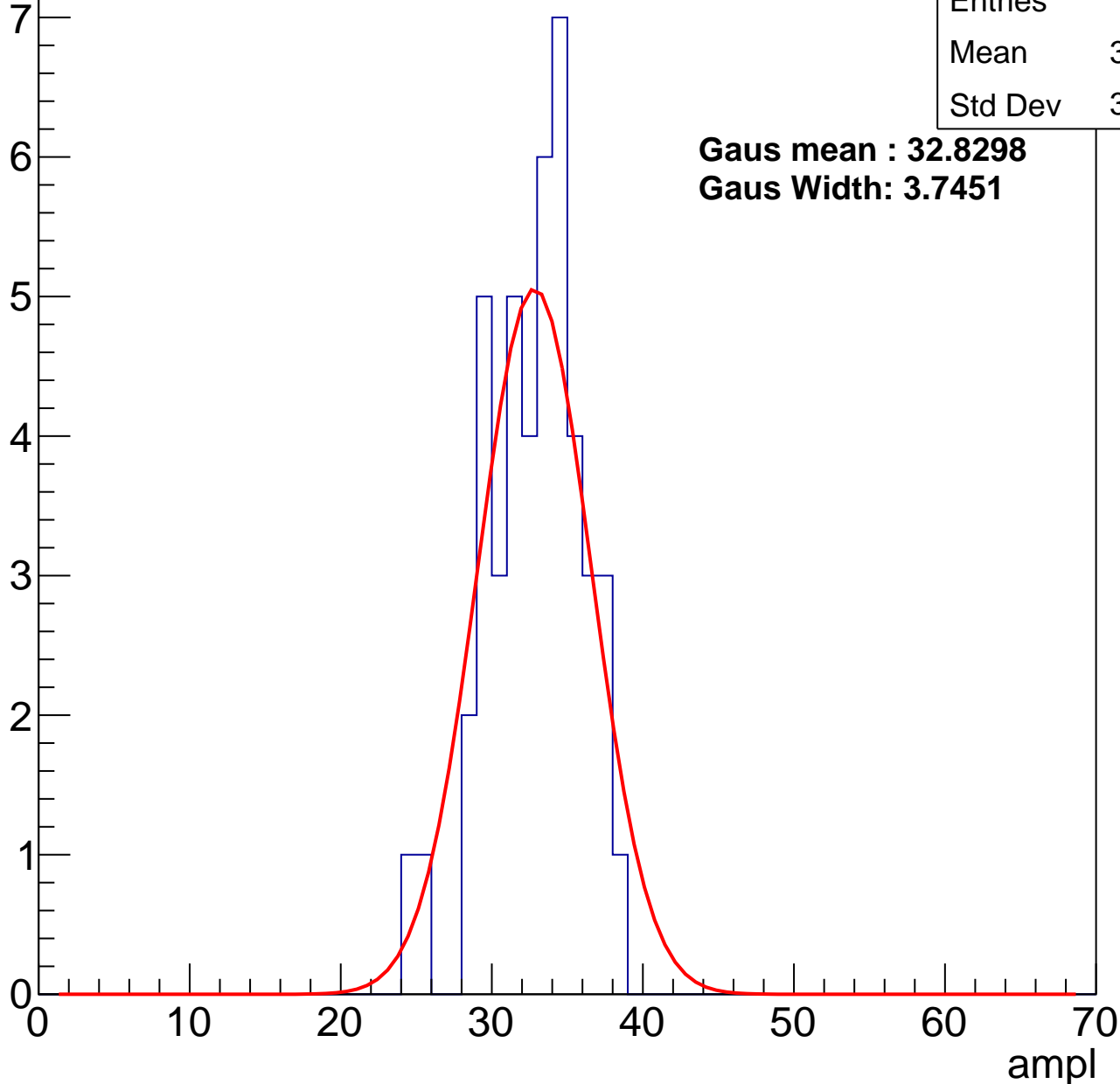
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	32.36
Std Dev	3.092

Gaus mean : 32.8298

Gaus Width: 3.7451



B0L001S, U25-ch11, adc1

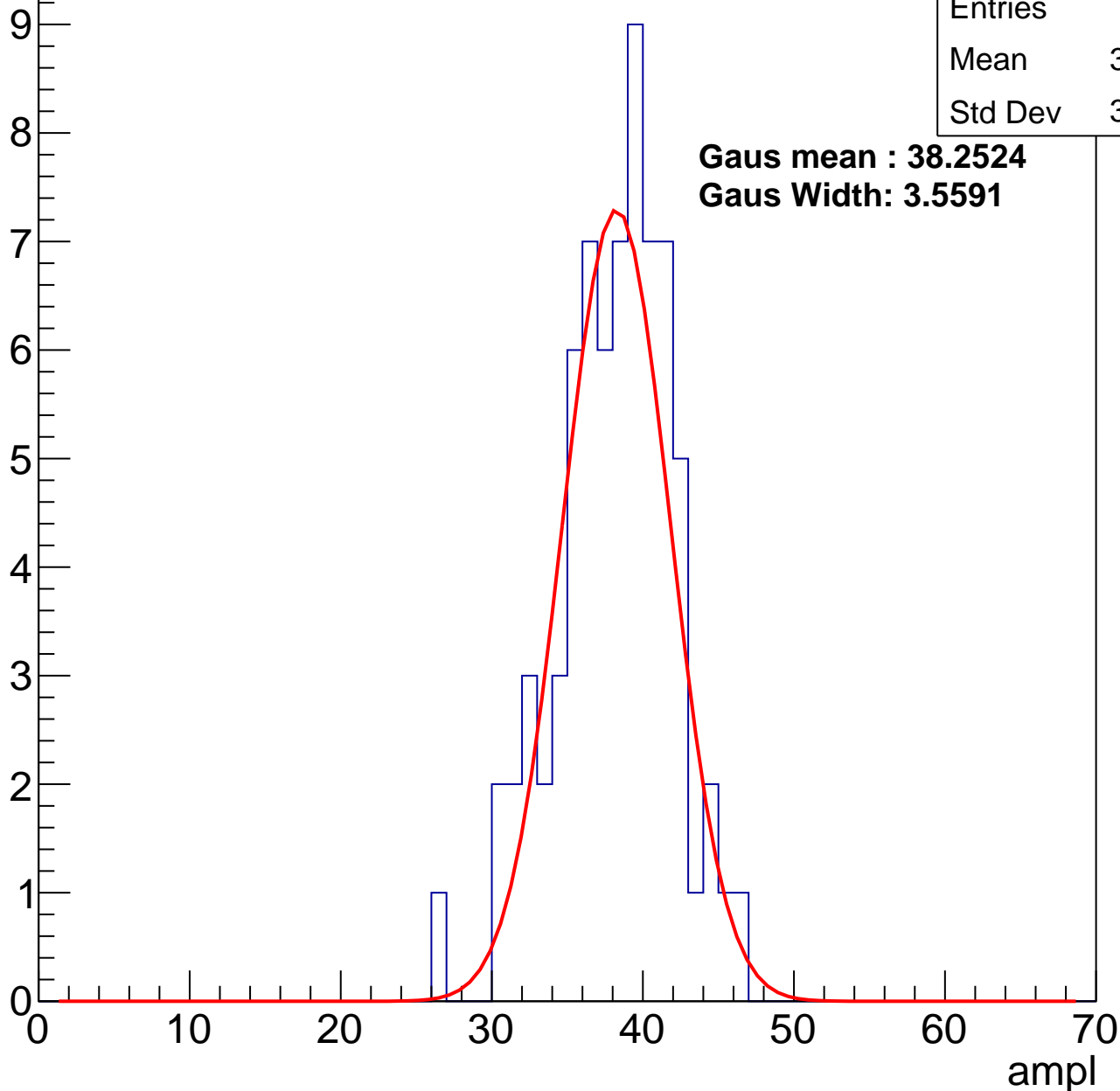
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.67
Std Dev	3.812

Gaus mean : 38.2524

Gaus Width: 3.5591



B0L001S, U25-ch11, adc2

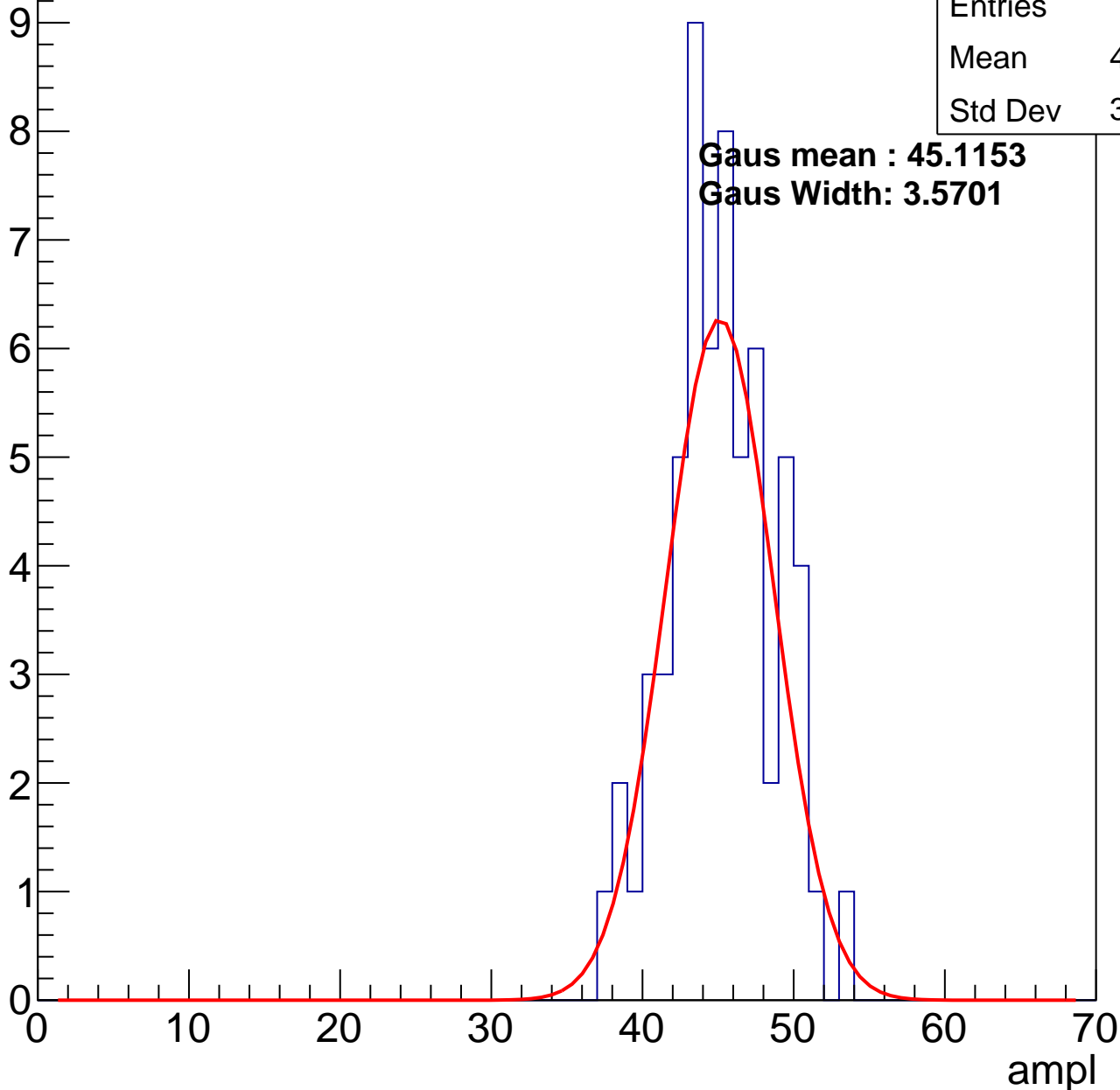
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.73
Std Dev	3.432

Gaus mean : 45.1153

Gaus Width: 3.5701

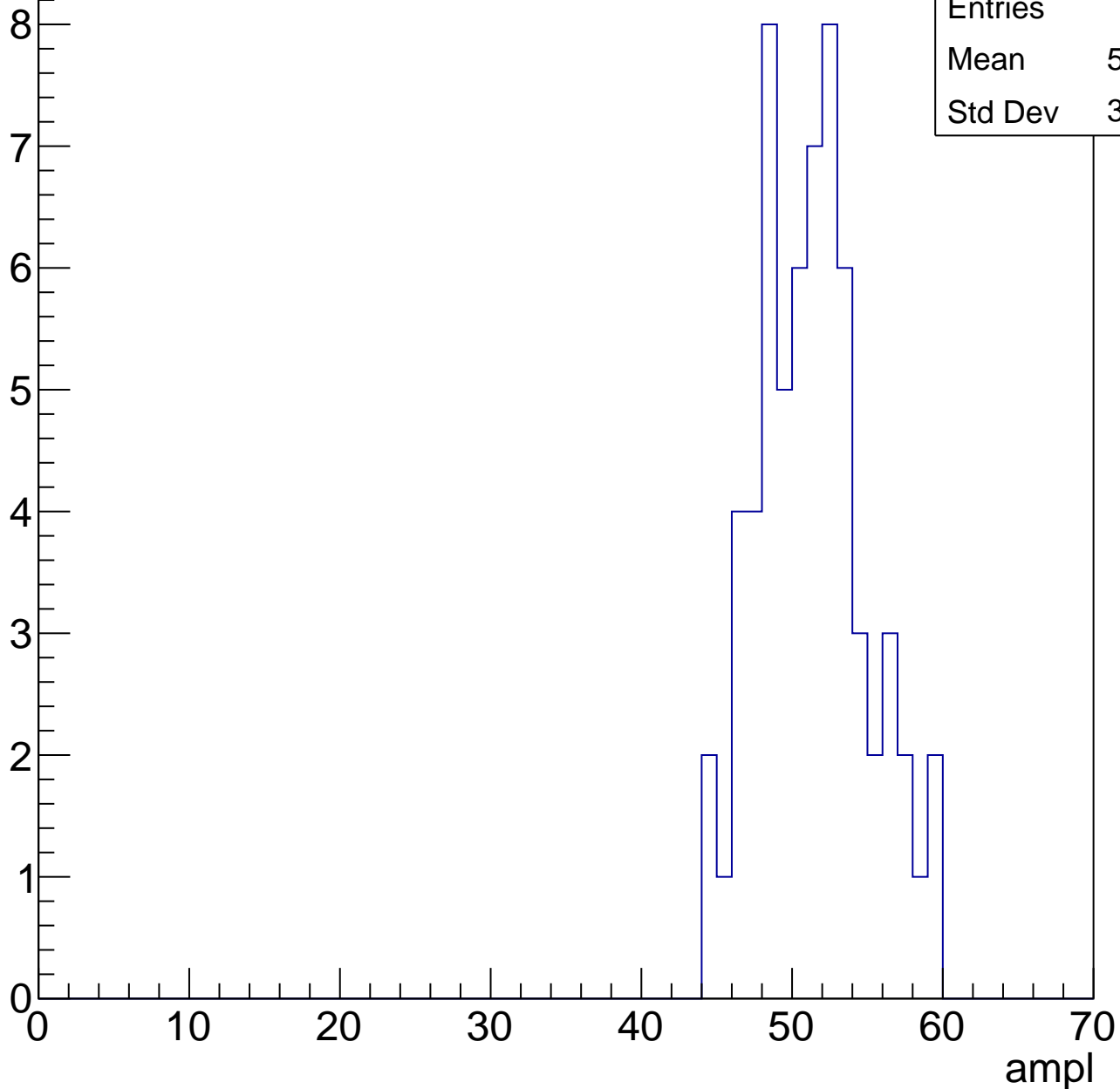


B0L001S, U25-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

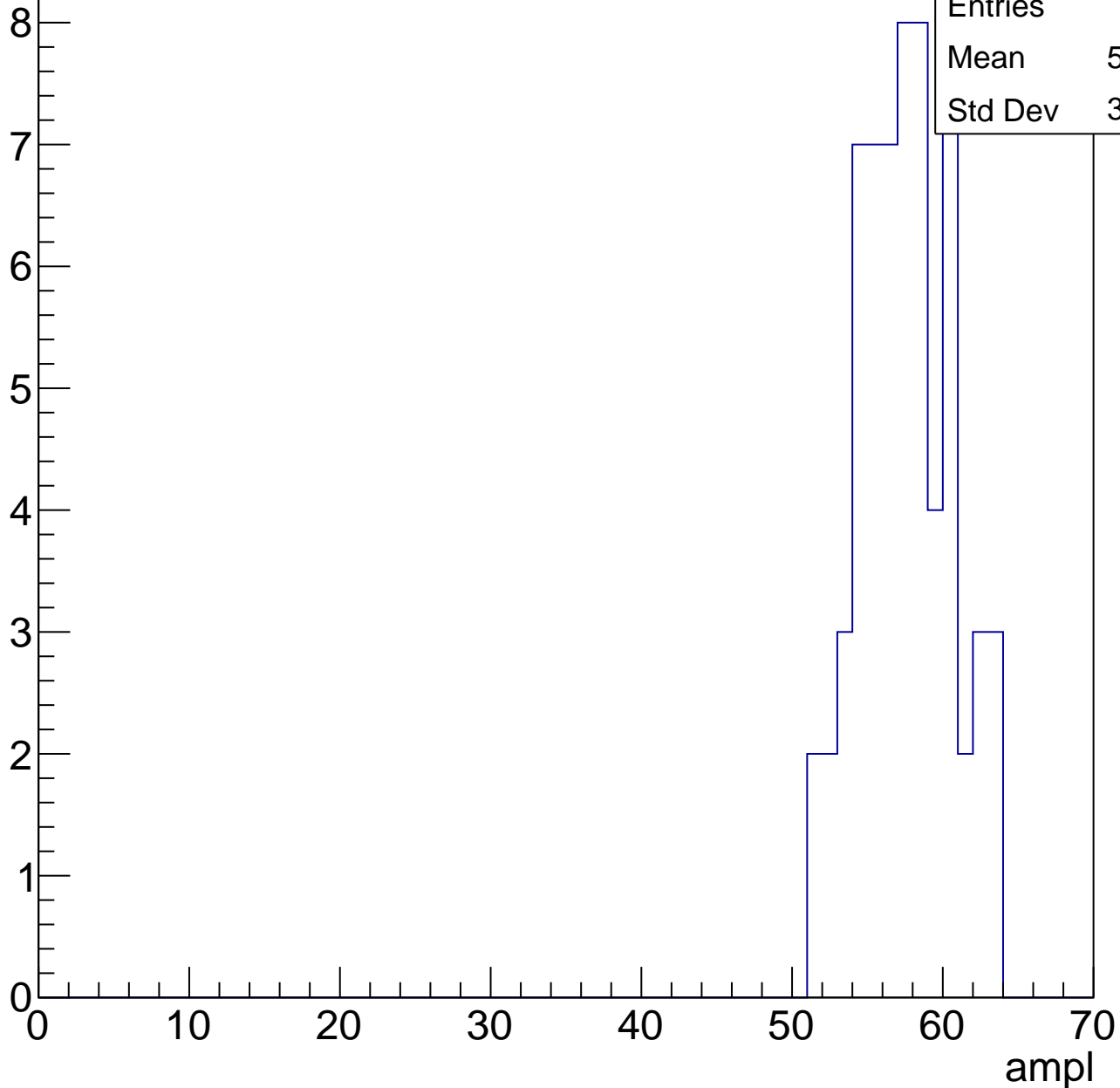
Entries	64
Mean	50.86
Std Dev	3.557



B0L001S, U25-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



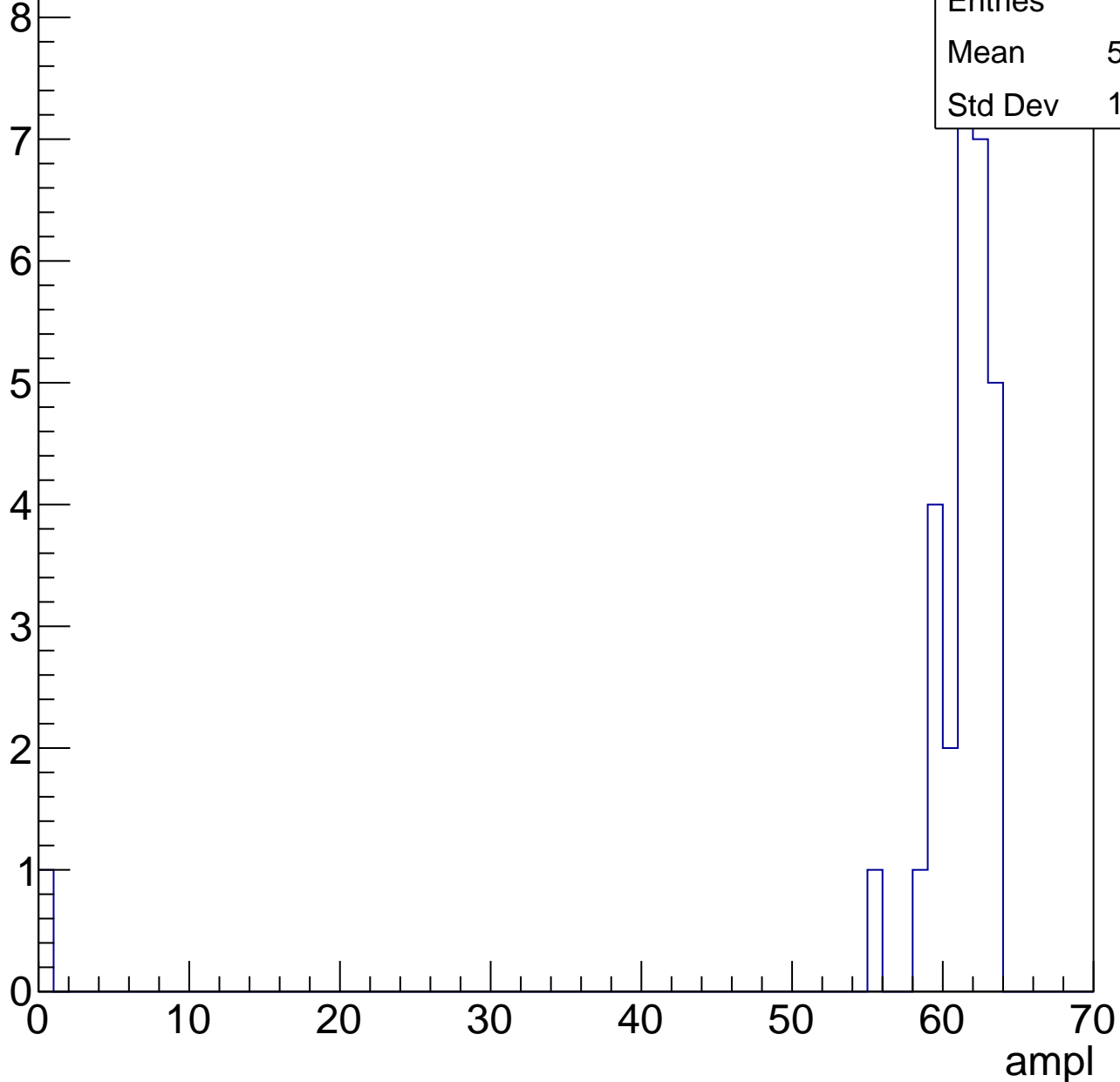
Entries	64
Mean	57.08
Std Dev	3.007

B0L001S, U25-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.83
Std Dev	11.26



B0L001S, U25-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U25-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch12, adc0

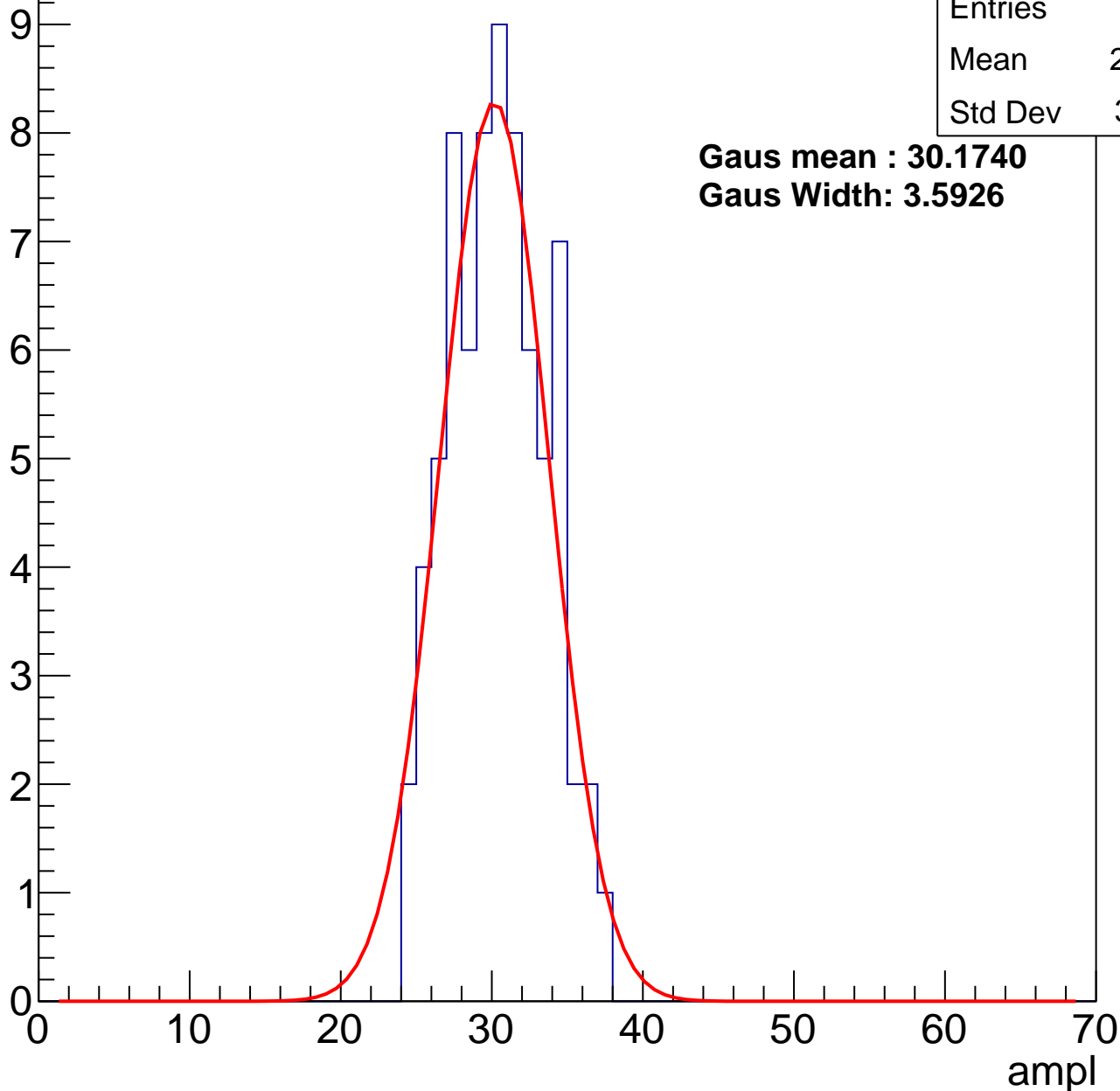
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.95
Std Dev	3.131

Gaus mean : 30.1740

Gaus Width: 3.5926



B0L001S, U25-ch12, adc1

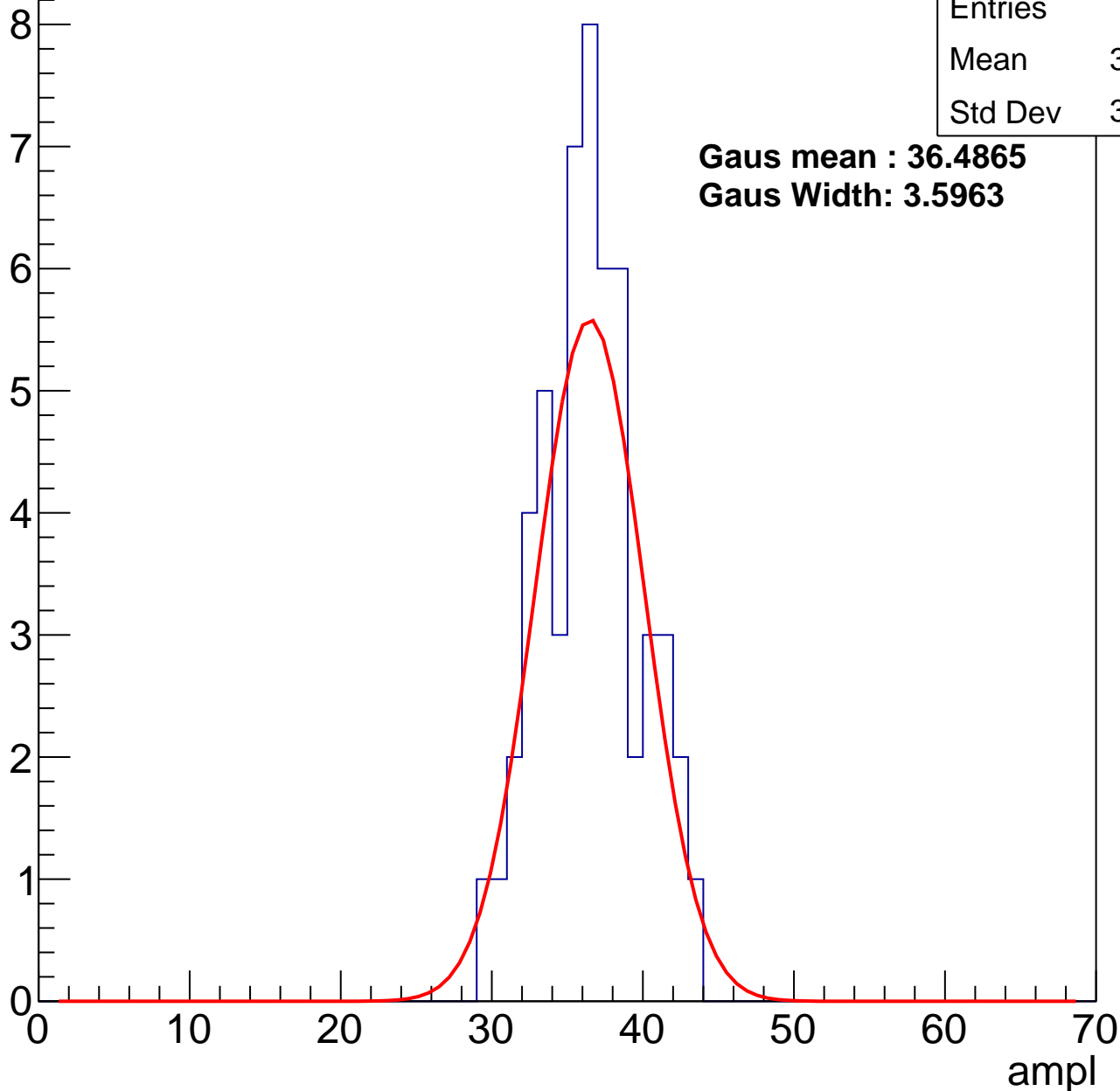
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	36.06
Std Dev	3.205

Gaus mean : 36.4865

Gaus Width: 3.5963



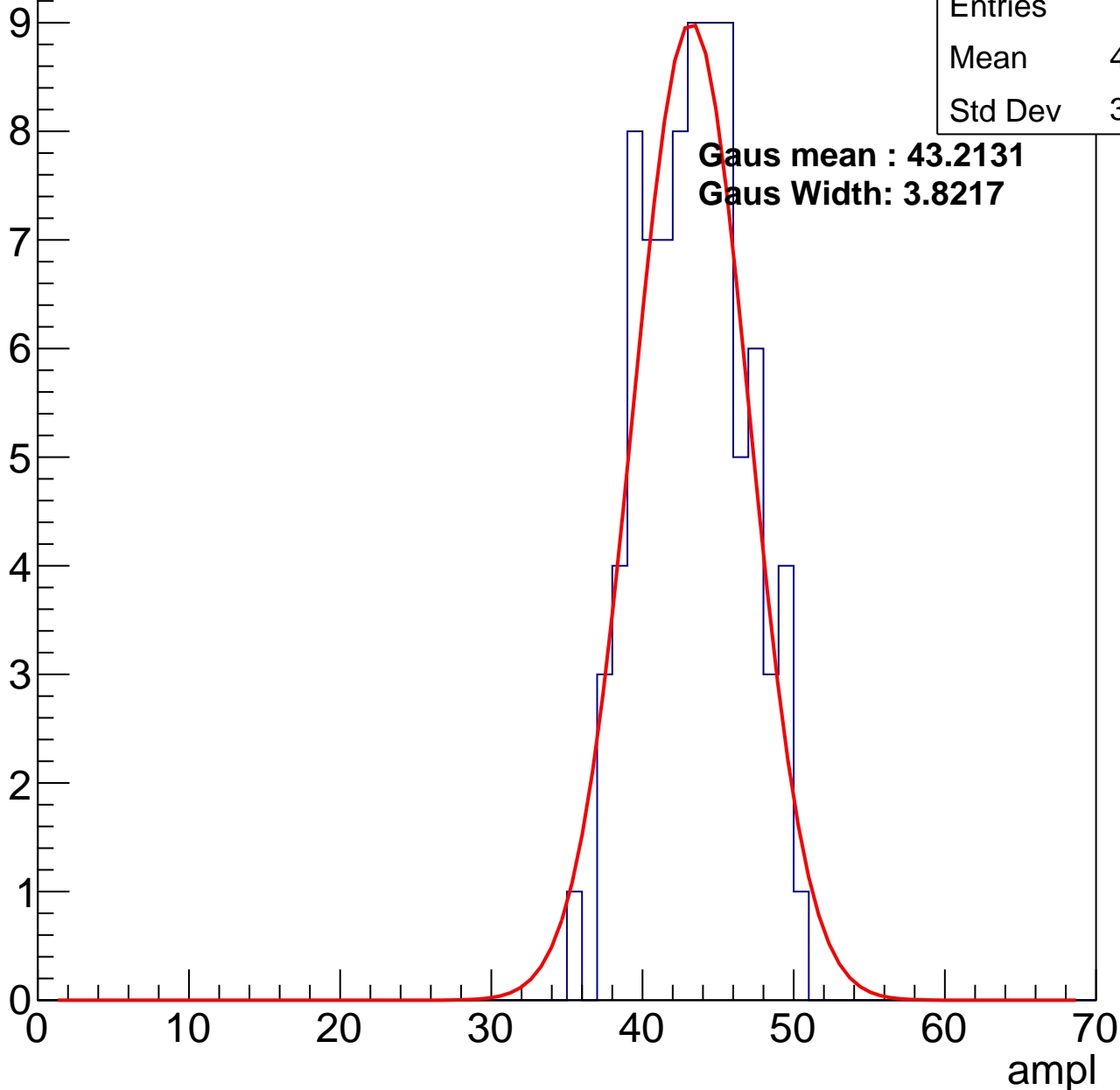
B0L001S, U25-ch12, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	42.89
Std Dev	3.367

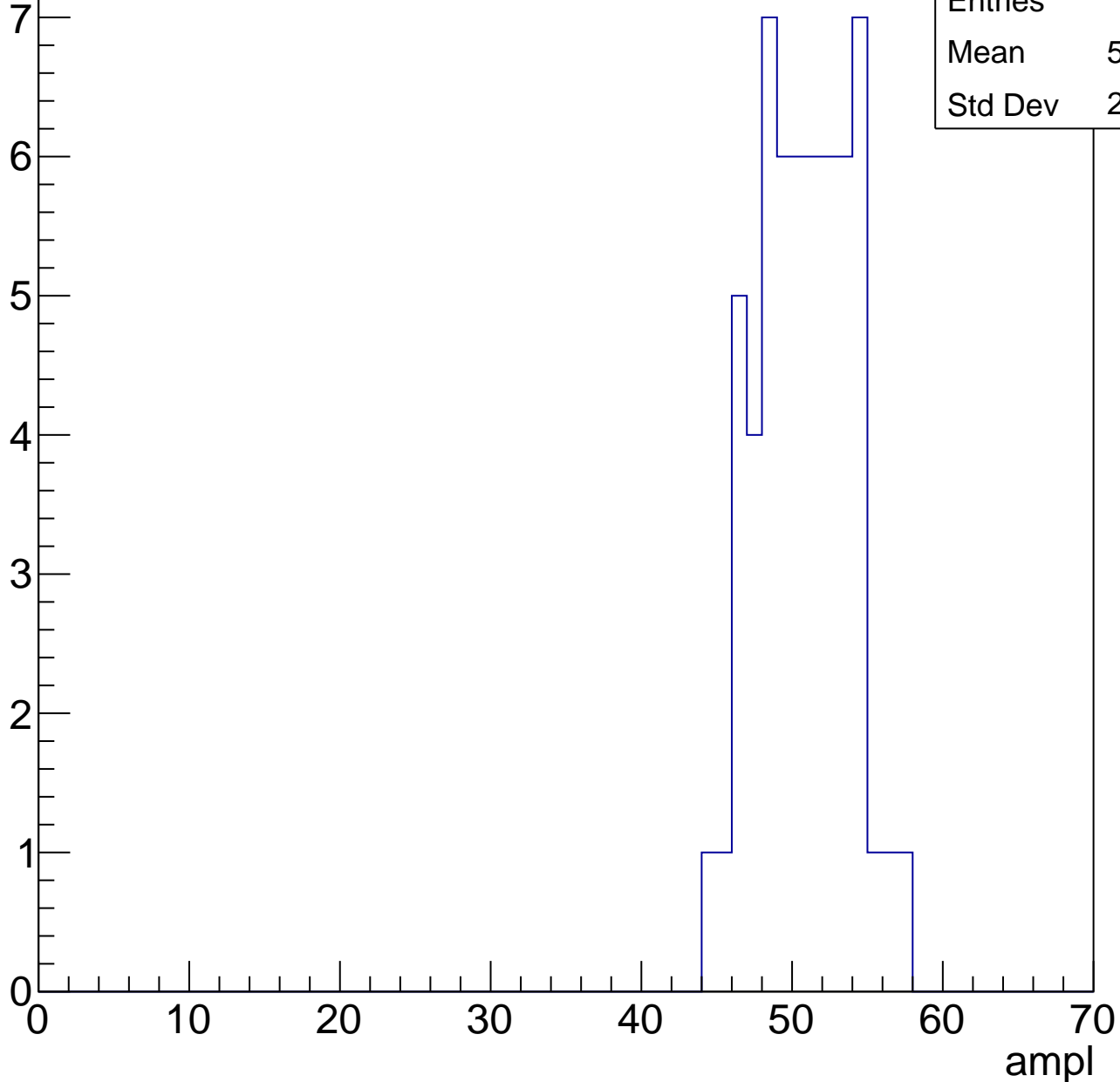
Gaus mean : 43.2131
Gaus Width: 3.8217



B0L001S, U25-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

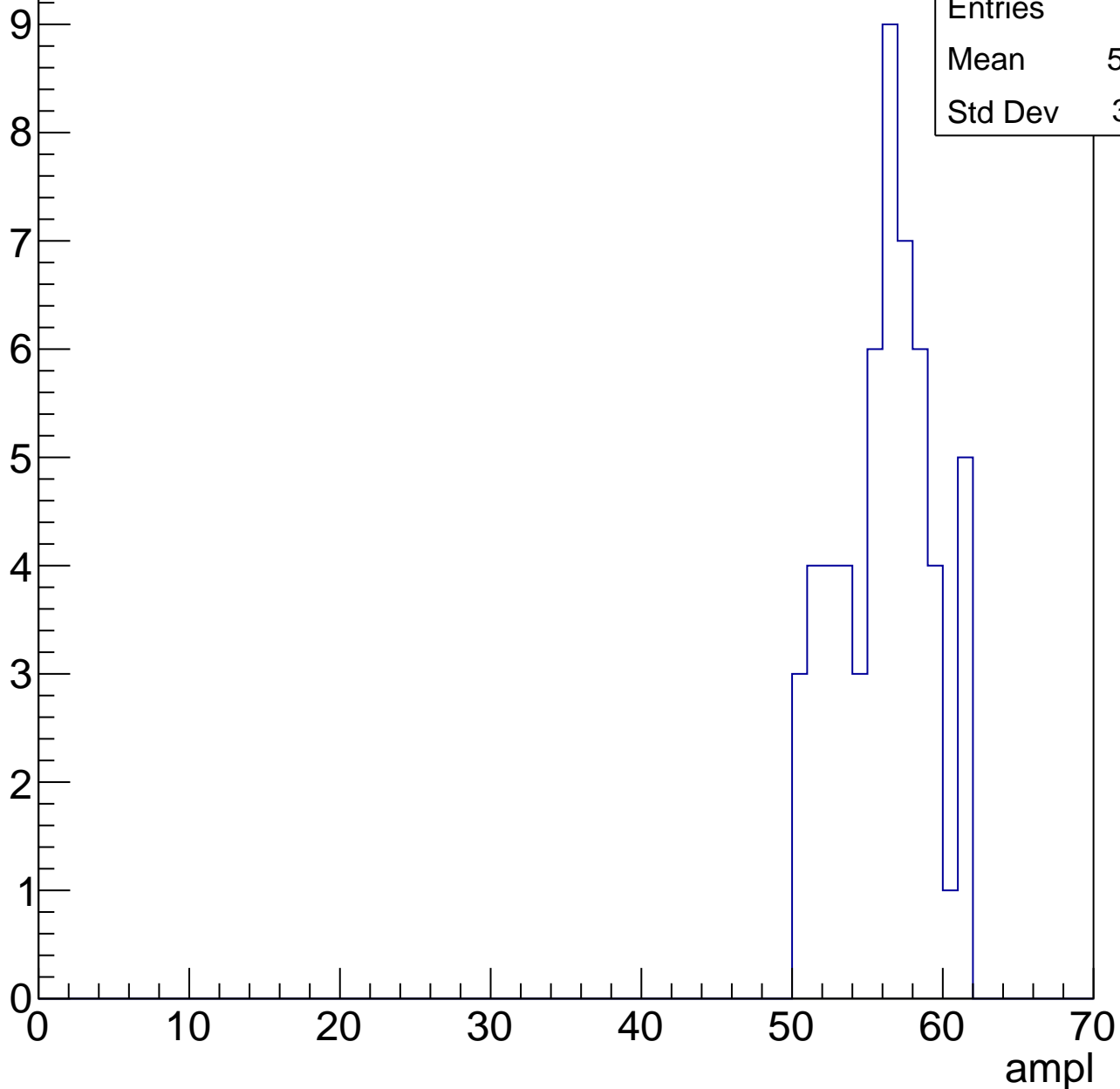


Entries	58
Mean	50.33
Std Dev	2.968

B0L001S, U25-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

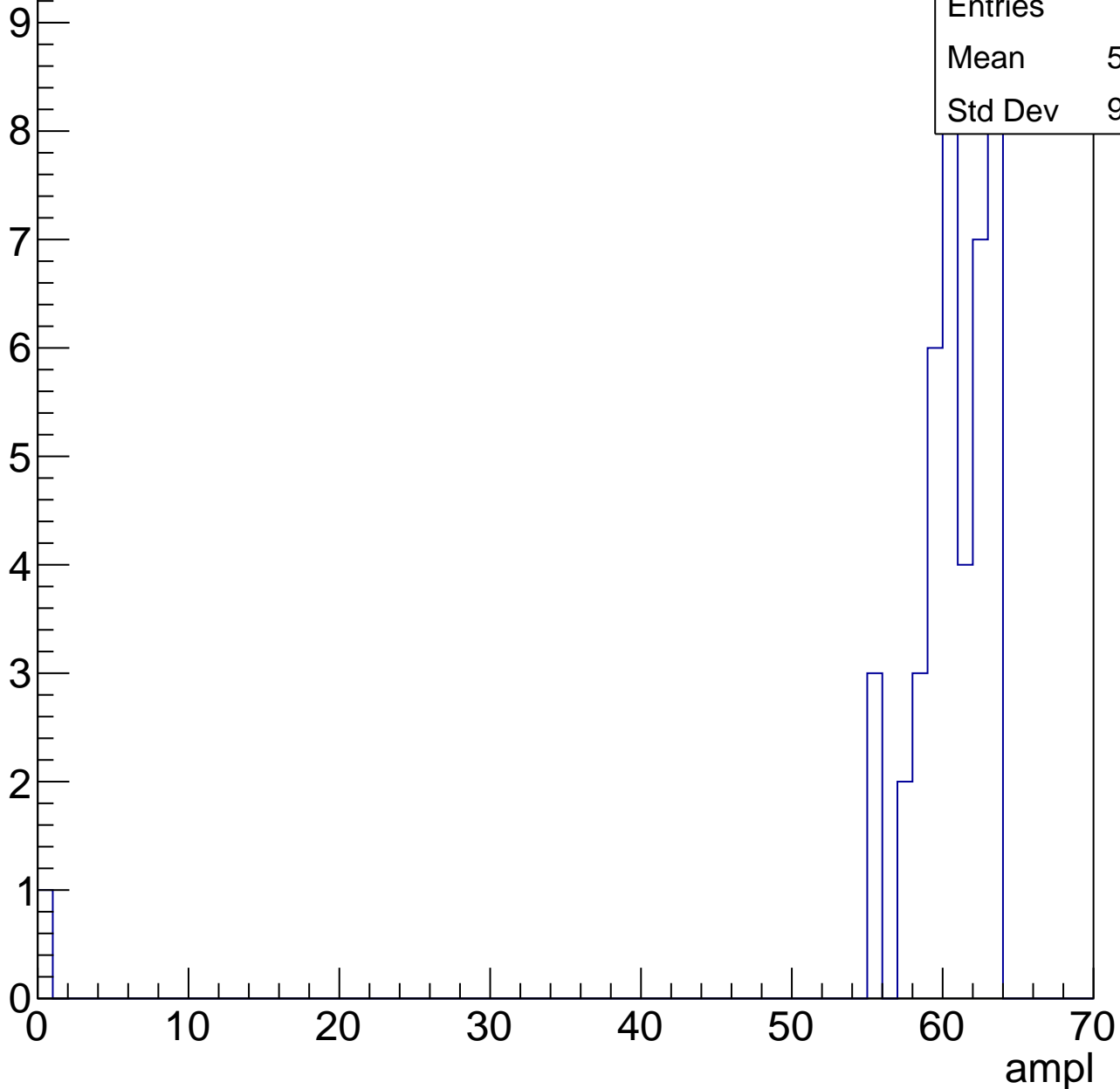


B0L001S, U25-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

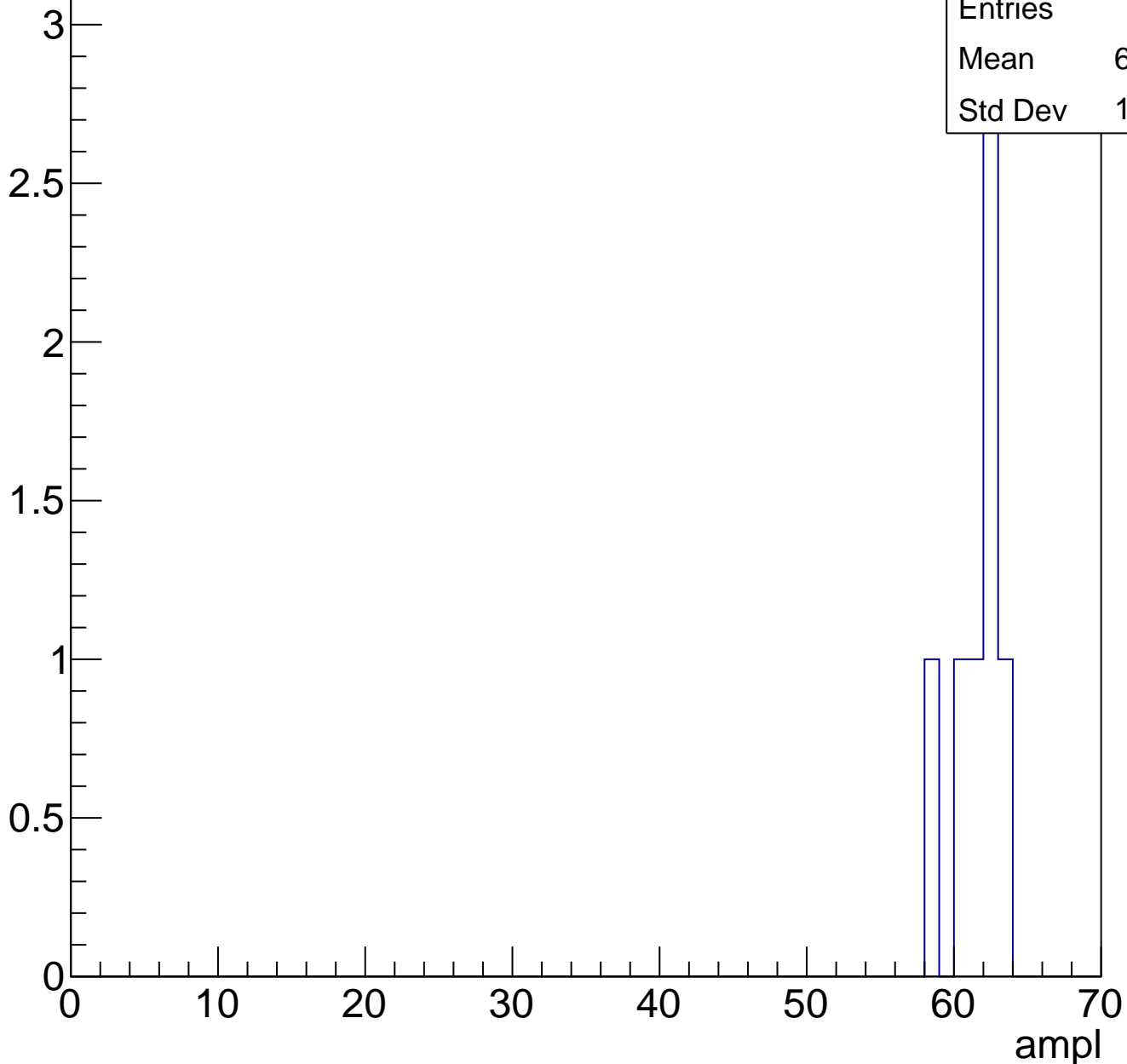
Entries	43
Mean	58.88
Std Dev	9.364



B0L001S, U25-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

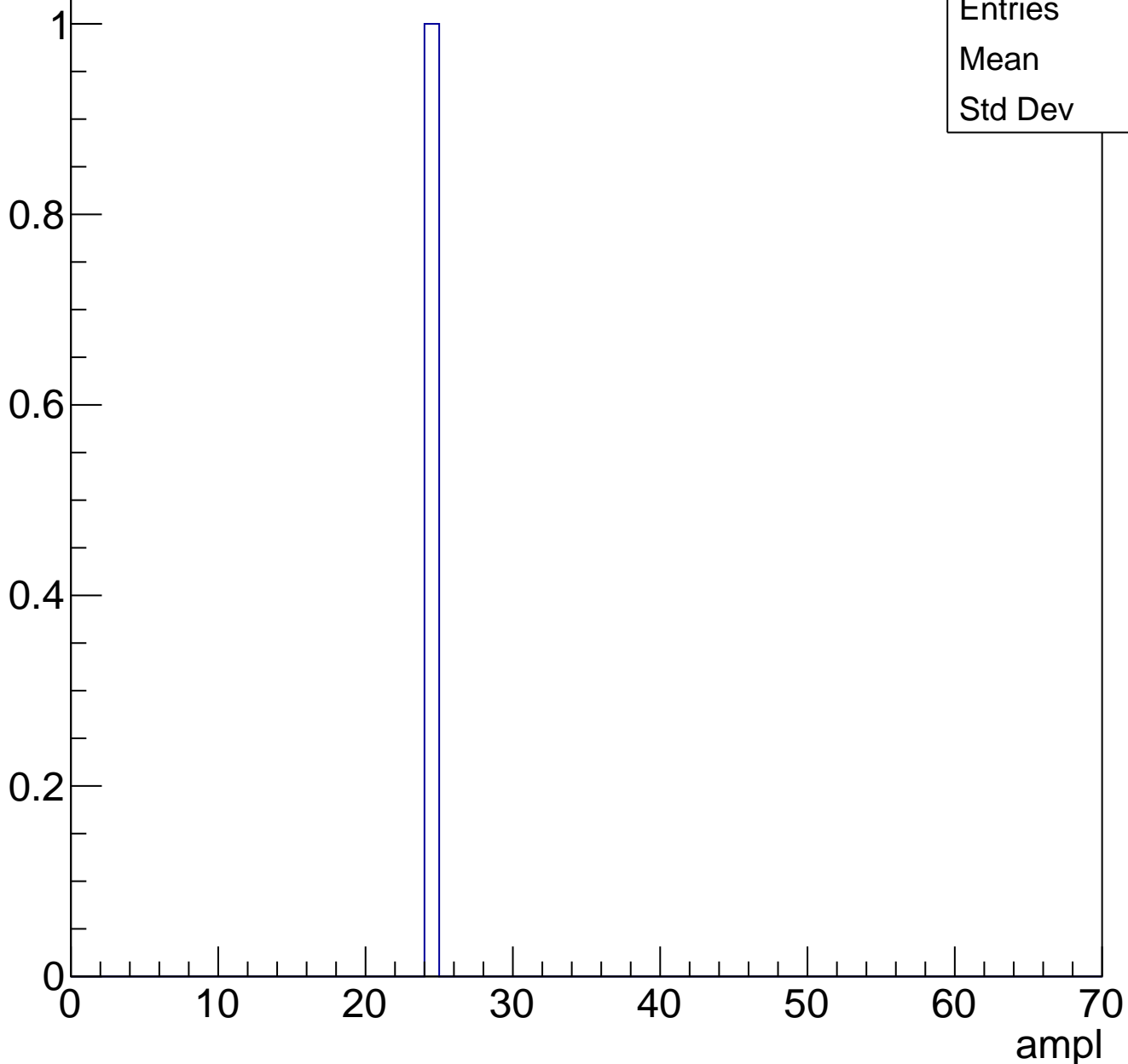
Entry



B0L001S, U25-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	24
Std Dev	0

B0L001S, U25-ch13, adc0

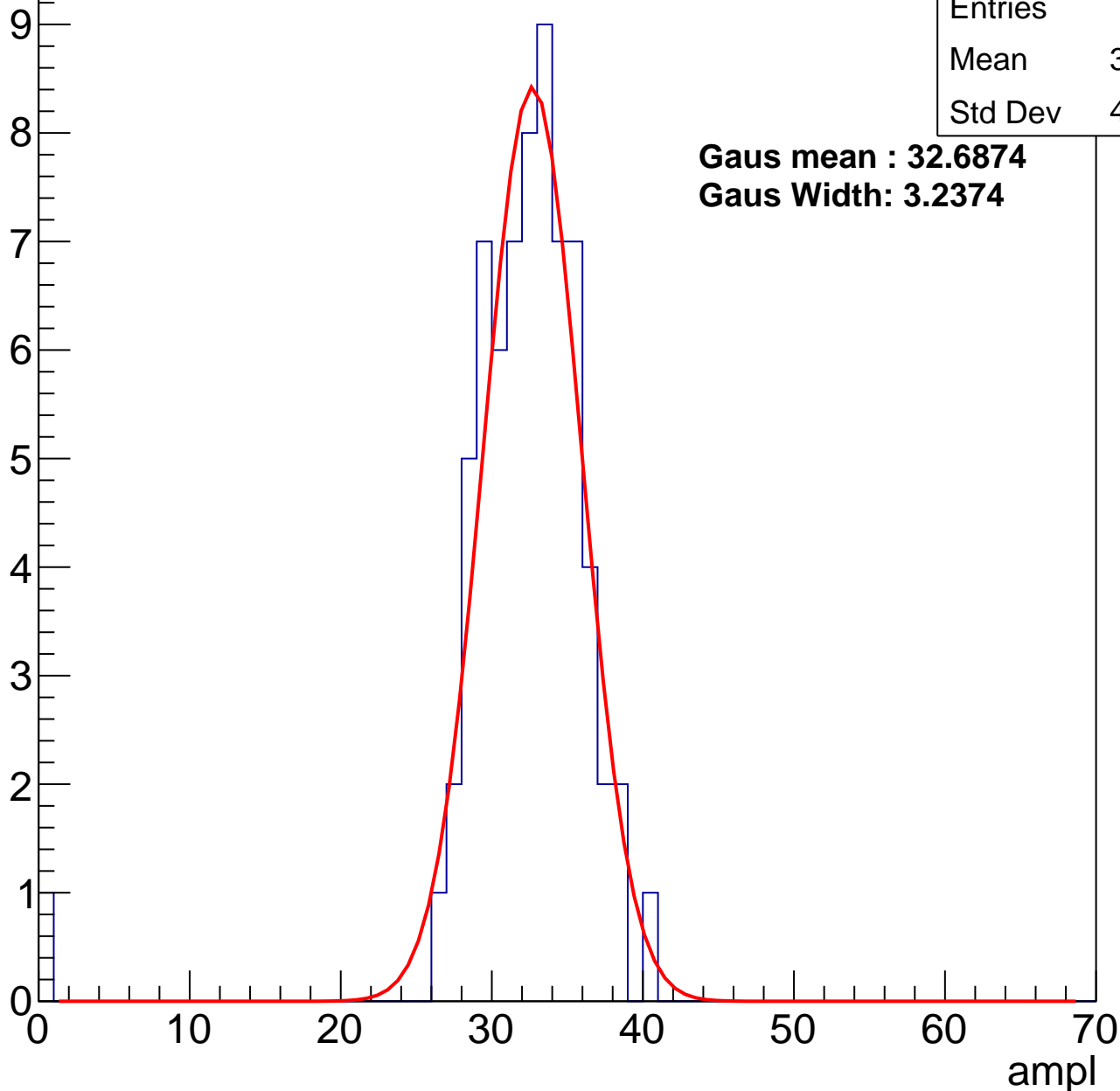
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.74
Std Dev	4.859

Gaus mean : 32.6874

Gaus Width: 3.2374



B0L001S, U25-ch13, adc1

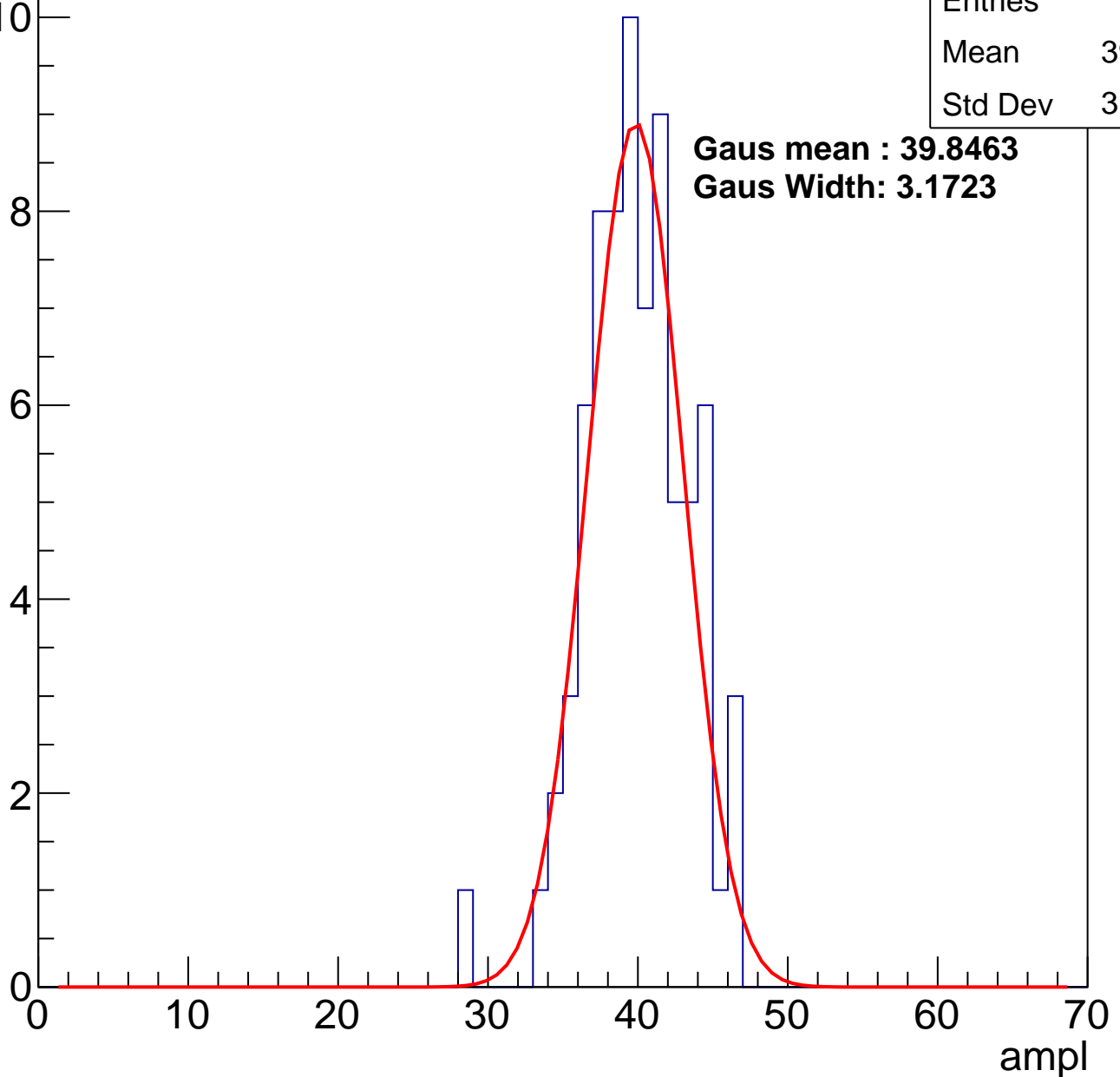
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	39.48
Std Dev	3.332

Gaus mean : 39.8463

Gaus Width: 3.1723



B0L001S, U25-ch13, adc2

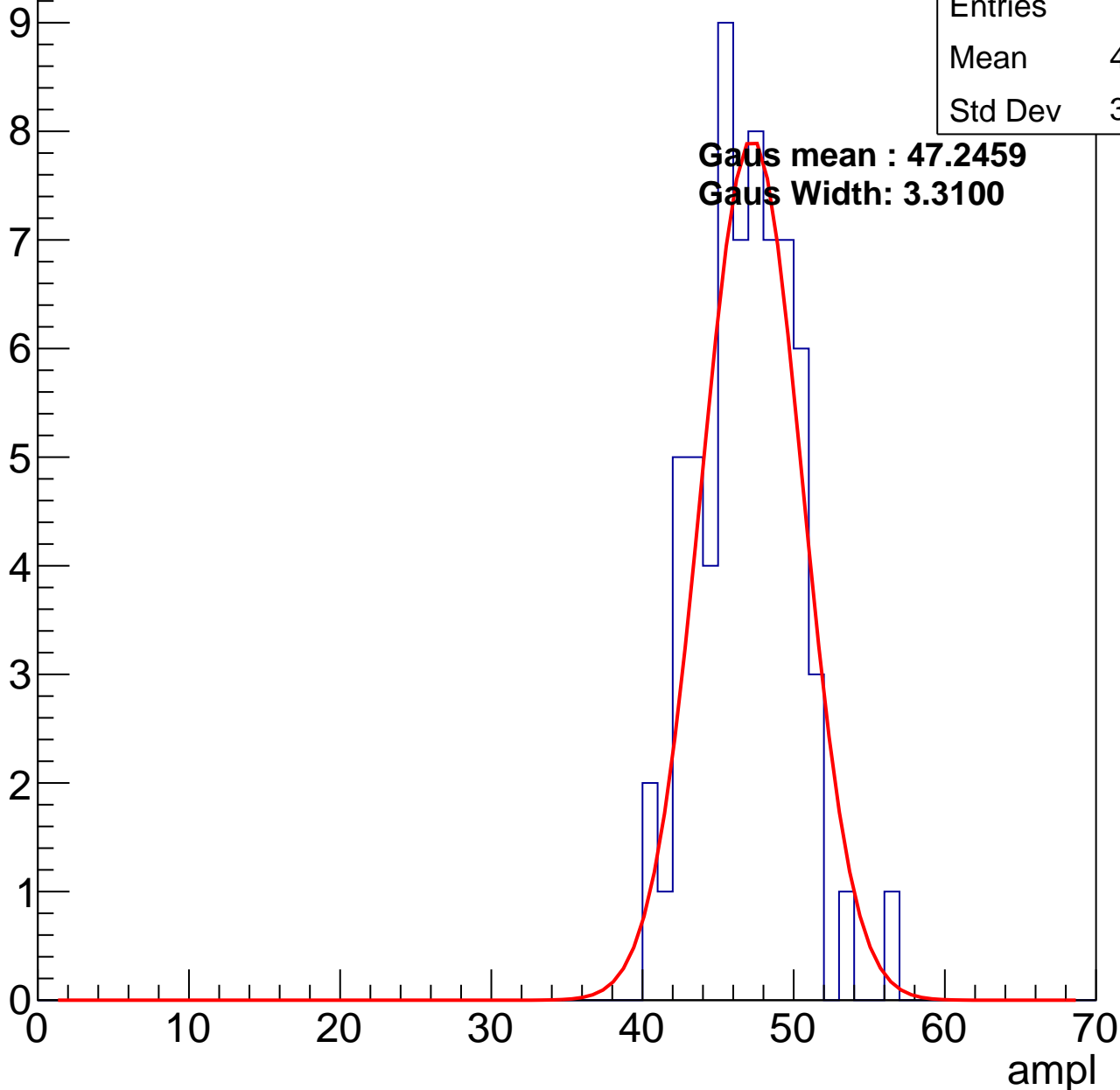
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	46.45
Std Dev	3.144

Gaus mean : 47.2459

Gaus Width: 3.3100

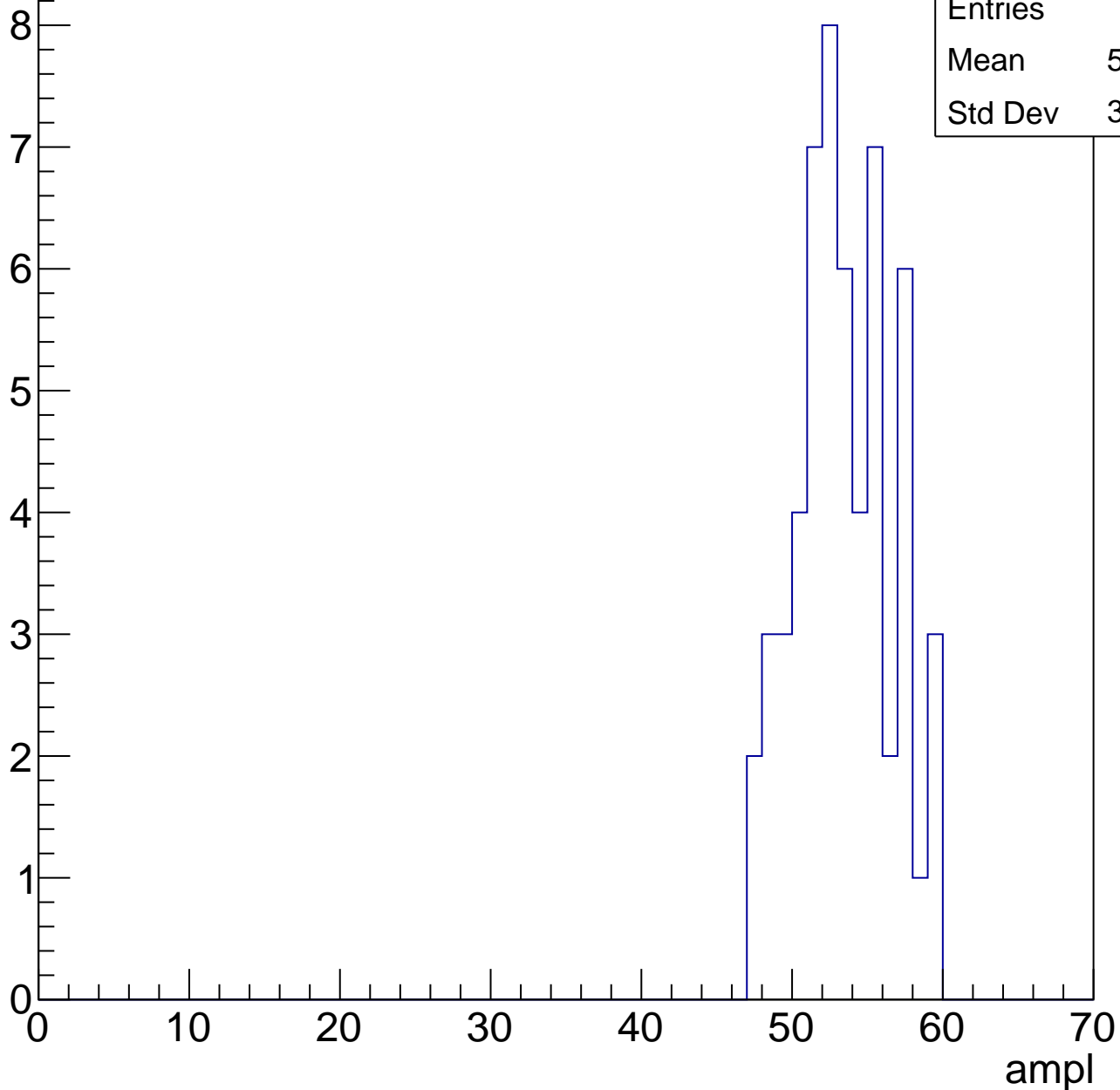


B0L001S, U25-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

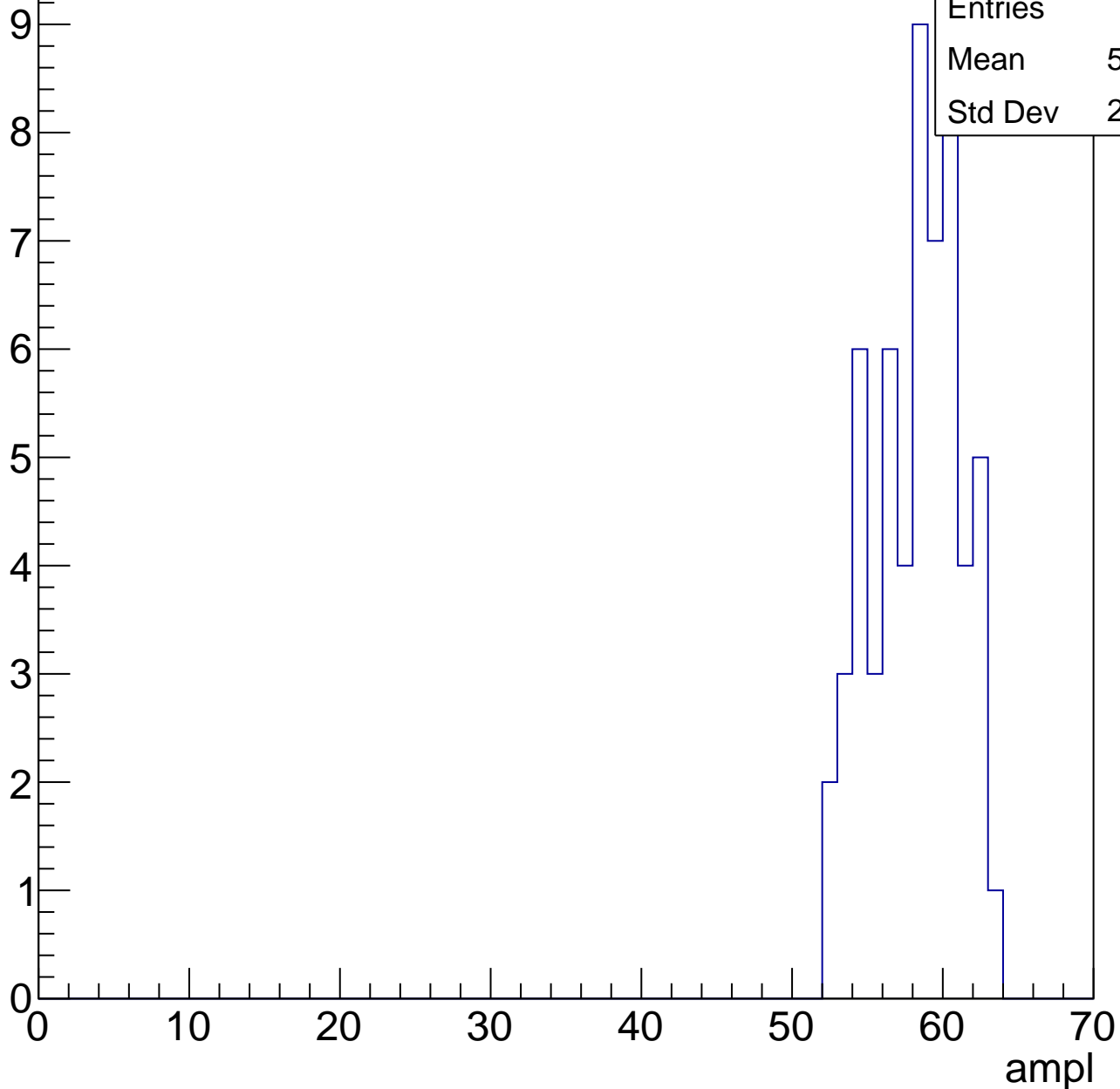
Entries	56
Mean	52.96
Std Dev	3.122



B0L001S, U25-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

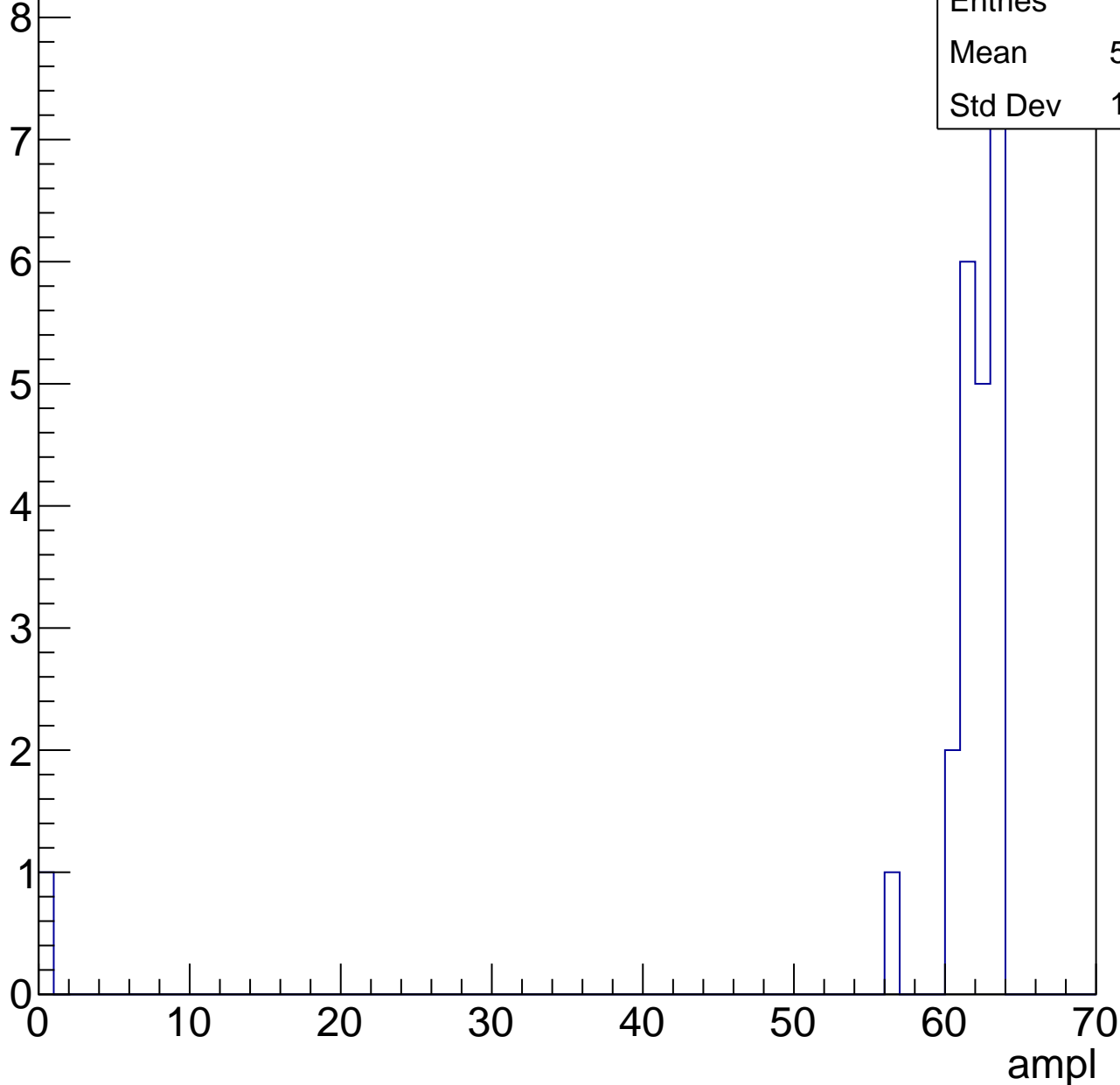


B0L001S, U25-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.96
Std Dev	12.66



B0L001S, U25-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L001S, U25-ch14, adc0

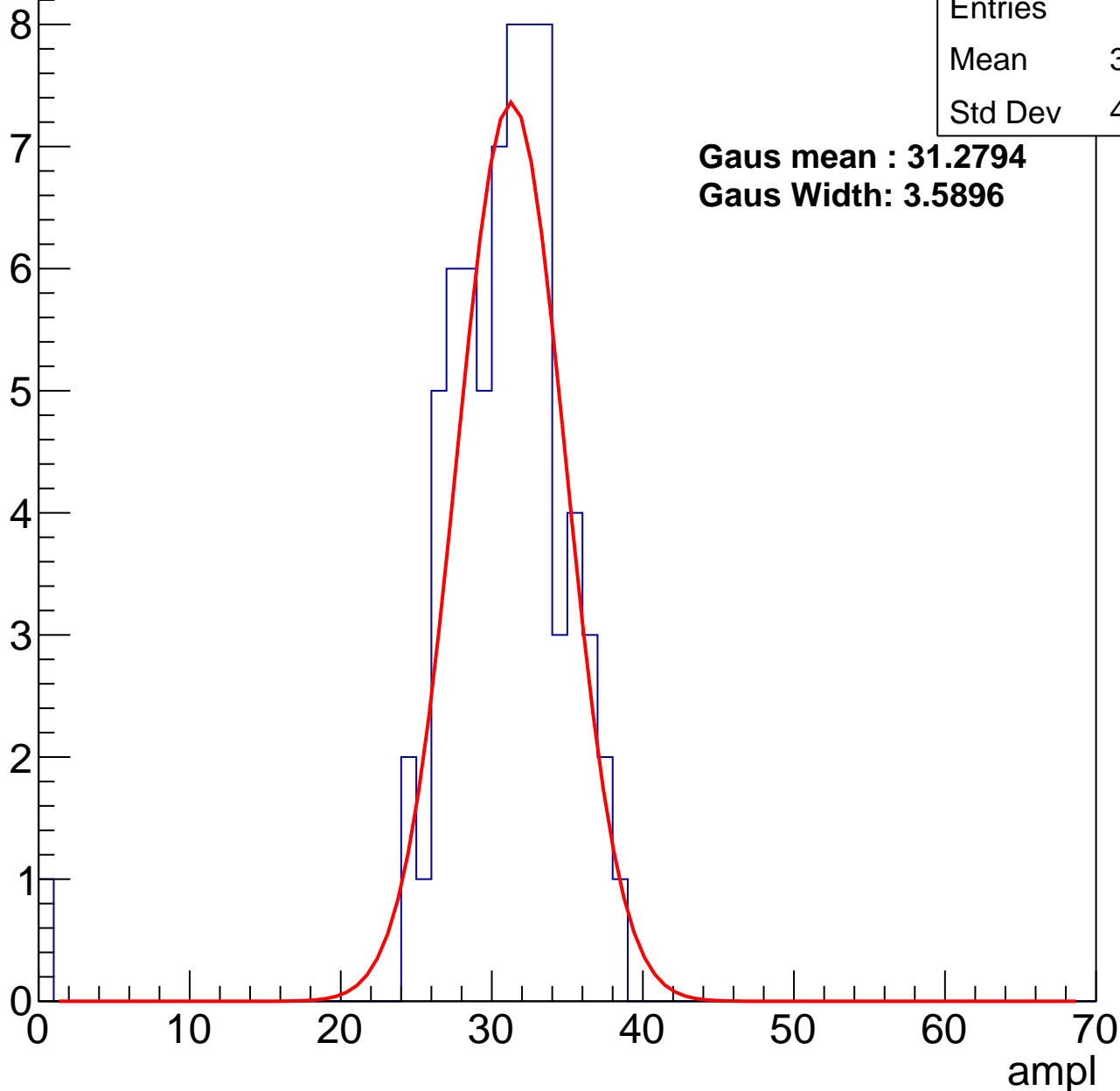
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.26
Std Dev	4.907

Gaus mean : 31.2794

Gaus Width: 3.5896



B0L001S, U25-ch14, adc1

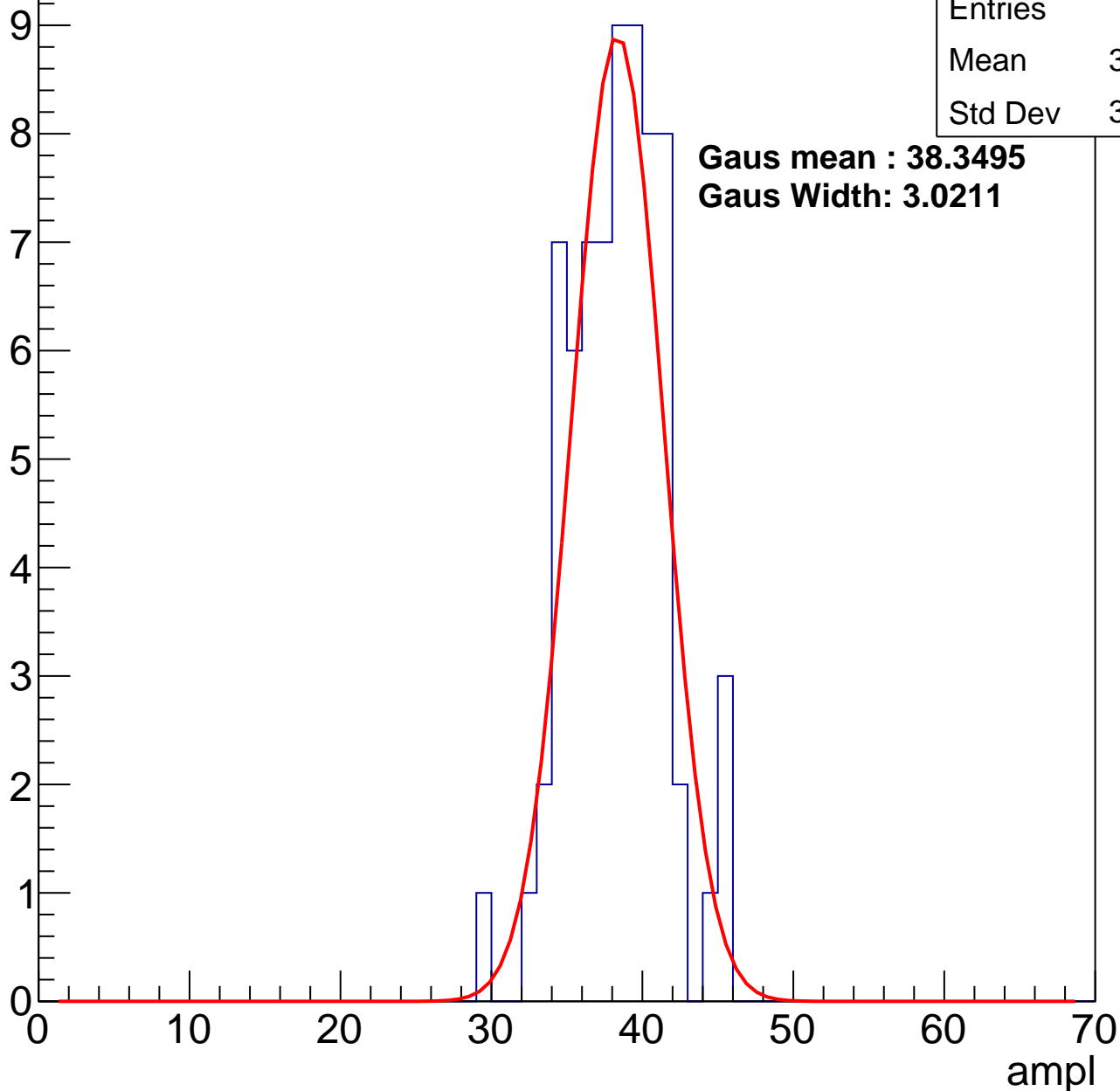
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.89
Std Dev	3.129

Gaus mean : 38.3495

Gaus Width: 3.0211



B0L001S, U25-ch14, adc2

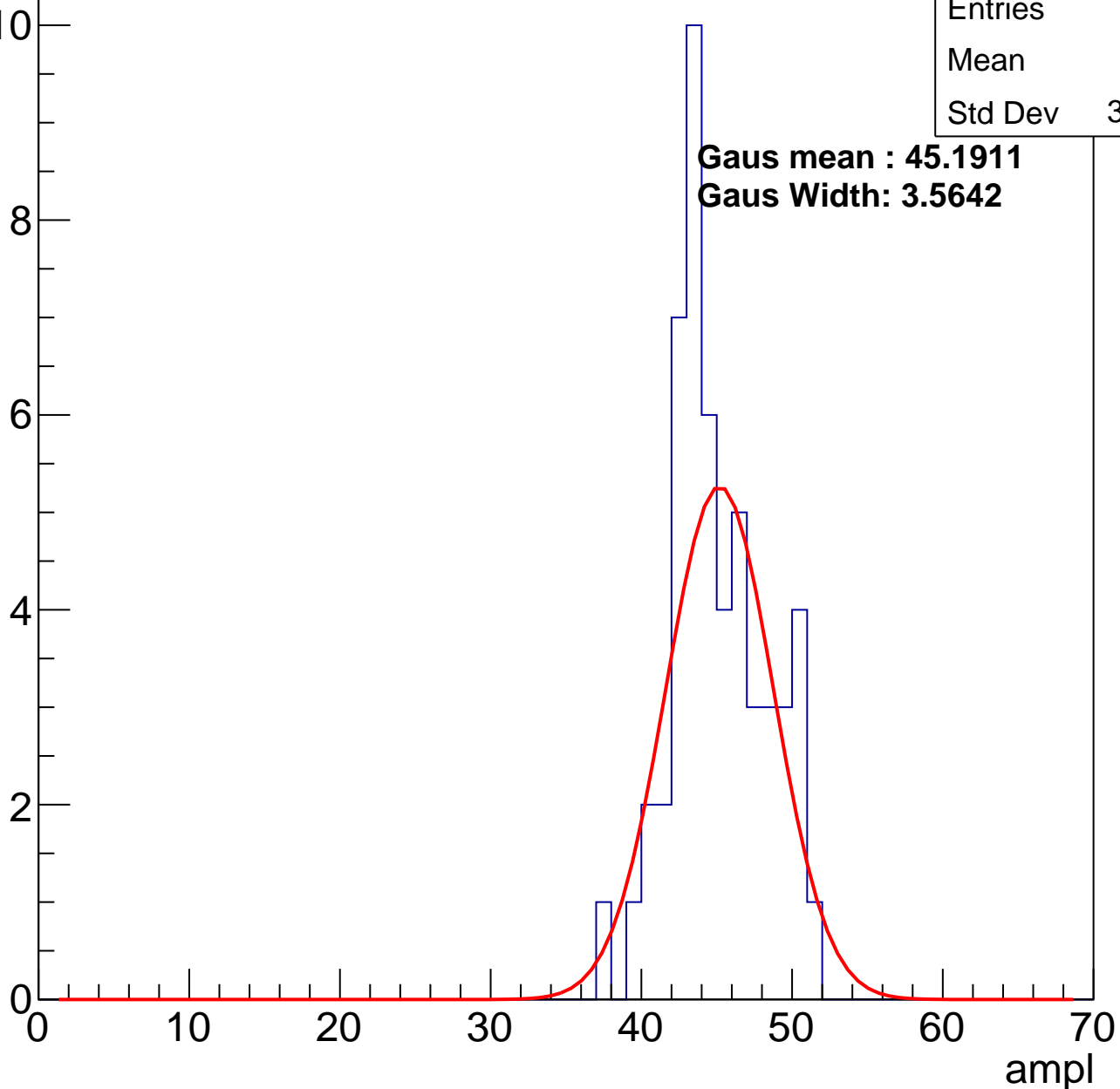
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.6
Std Dev	3.133

Gaus mean : 45.1911

Gaus Width: 3.5642

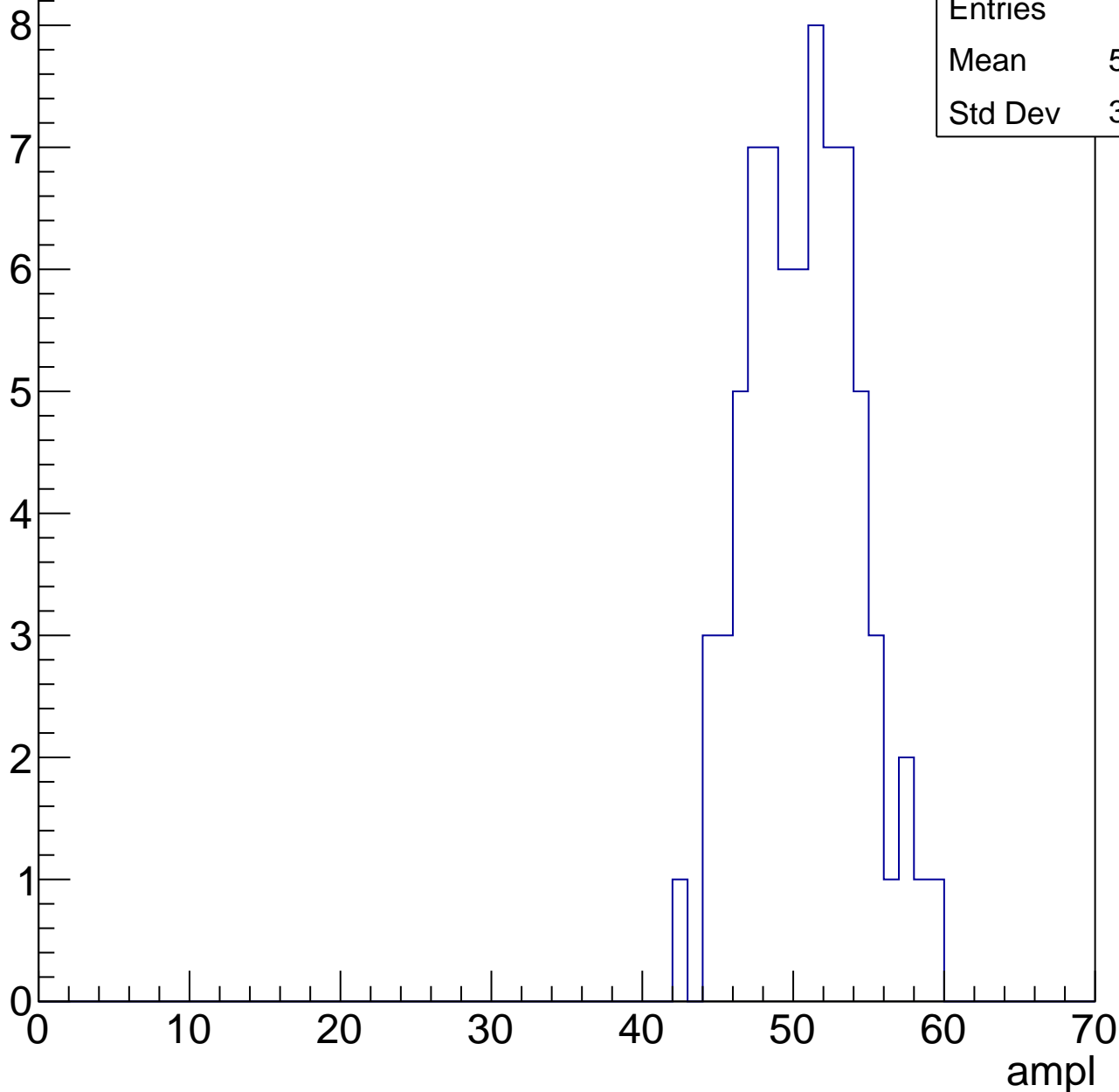


B0L001S, U25-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

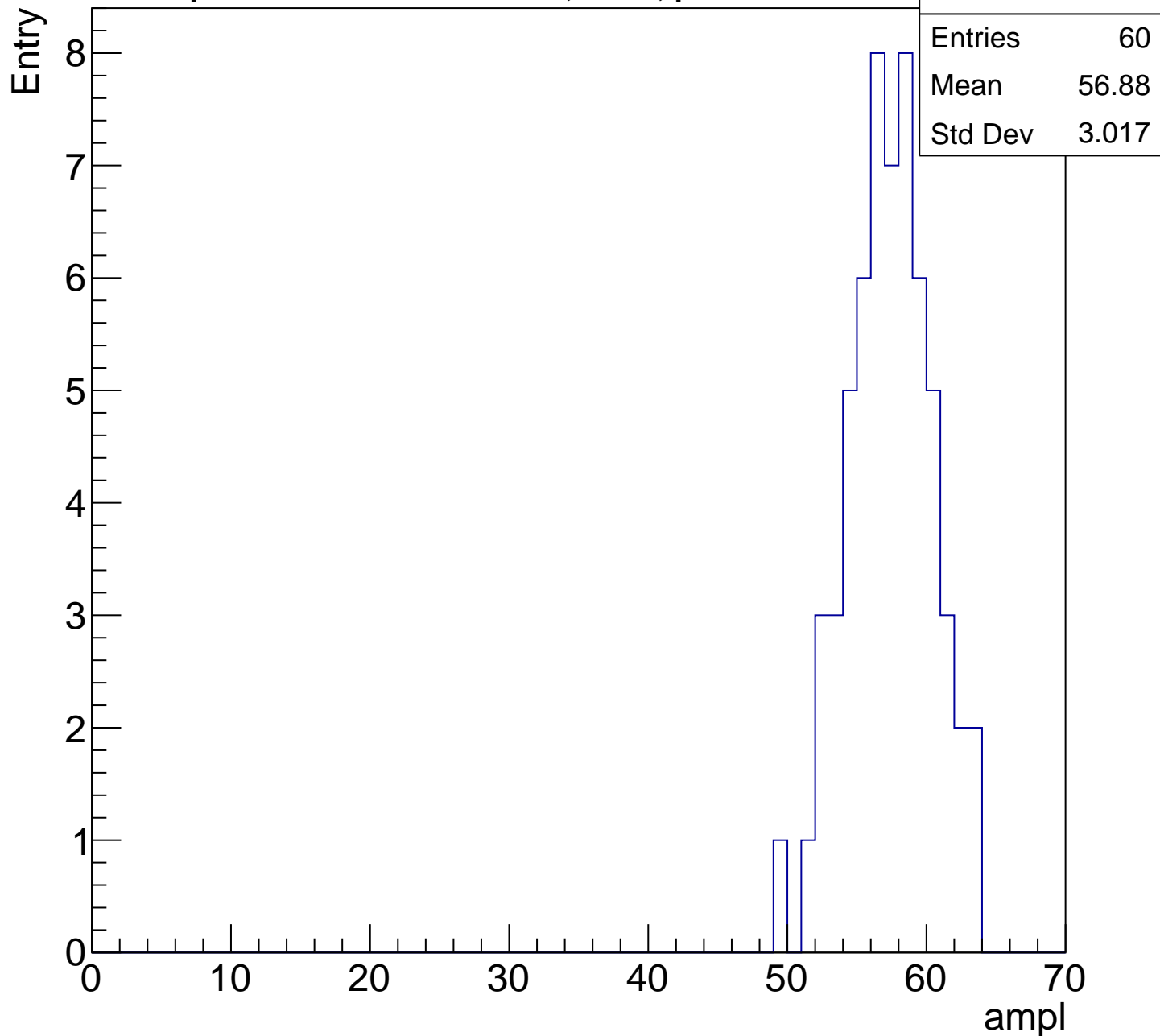
Entry

Entries	73
Mean	50.18
Std Dev	3.616



B0L001S, U25-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

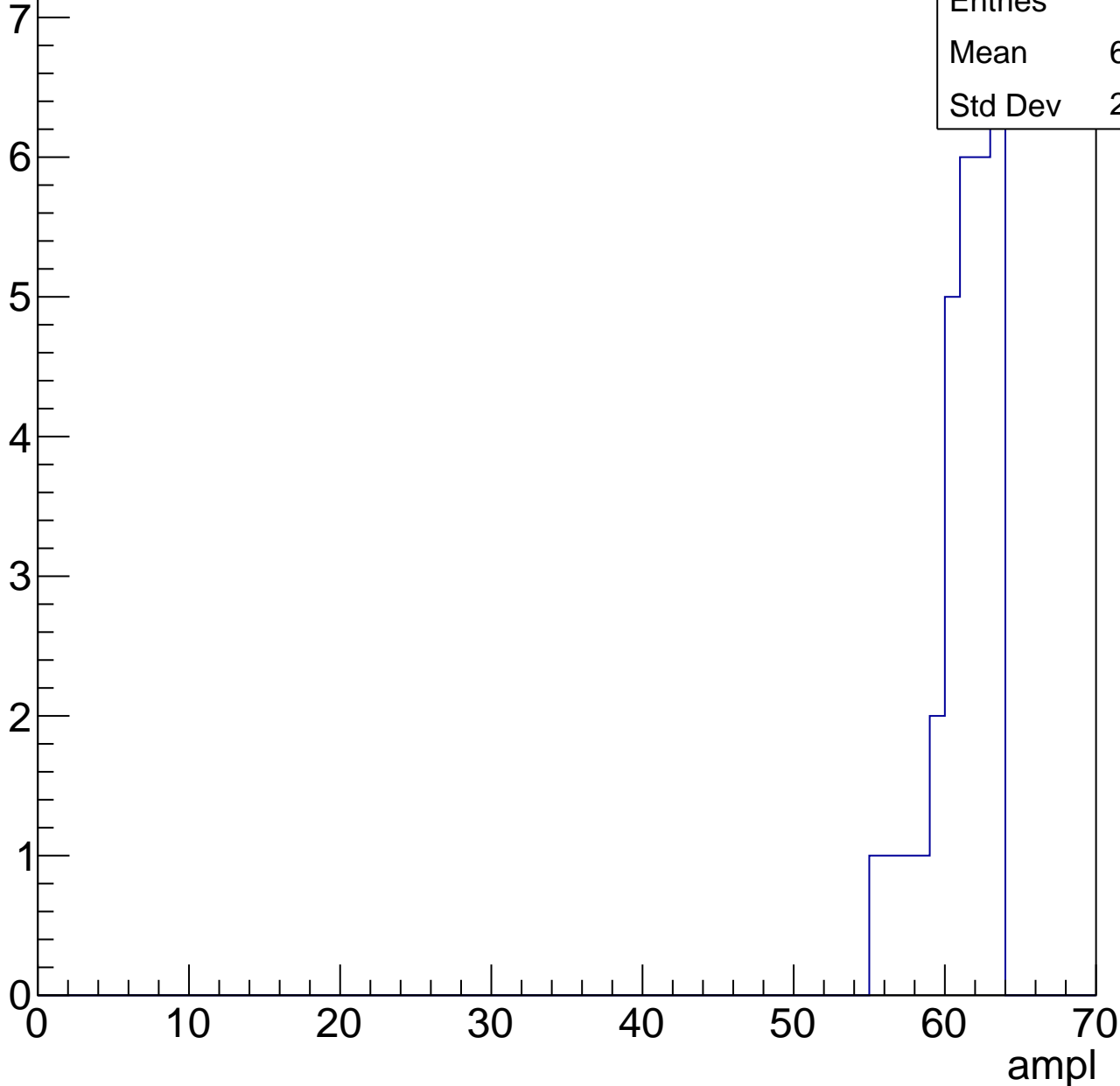


B0L001S, U25-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	60.77
Std Dev	2.093



B0L001S, U25-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

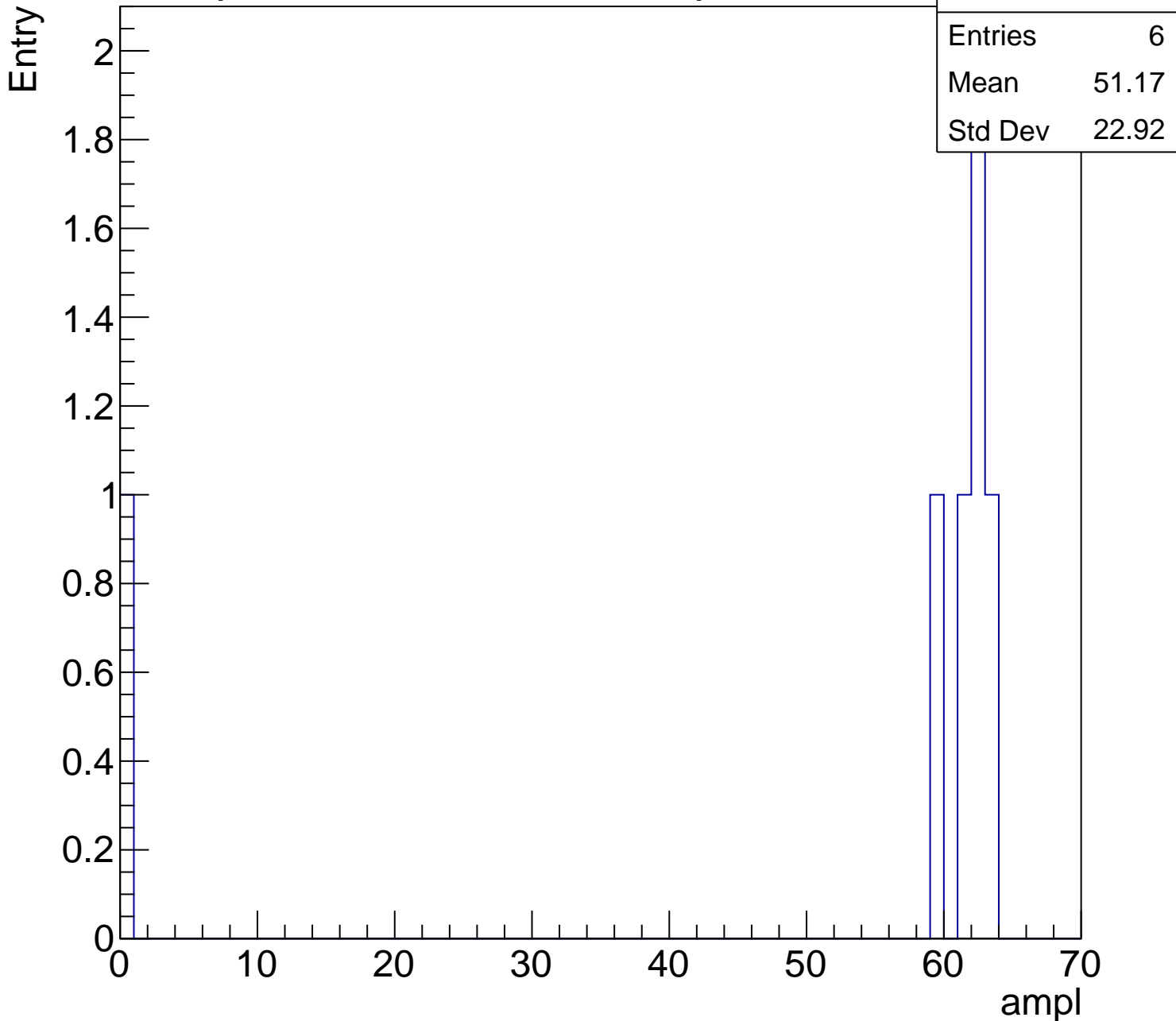
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.92

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch15, adc0

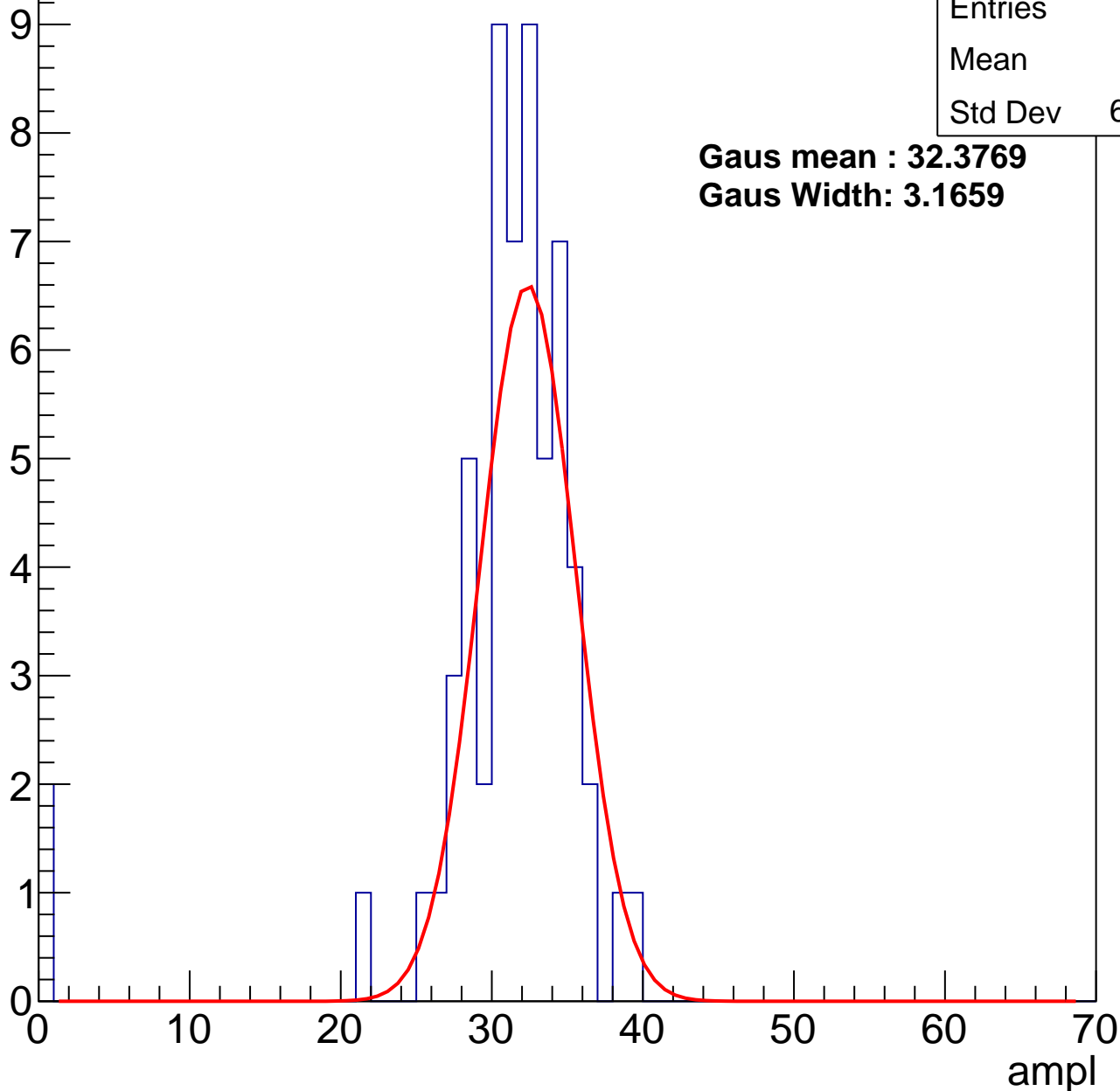
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.3
Std Dev	6.432

Gaus mean : 32.3769

Gaus Width: 3.1659



B0L001S, U25-ch15, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	37.49
Std Dev	3.371

Gaus mean : 38.3259

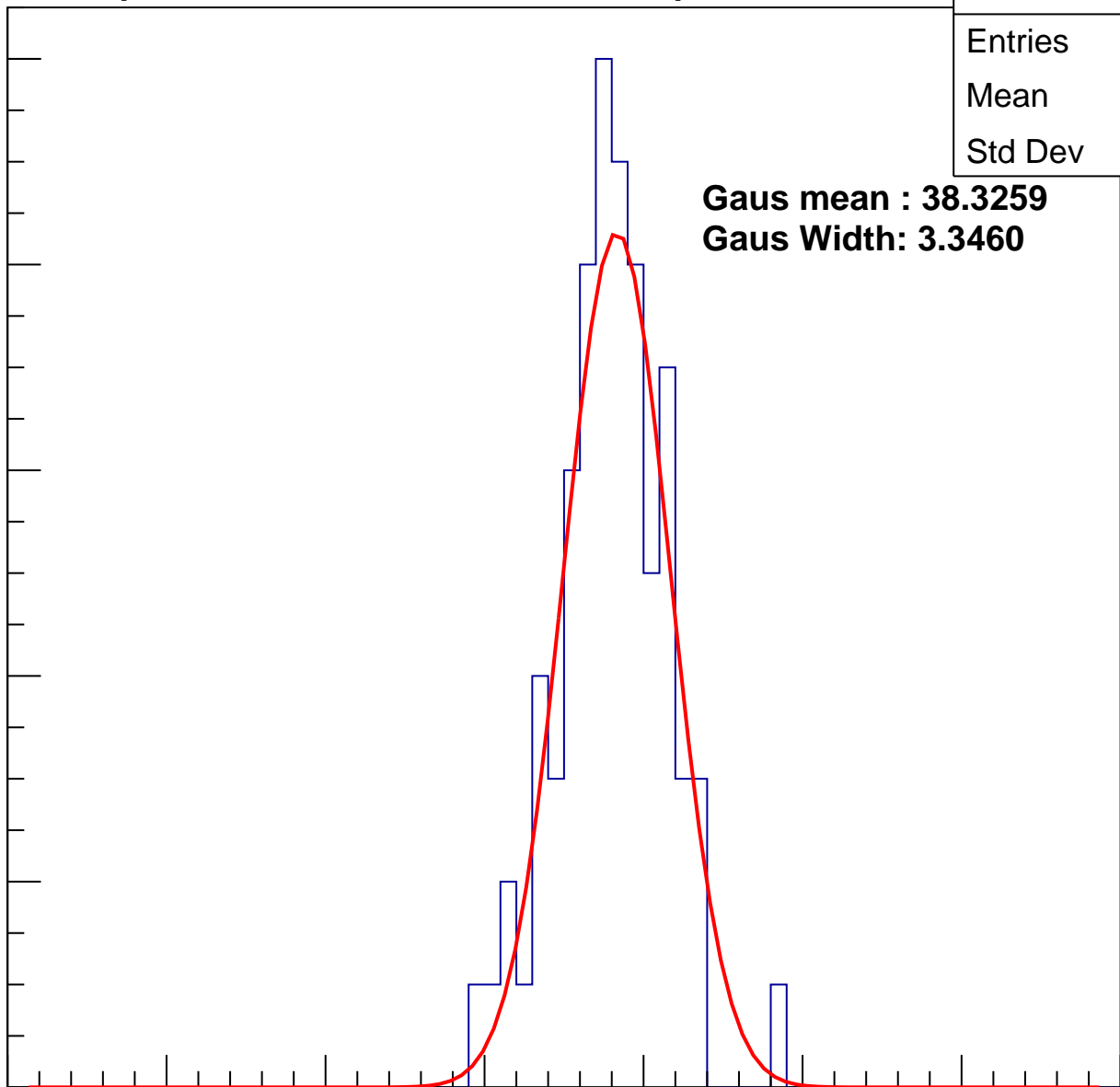
Gaus Width: 3.3460

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch15, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.28
Std Dev	3.438

Gaus mean : 44.5836

Gaus Width: 2.8860

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

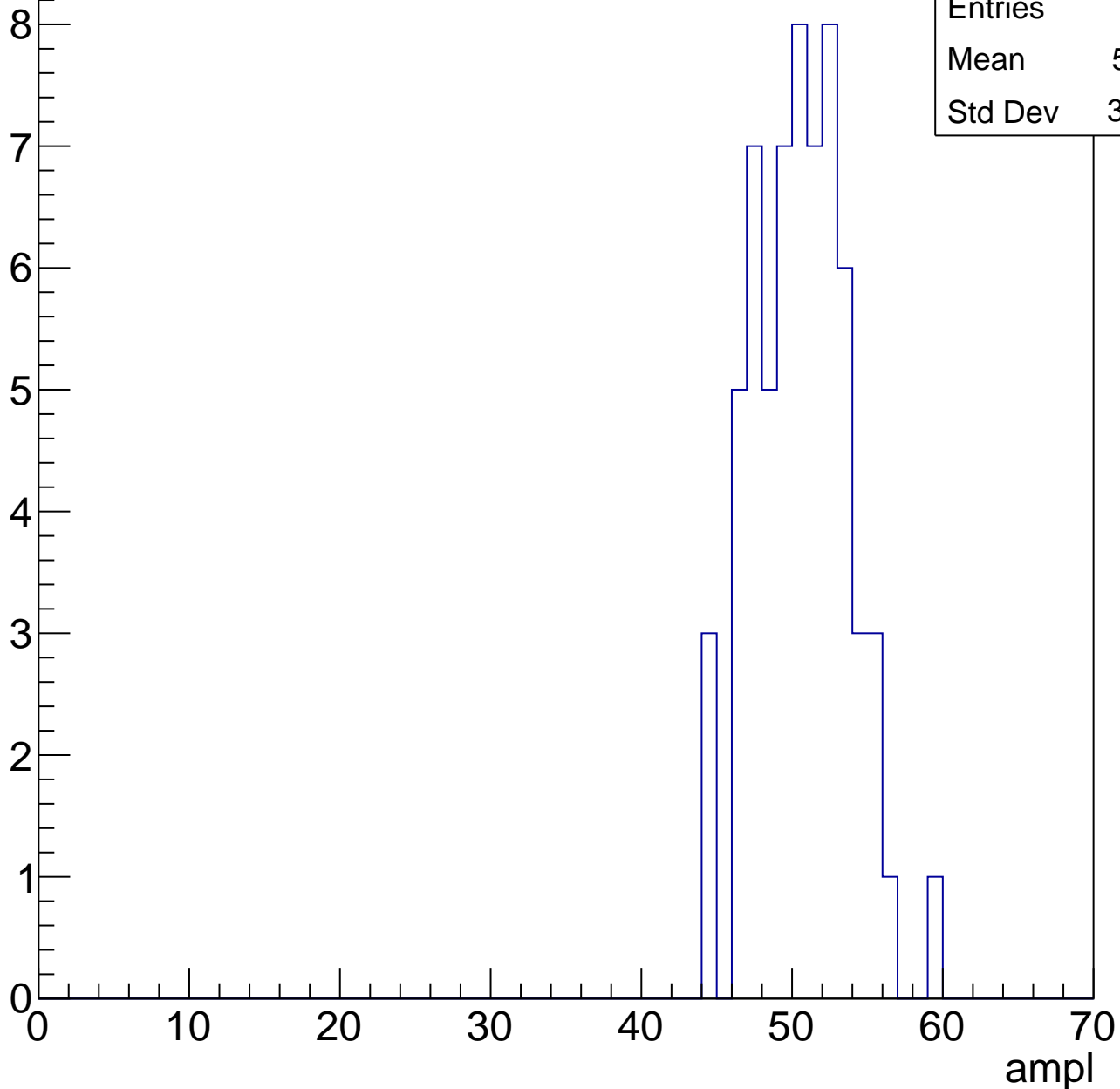
10

B0L001S, U25-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.11
Std Dev	3.088

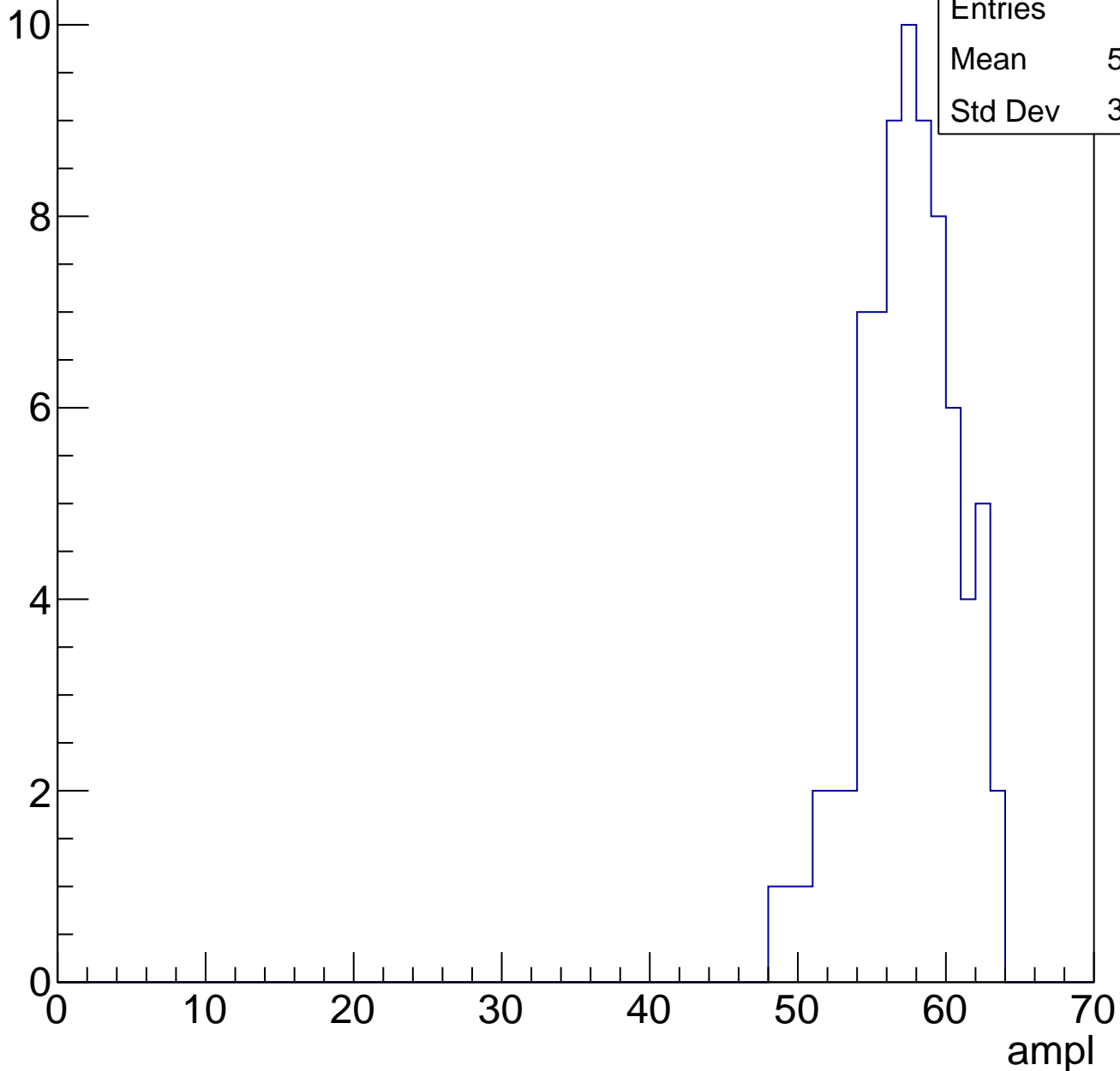


B0L001S, U25-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	56.97
Std Dev	3.252

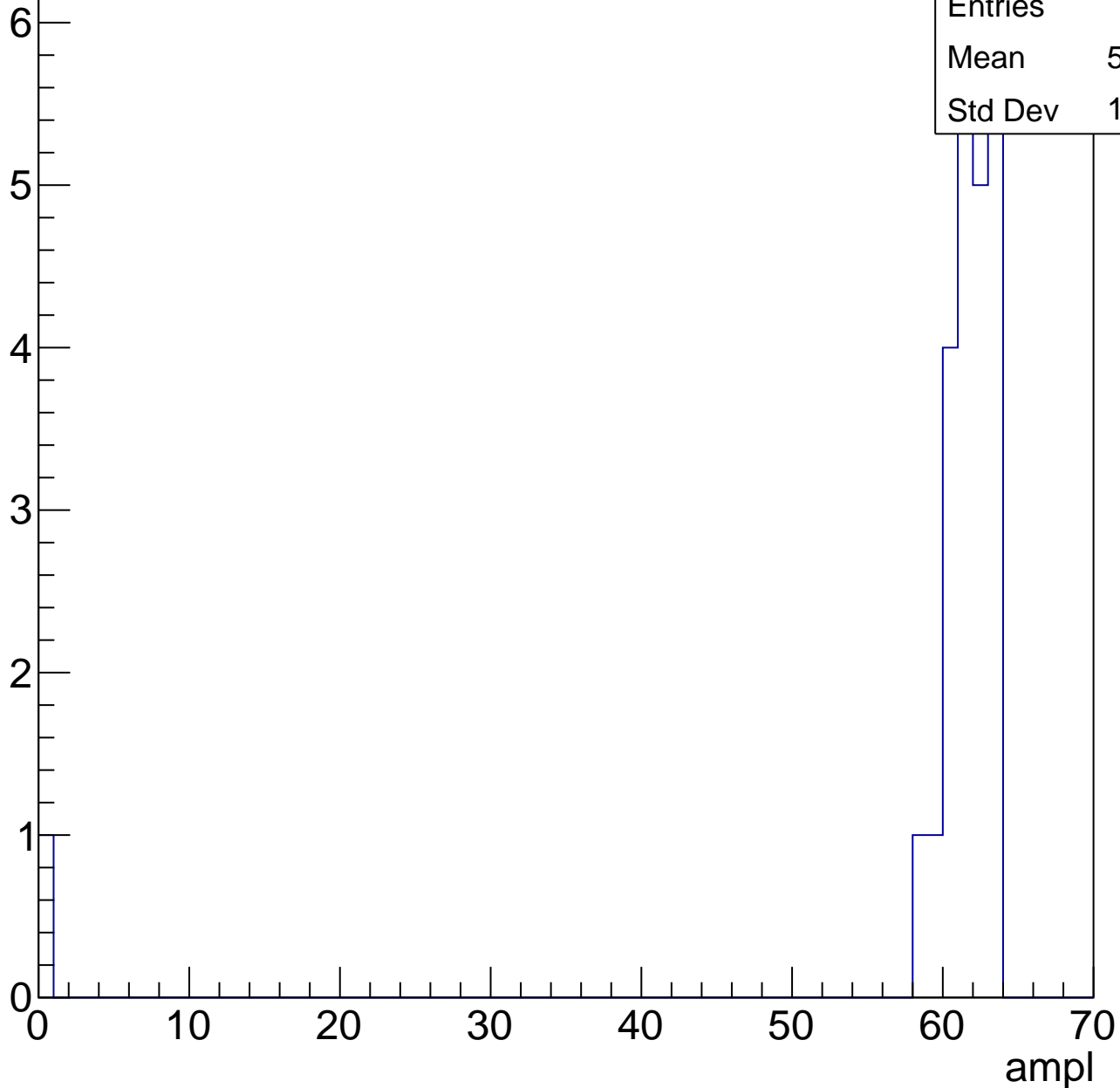
Entry



B0L001S, U25-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U25-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch16, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	31.75
Std Dev	3.014

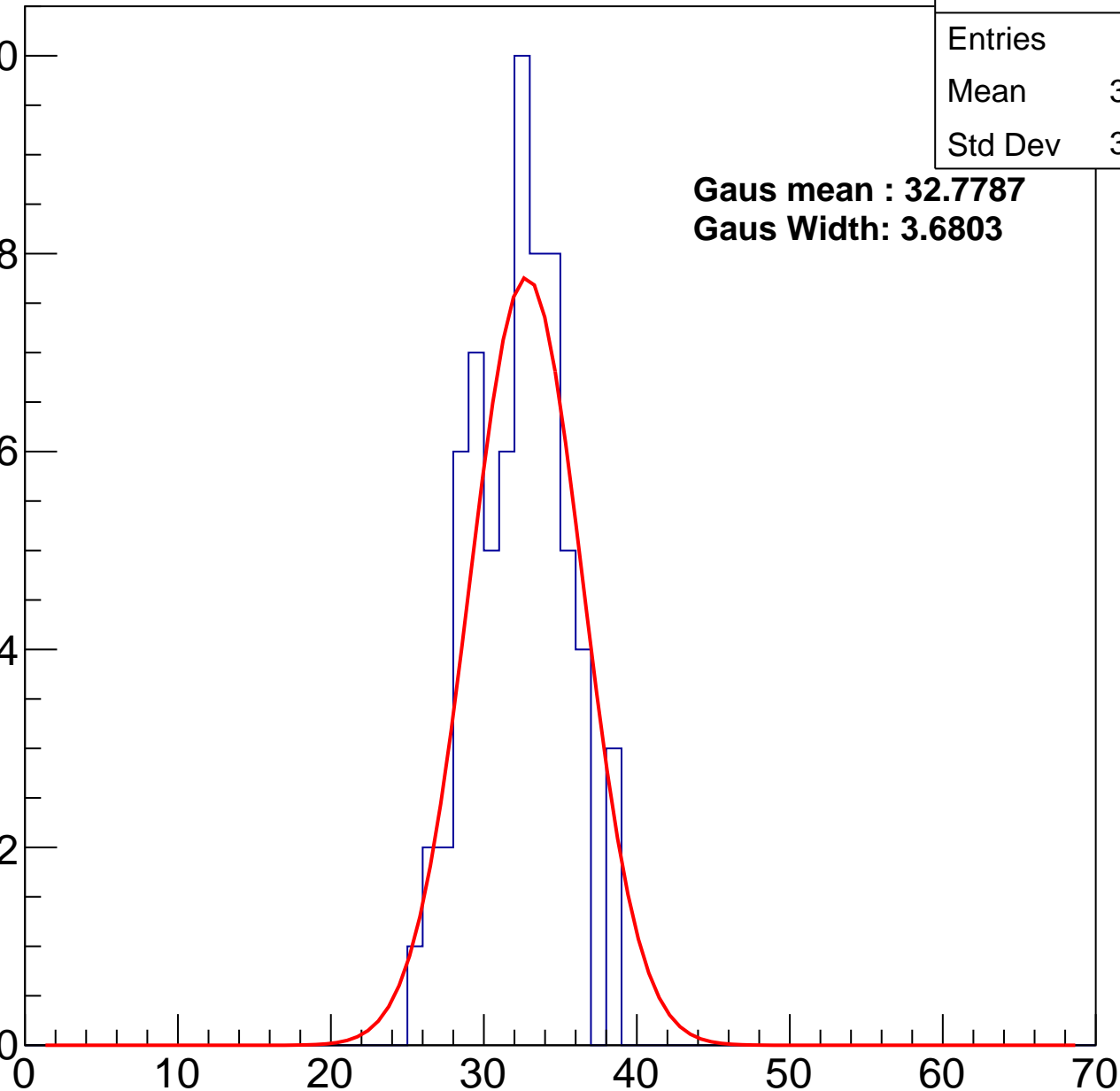
Gaus mean : 32.7787

Gaus Width: 3.6803

Entry

10
8
6
4
2
0

ampl



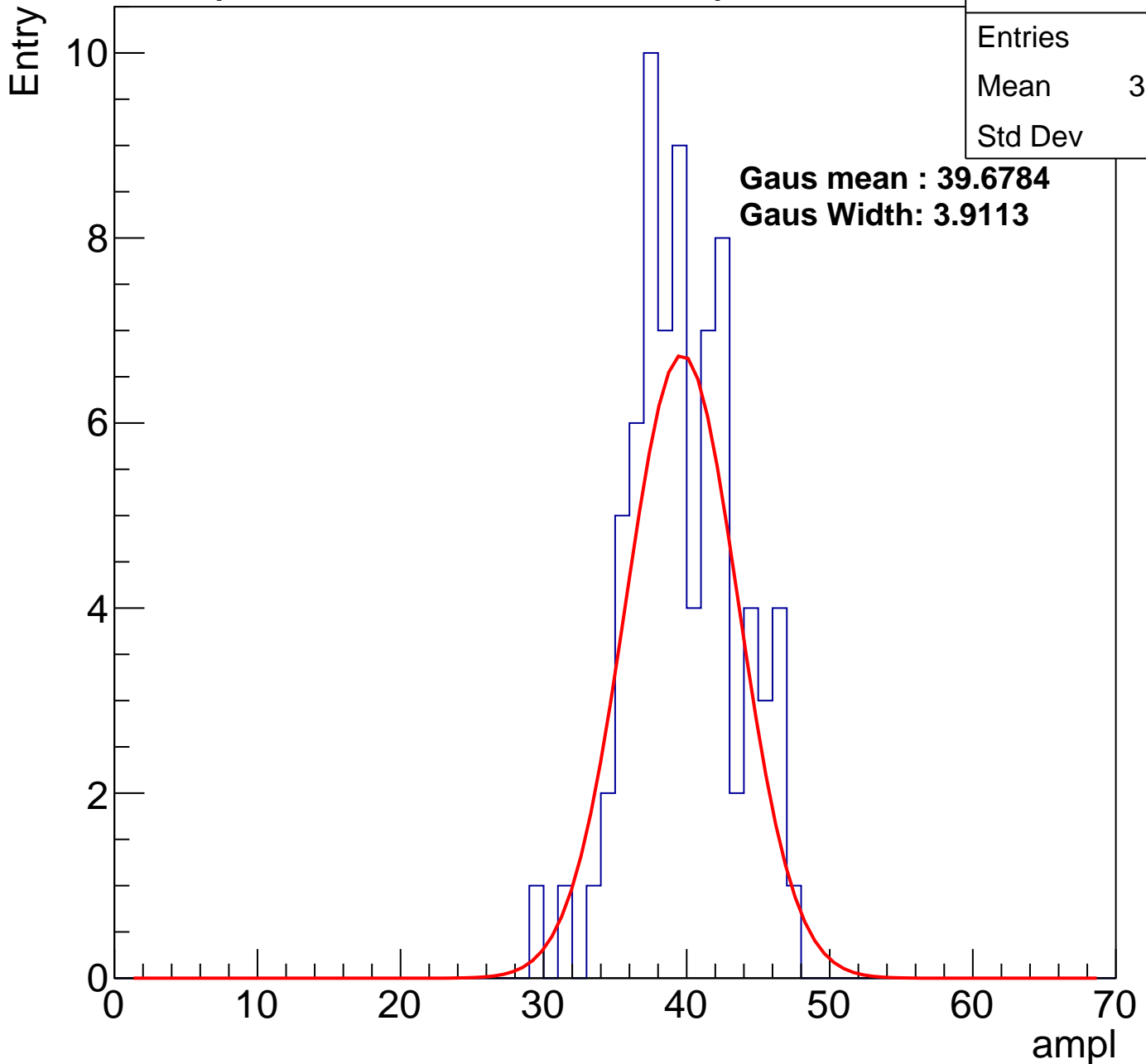
B0L001S, U25-ch16, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	39.33
Std Dev	3.7

Gaus mean : 39.6784

Gaus Width: 3.9113



B0L001S, U25-ch16, adc2

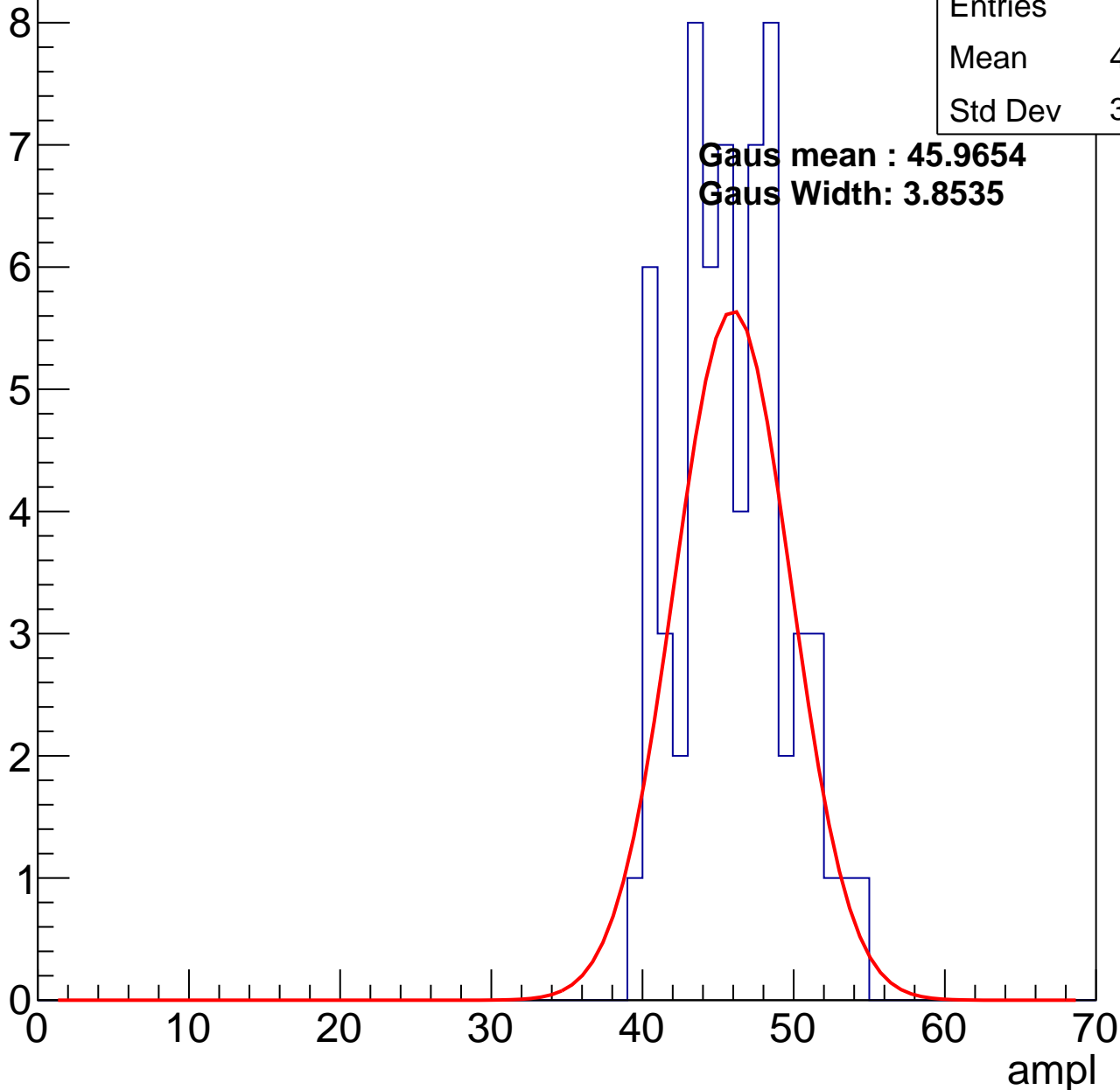
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	45.49
Std Dev	3.536

Gaus mean : 45.9654

Gaus Width: 3.8535

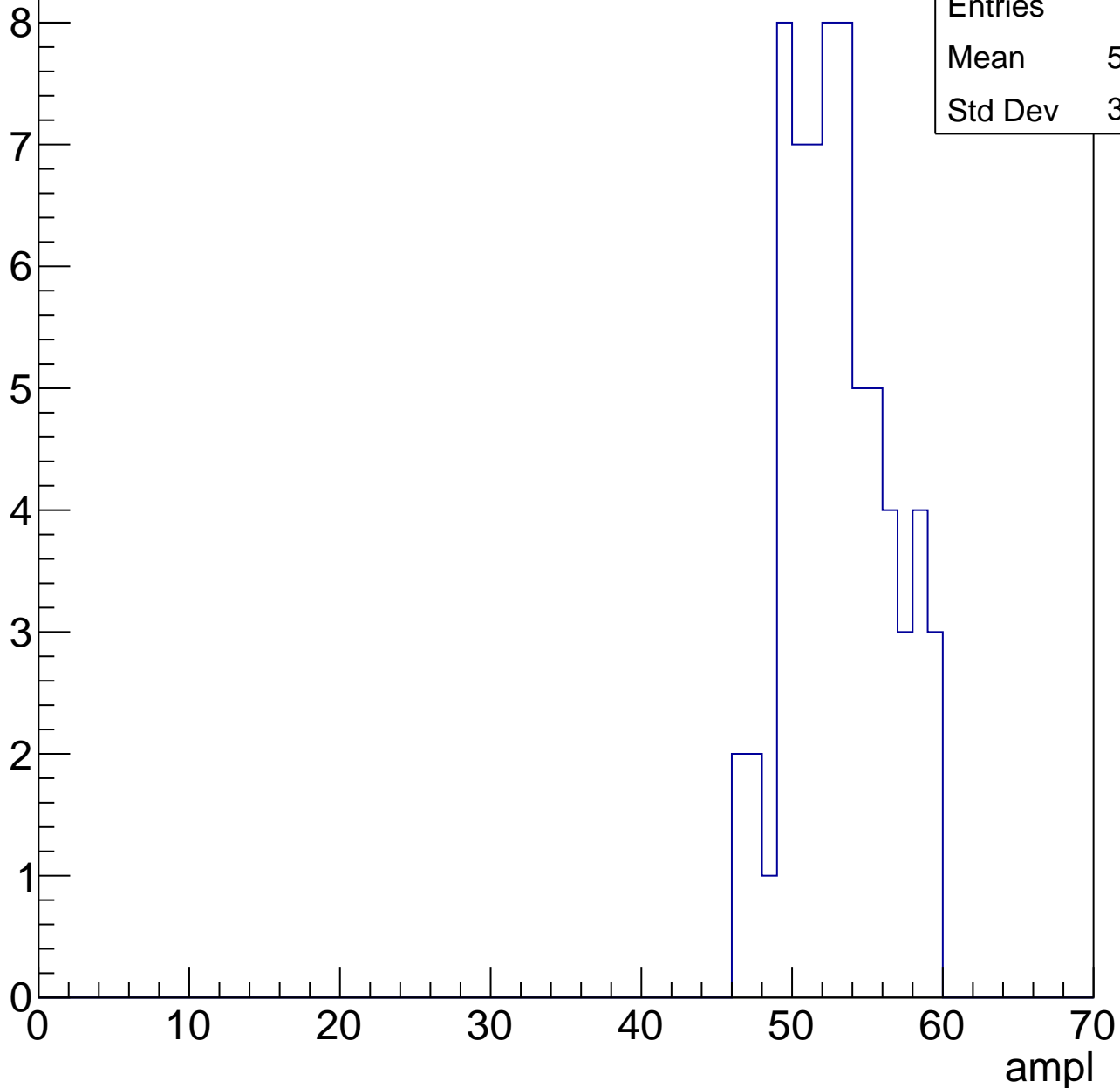


B0L001S, U25-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

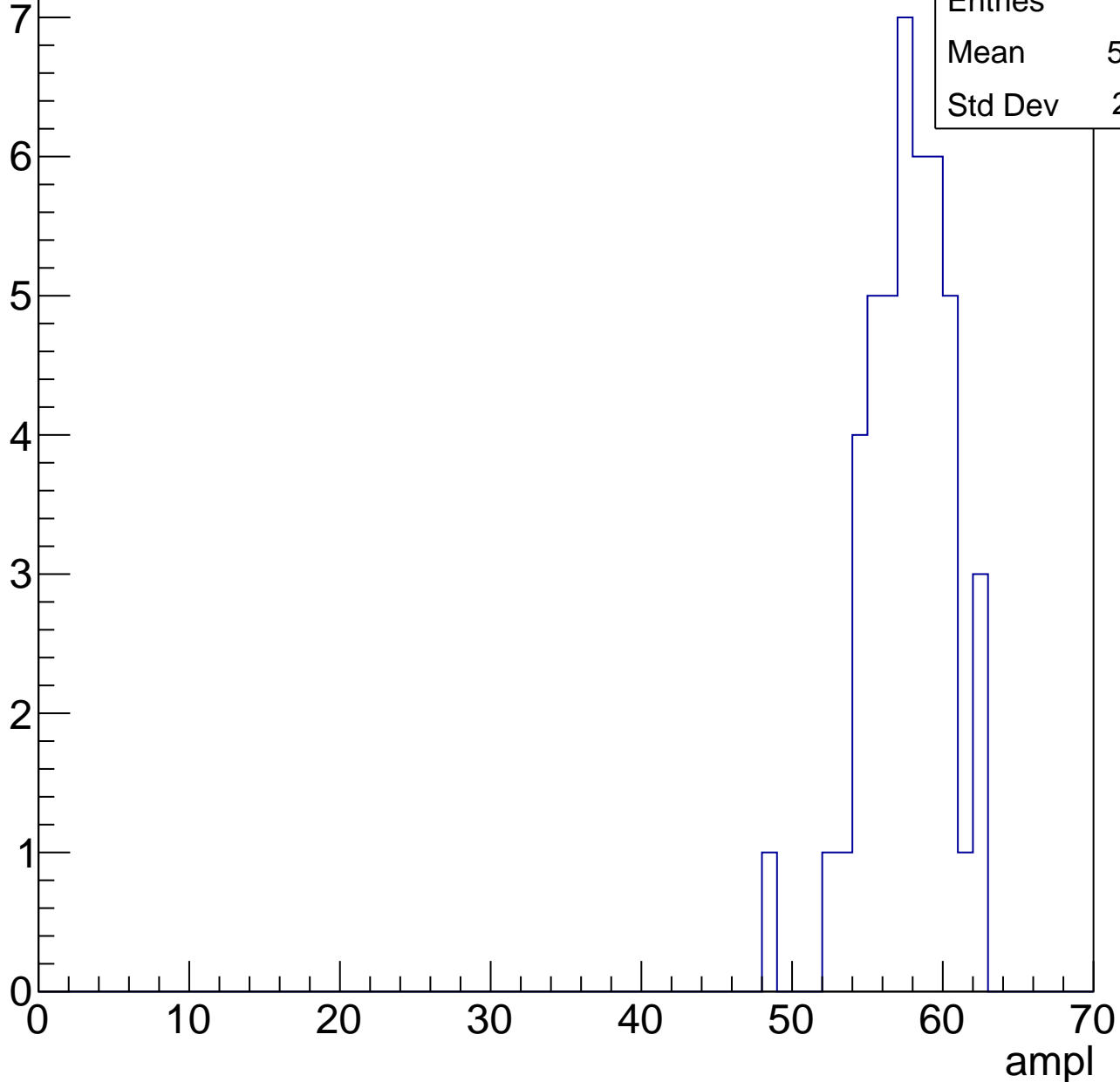
Entries	67
Mean	52.57
Std Dev	3.293



B0L001S, U25-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

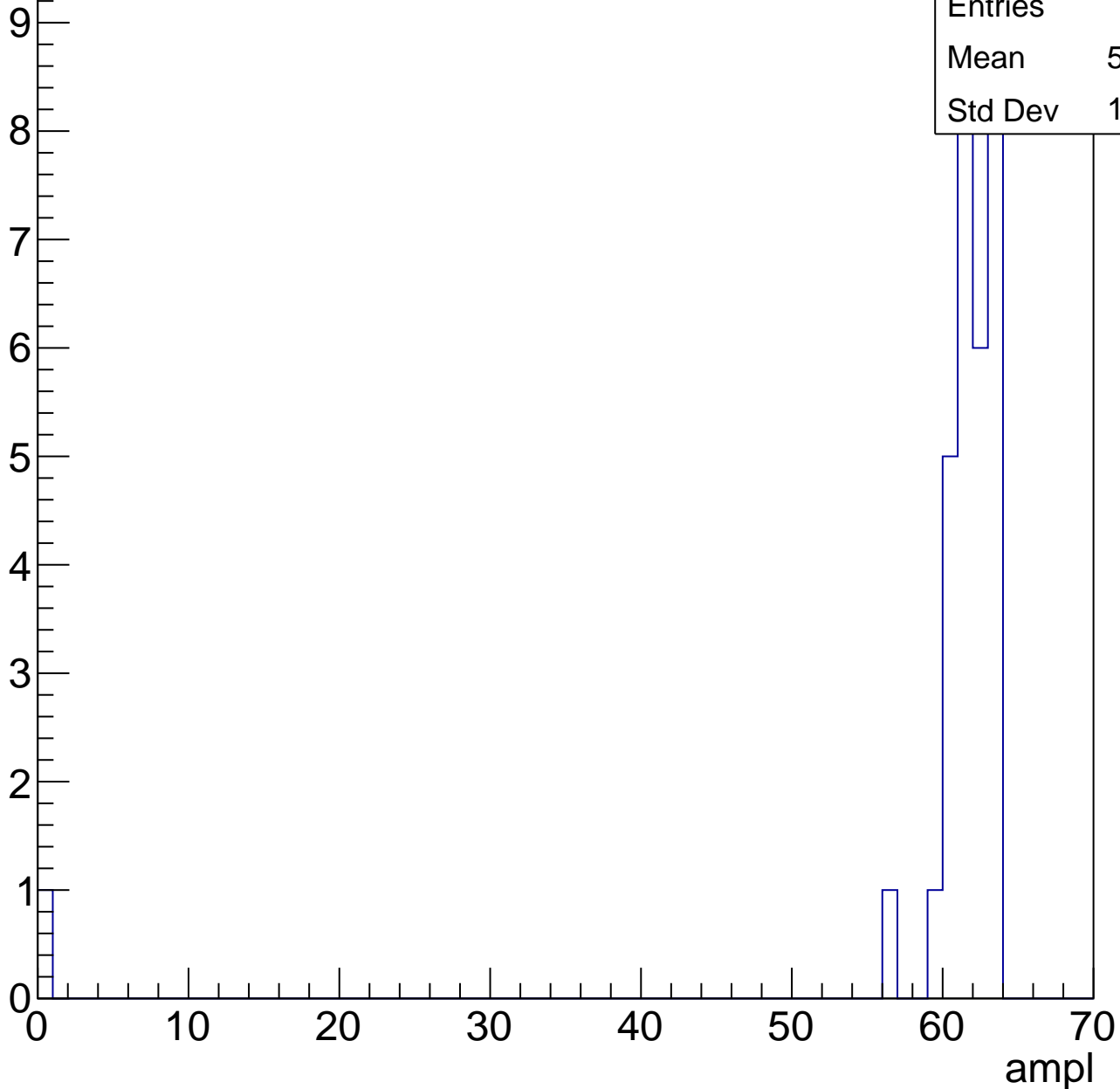


B0L001S, U25-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59.47
Std Dev	10.78



B0L001S, U25-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch17, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	33.3
Std Dev	3.279

Gaus mean : 33.8568

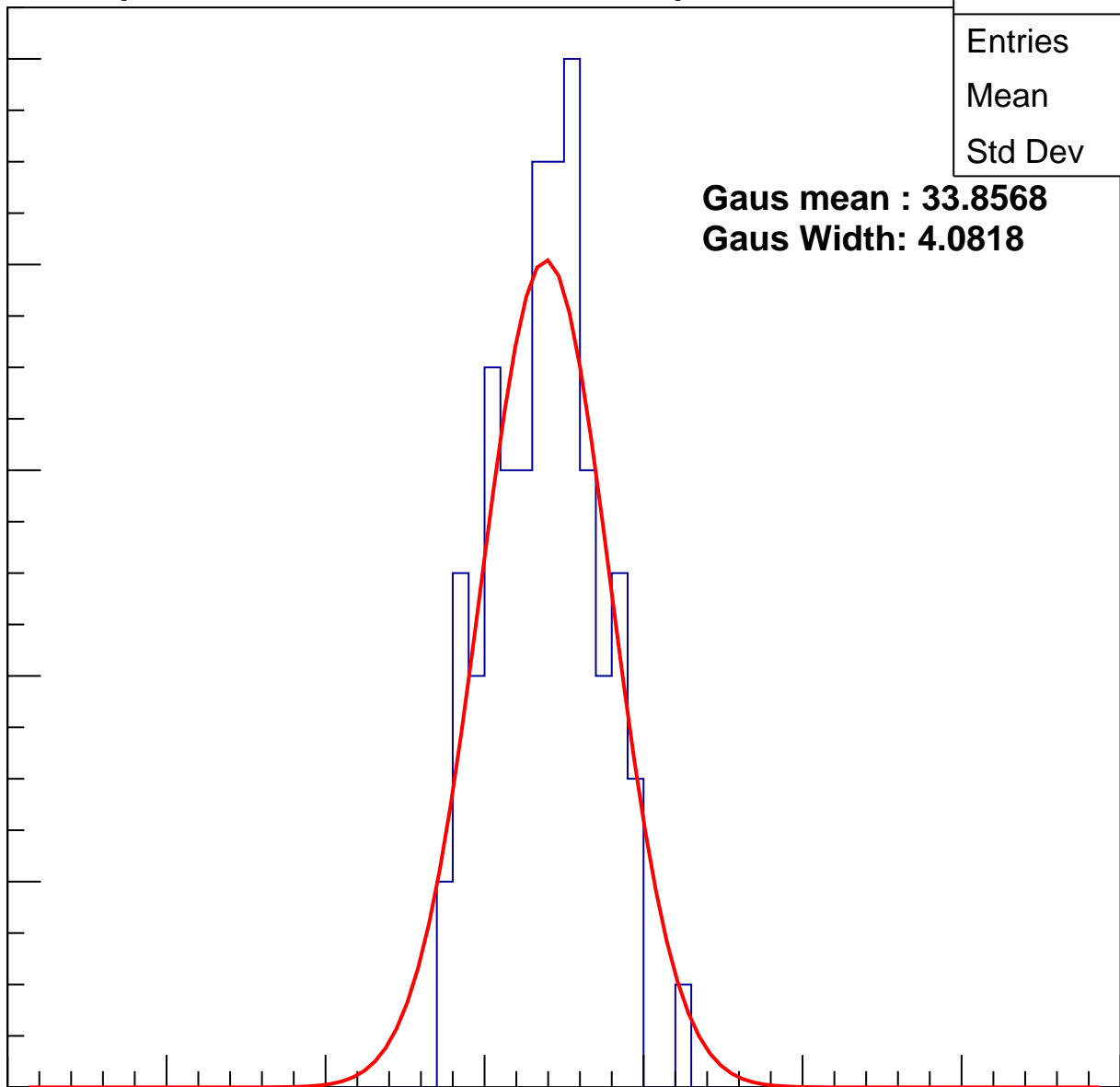
Gaus Width: 4.0818

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



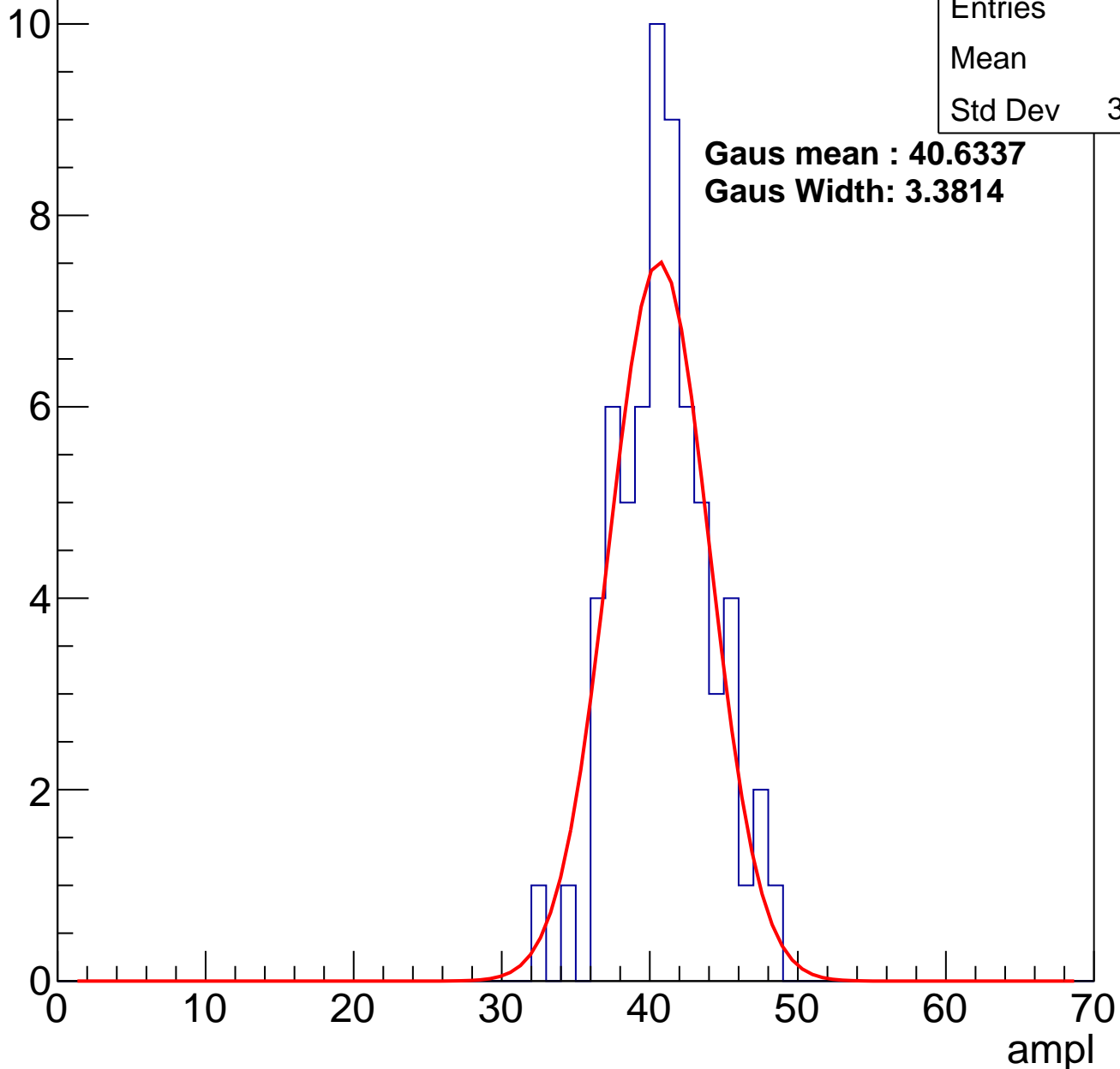
B0L001S, U25-ch17, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	40.5
Std Dev	3.192

Gaus mean : 40.6337
Gaus Width: 3.3814

Entry



B0L001S, U25-ch17, adc2

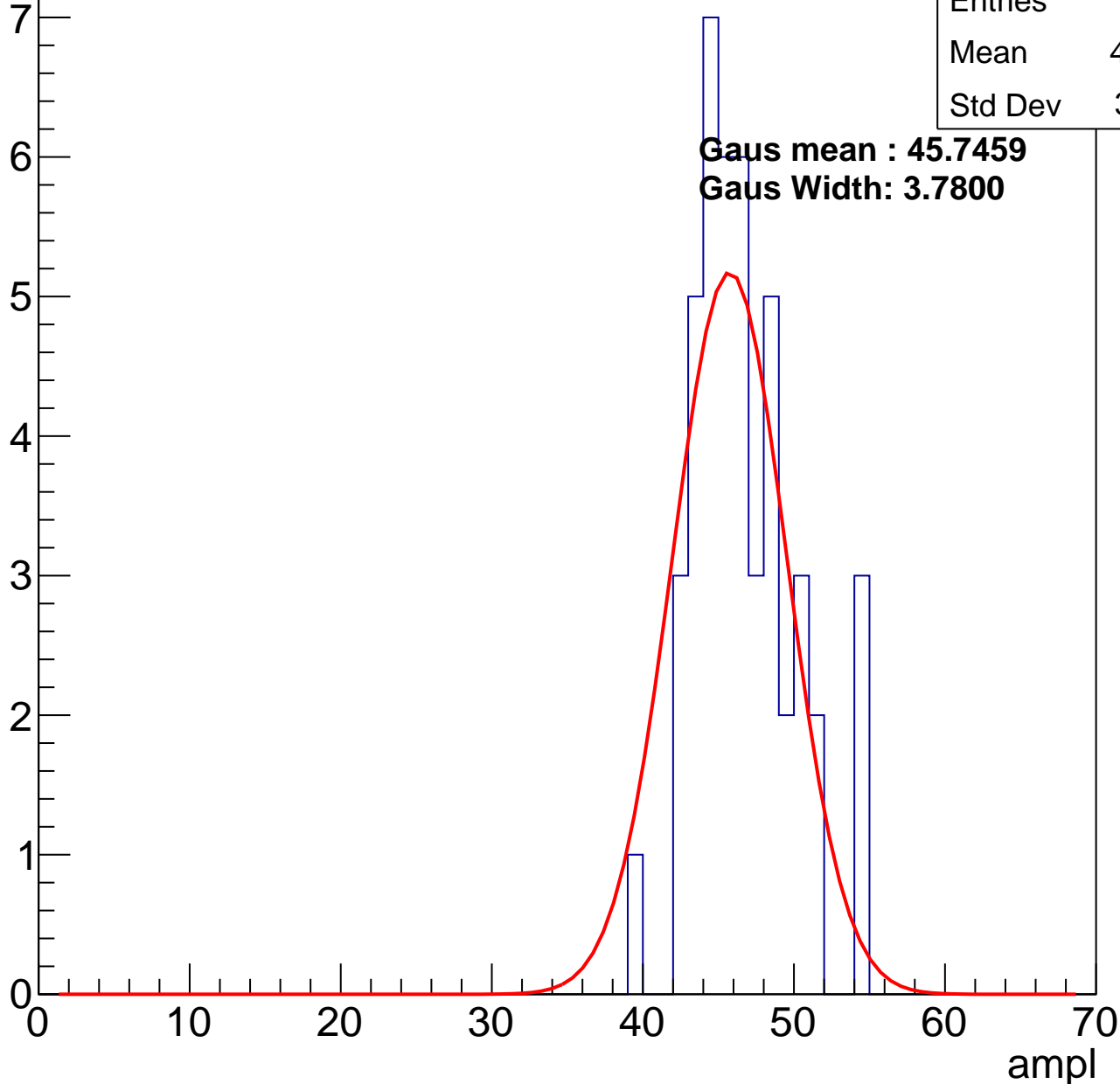
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	46.24
Std Dev	3.311

Gaus mean : 45.7459

Gaus Width: 3.7800

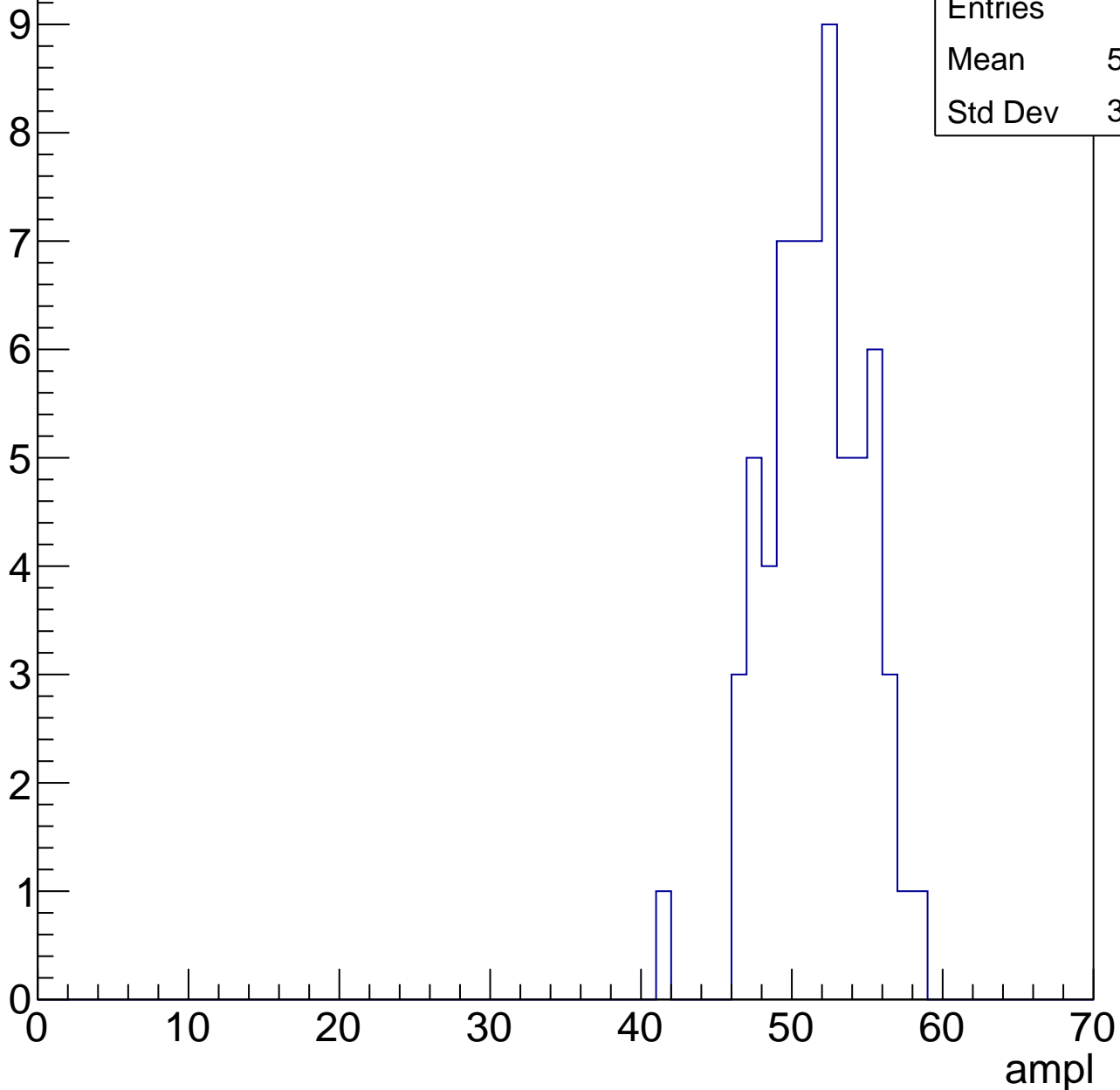


B0L001S, U25-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

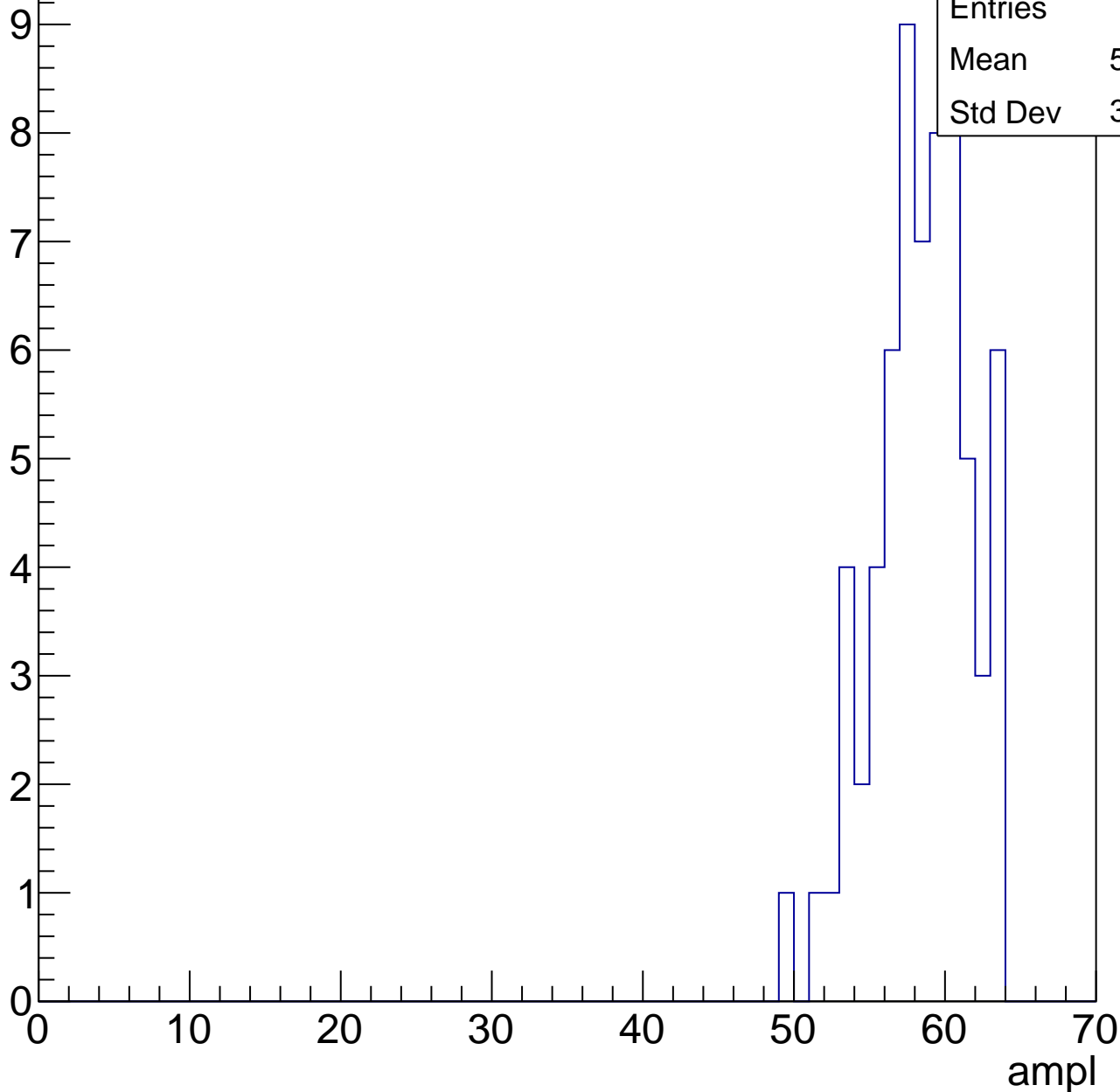
Entries	64
Mean	51.12
Std Dev	3.199



B0L001S, U25-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

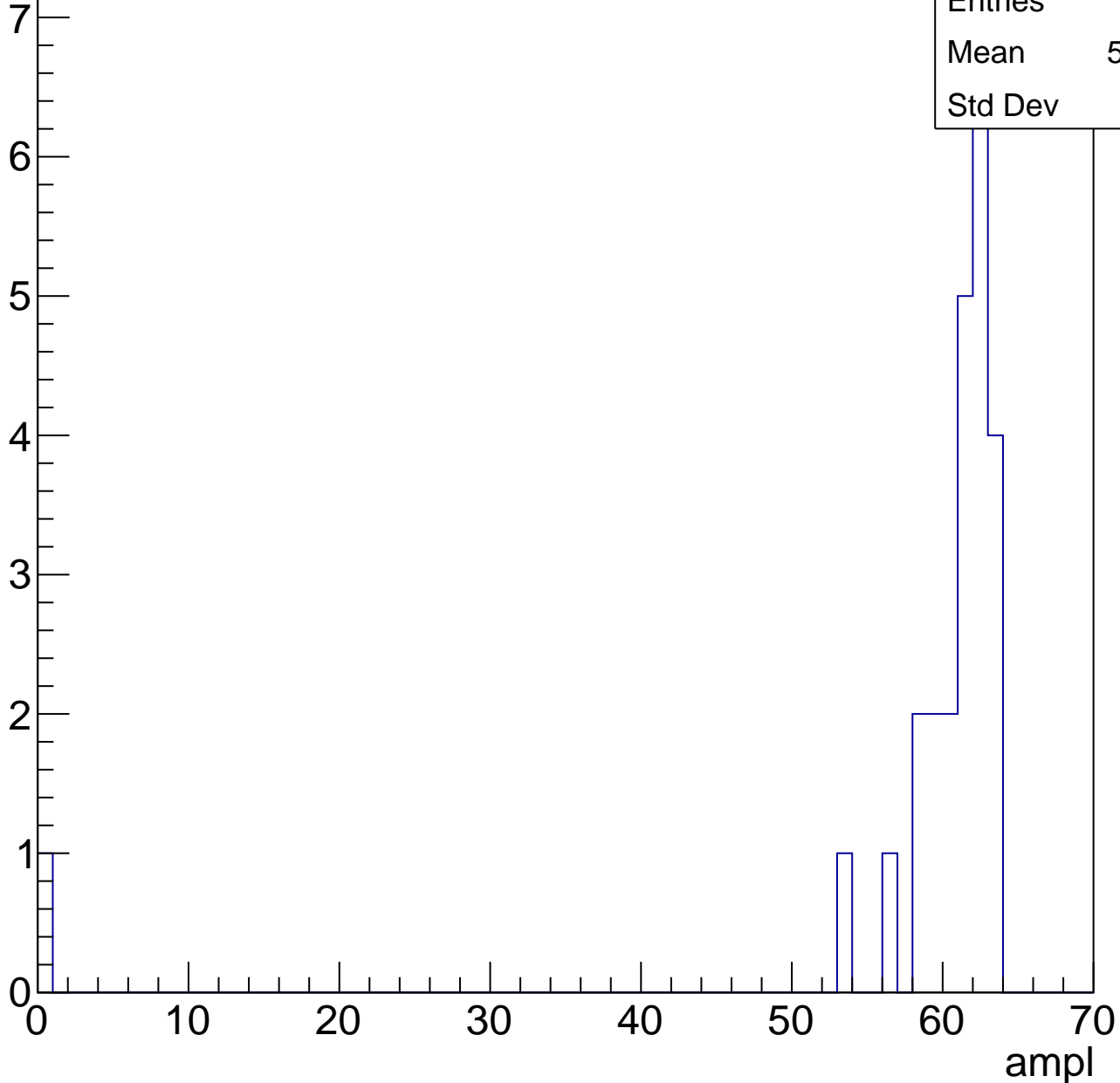


B0L001S, U25-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.16
Std Dev	12.1



B0L001S, U25-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

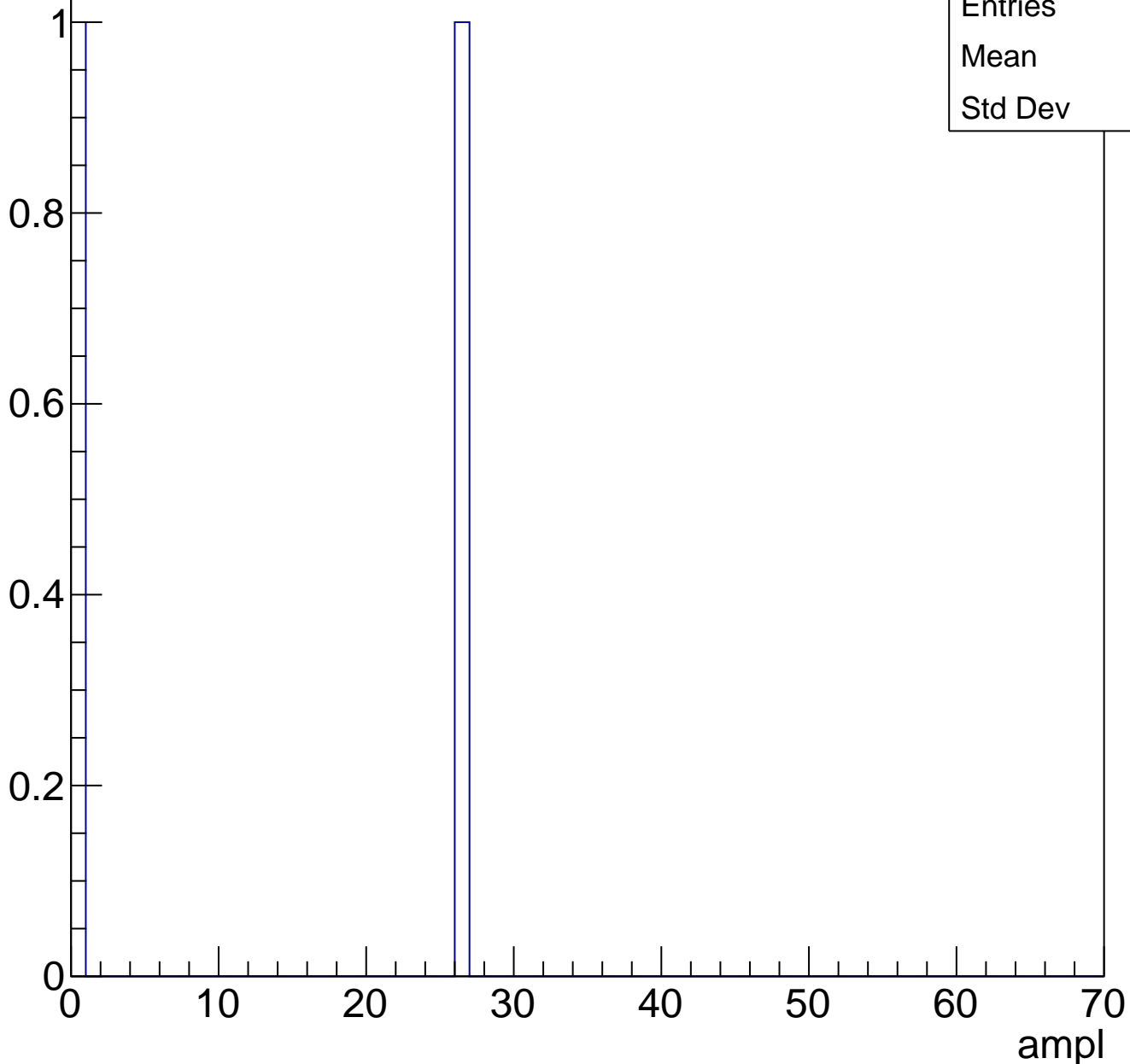
Std Dev

0

B0L001S, U25-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch18, adc0

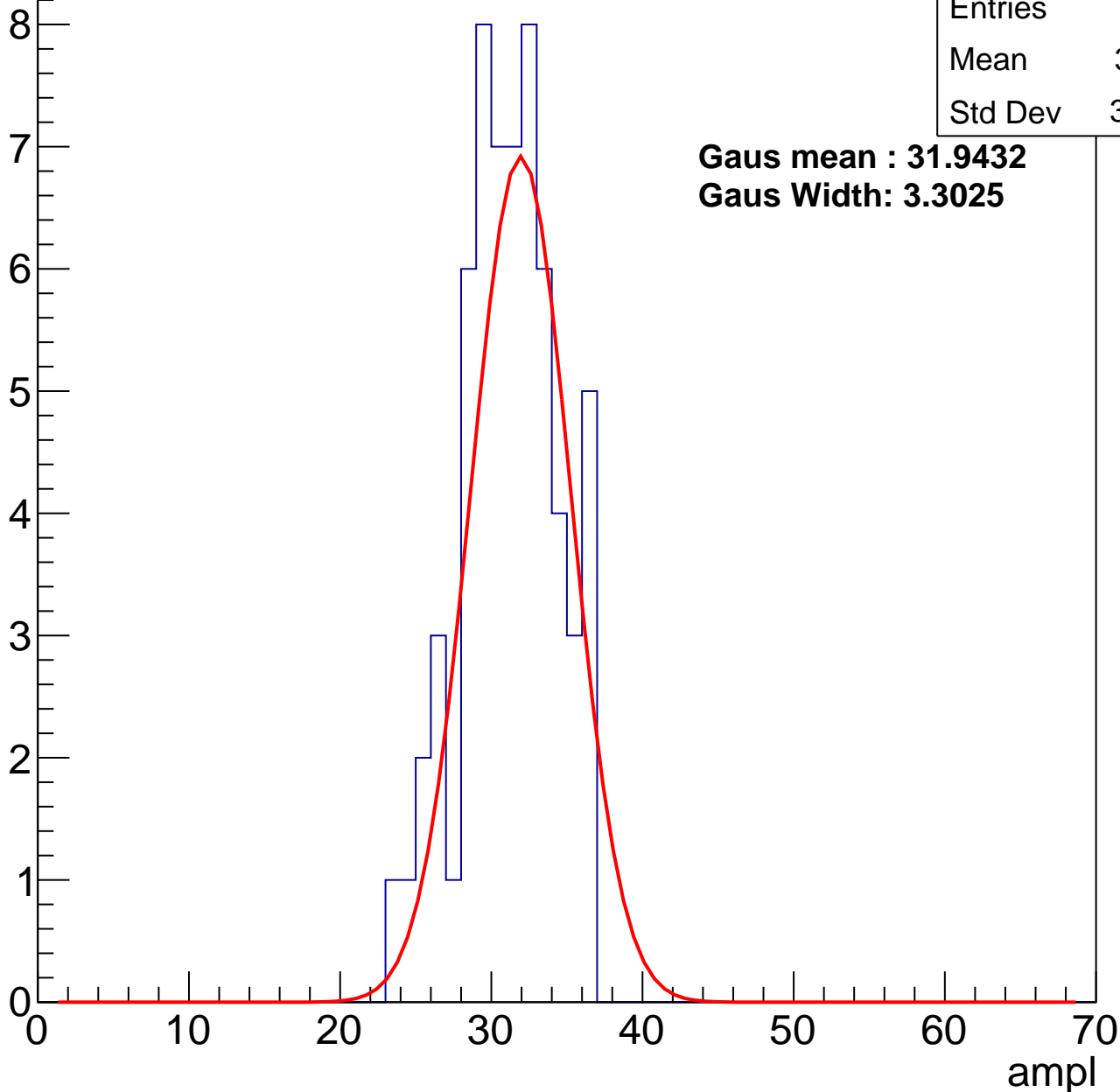
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.71
Std Dev	3.123

Gaus mean : 31.9432

Gaus Width: 3.3025



B0L001S, U25-ch18, adc1

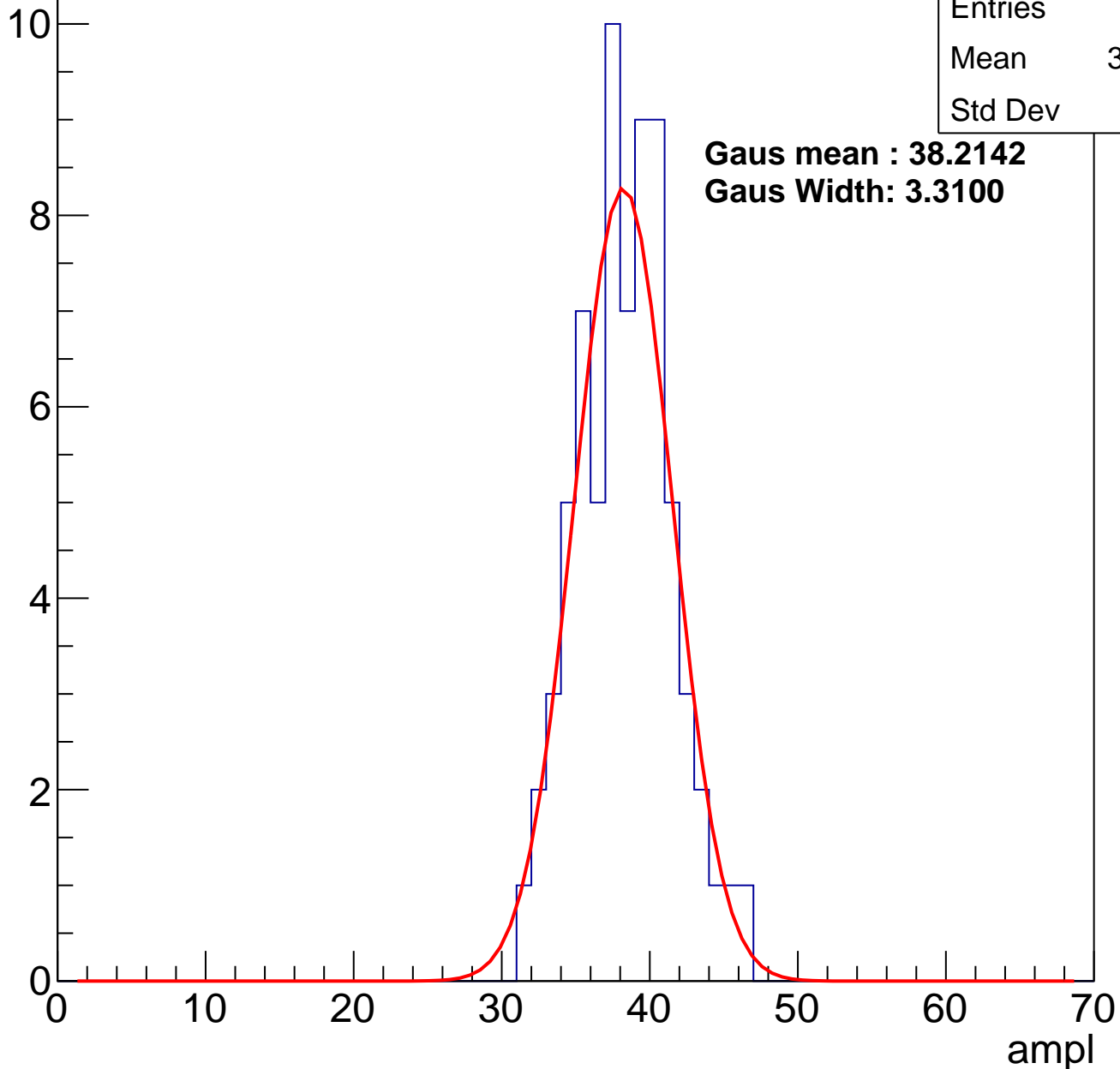
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.86
Std Dev	3.15

Gaus mean : 38.2142

Gaus Width: 3.3100

Entry



B0L001S, U25-ch18, adc2

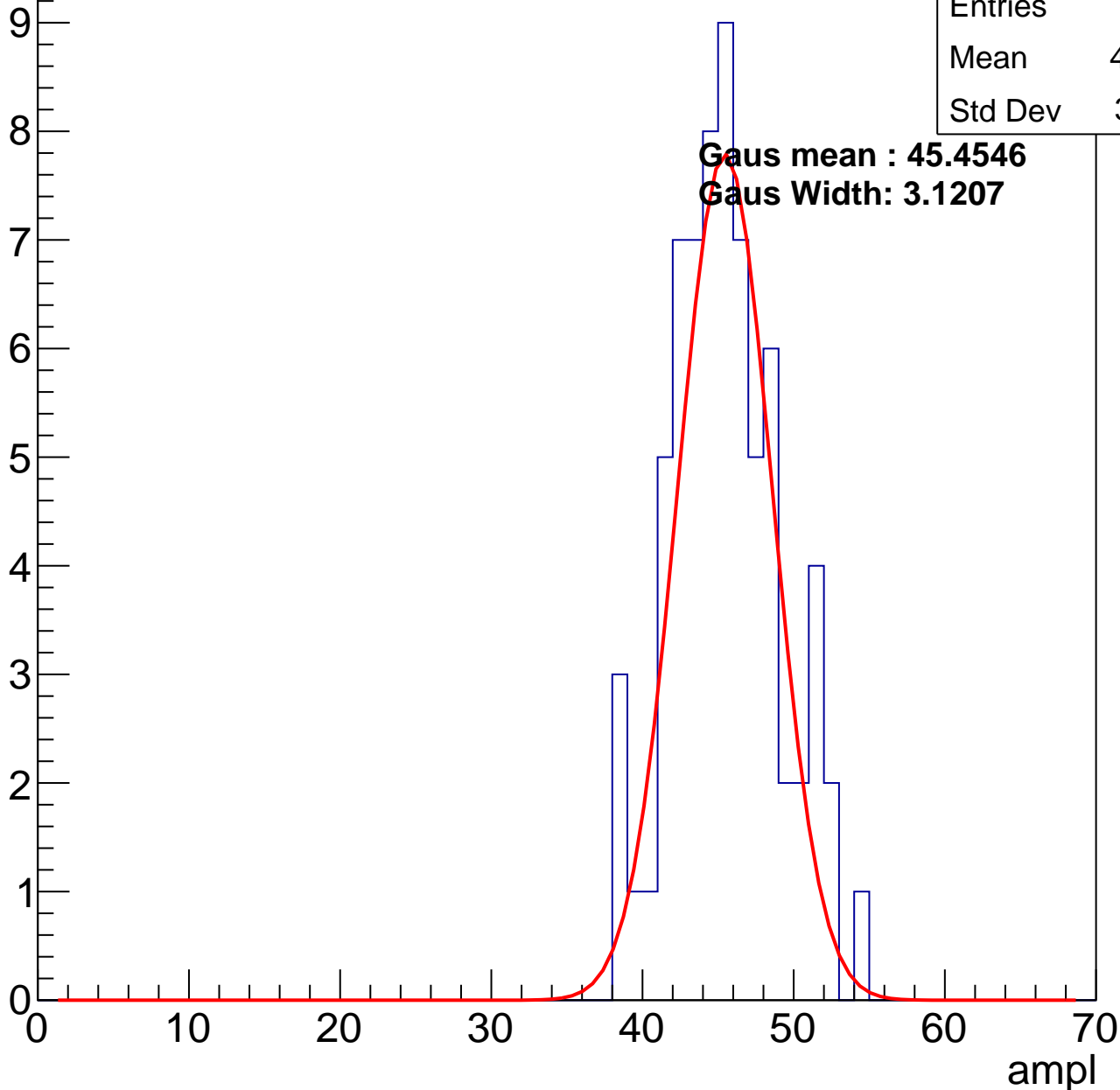
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	45.07
Std Dev	3.531

Gaus mean : 45.4546

Gaus Width: 3.1207

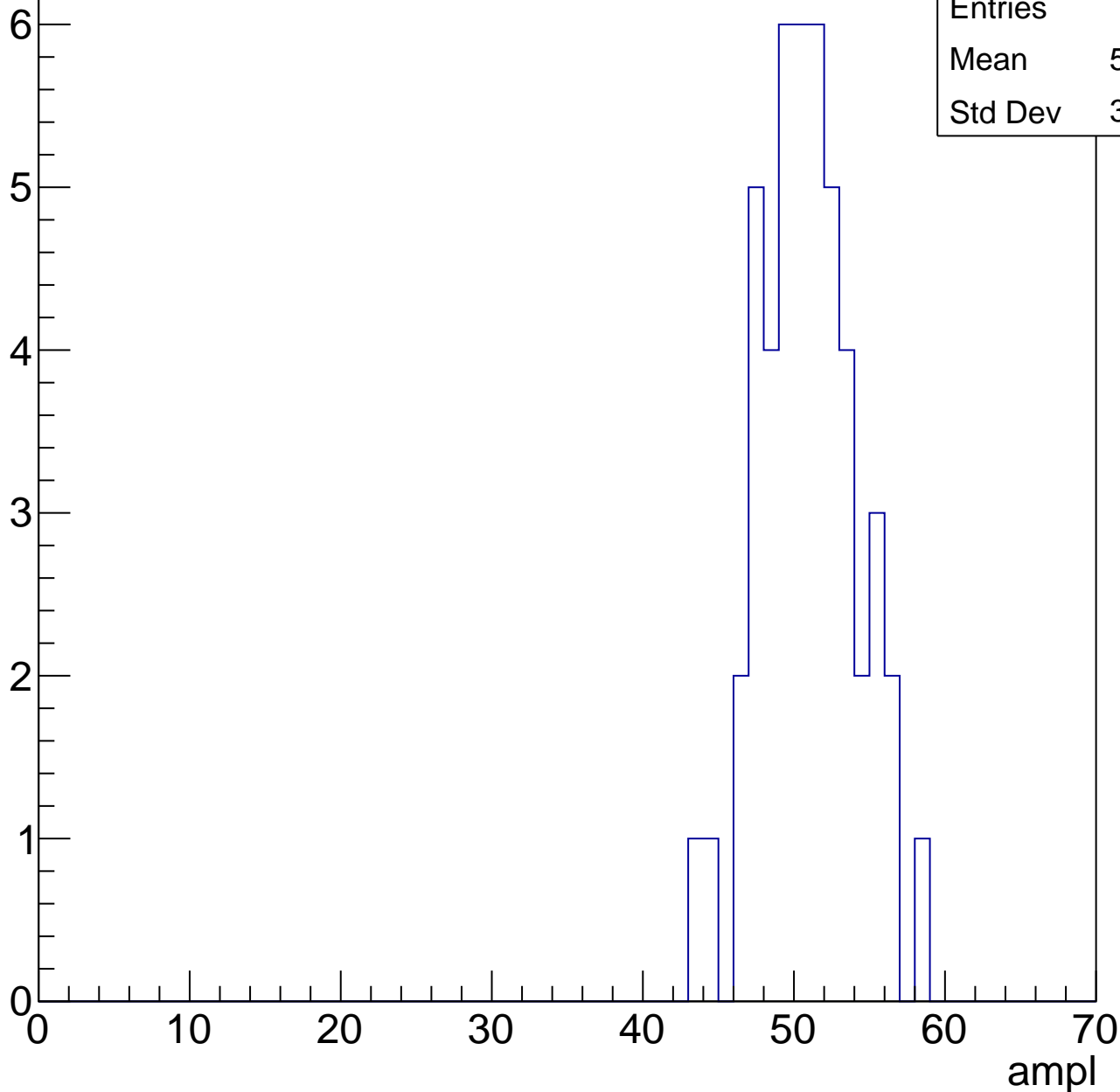


B0L001S, U25-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	50.44
Std Dev	3.162

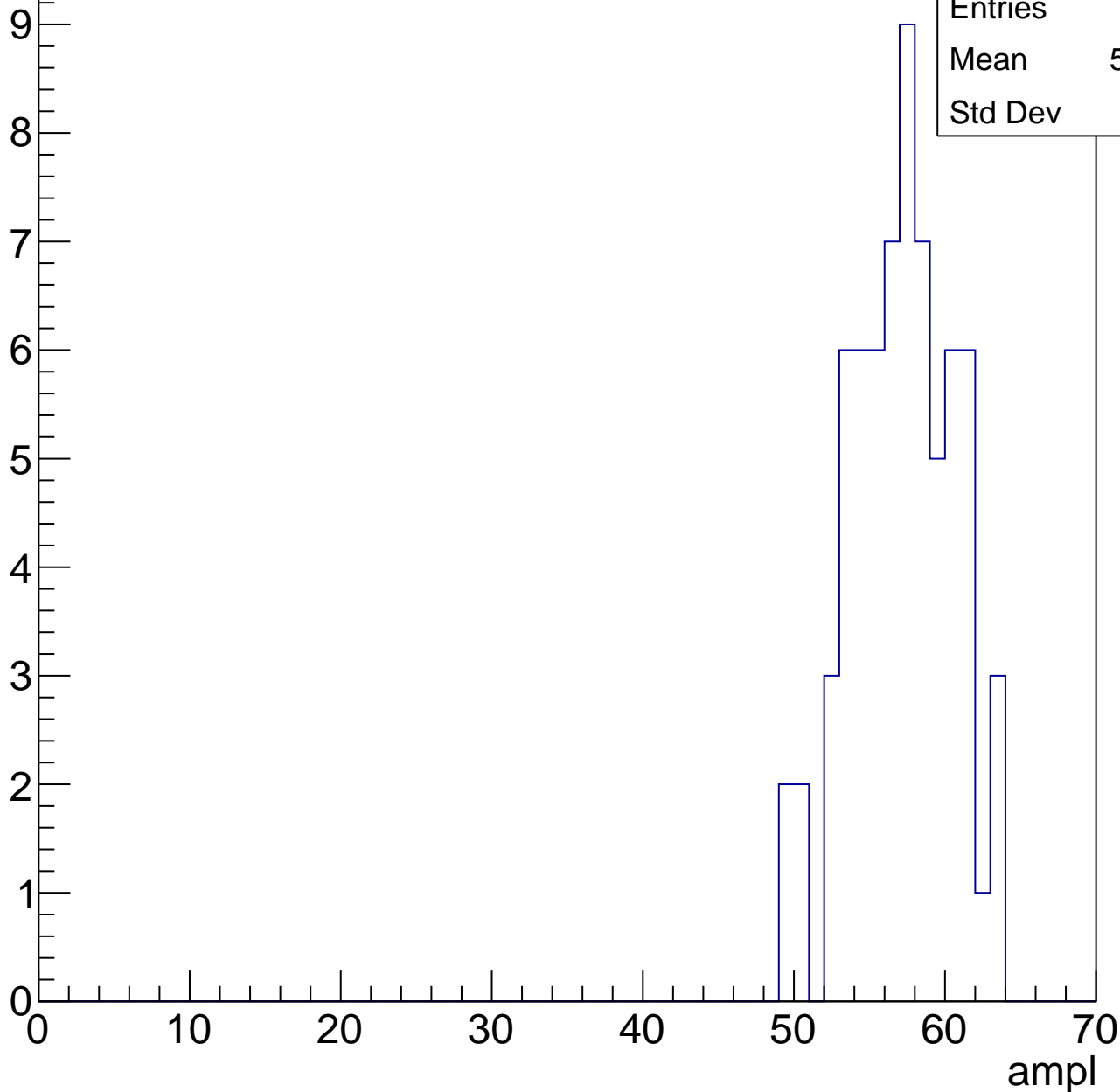


B0L001S, U25-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

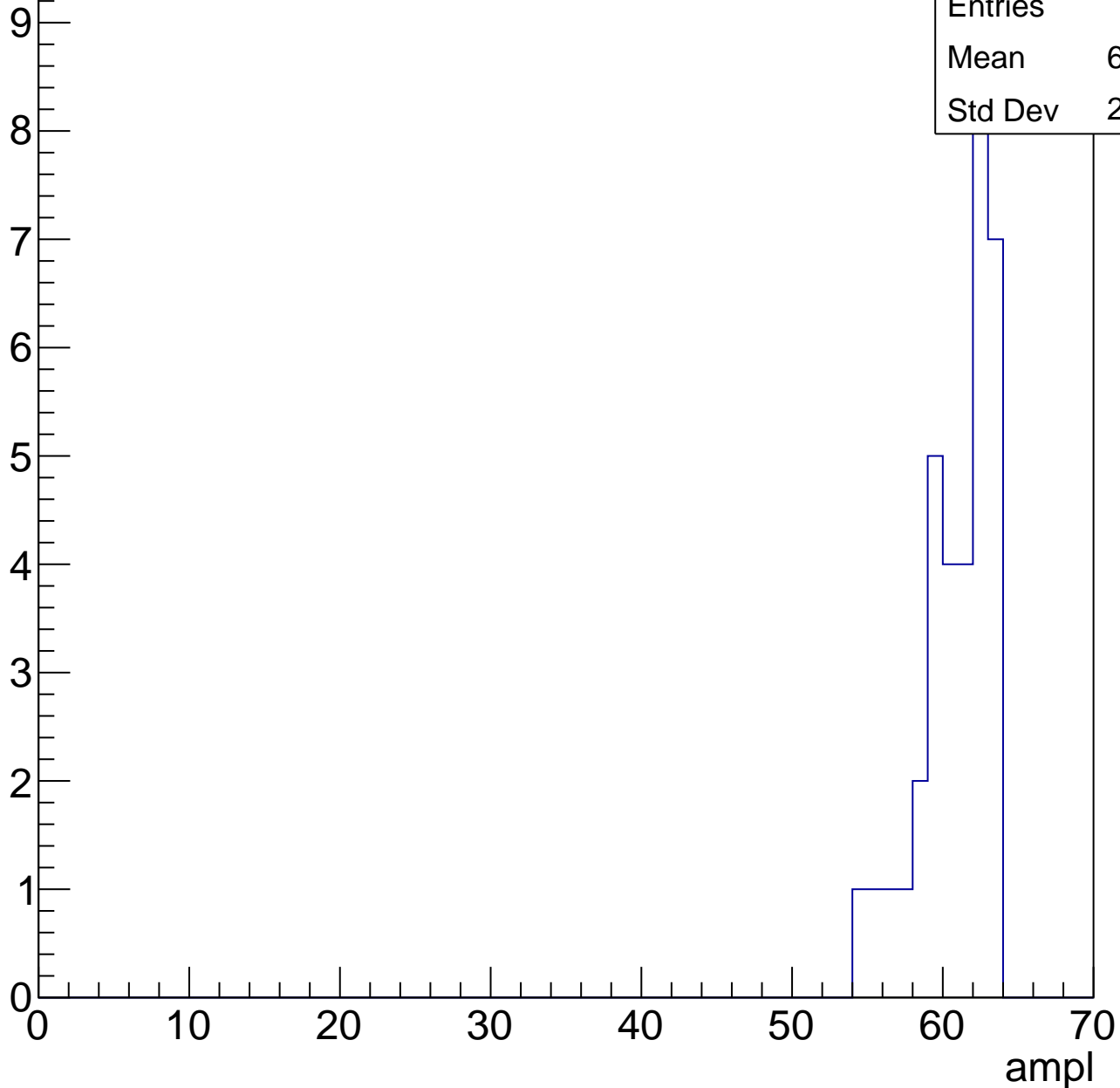
Entries	69
Mean	56.65
Std Dev	3.37



B0L001S, U25-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

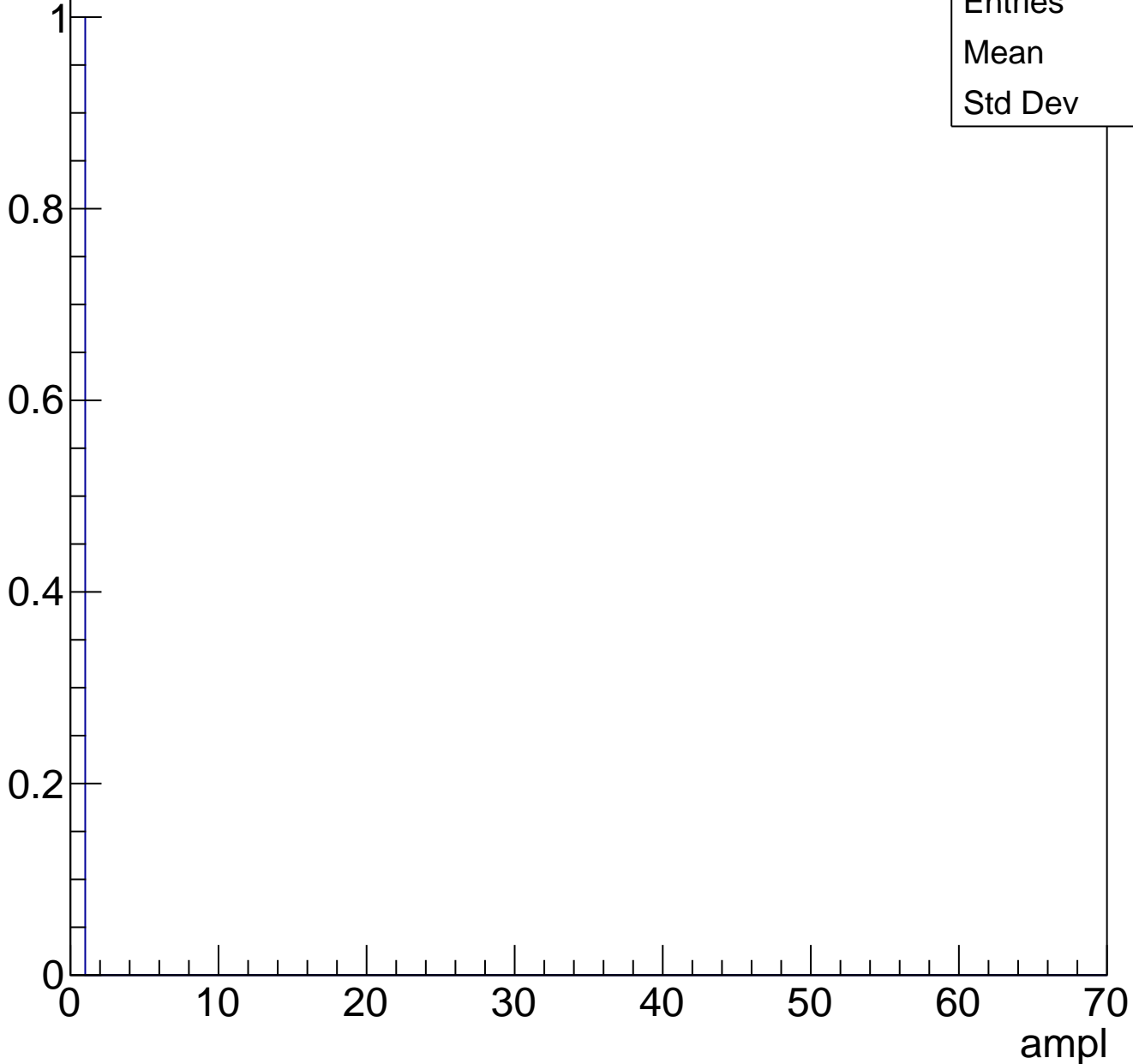
Entry



B0L001S, U25-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch19, adc0

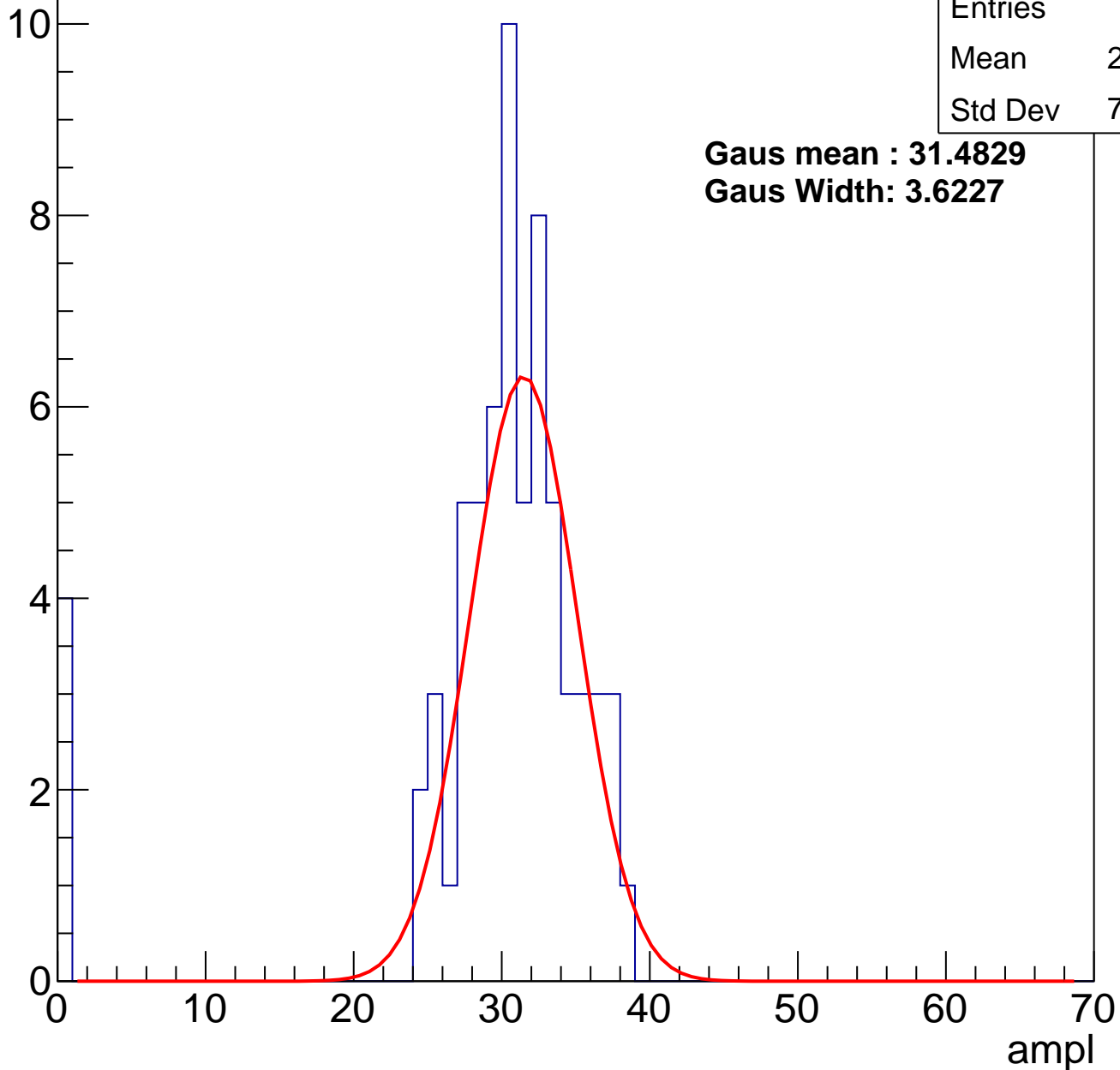
calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	28.93
Std Dev	7.993

Gaus mean : 31.4829

Gaus Width: 3.6227

Entry



B0L001S, U25-ch19, adc1

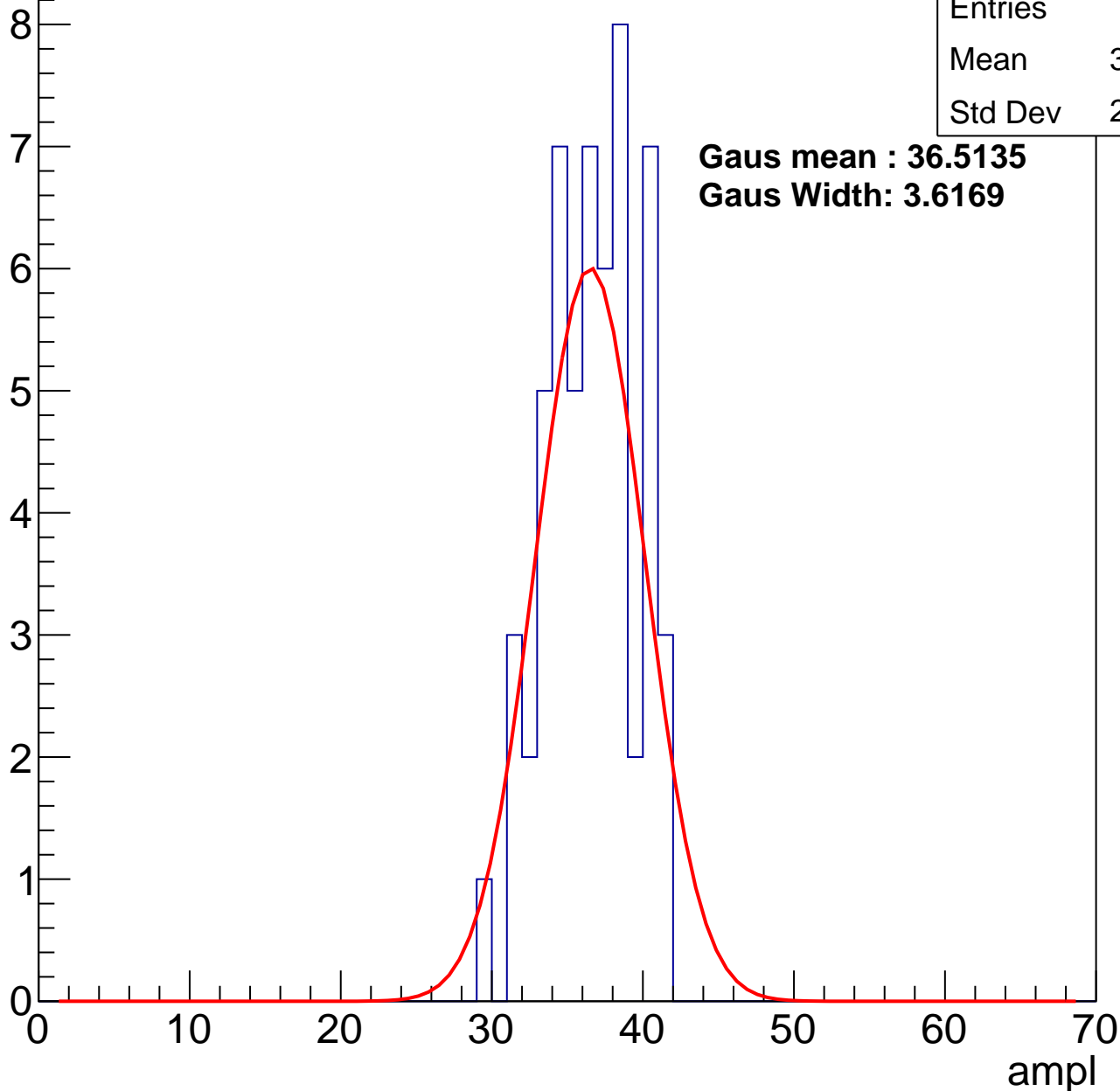
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	36.12
Std Dev	2.916

Gaus mean : 36.5135

Gaus Width: 3.6169



B0L001S, U25-ch19, adc2

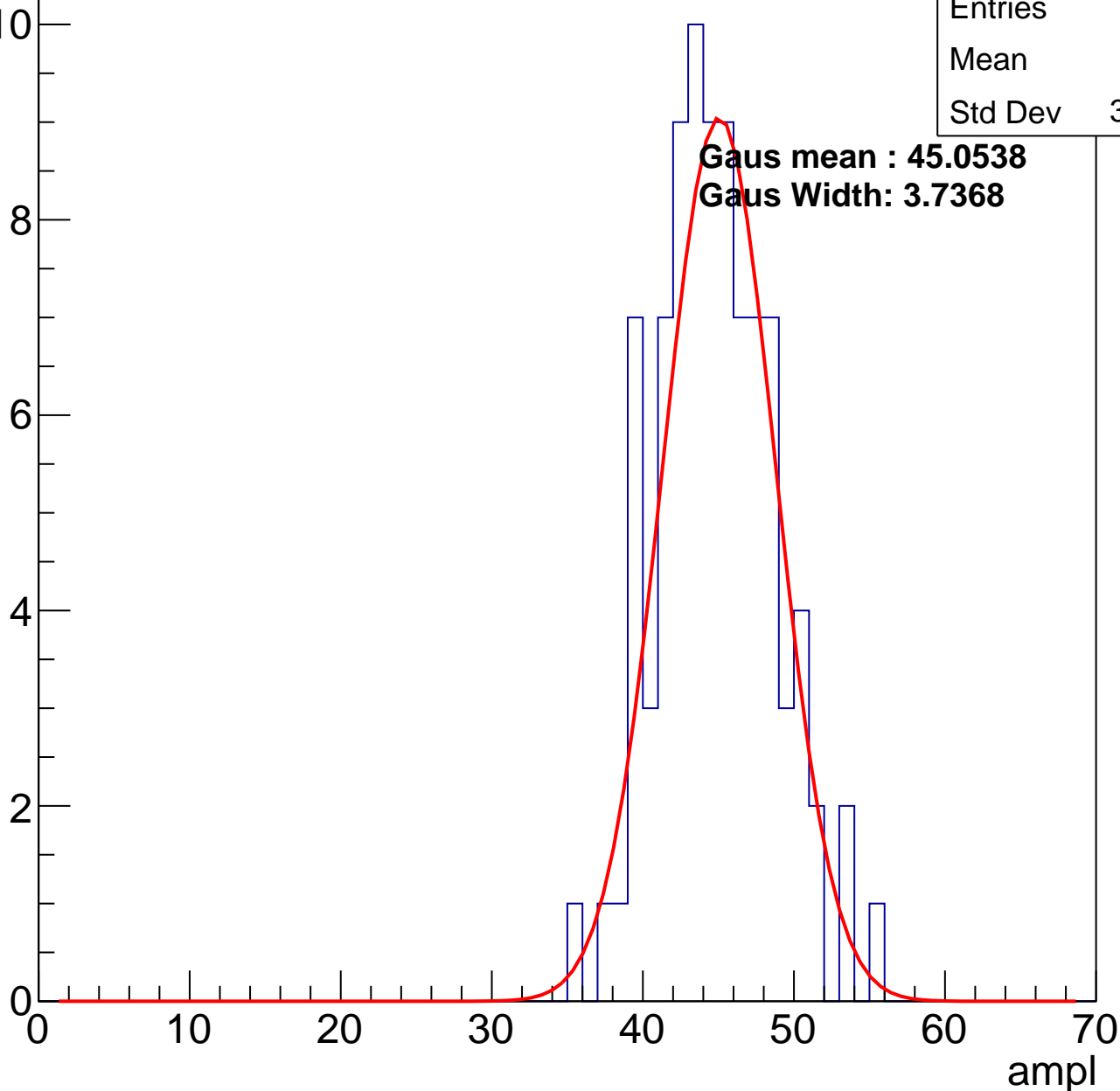
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	90
Mean	44.4
Std Dev	3.794

Gaus mean : 45.0538

Gaus Width: 3.7368

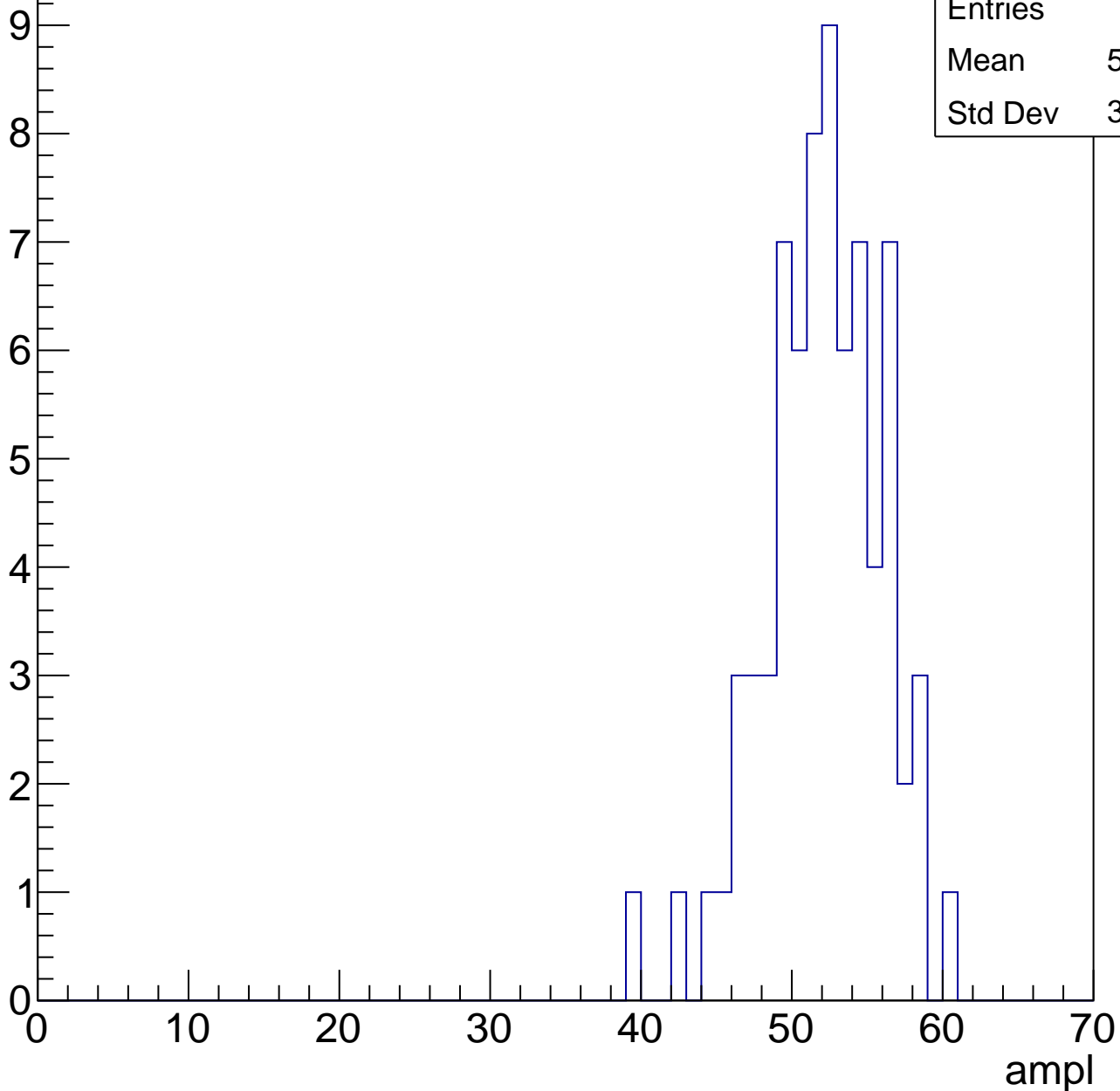


B0L001S, U25-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

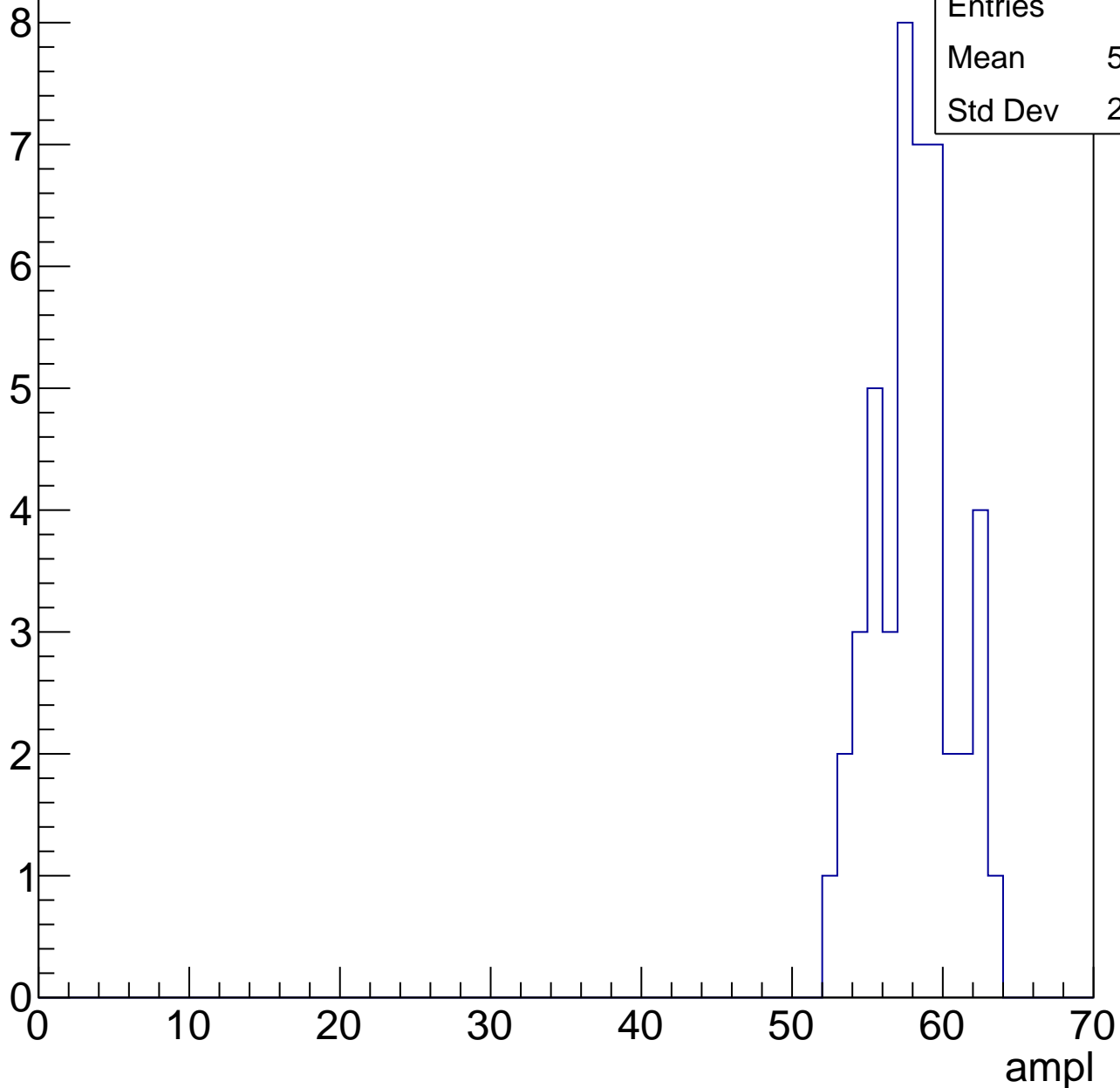
Entries	73
Mean	51.62
Std Dev	3.884



B0L001S, U25-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	45
Mean	57.58
Std Dev	2.637

B0L001S, U25-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

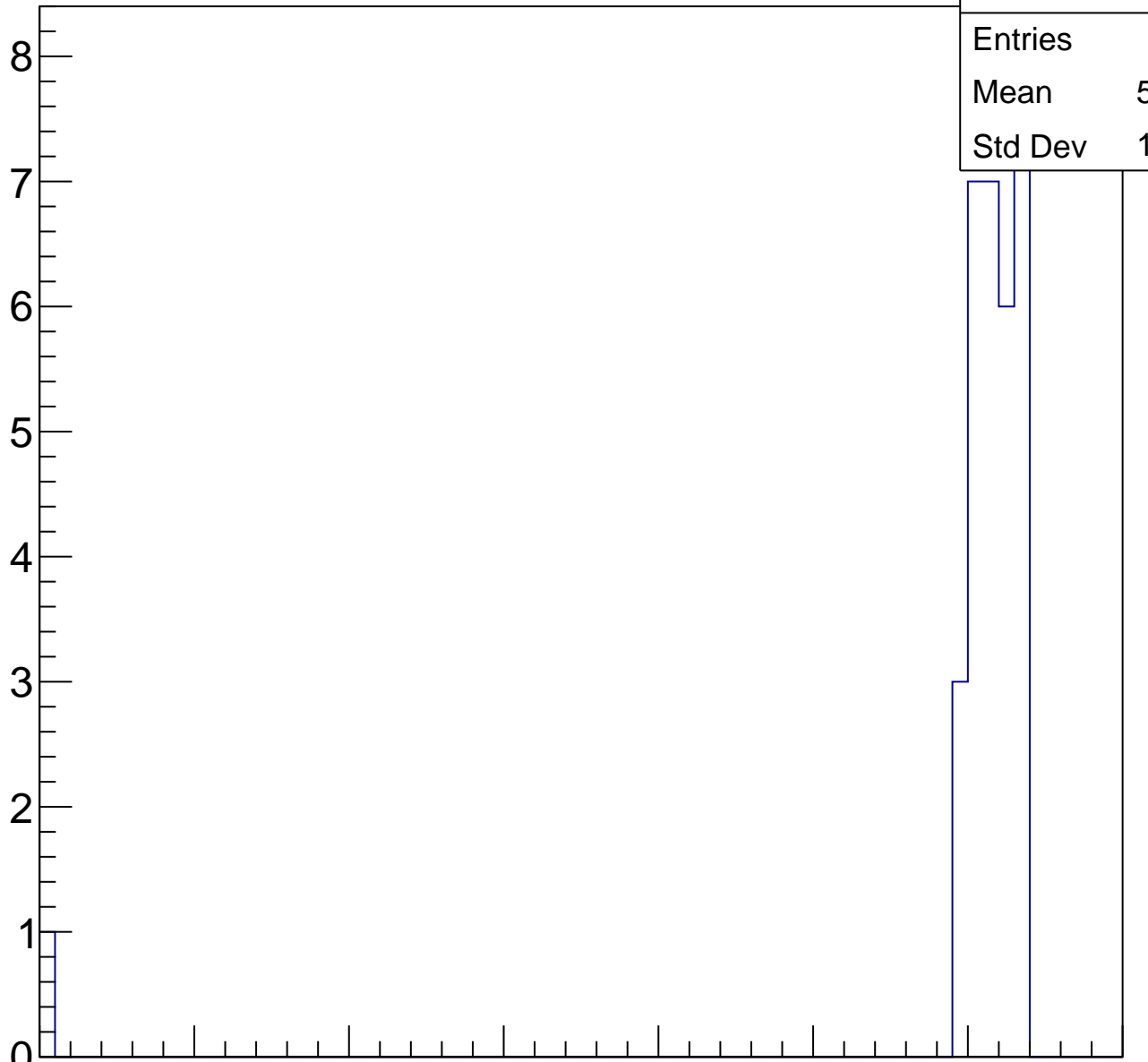
Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	59.38
Std Dev	10.74

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch20, adc0

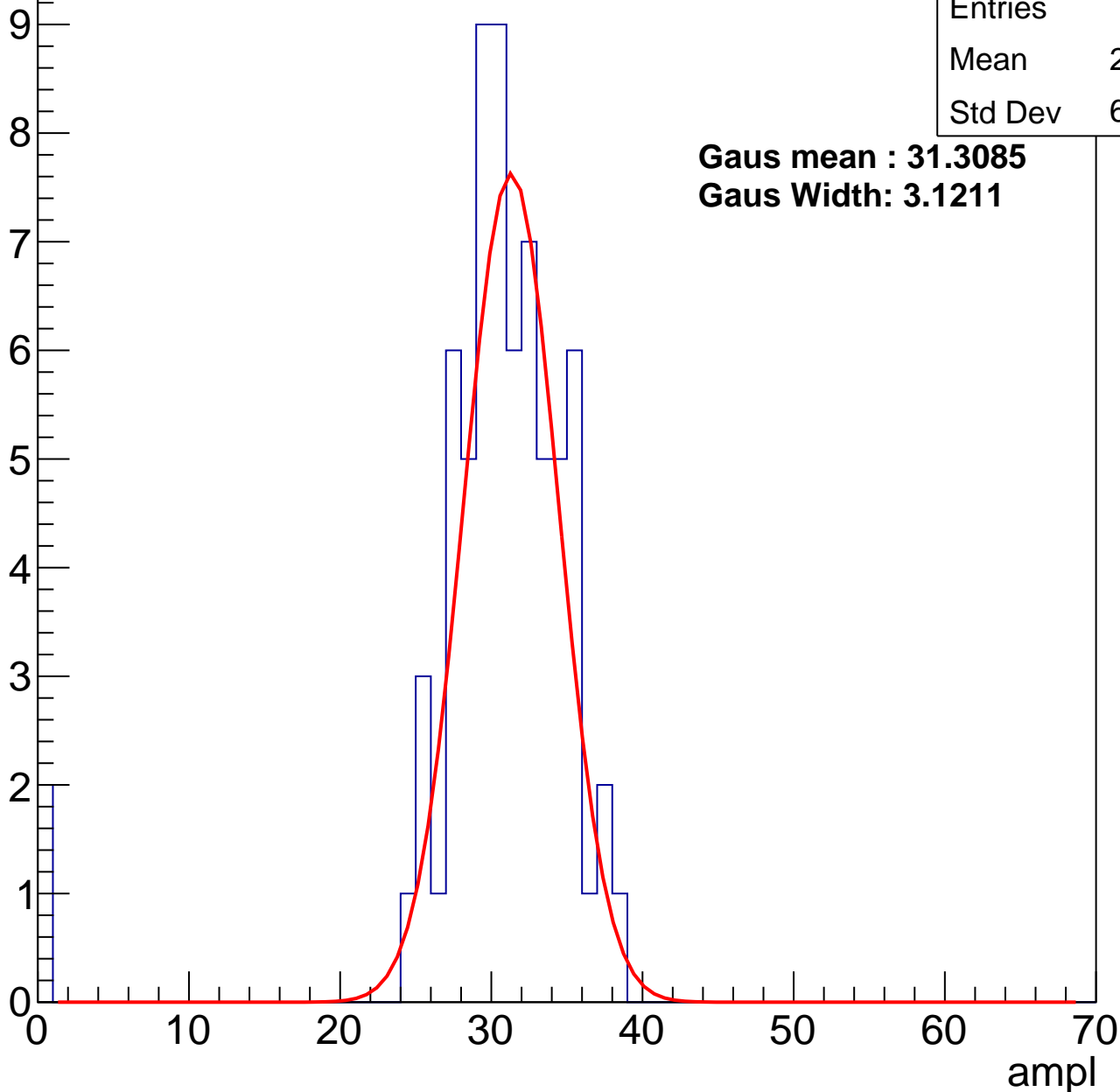
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	29.87
Std Dev	6.038

Gaus mean : 31.3085

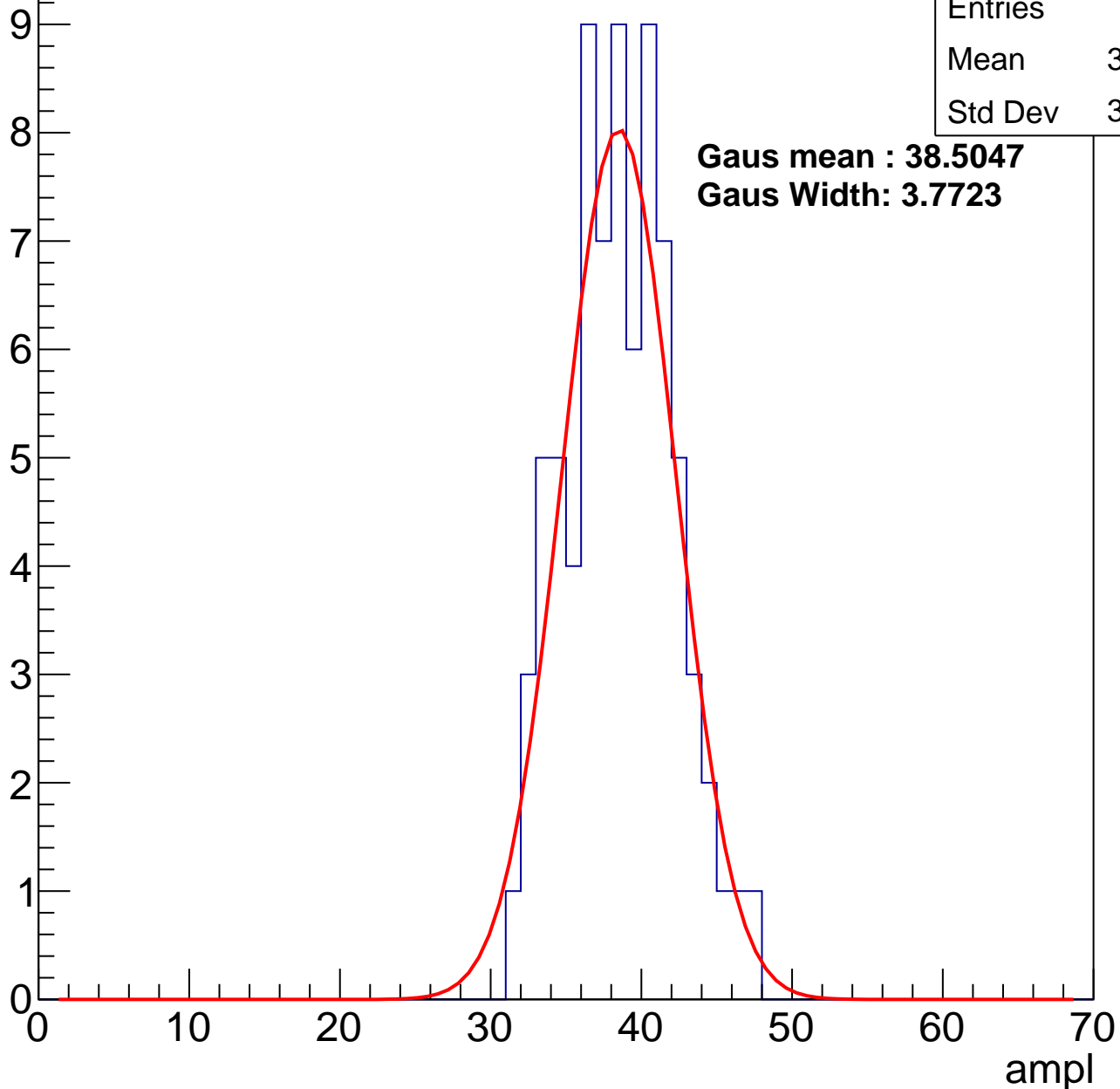
Gaus Width: 3.1211



B0L001S, U25-ch20, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



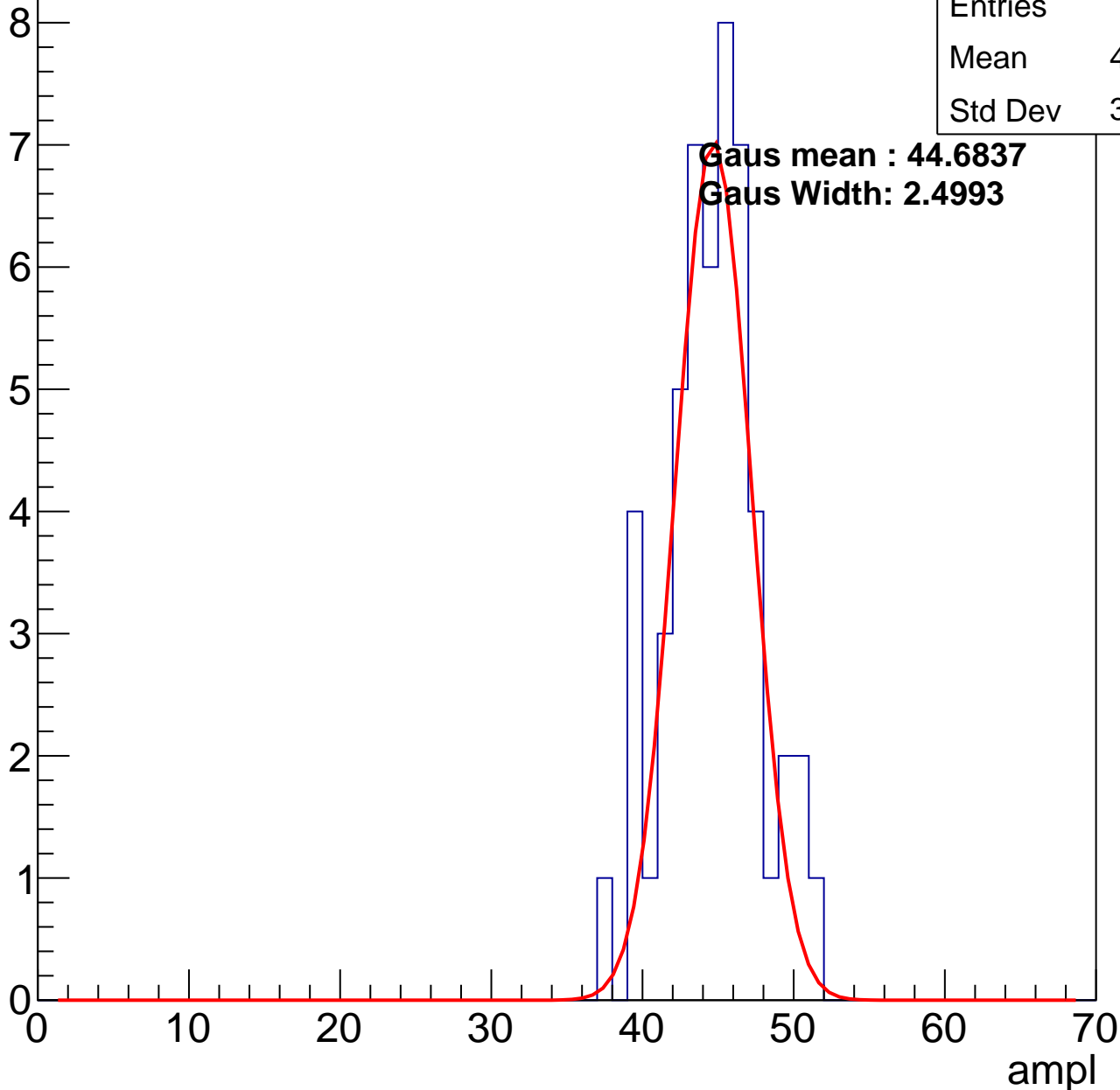
B0L001S, U25-ch20, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.19
Std Dev	3.026

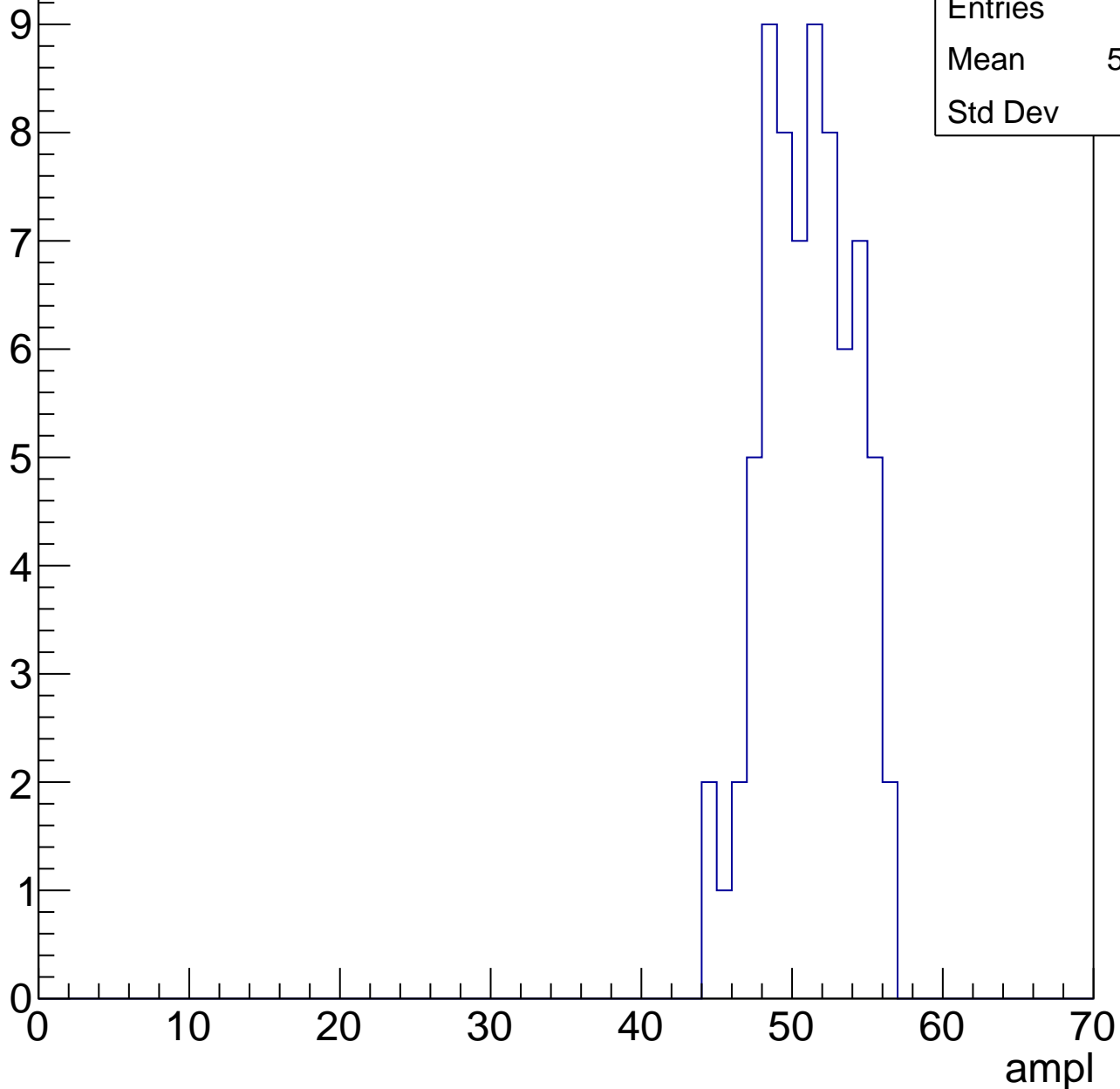
Gaus mean : 44.6837
Gaus Width: 2.4993



B0L001S, U25-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



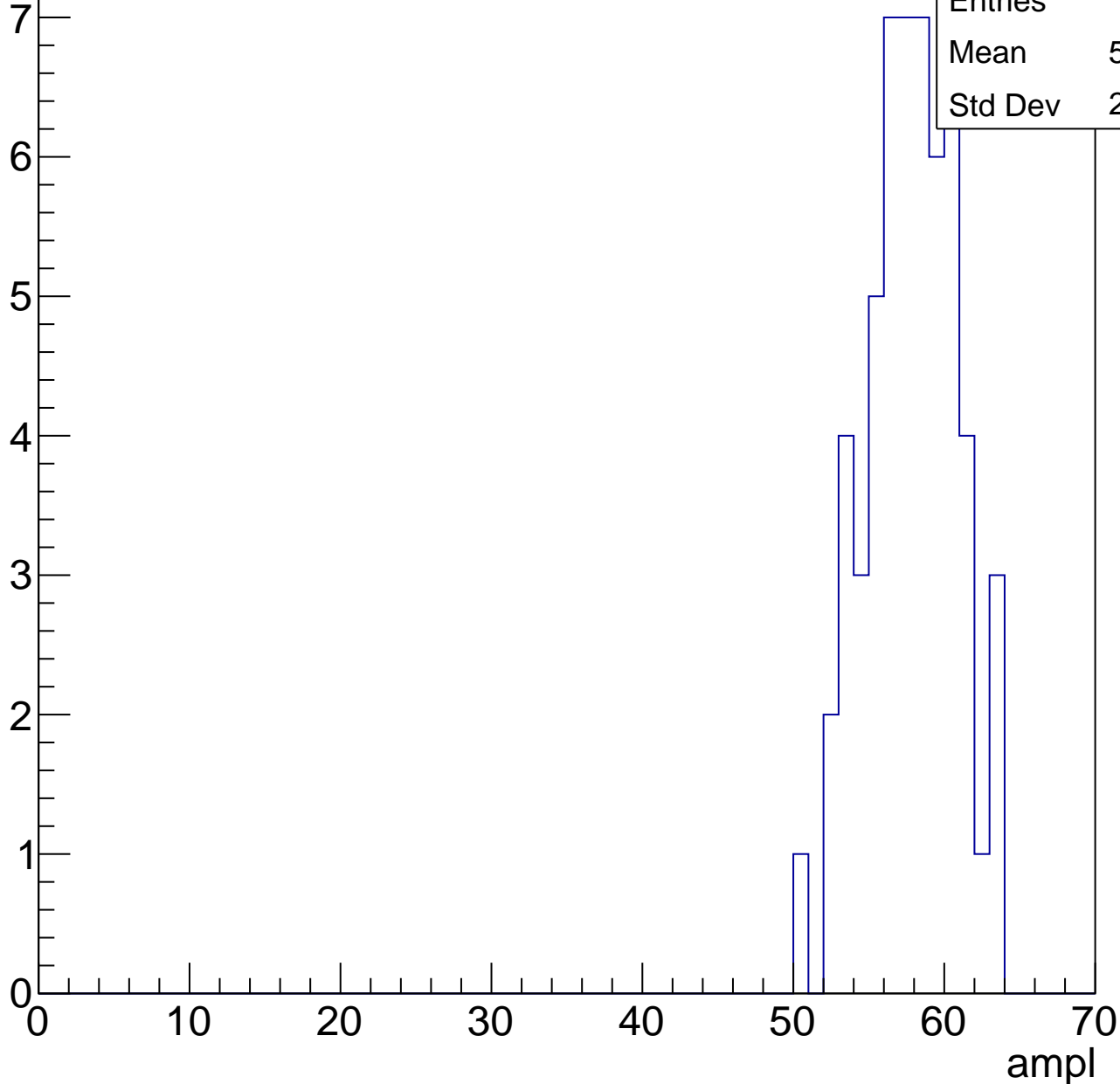
Entries	71
Mean	50.59
Std Dev	2.9

B0L001S, U25-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.35
Std Dev	2.965

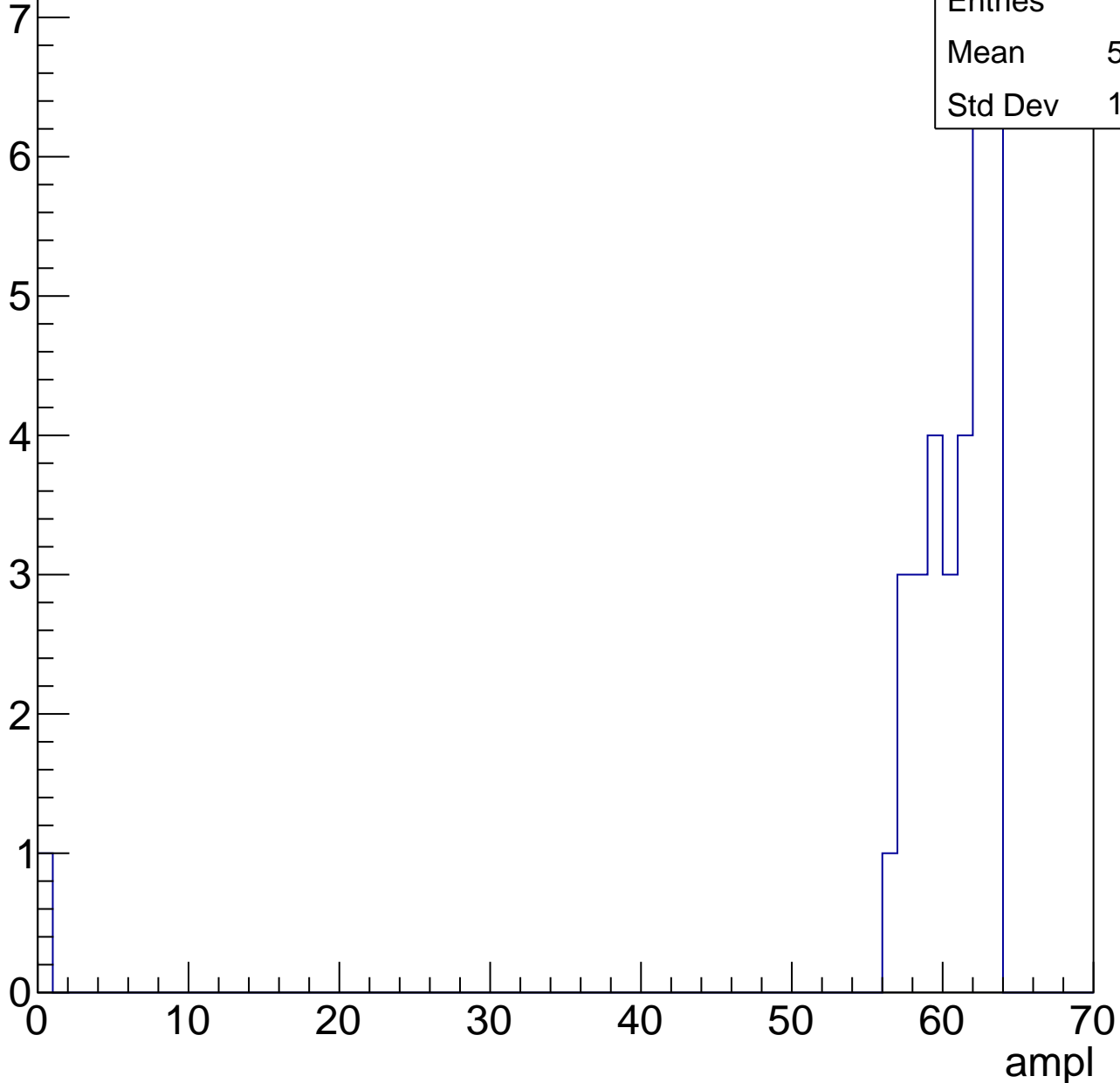


B0L001S, U25-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.67
Std Dev	10.58



B0L001S, U25-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	0.5

ampl

B0L001S, U25-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch21, adc0

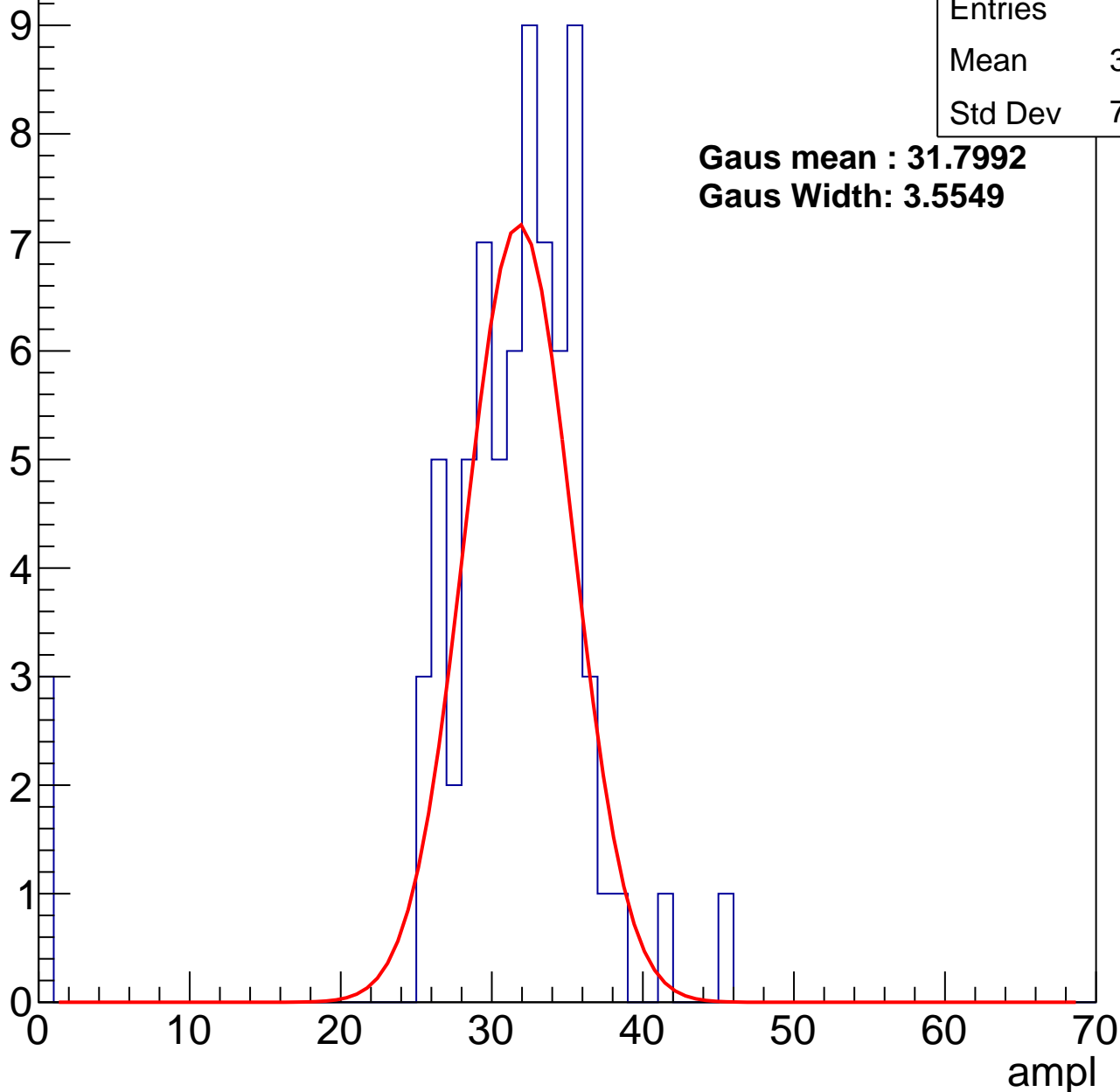
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.34
Std Dev	7.245

Gaus mean : 31.7992

Gaus Width: 3.5549



B0L001S, U25-ch21, adc1

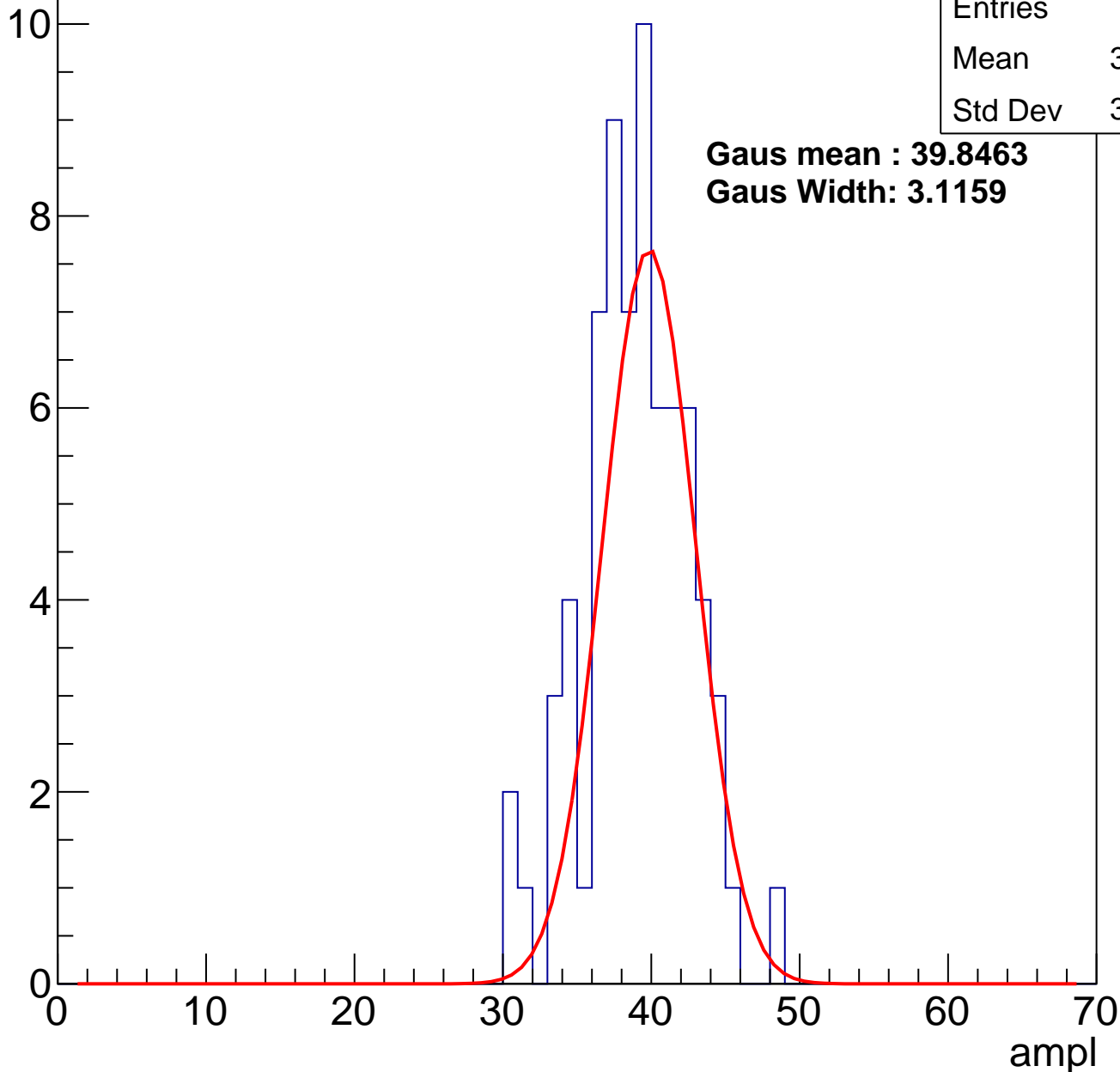
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	38.55
Std Dev	3.524

Gaus mean : 39.8463

Gaus Width: 3.1159

Entry



B0L001S, U25-ch21, adc2

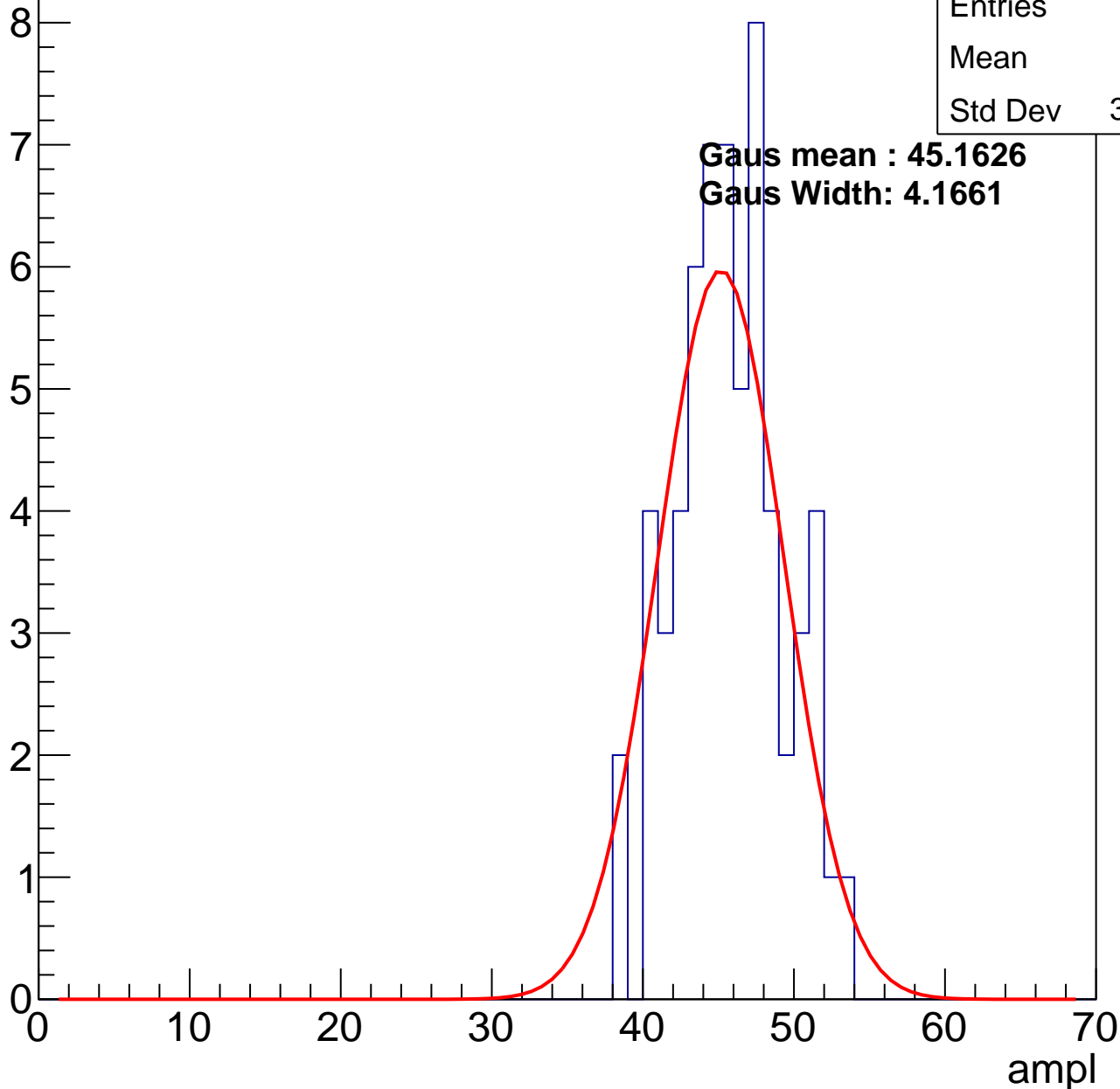
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	45.3
Std Dev	3.494

Gaus mean : 45.1626

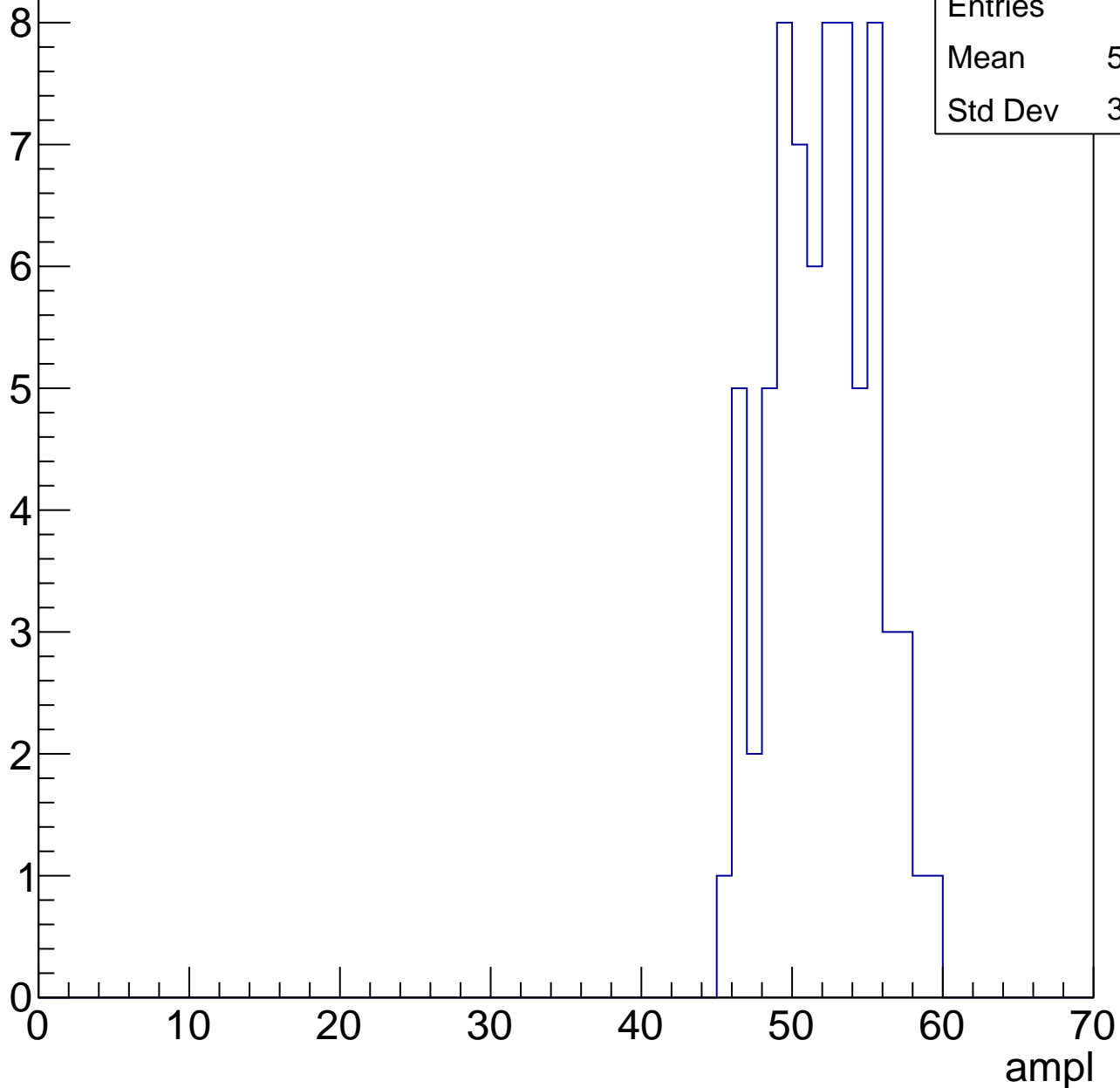
Gaus Width: 4.1661



B0L001S, U25-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



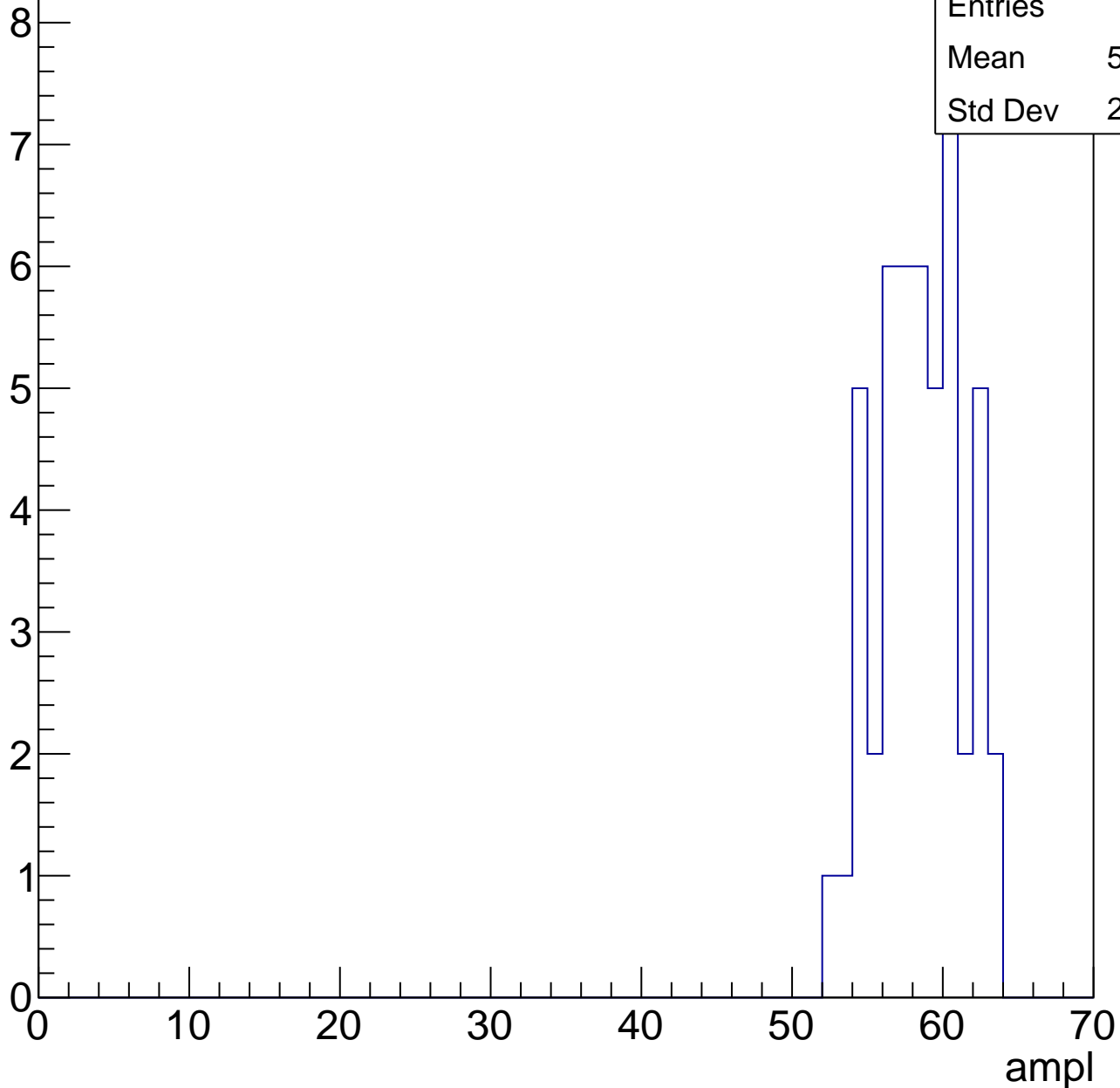
Entries	71
Mean	51.59
Std Dev	3.274

B0L001S, U25-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.04
Std Dev	2.762

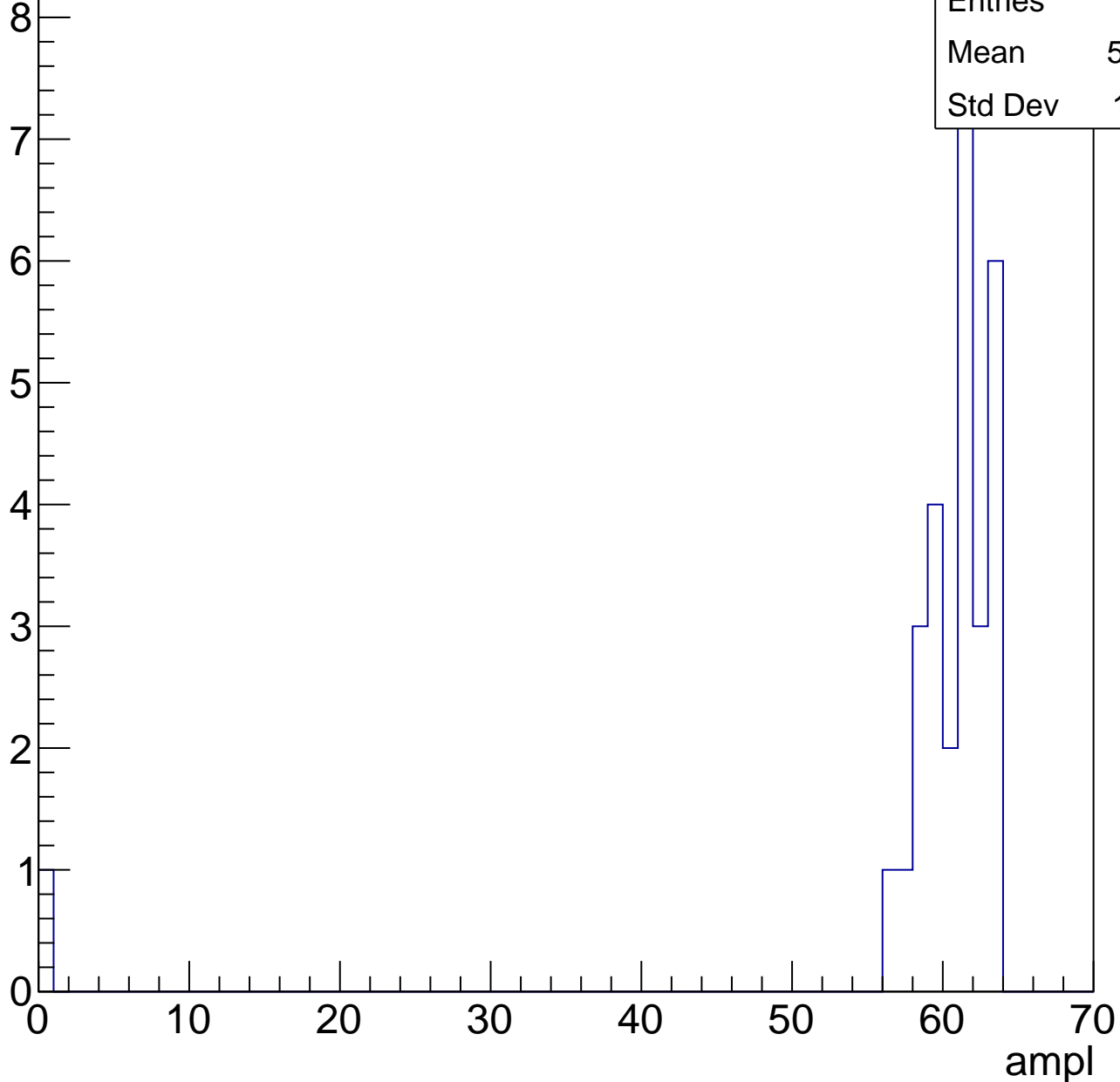


B0L001S, U25-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.45
Std Dev	11.21



B0L001S, U25-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch22, adc0

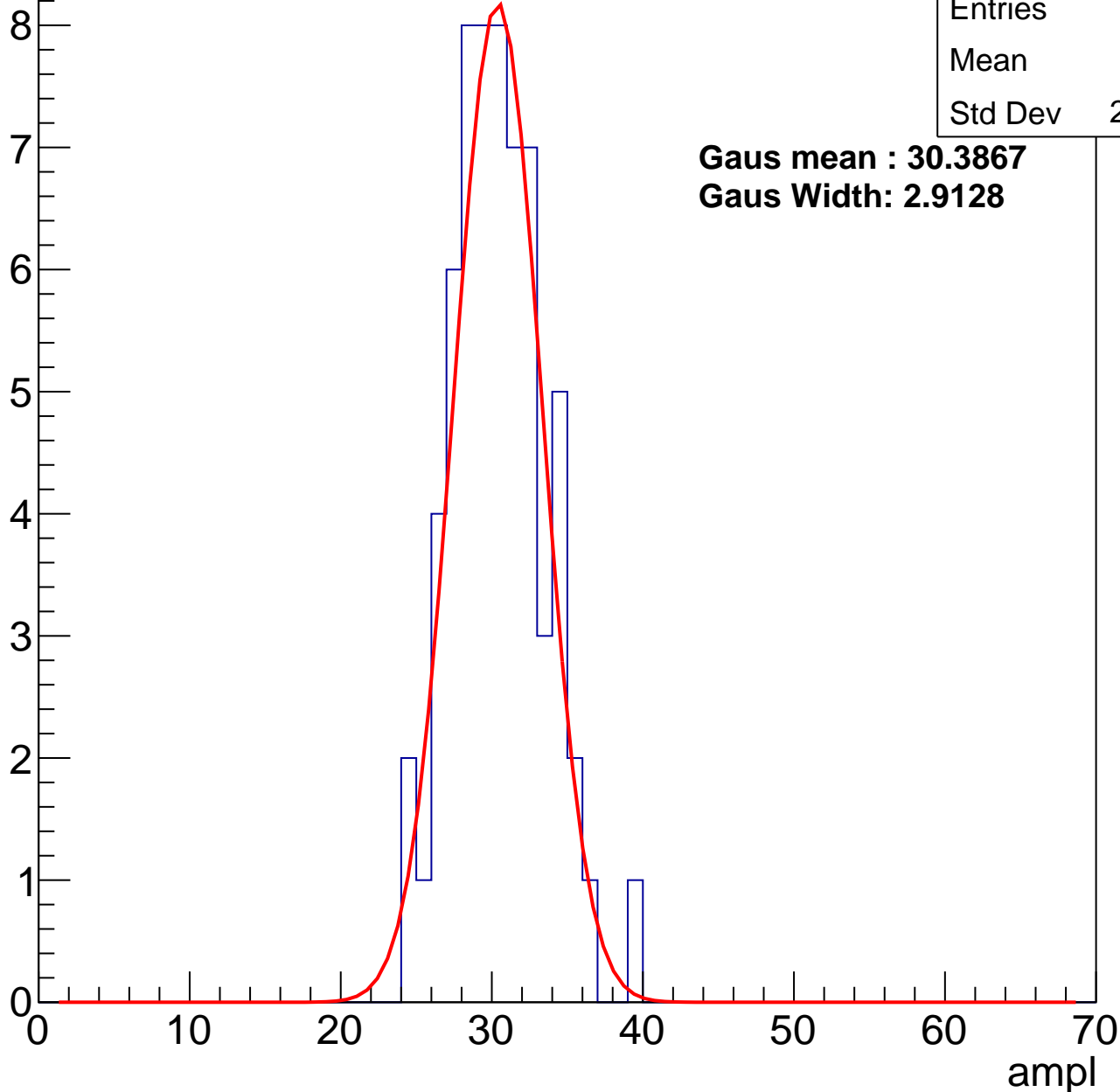
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30
Std Dev	2.992

Gaus mean : 30.3867

Gaus Width: 2.9128



B0L001S, U25-ch22, adc1

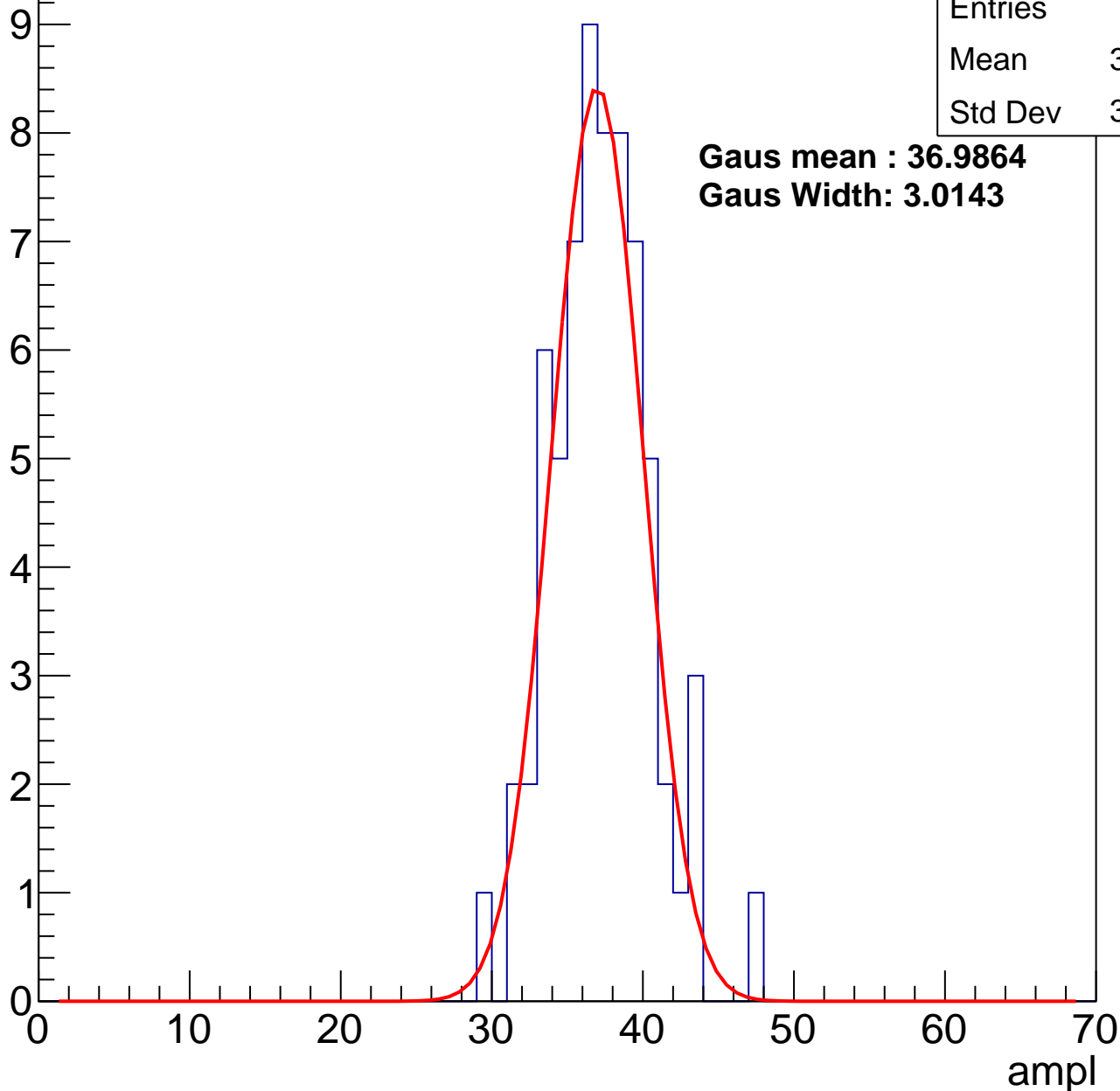
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.79
Std Dev	3.249

Gaus mean : 36.9864

Gaus Width: 3.0143



B0L001S, U25-ch22, adc2

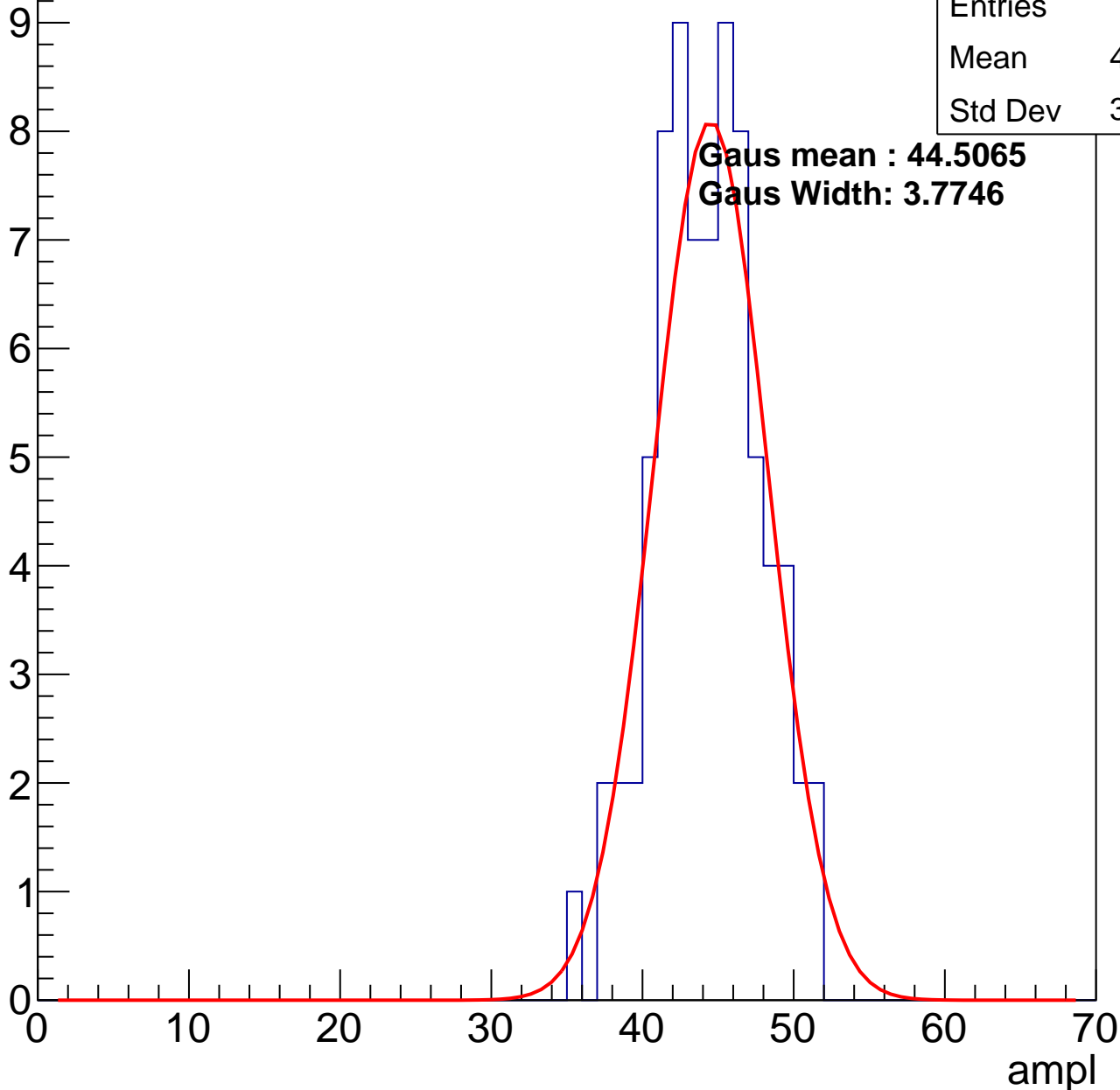
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	43.84
Std Dev	3.446

Gaus mean : 44.5065

Gaus Width: 3.7746

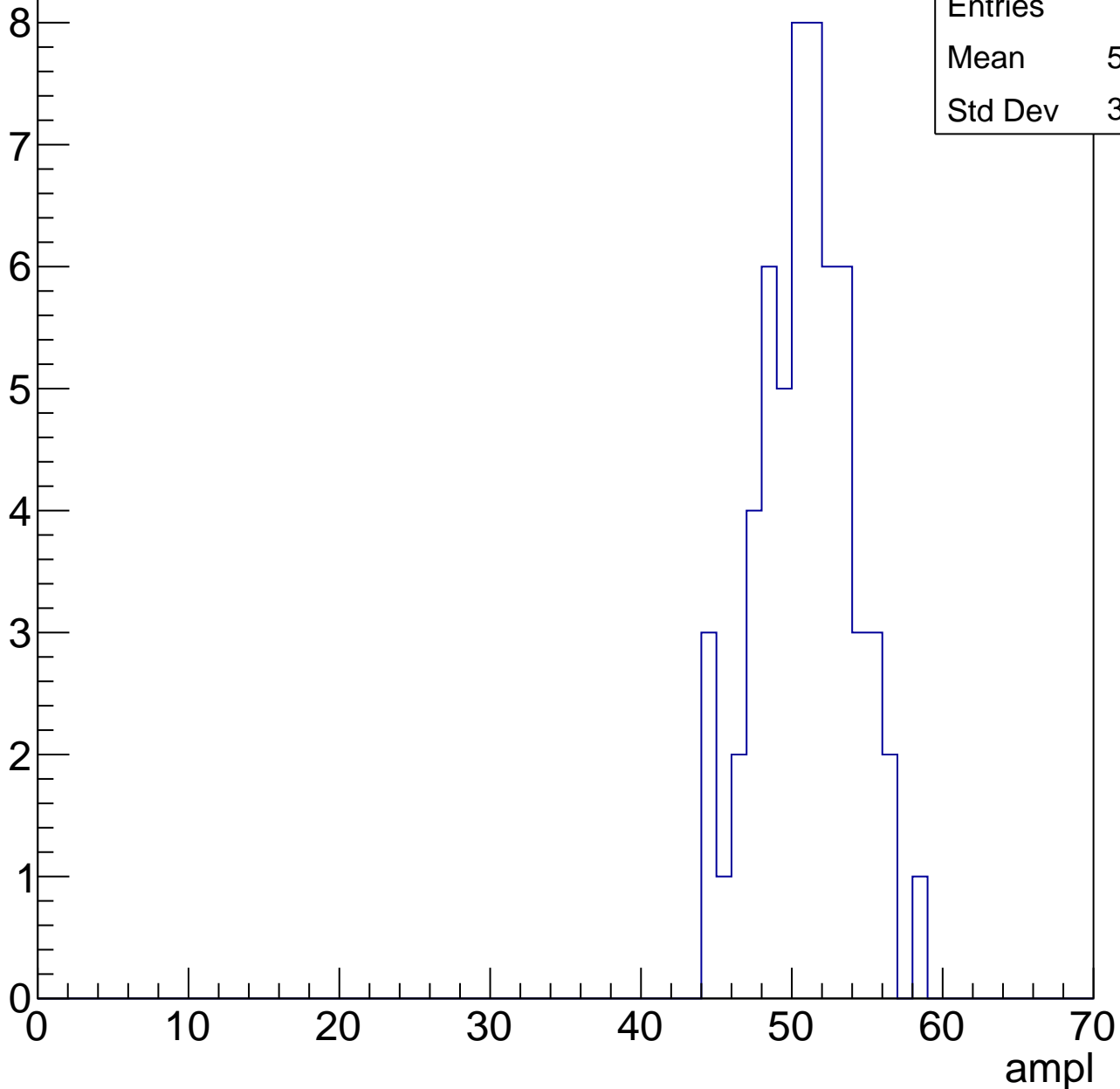


B0L001S, U25-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.43
Std Dev	3.119

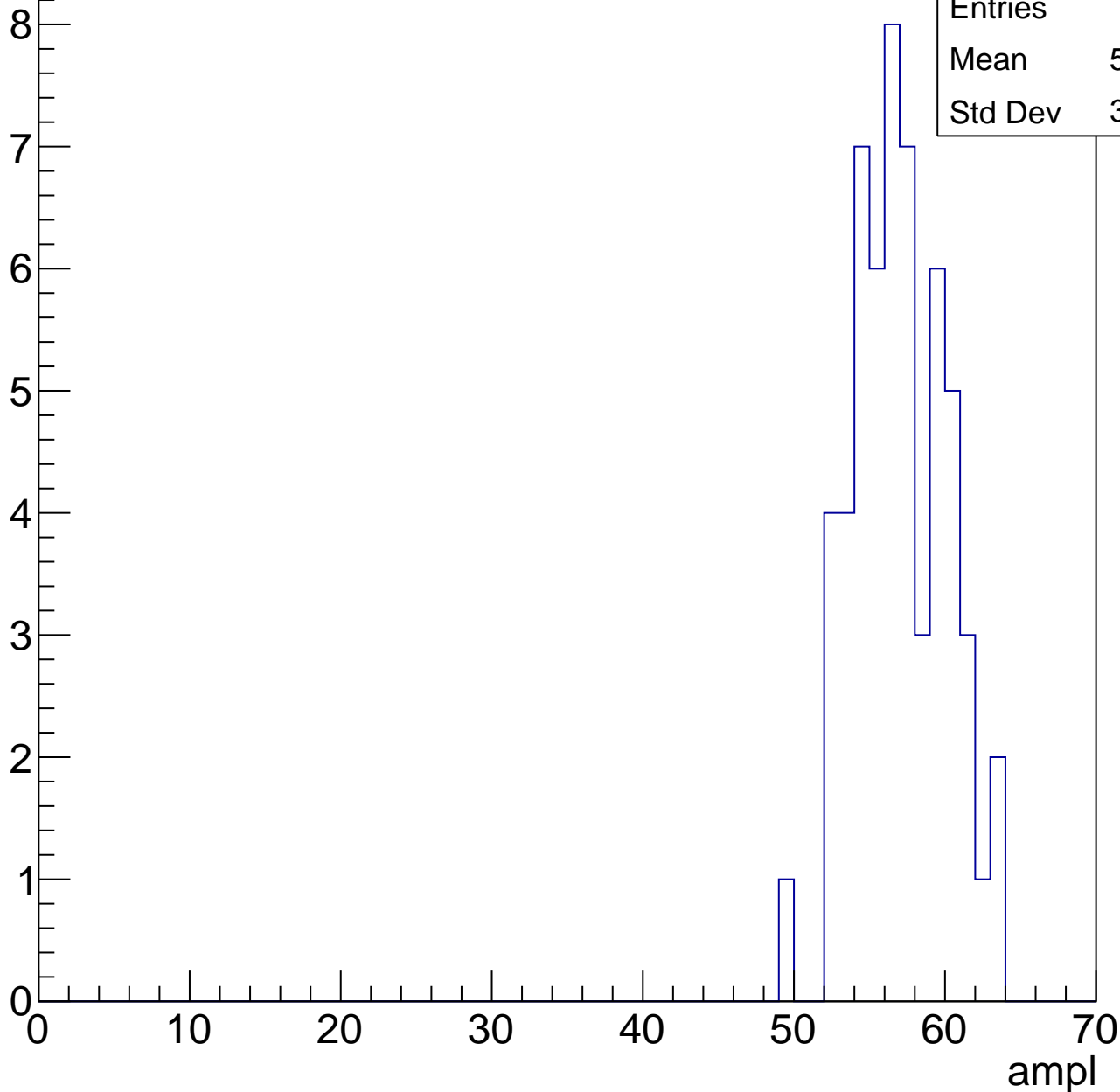


B0L001S, U25-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.54
Std Dev	3.044

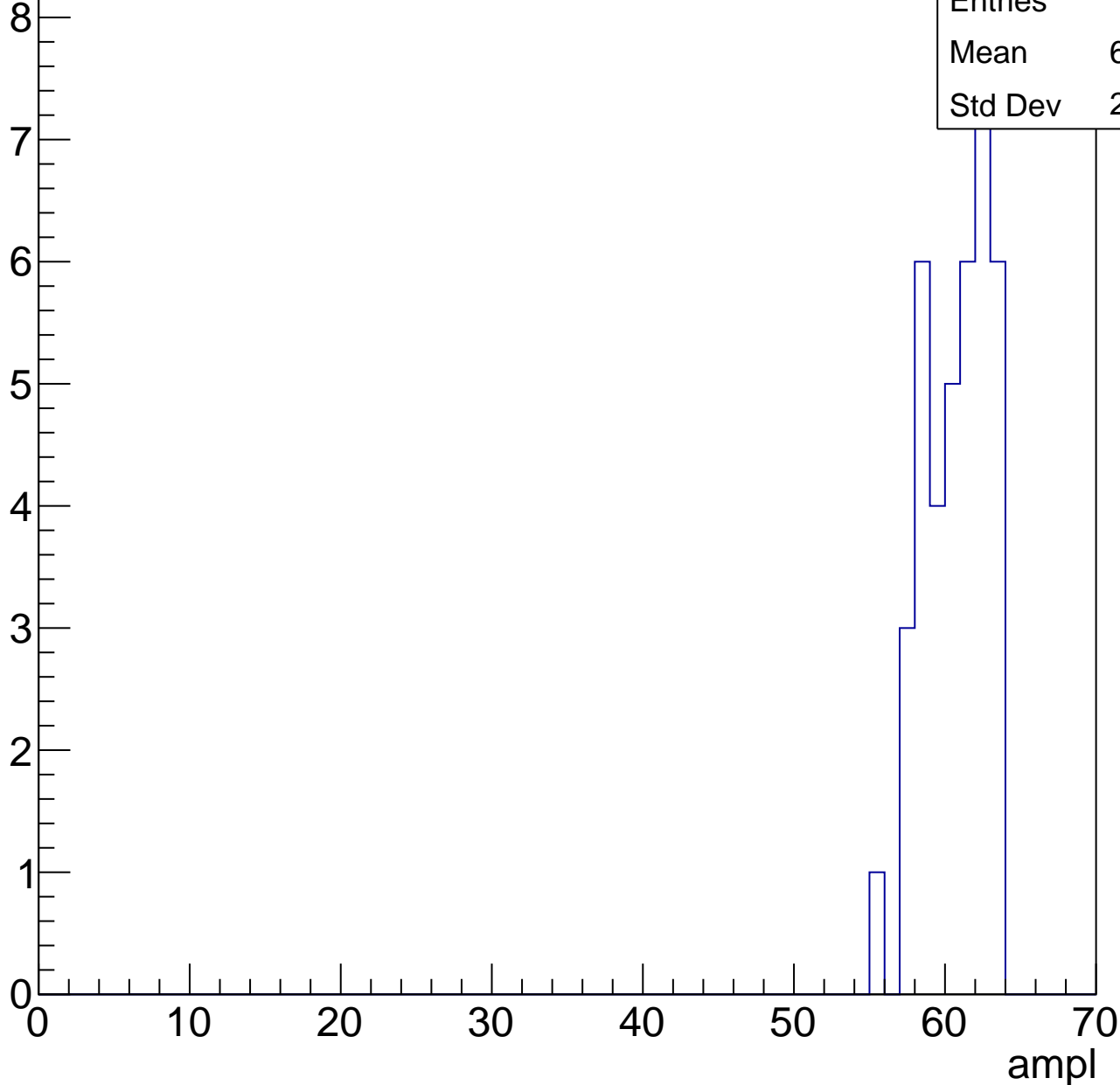


B0L001S, U25-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	60.26
Std Dev	2.084



B0L001S, U25-ch22, adc6

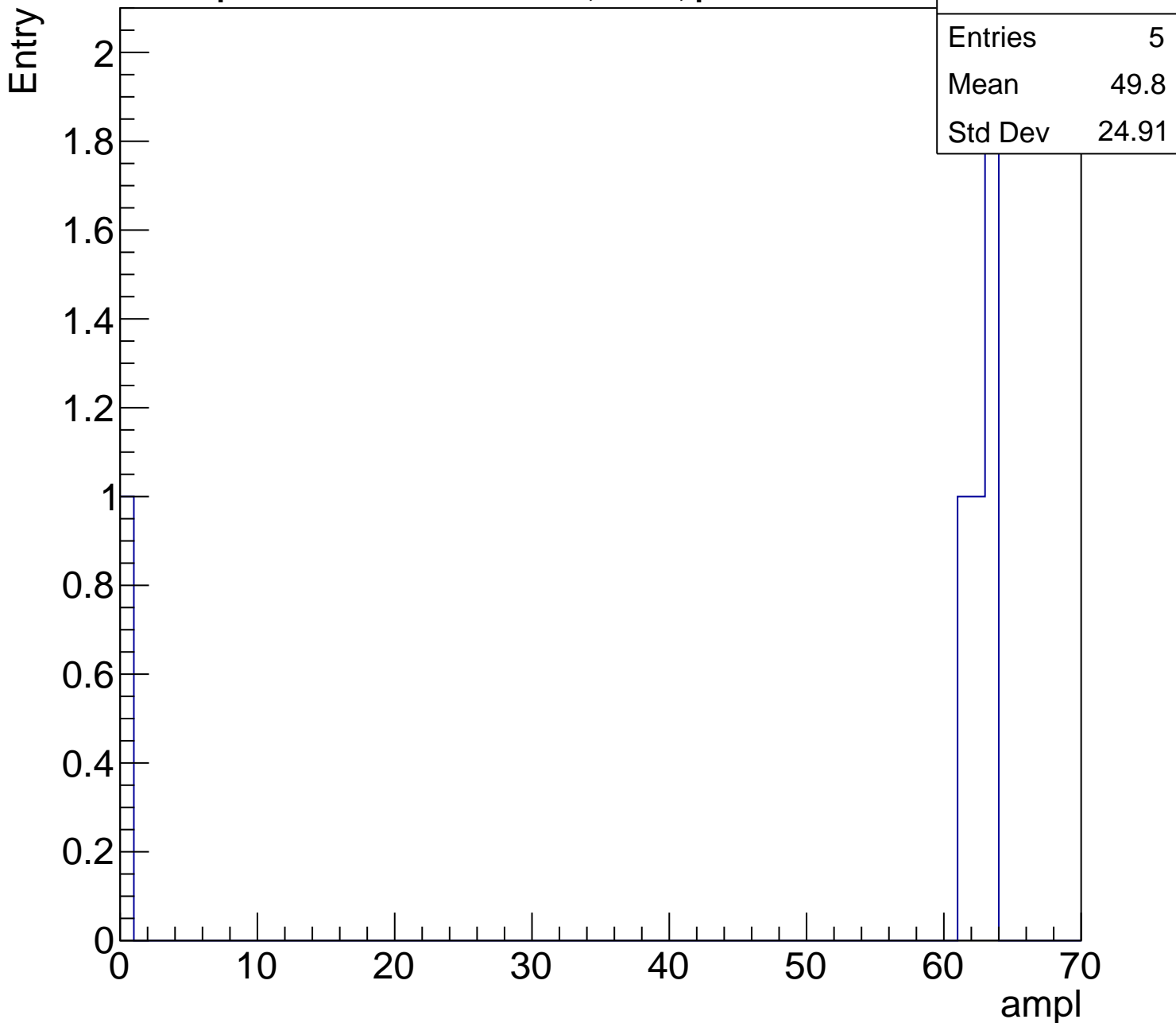
calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.8
Std Dev	24.91

ampl



B0L001S, U25-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch23, adc0

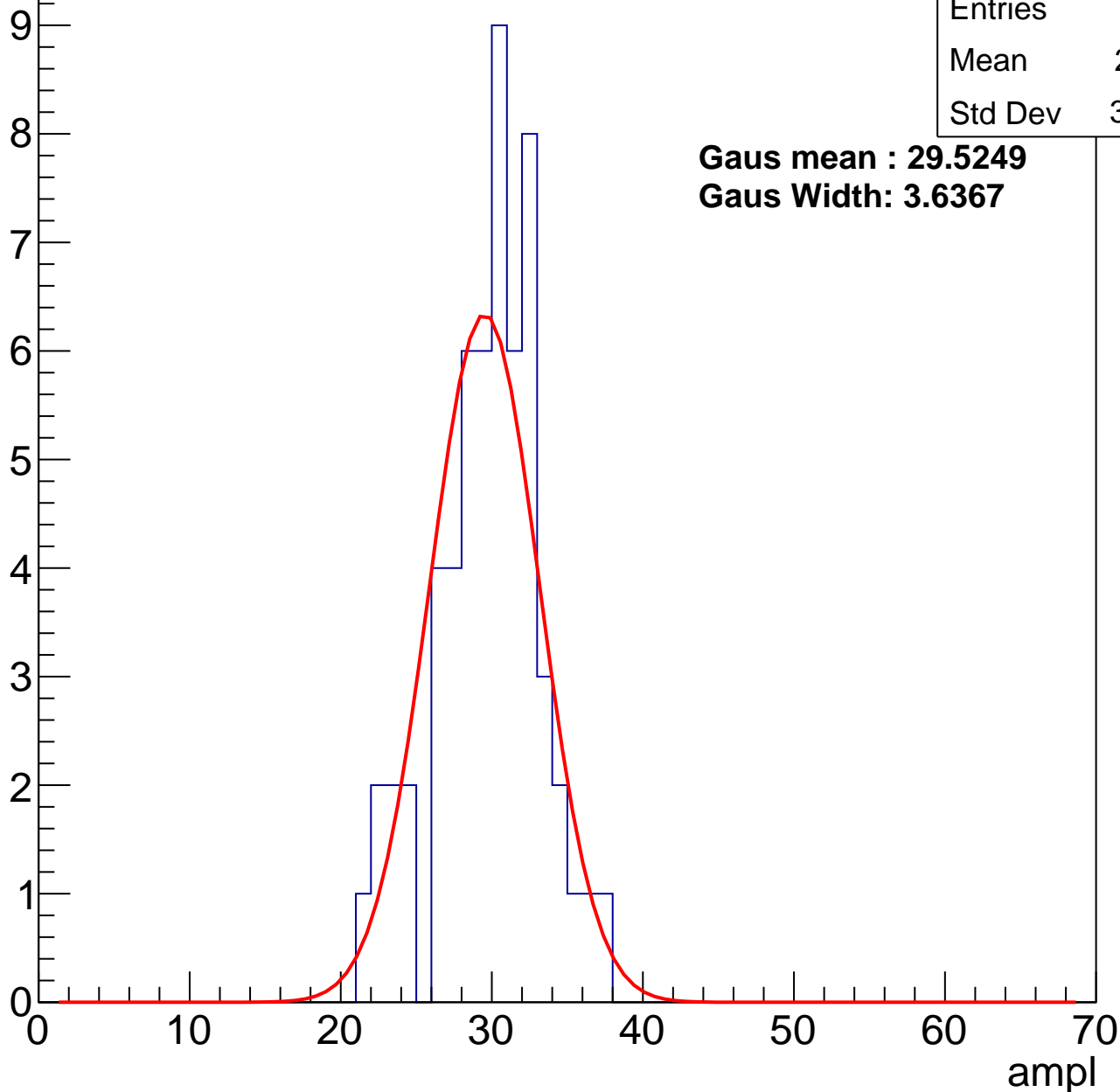
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	29.31
Std Dev	3.445

Gaus mean : 29.5249

Gaus Width: 3.6367

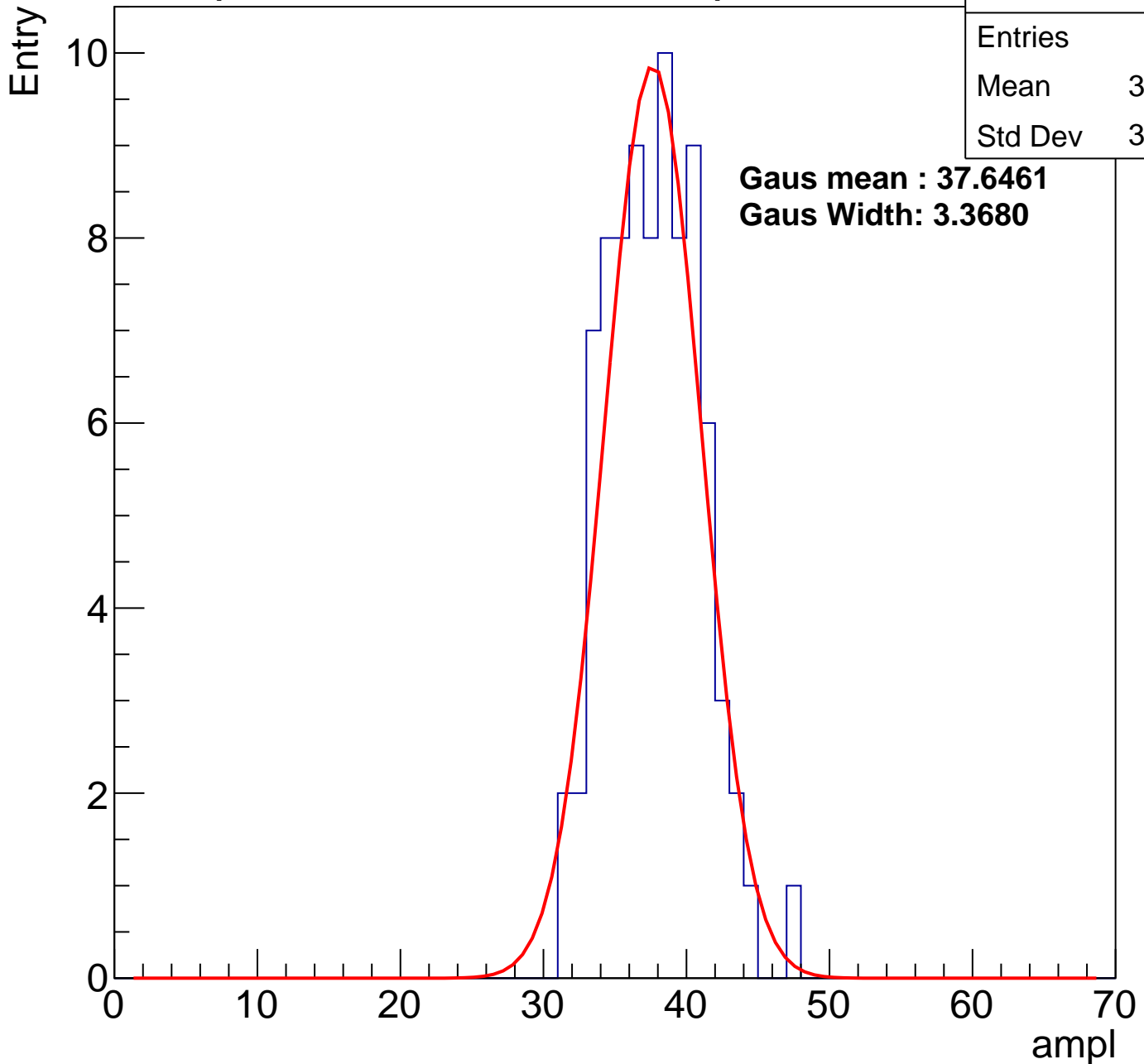


B0L001S, U25-ch23, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	37.26
Std Dev	3.193

Gaus mean : 37.6461
Gaus Width: 3.3680



B0L001S, U25-ch23, adc2

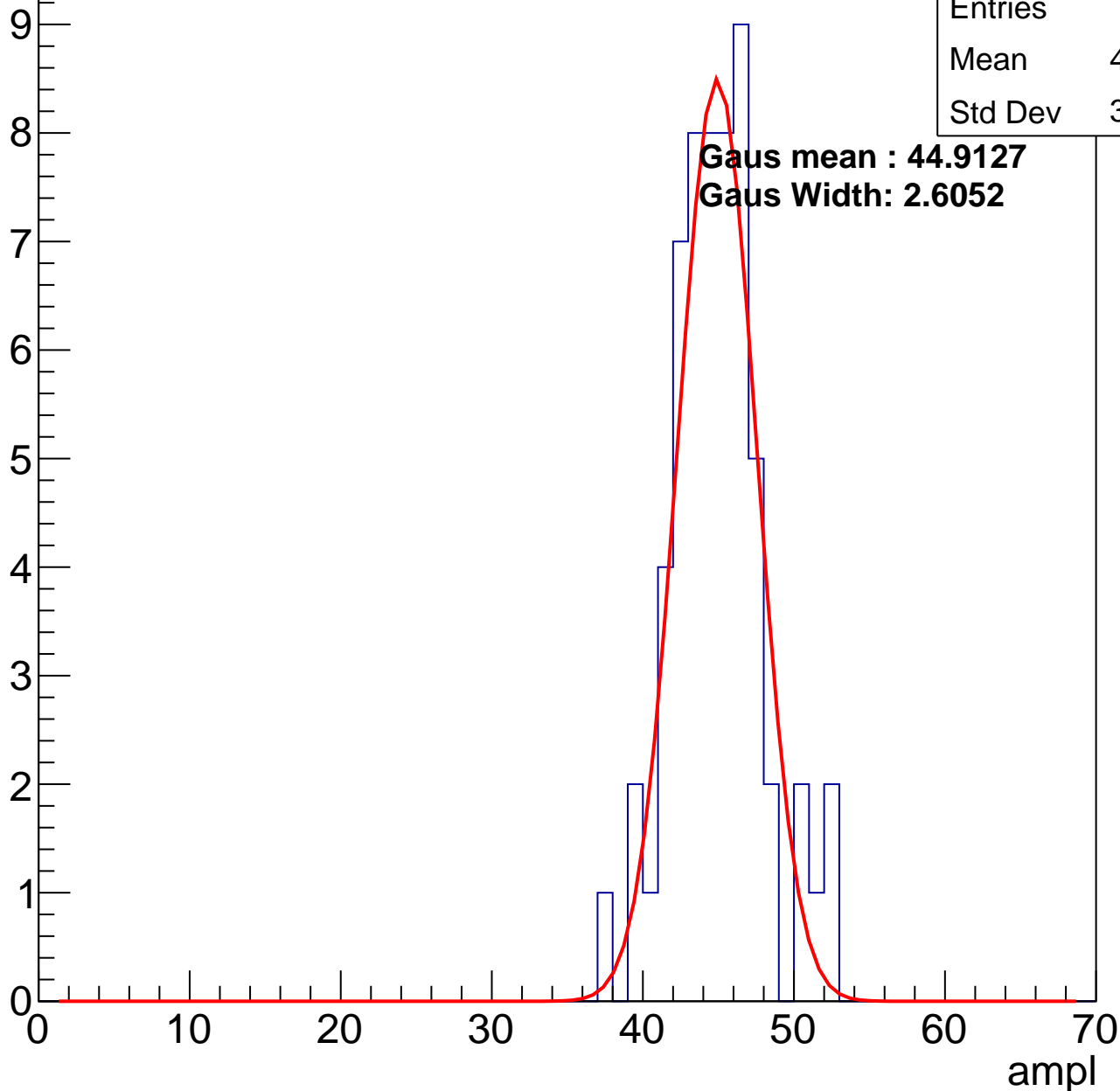
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.48
Std Dev	3.008

Gaus mean : 44.9127

Gaus Width: 2.6052



B0L001S, U25-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

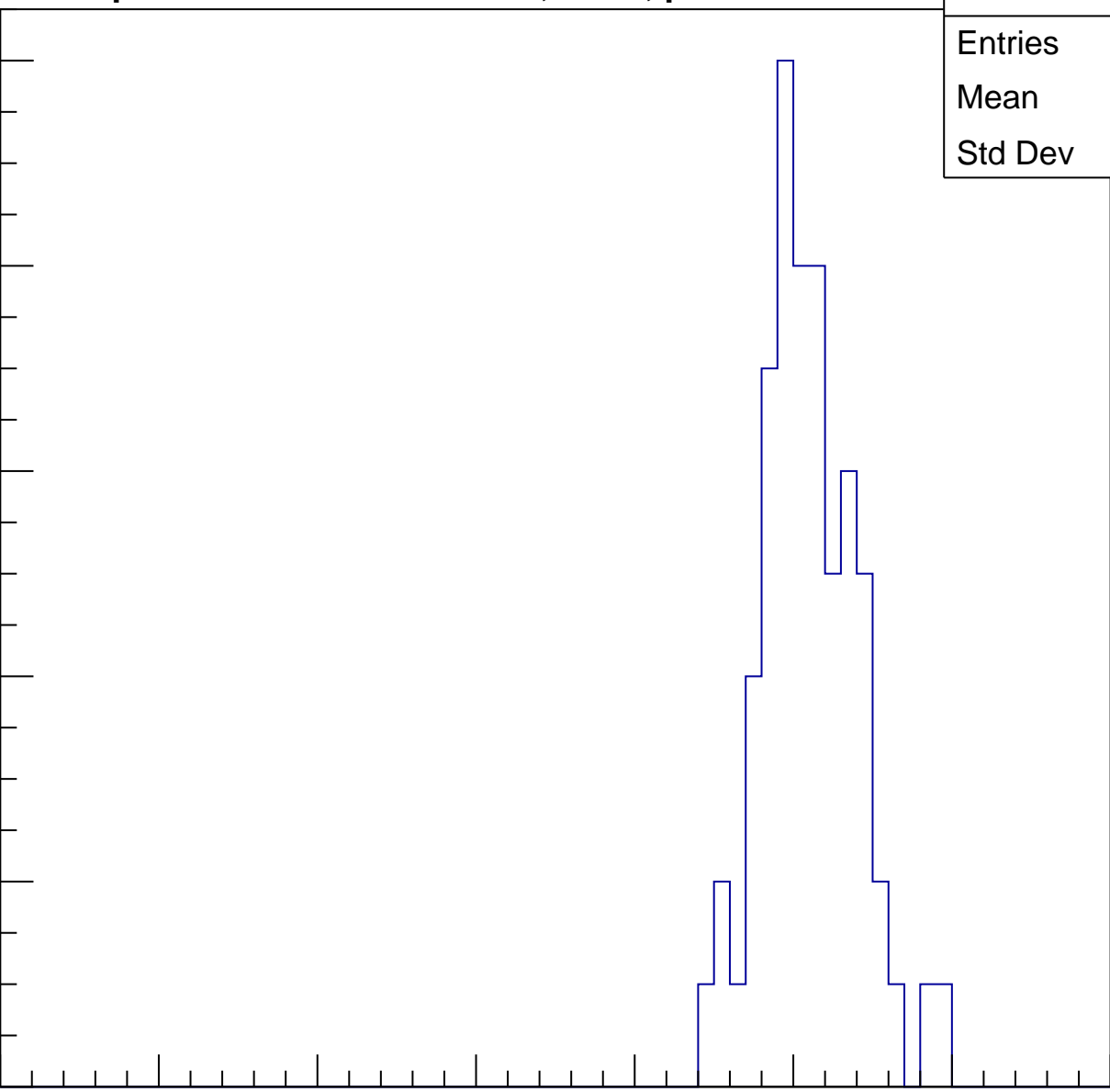
Entries	62
Mean	50.53
Std Dev	2.982

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

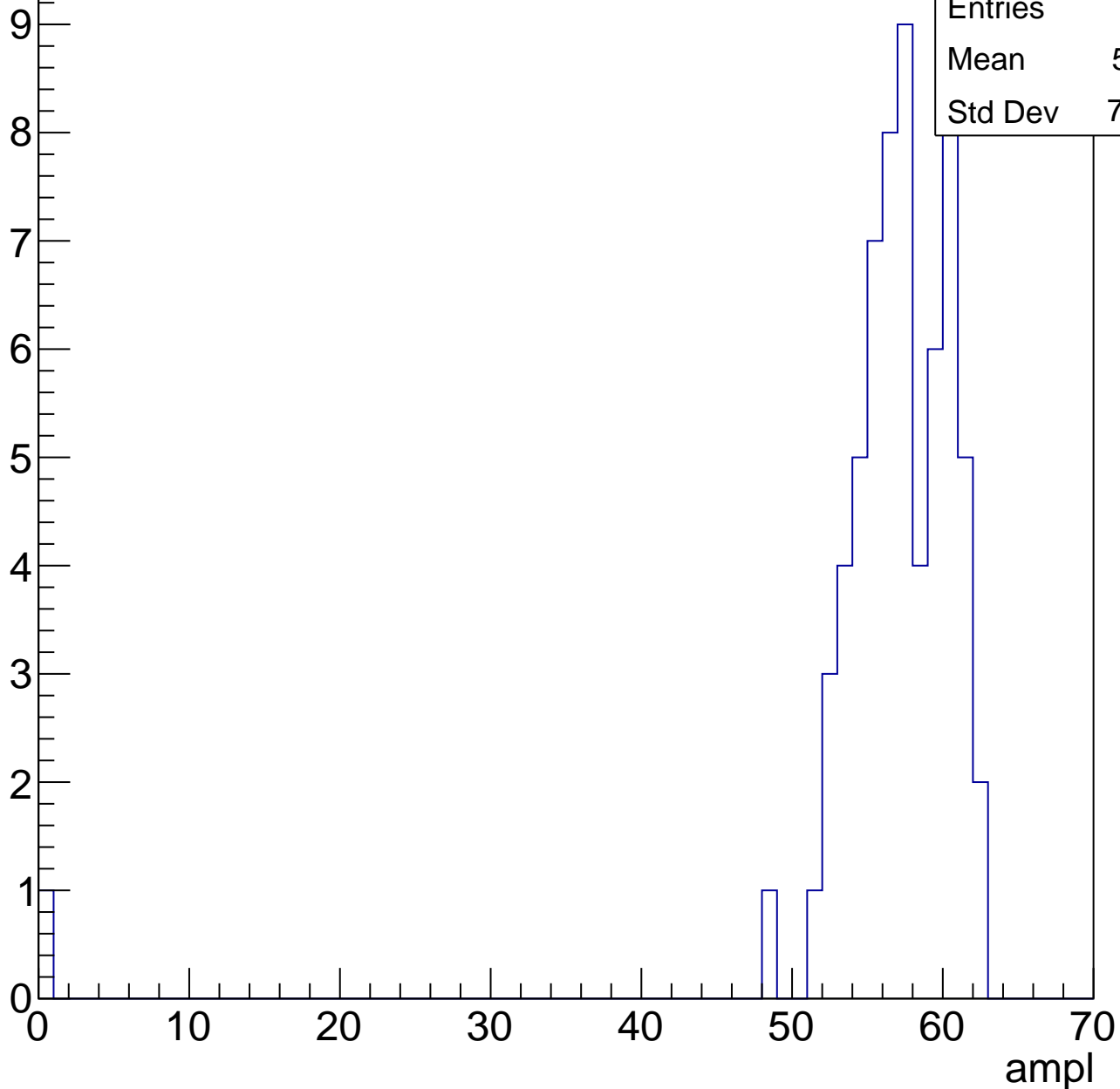
ampl



B0L001S, U25-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

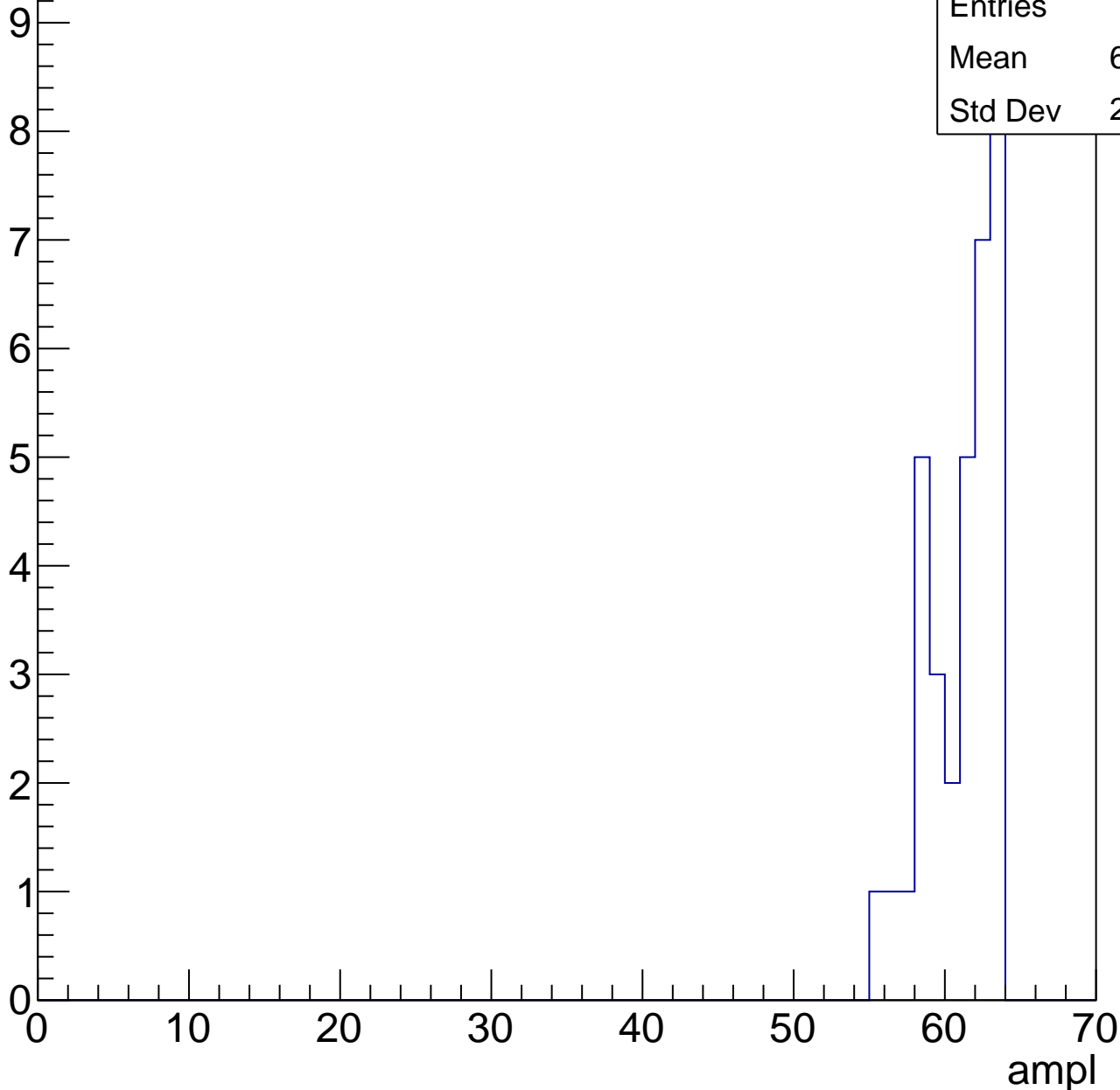


B0L001S, U25-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	60.62
Std Dev	2.262



B0L001S, U25-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch24, adc0

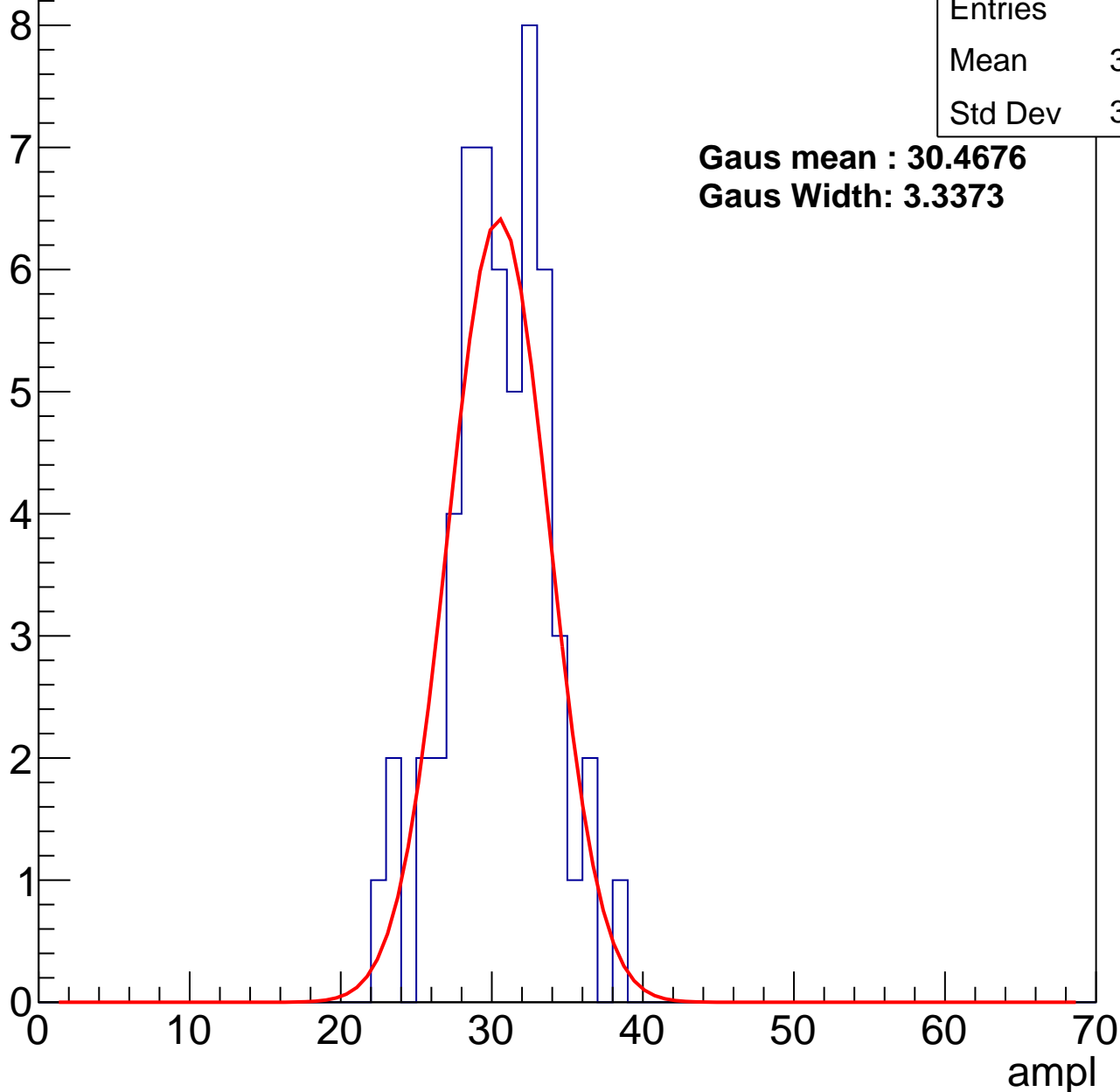
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.05
Std Dev	3.284

Gaus mean : 30.4676

Gaus Width: 3.3373



B0L001S, U25-ch24, adc1

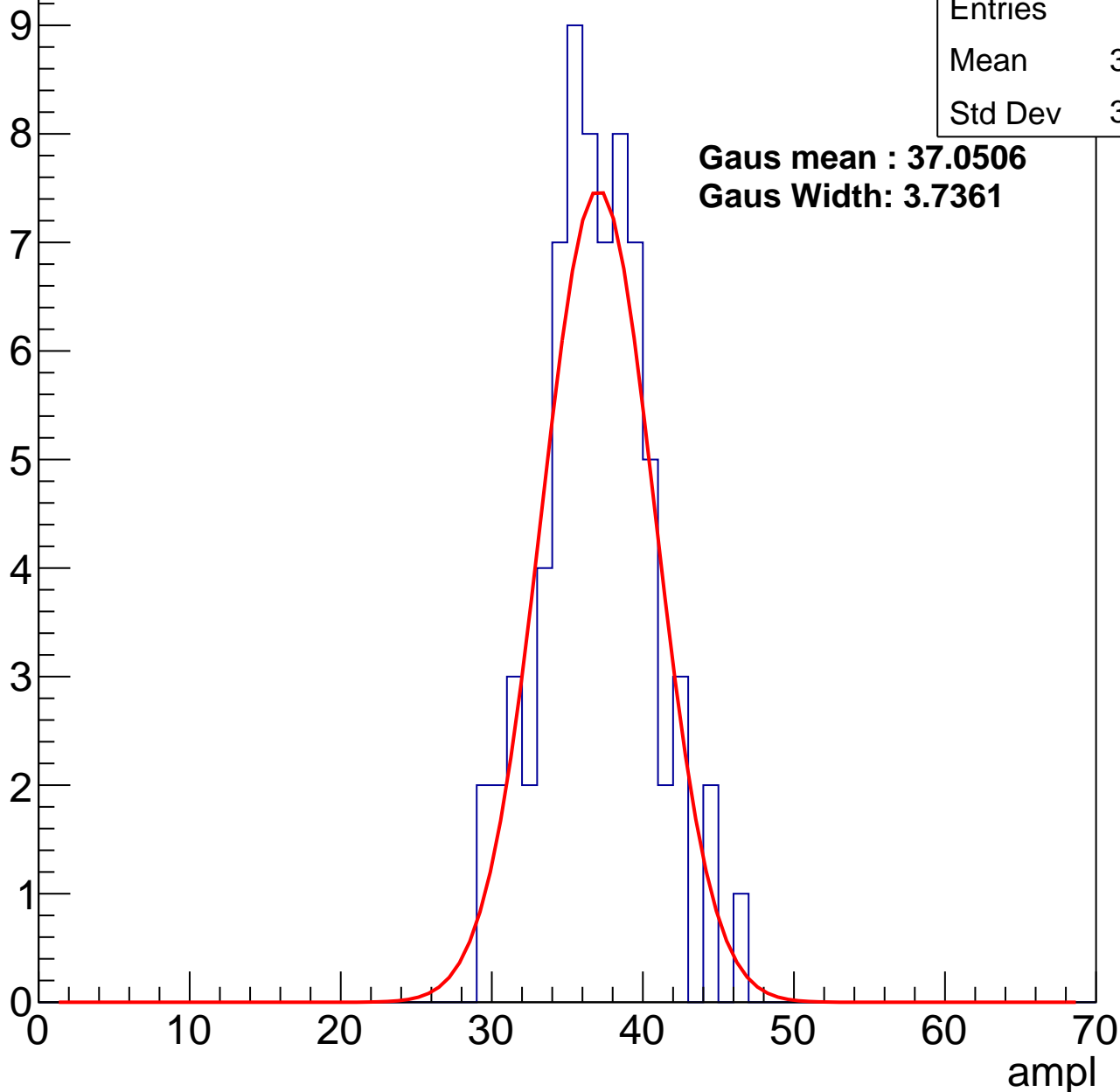
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.47
Std Dev	3.539

Gaus mean : 37.0506

Gaus Width: 3.7361



B0L001S, U25-ch24, adc2

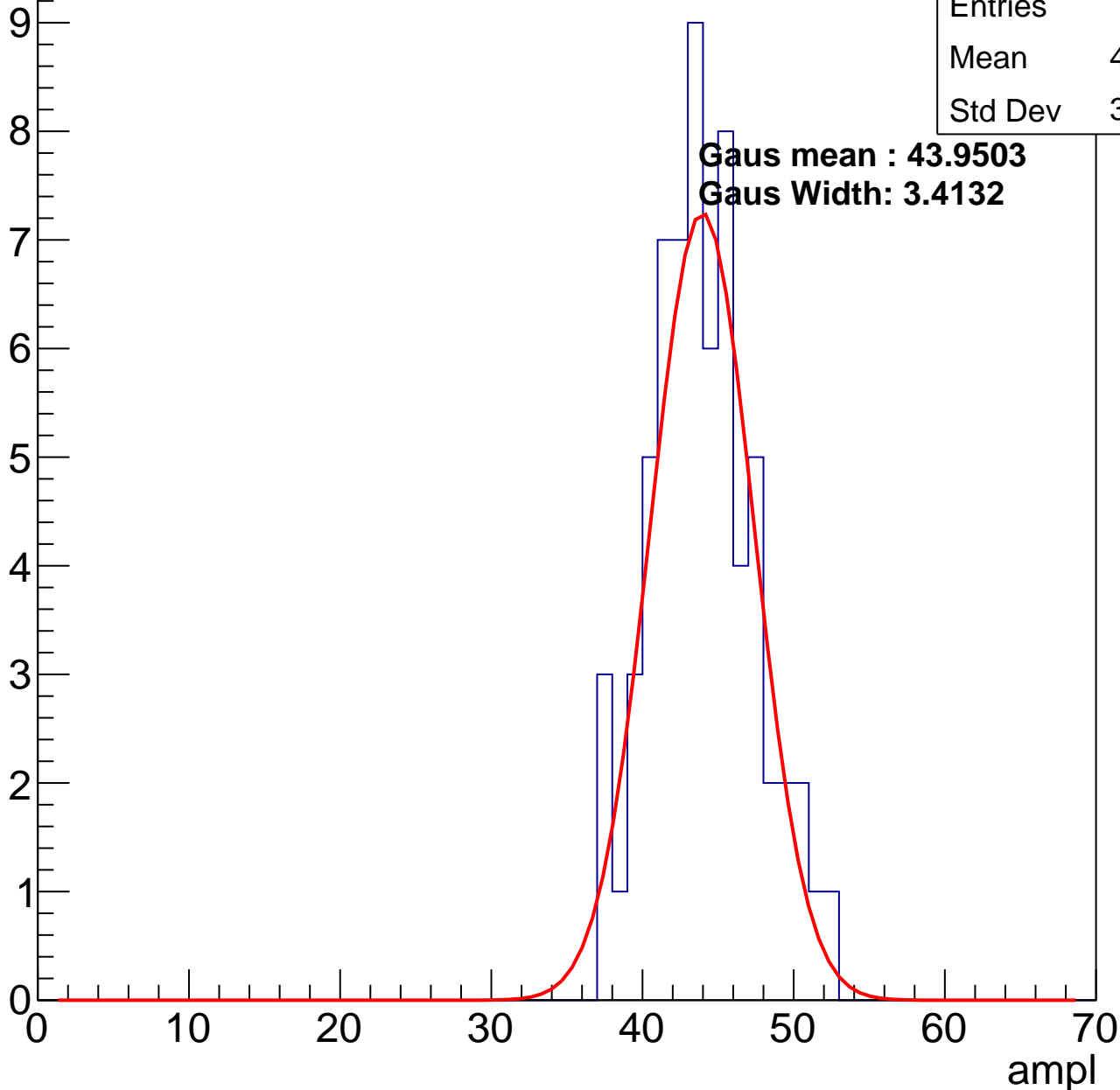
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.55
Std Dev	3.394

Gaus mean : 43.9503

Gaus Width: 3.4132

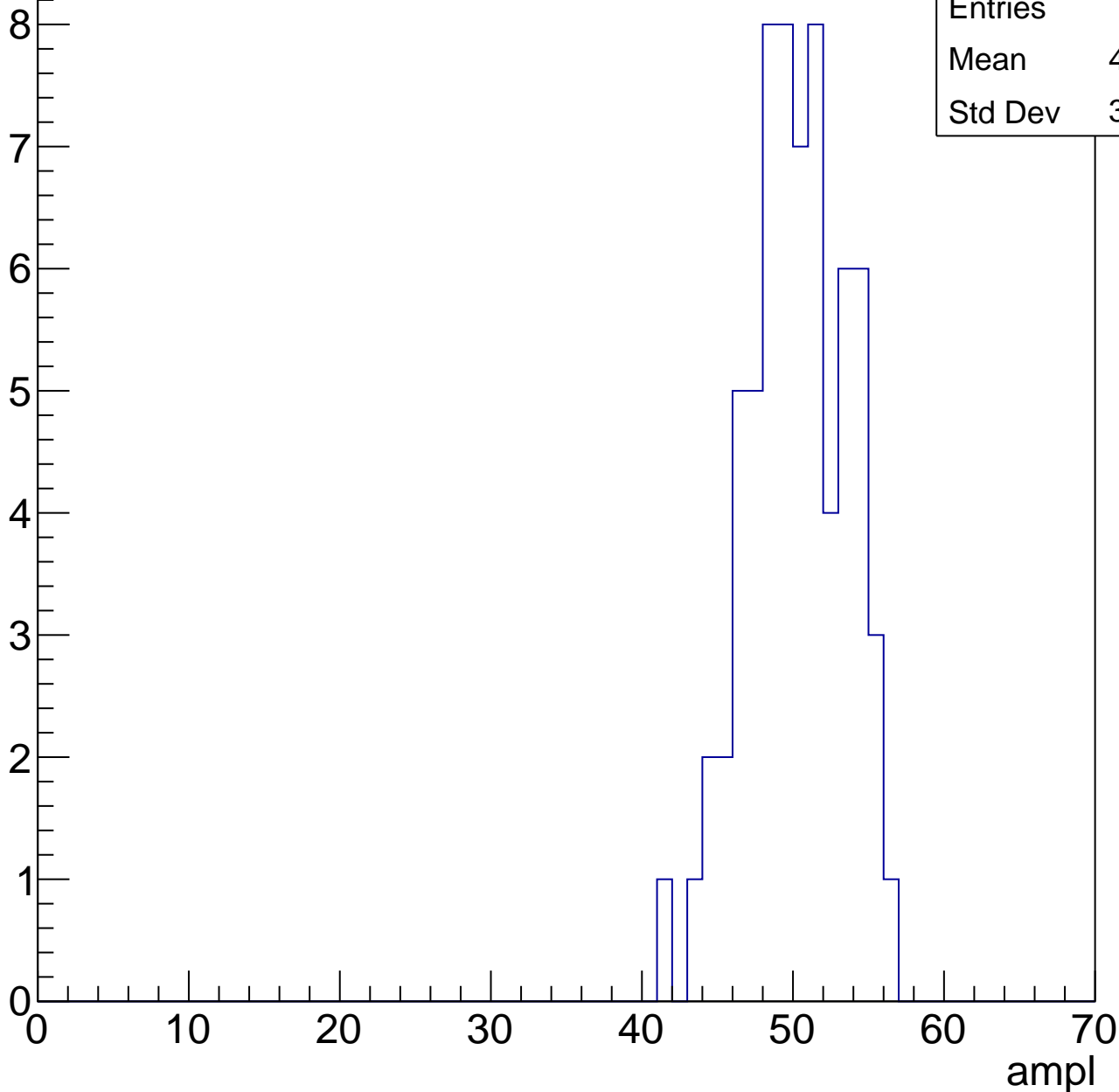


B0L001S, U25-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

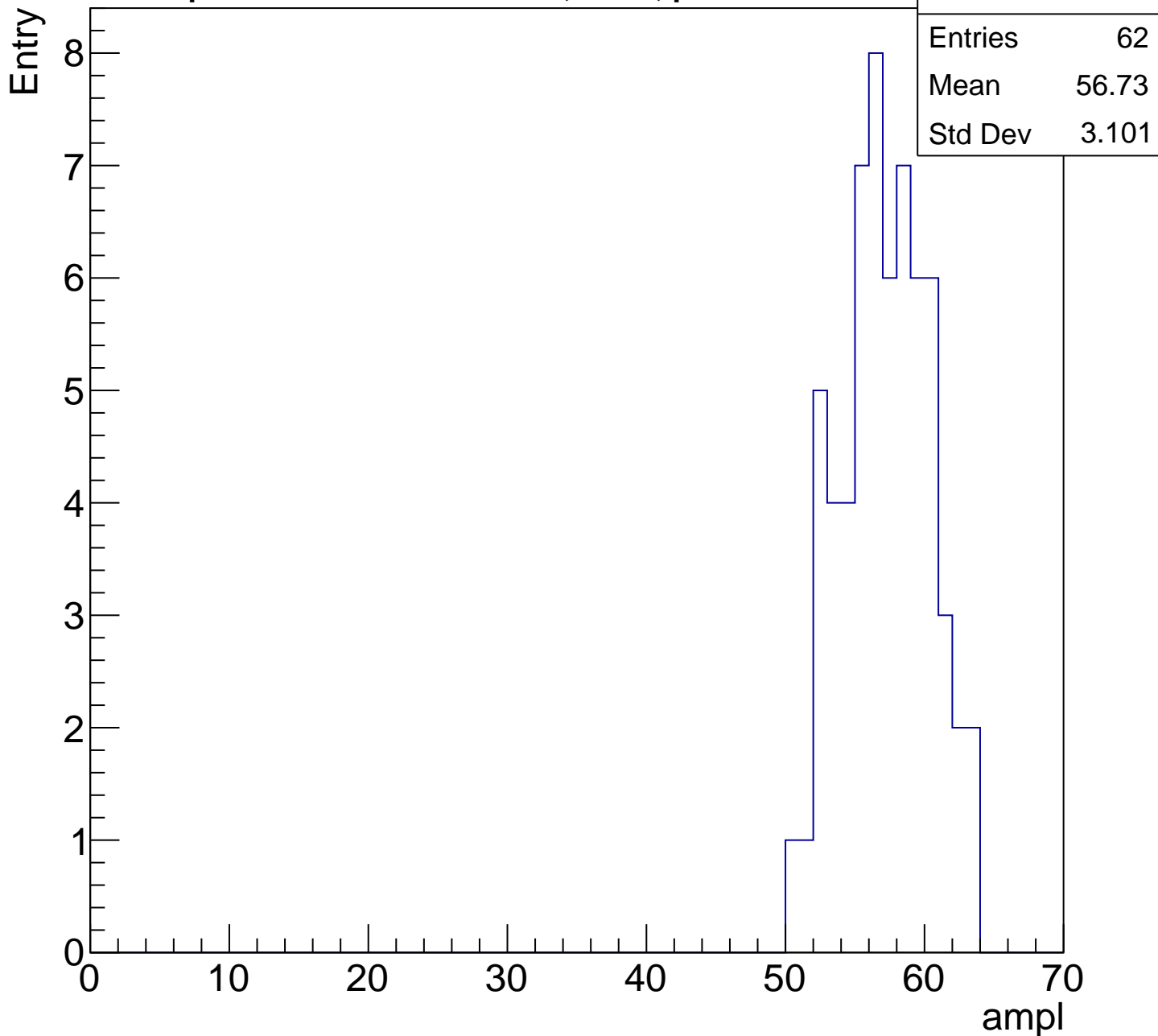
Entry

Entries	67
Mean	49.73
Std Dev	3.226



B0L001S, U25-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

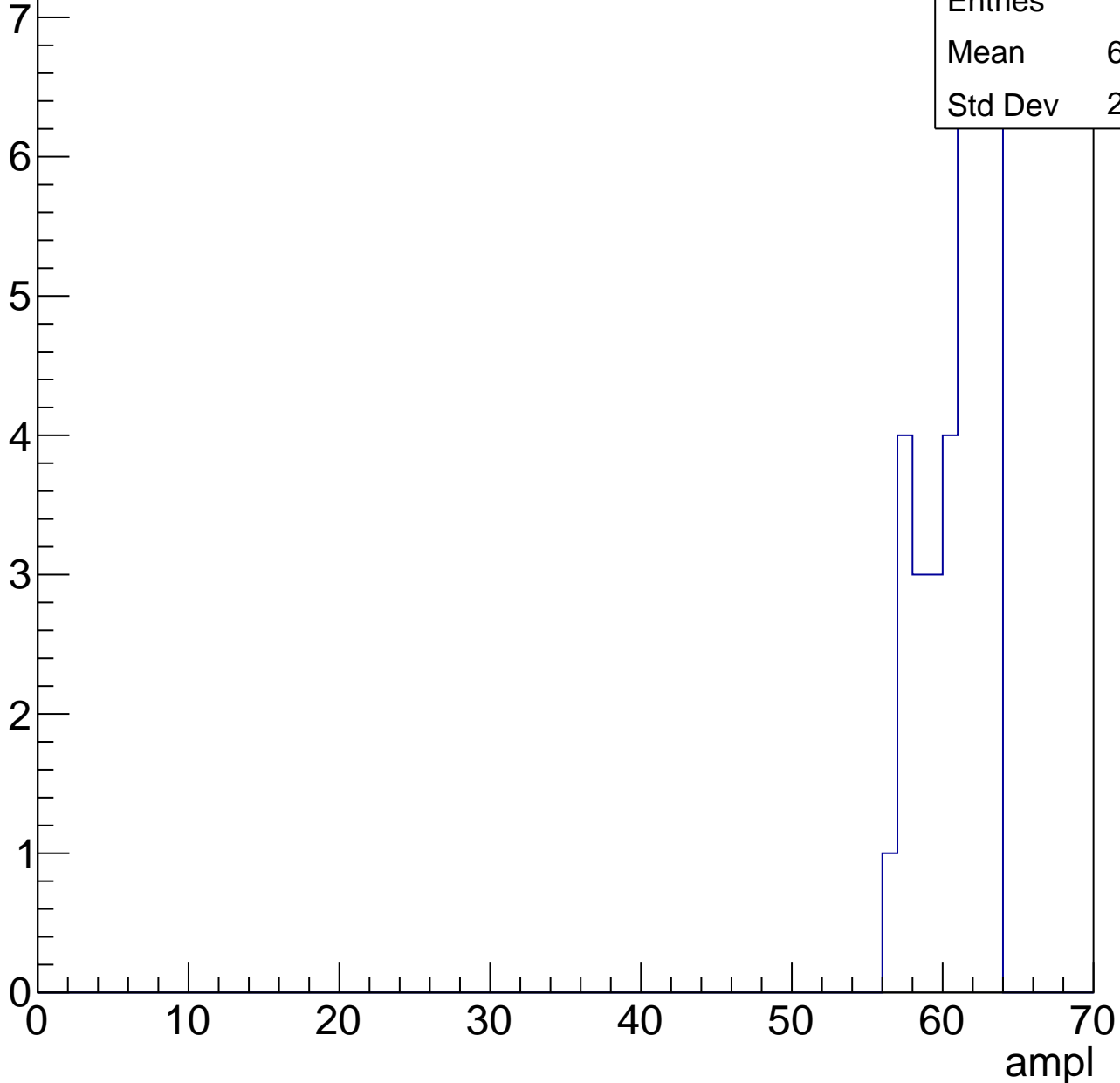


B0L001S, U25-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

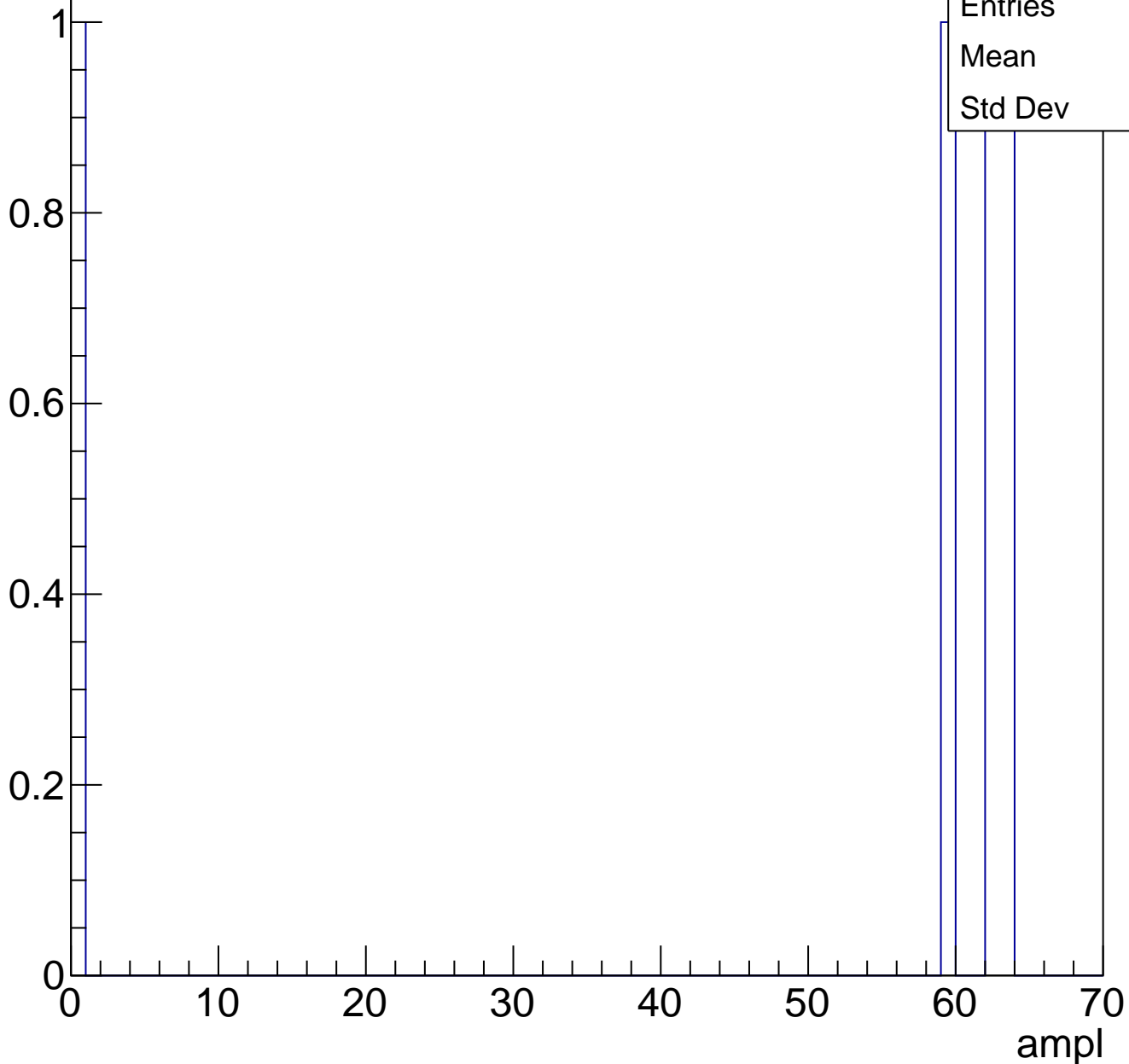
Entries	36
Mean	60.47
Std Dev	2.088



B0L001S, U25-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch25, adc0

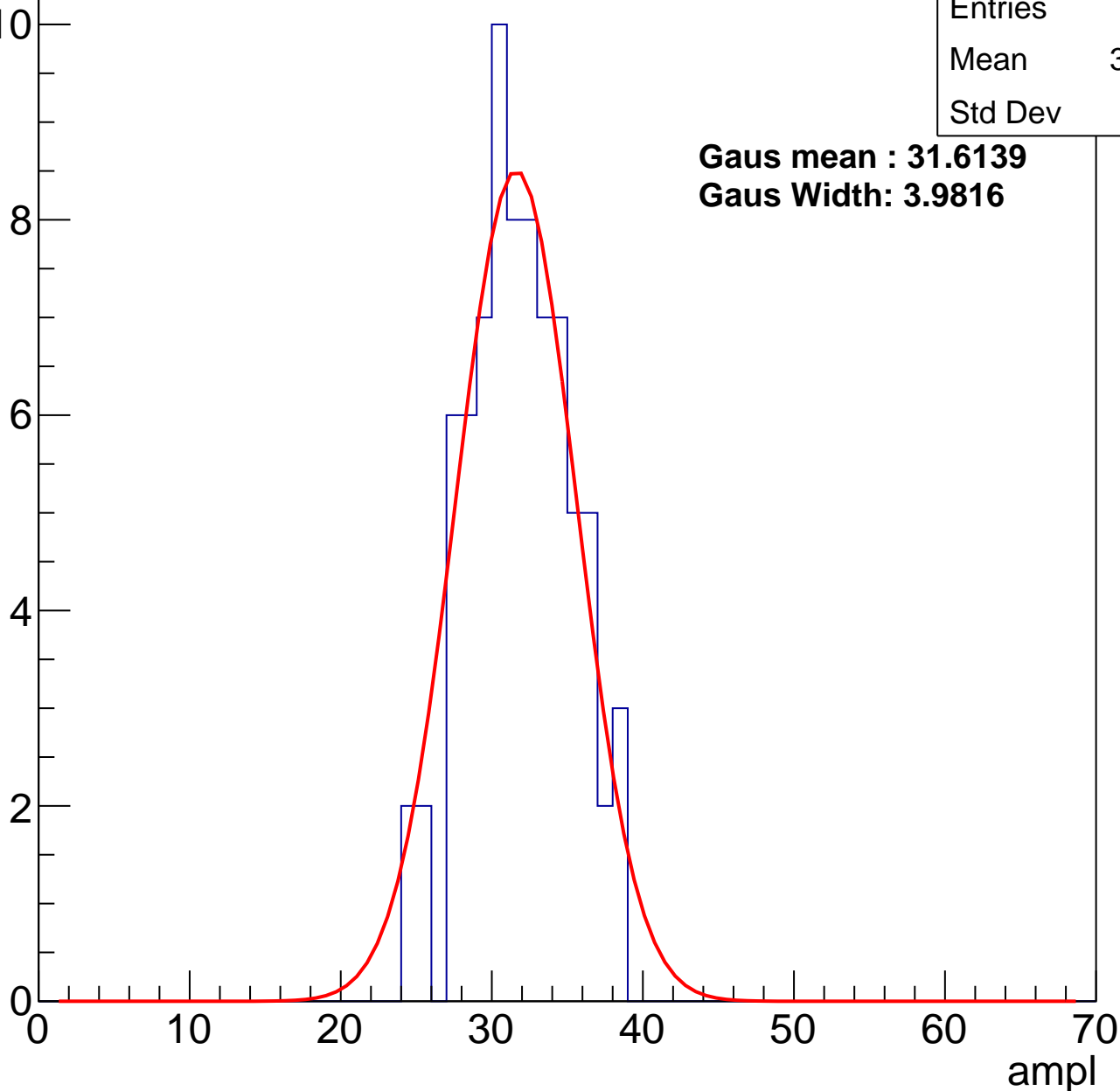
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	31.37
Std Dev	3.34

Gaus mean : 31.6139

Gaus Width: 3.9816



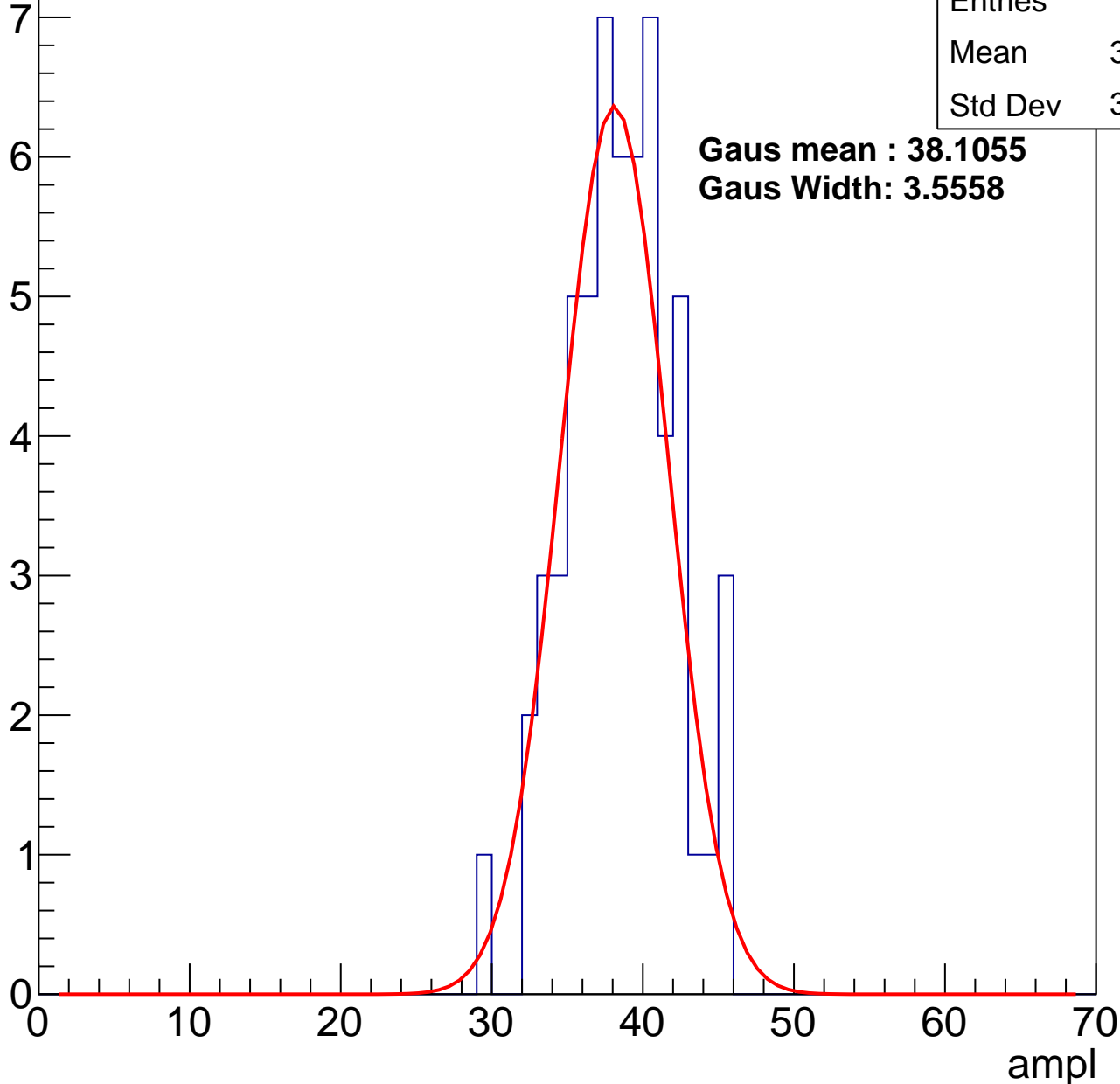
B0L001S, U25-ch25, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.07
Std Dev	3.459

Gaus mean : 38.1055
Gaus Width: 3.5558



B0L001S, U25-ch25, adc2

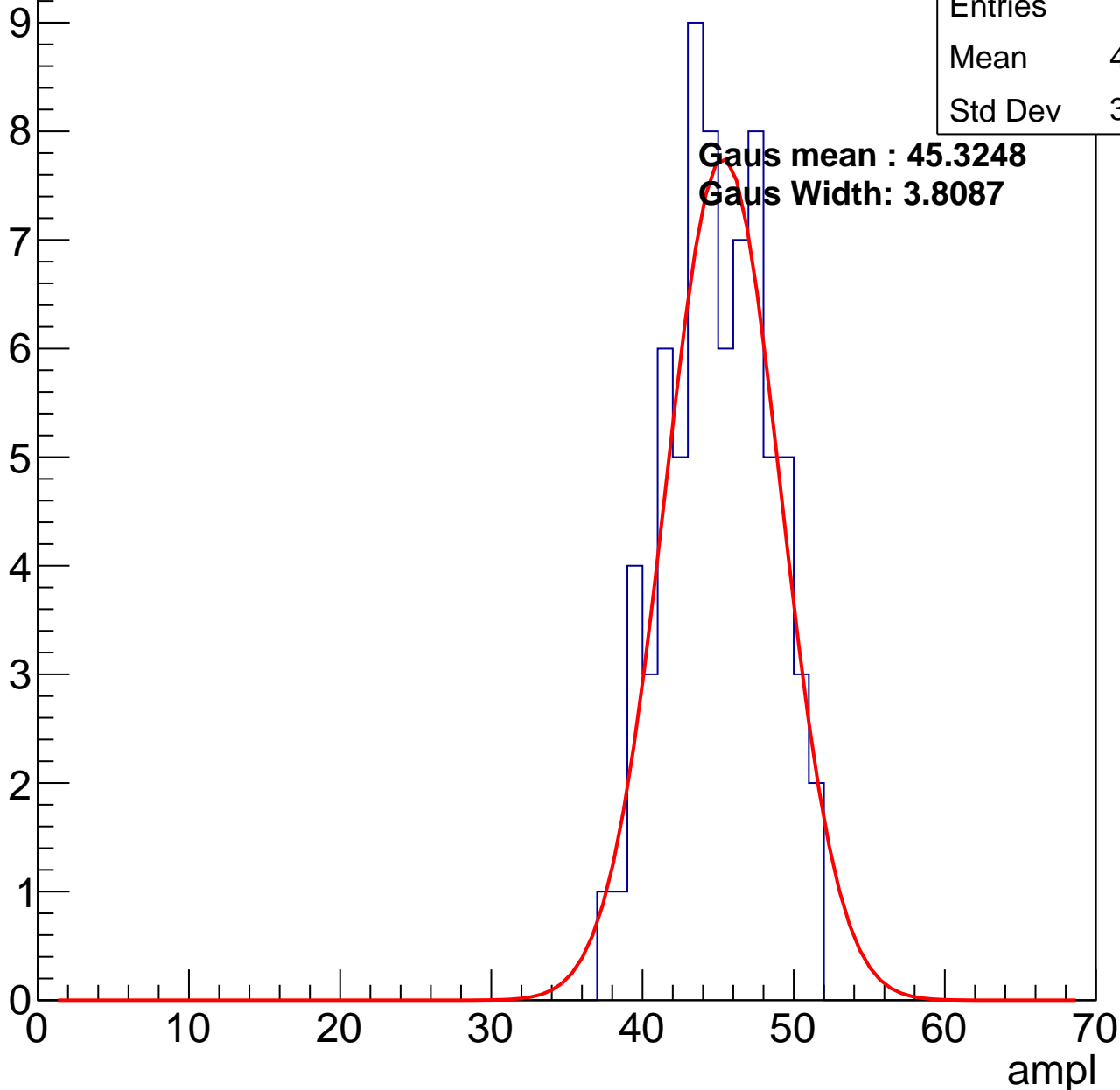
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.53
Std Dev	3.335

Gaus mean : 45.3248

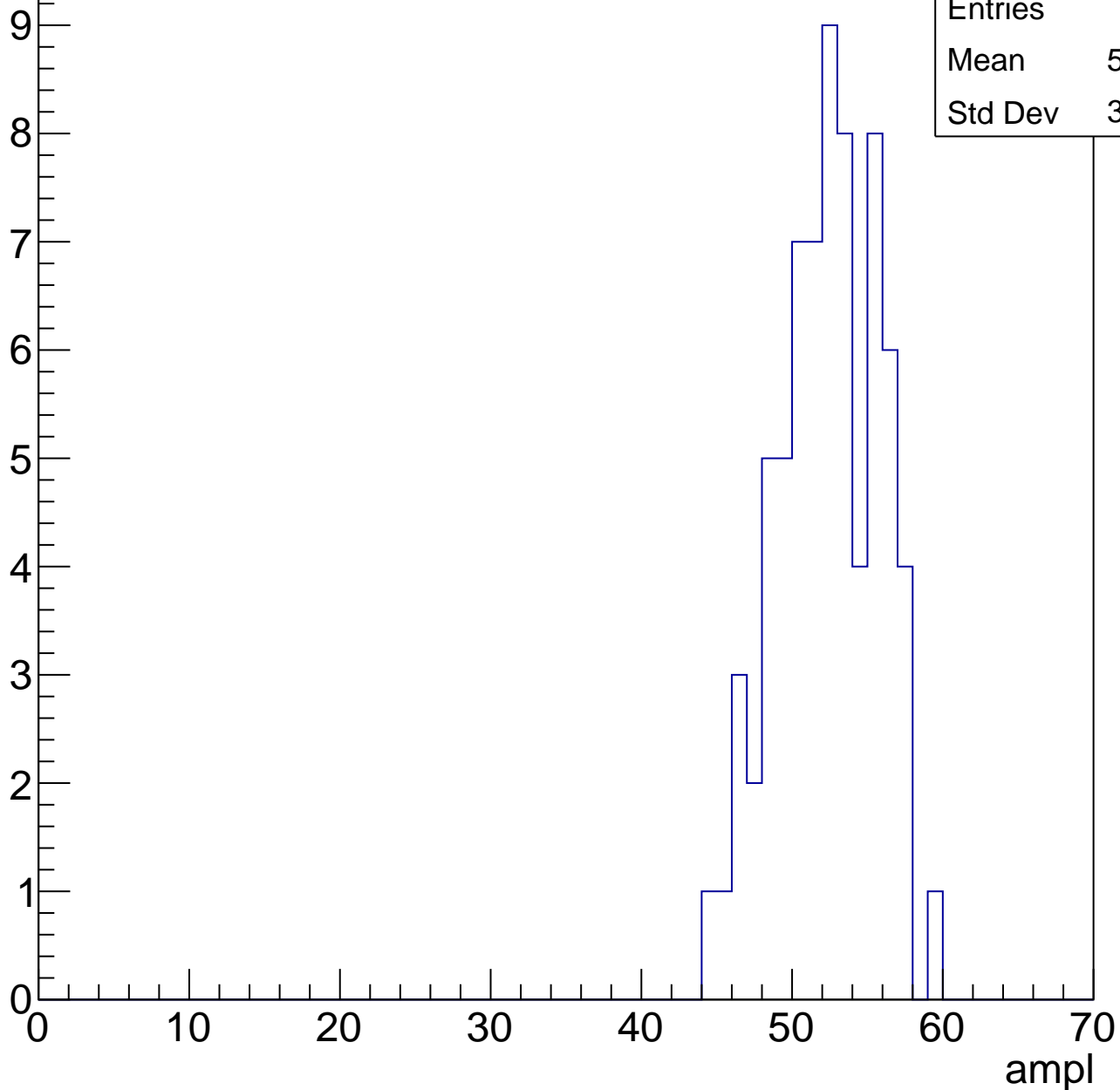
Gaus Width: 3.8087



B0L001S, U25-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

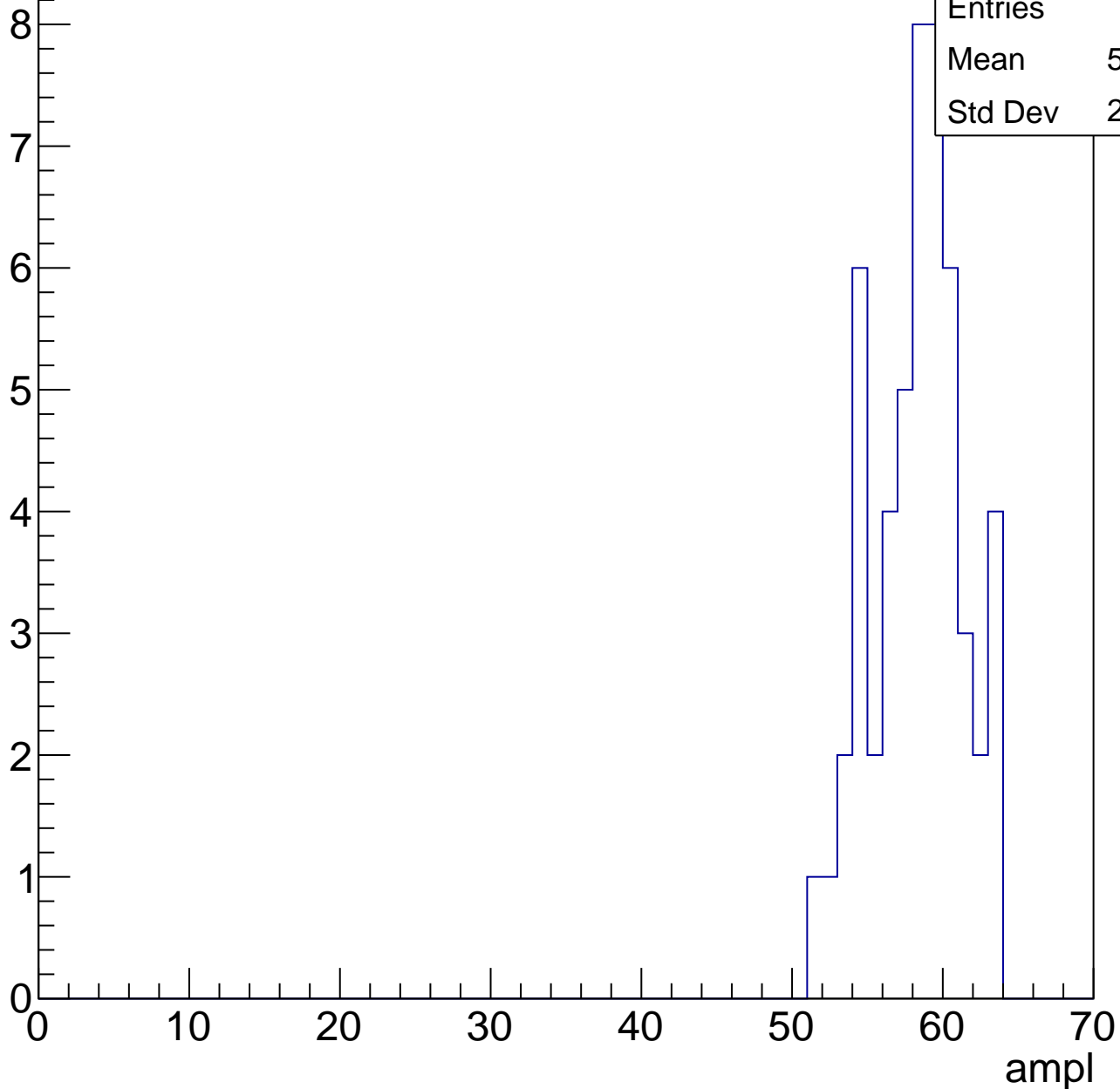
Entry



B0L001S, U25-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

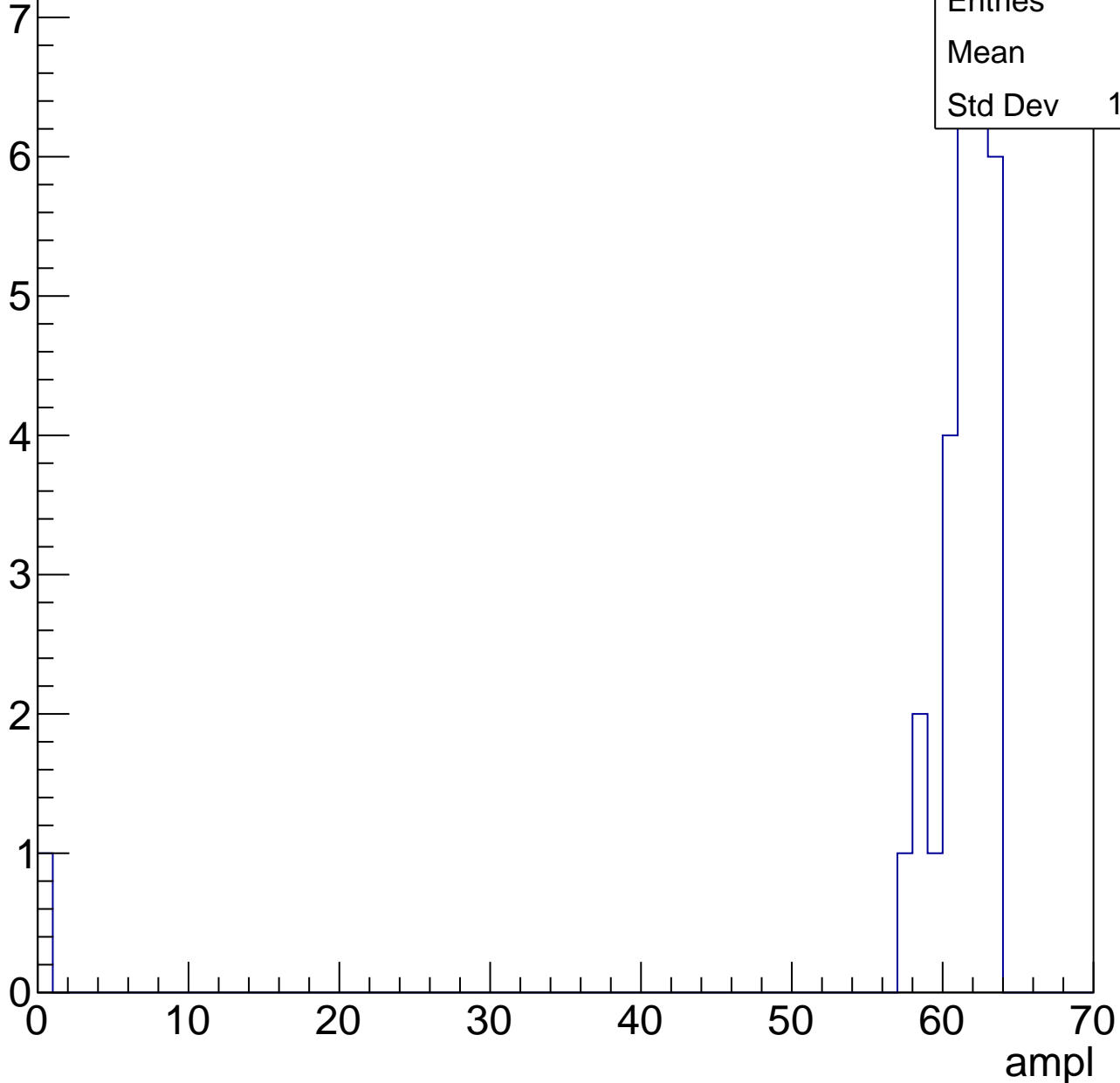


B0L001S, U25-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59
Std Dev	11.26



B0L001S, U25-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch26, adc0

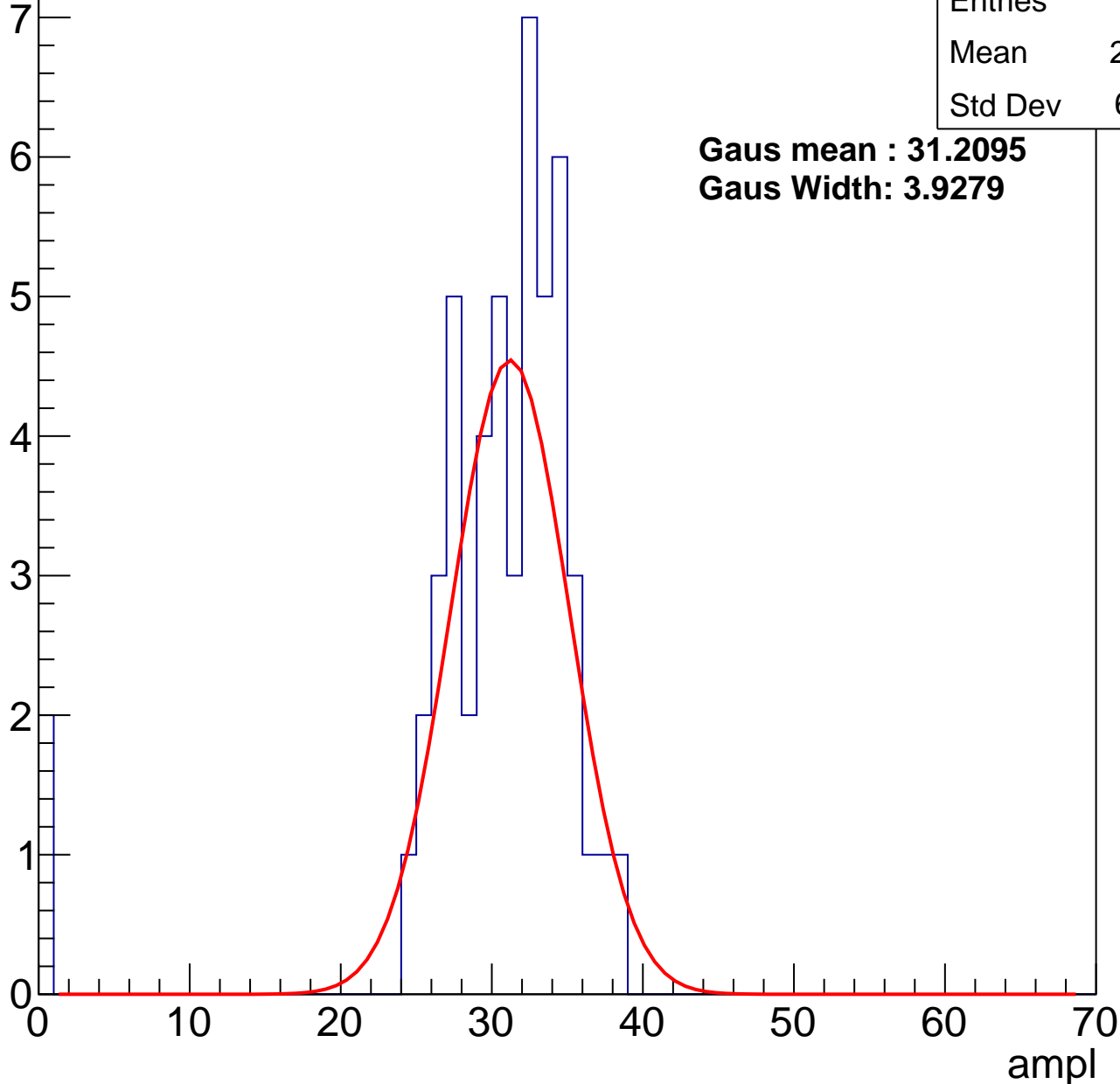
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	29.63
Std Dev	6.831

Gaus mean : 31.2095

Gaus Width: 3.9279



B0L001S, U25-ch26, adc1

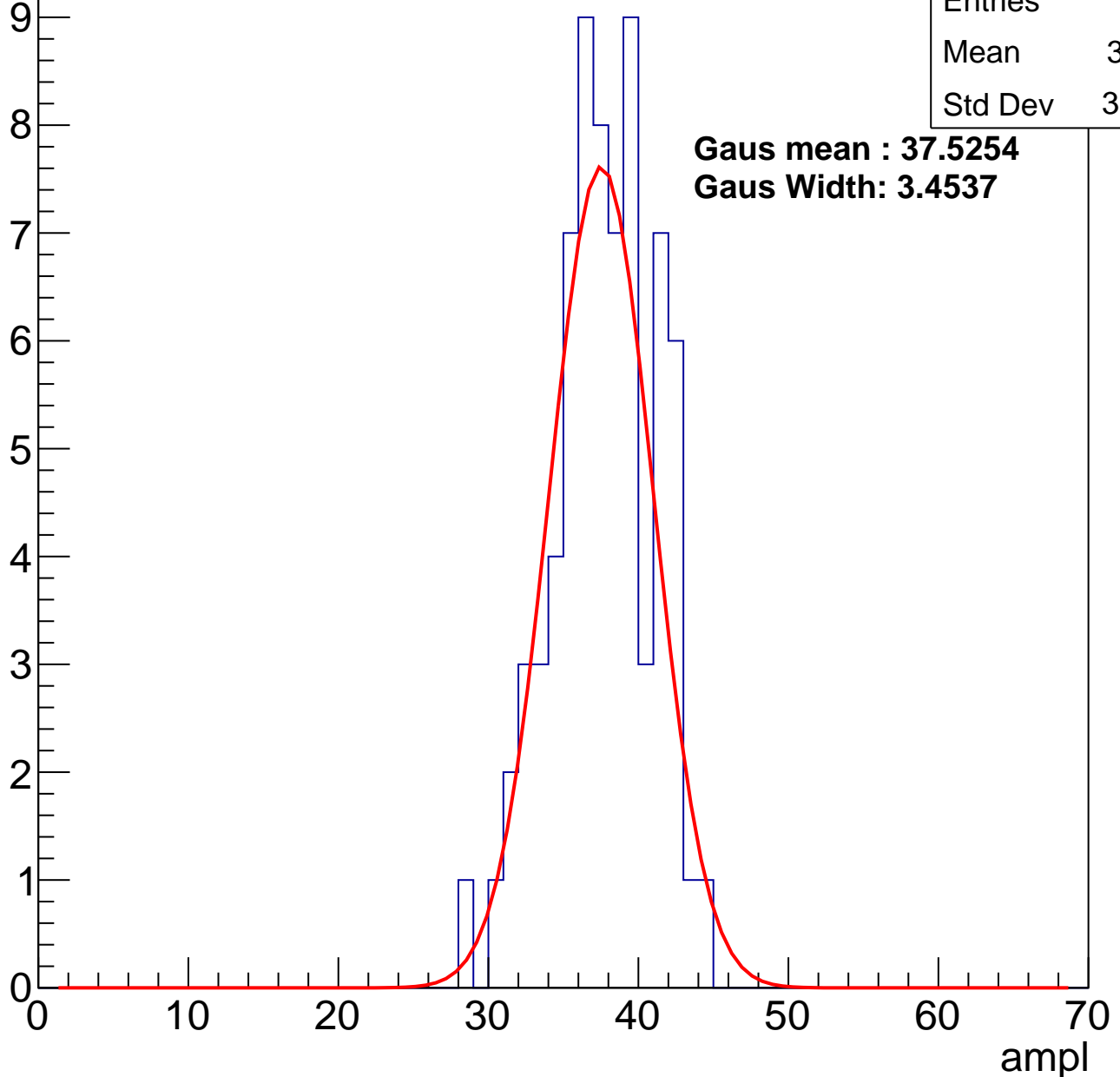
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.21
Std Dev	3.358

Gaus mean : 37.5254

Gaus Width: 3.4537

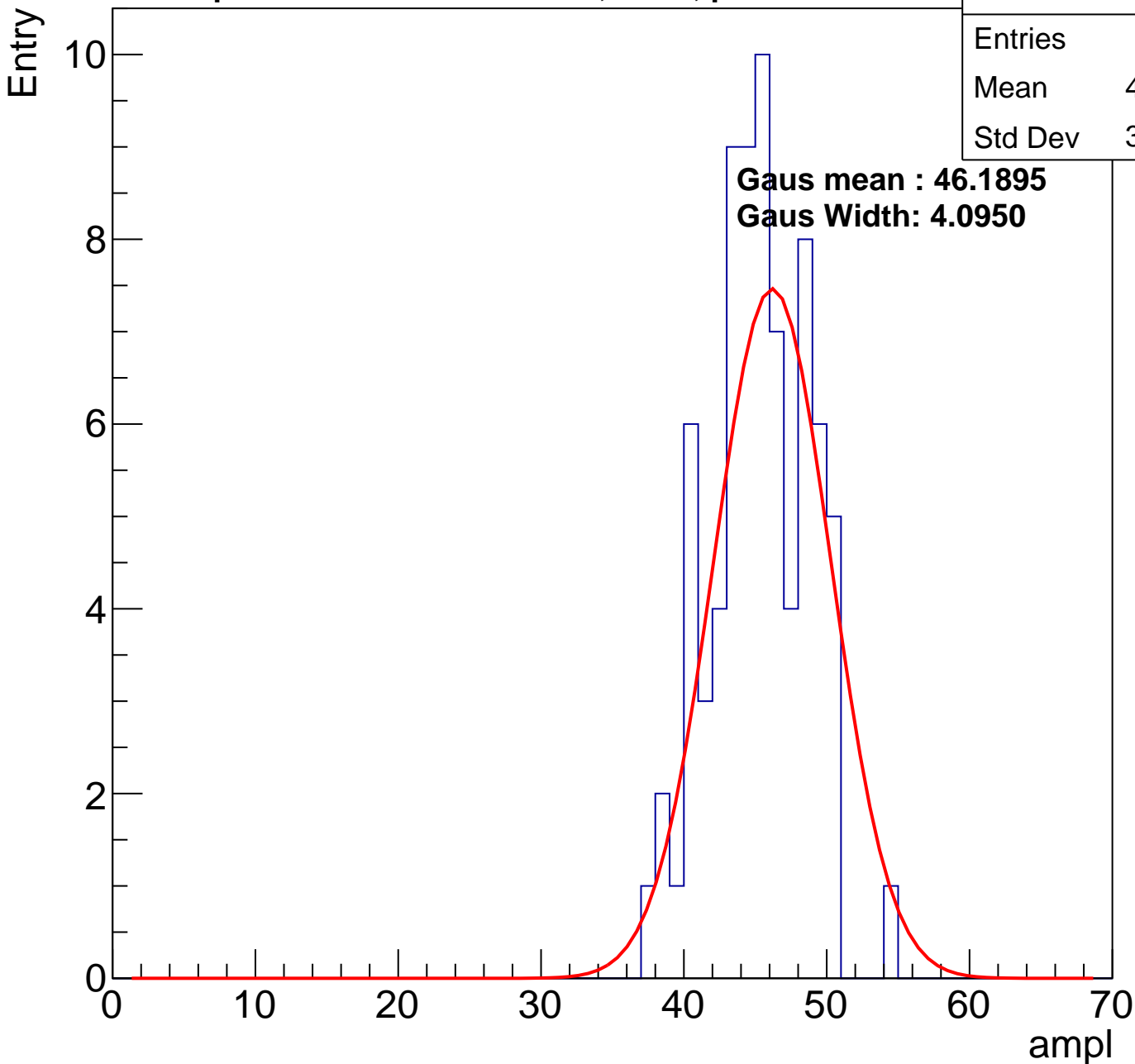


B0L001S, U25-ch26, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	44.84
Std Dev	3.387

Gaus mean : 46.1895
Gaus Width: 4.0950

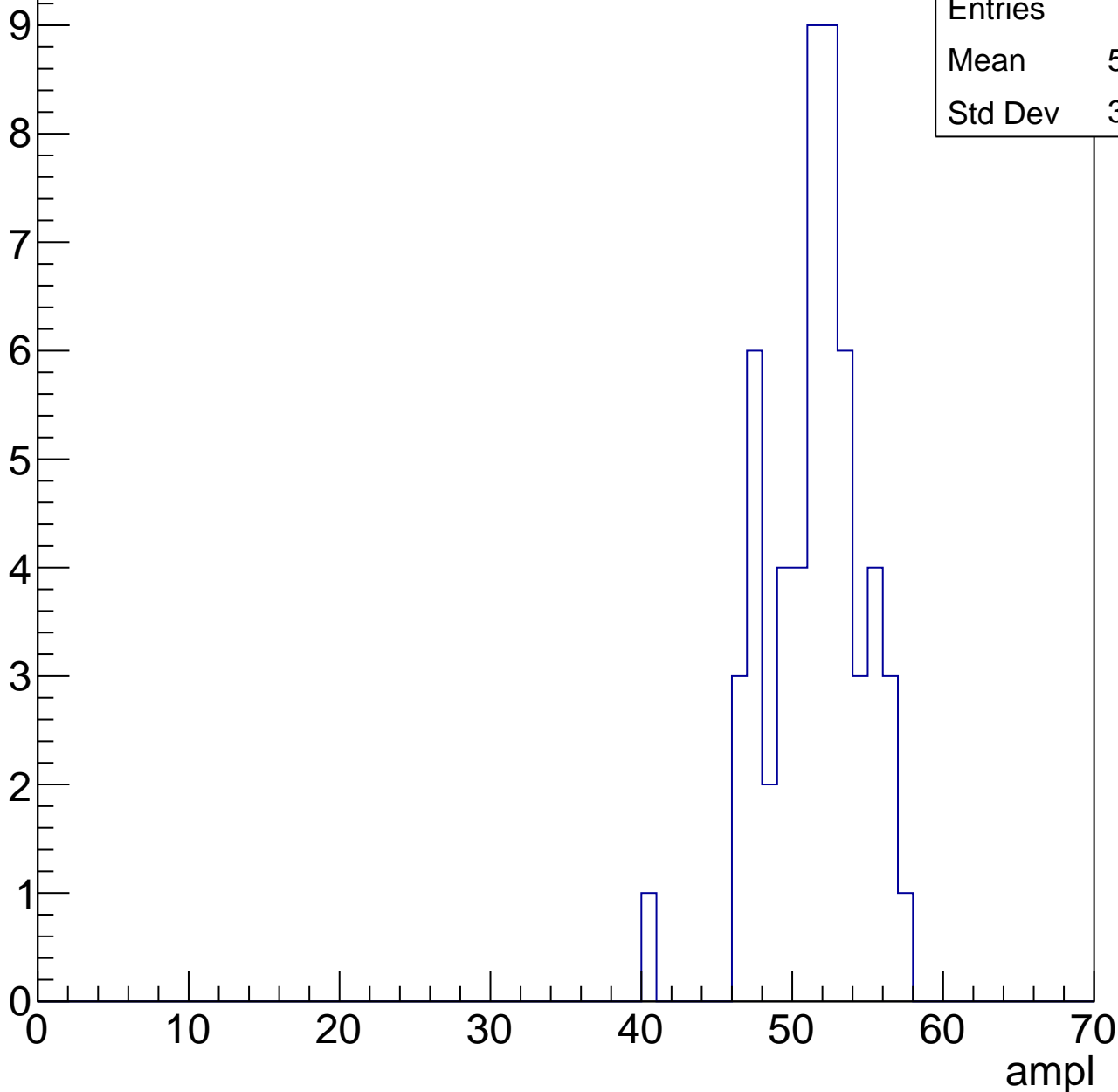


B0L001S, U25-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.98
Std Dev	3.205

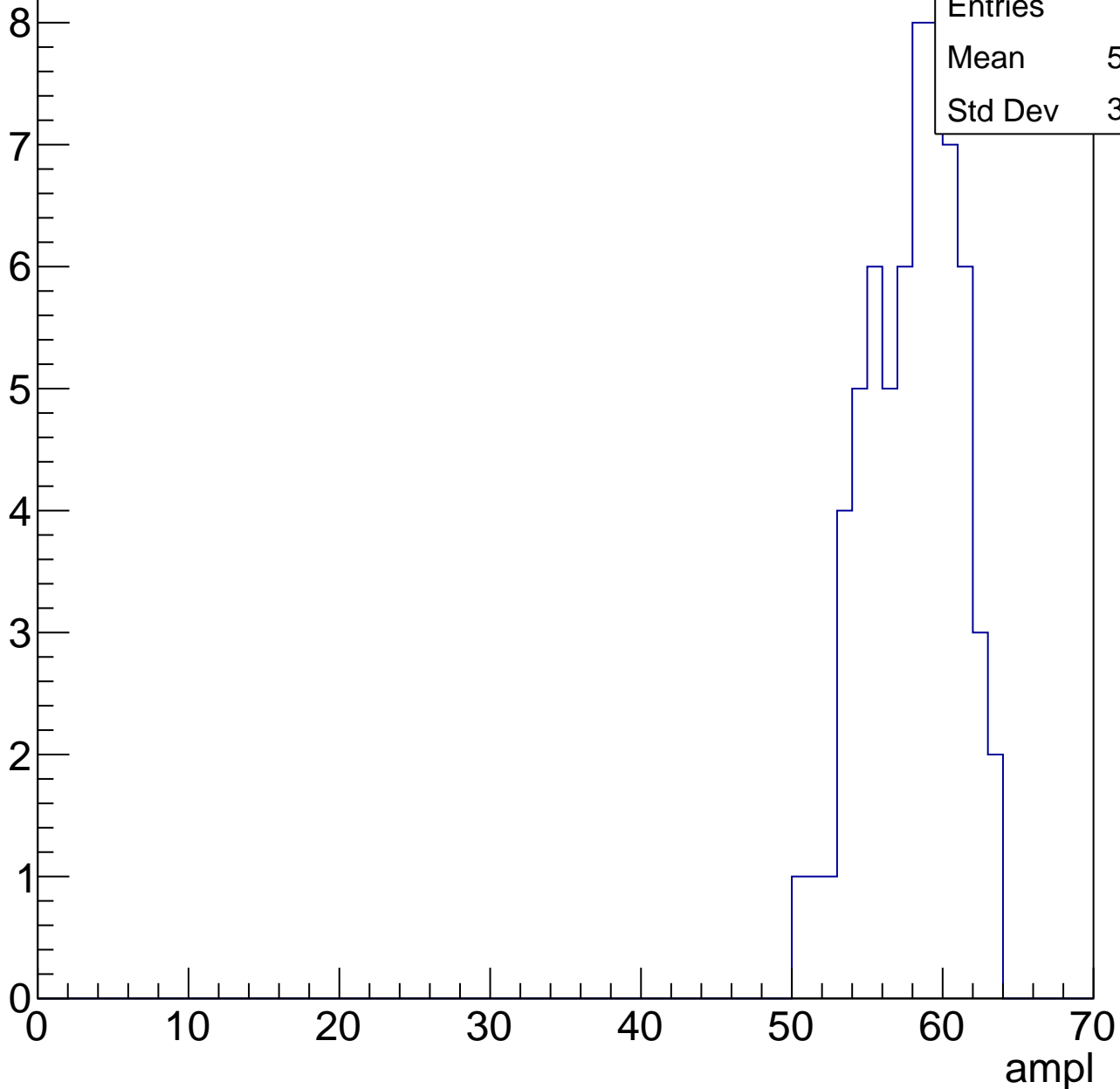


B0L001S, U25-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.48
Std Dev	3.033

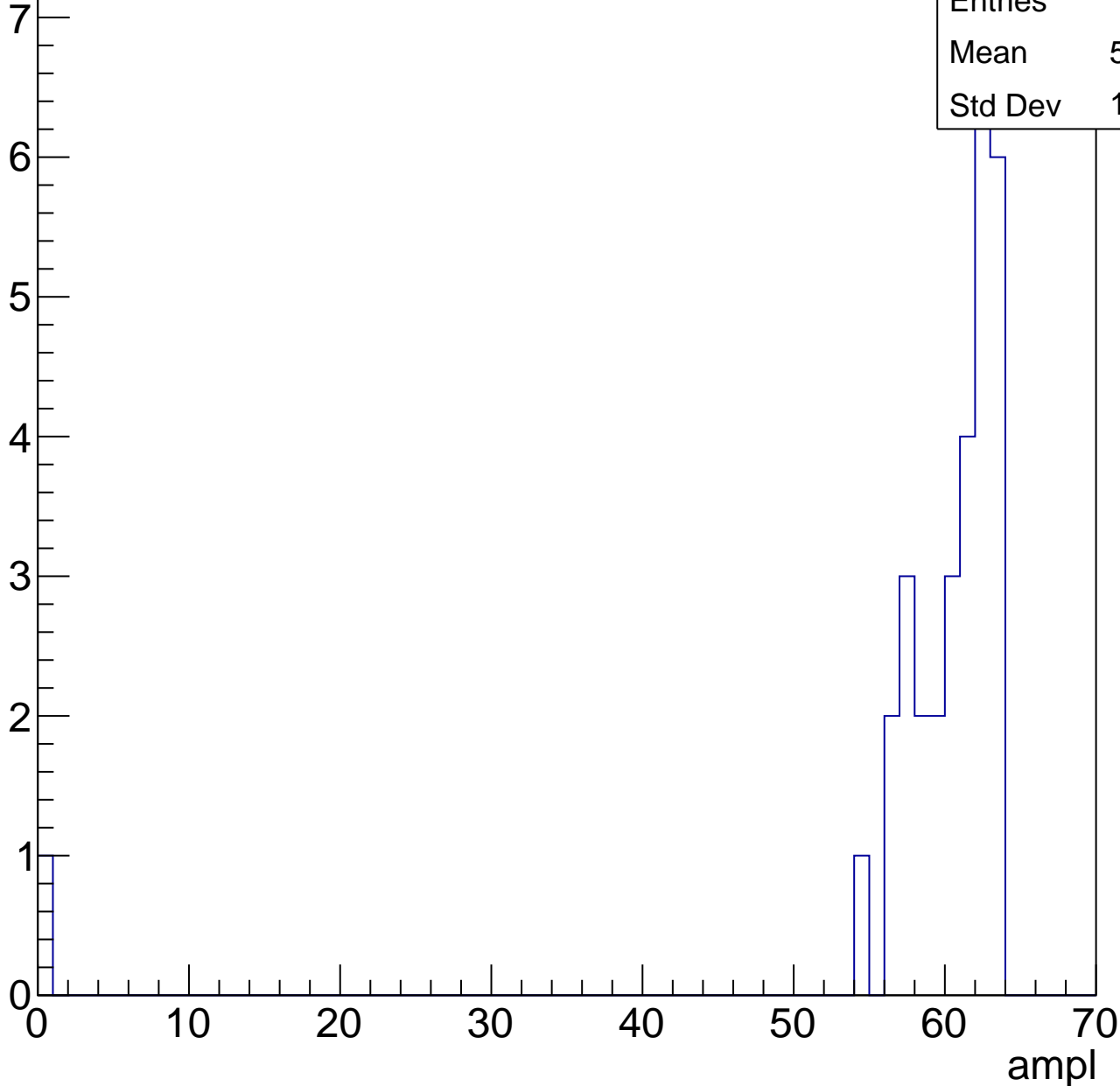


B0L001S, U25-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.29
Std Dev	10.93



B0L001S, U25-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch27, adc0

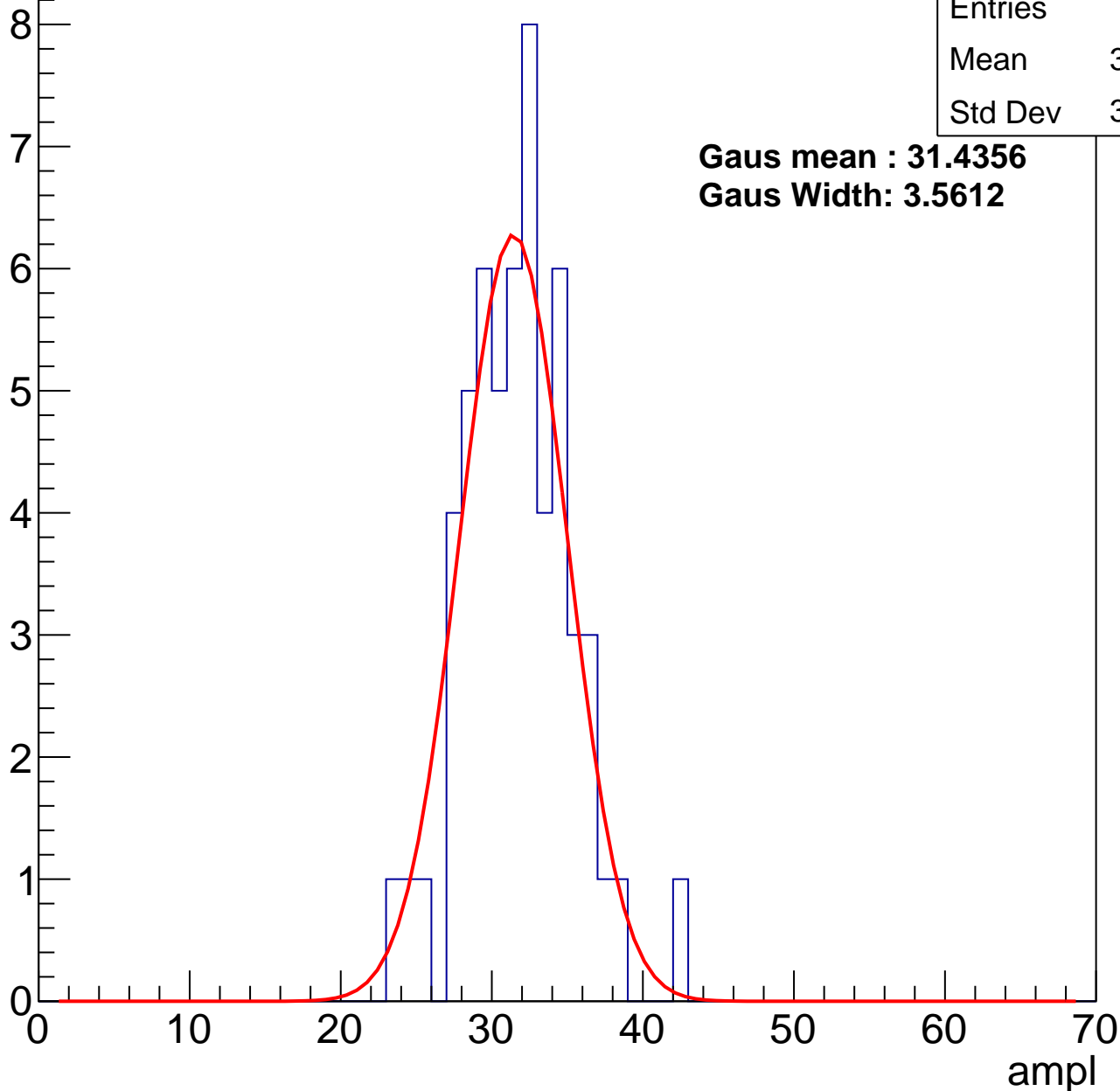
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	31.29
Std Dev	3.509

Gaus mean : 31.4356

Gaus Width: 3.5612



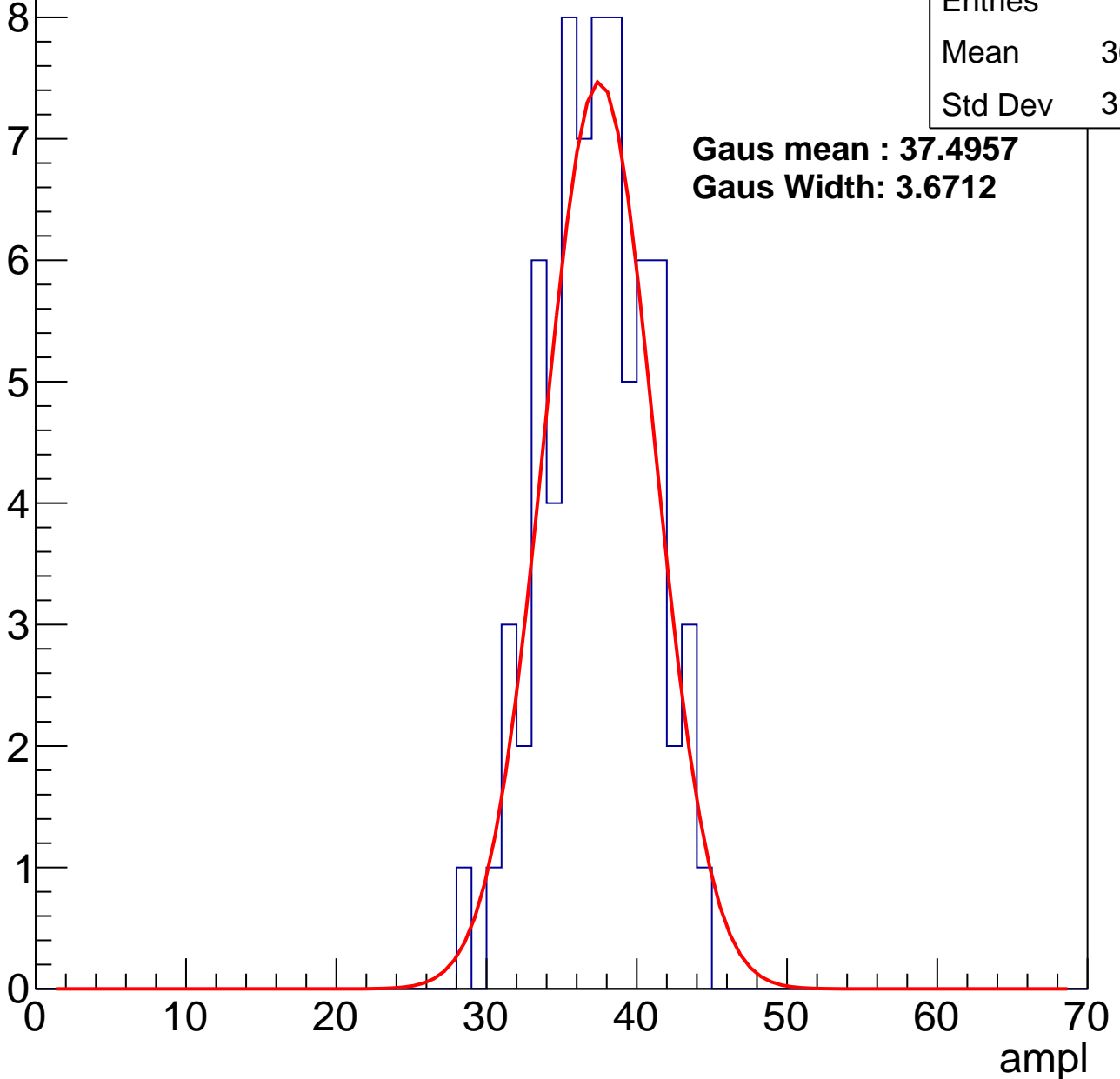
B0L001S, U25-ch27, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.89
Std Dev	3.446

Gaus mean : 37.4957
Gaus Width: 3.6712



B0L001S, U25-ch27, adc2

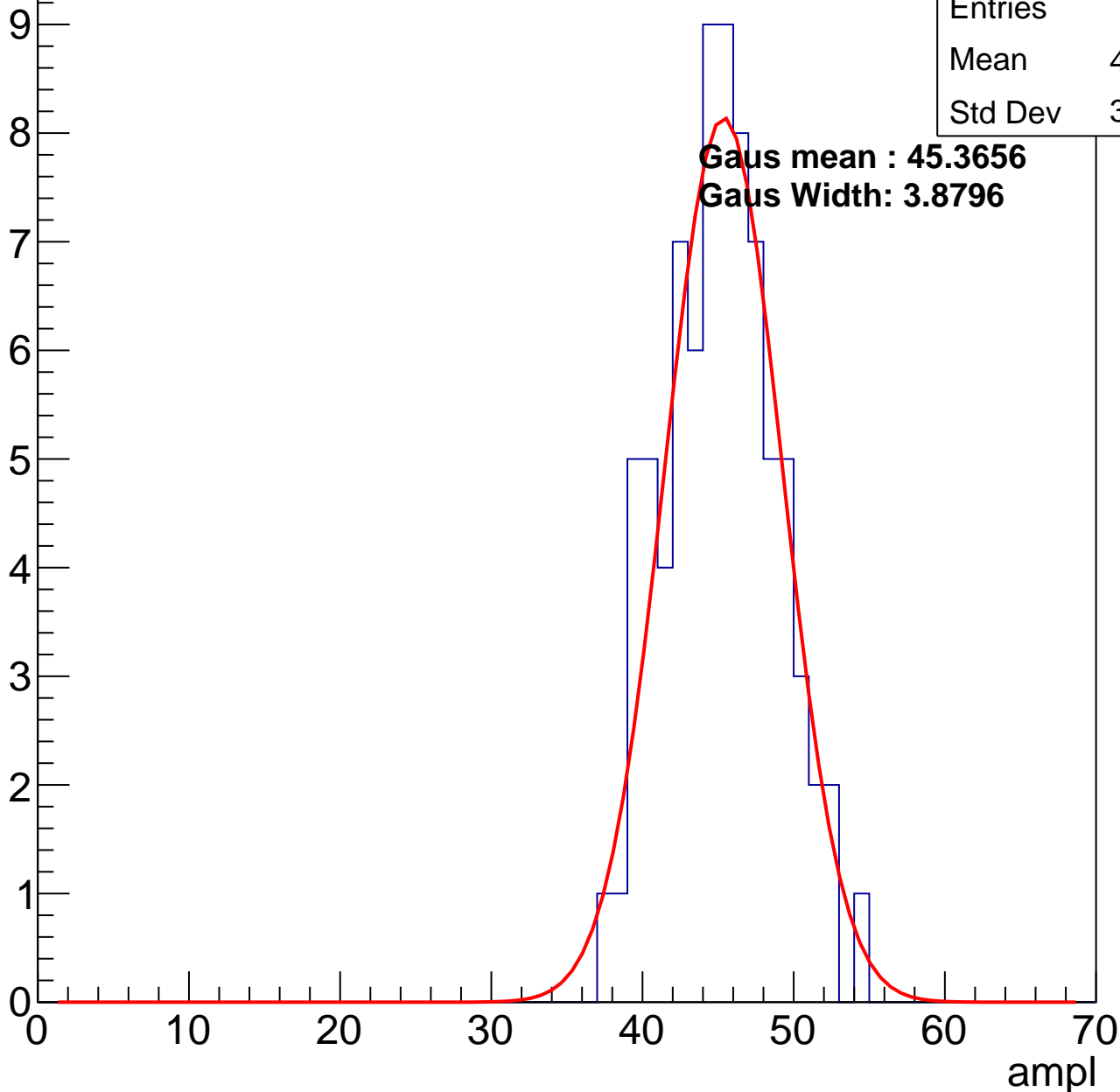
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	44.74
Std Dev	3.639

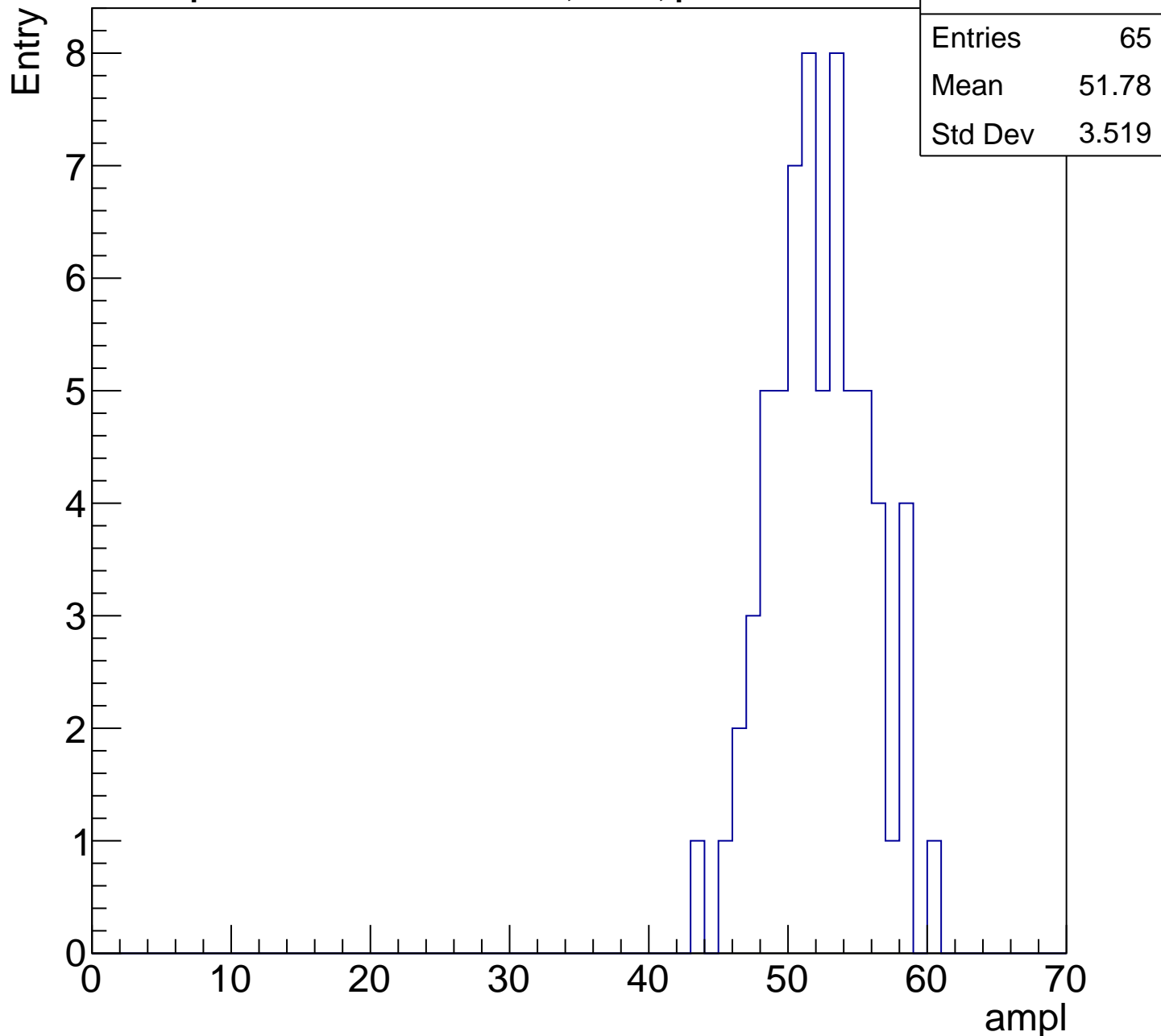
Gaus mean : 45.3656

Gaus Width: 3.8796



B0L001S, U25-ch27, adc3

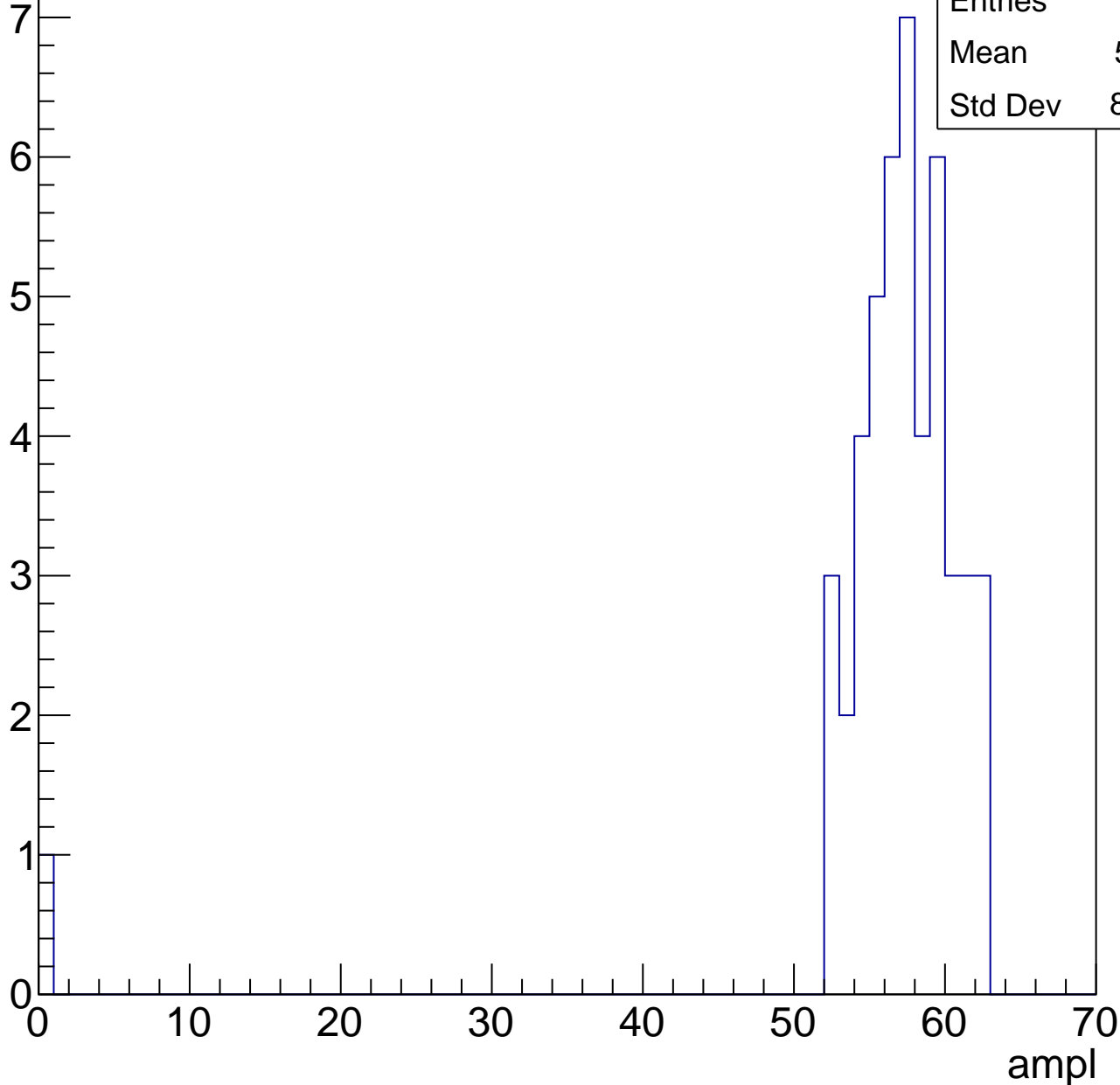
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



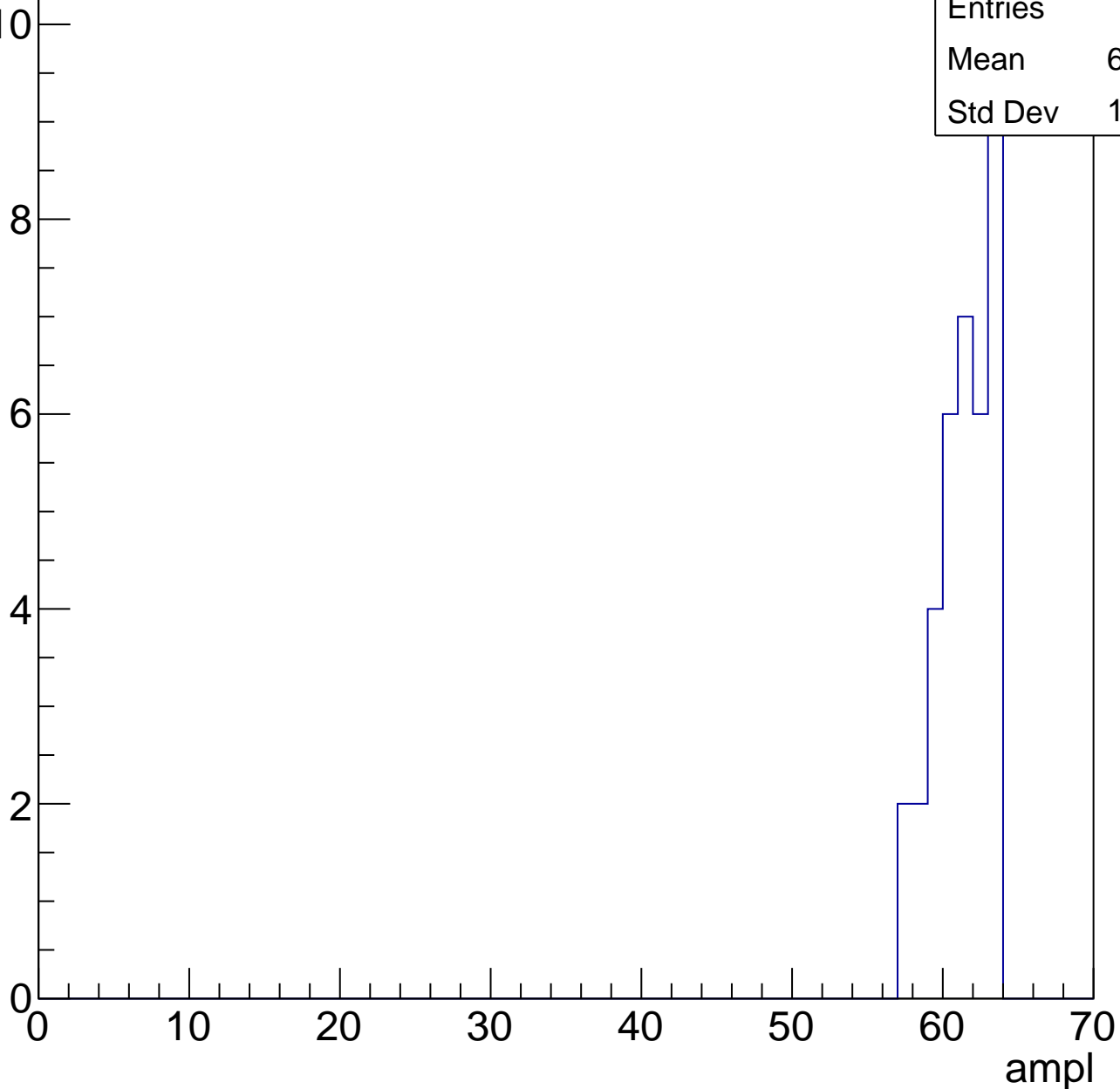
Entries	47
Mean	55.81
Std Dev	8.666

B0L001S, U25-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	60.95
Std Dev	1.785



B0L001S, U25-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch28, adc0

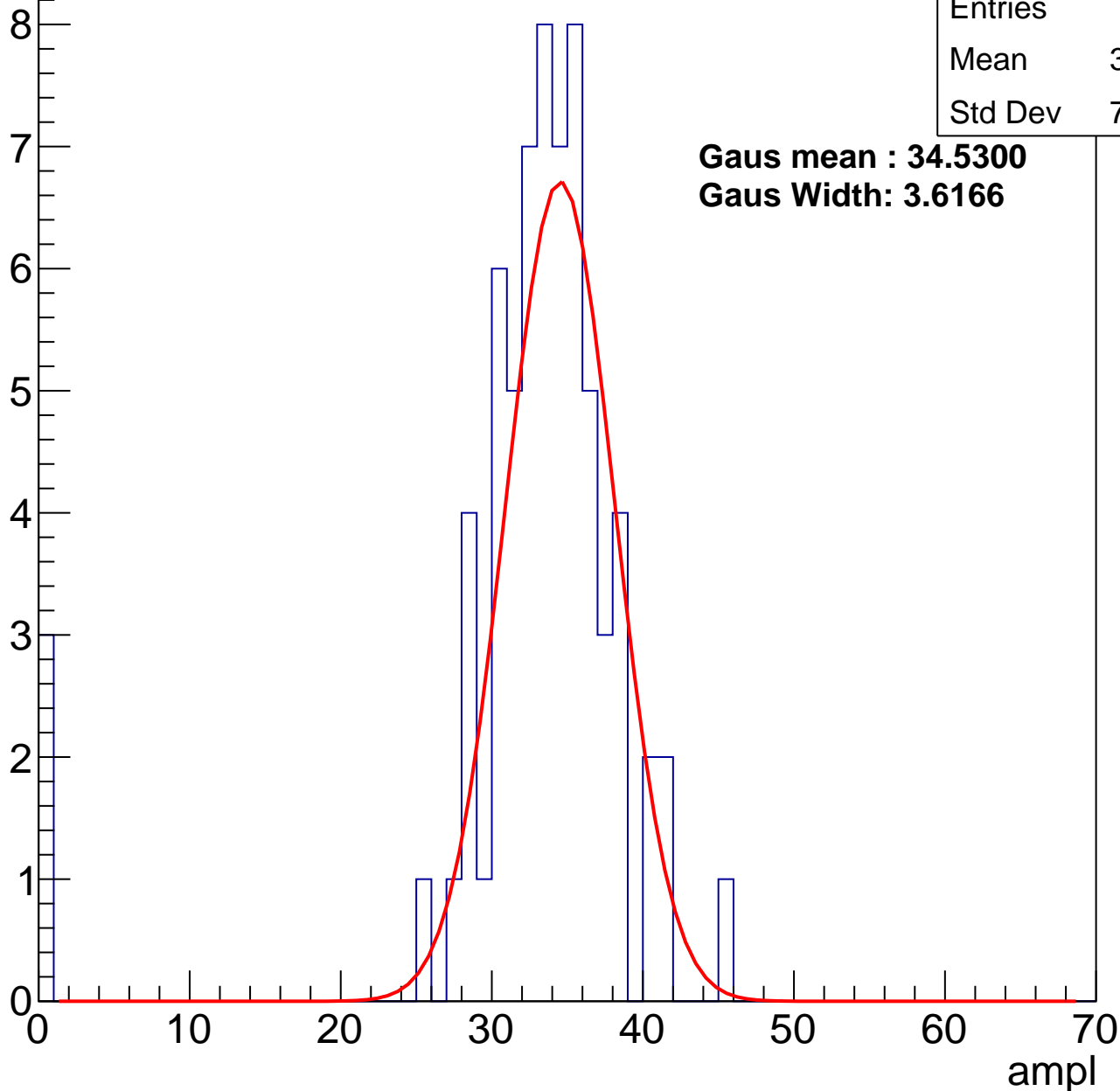
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	32.12
Std Dev	7.777

Gaus mean : 34.5300

Gaus Width: 3.6166



B0L001S, U25-ch28, adc1

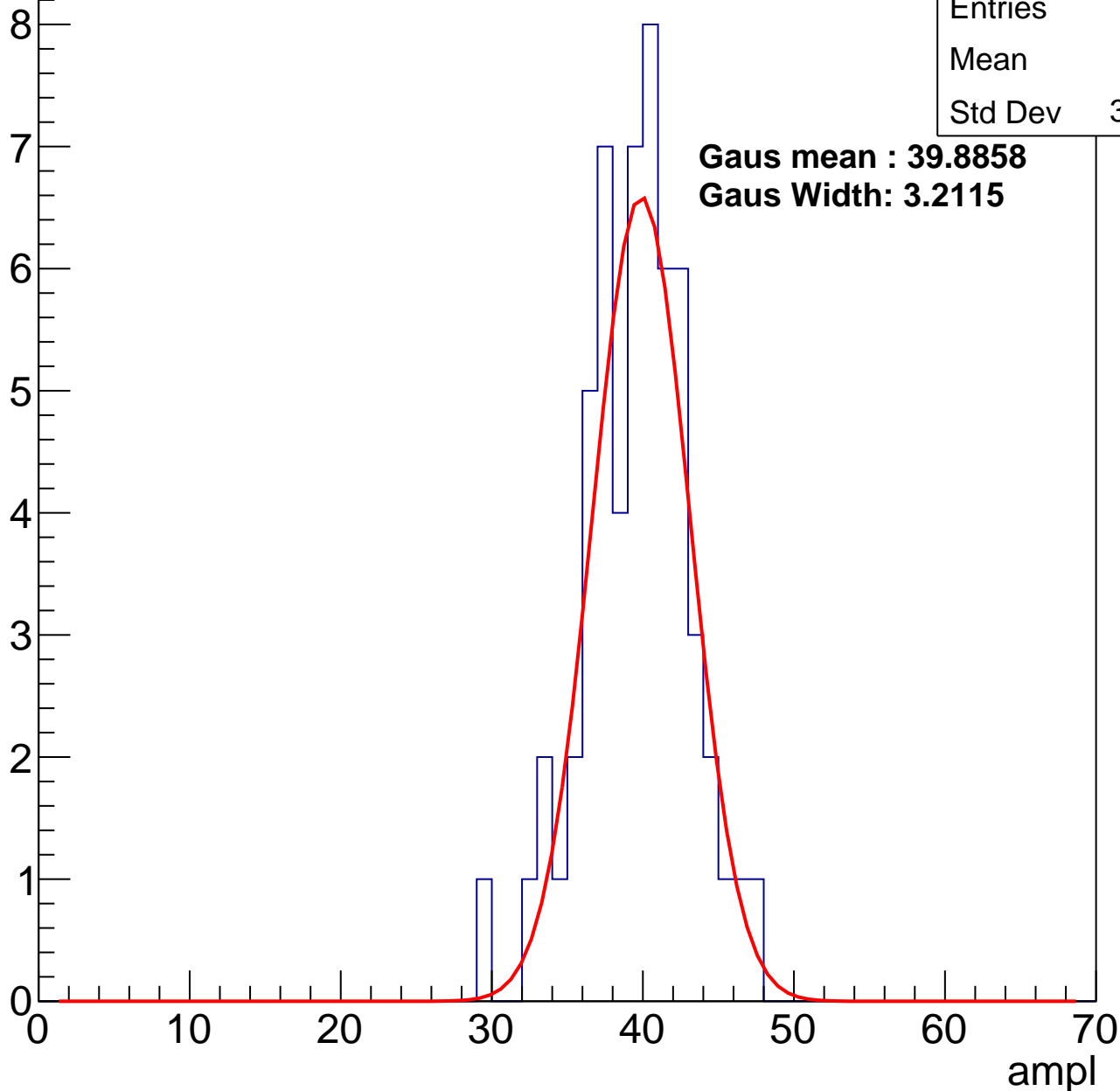
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	39.1
Std Dev	3.443

Gaus mean : 39.8858

Gaus Width: 3.2115



B0L001S, U25-ch28, adc2

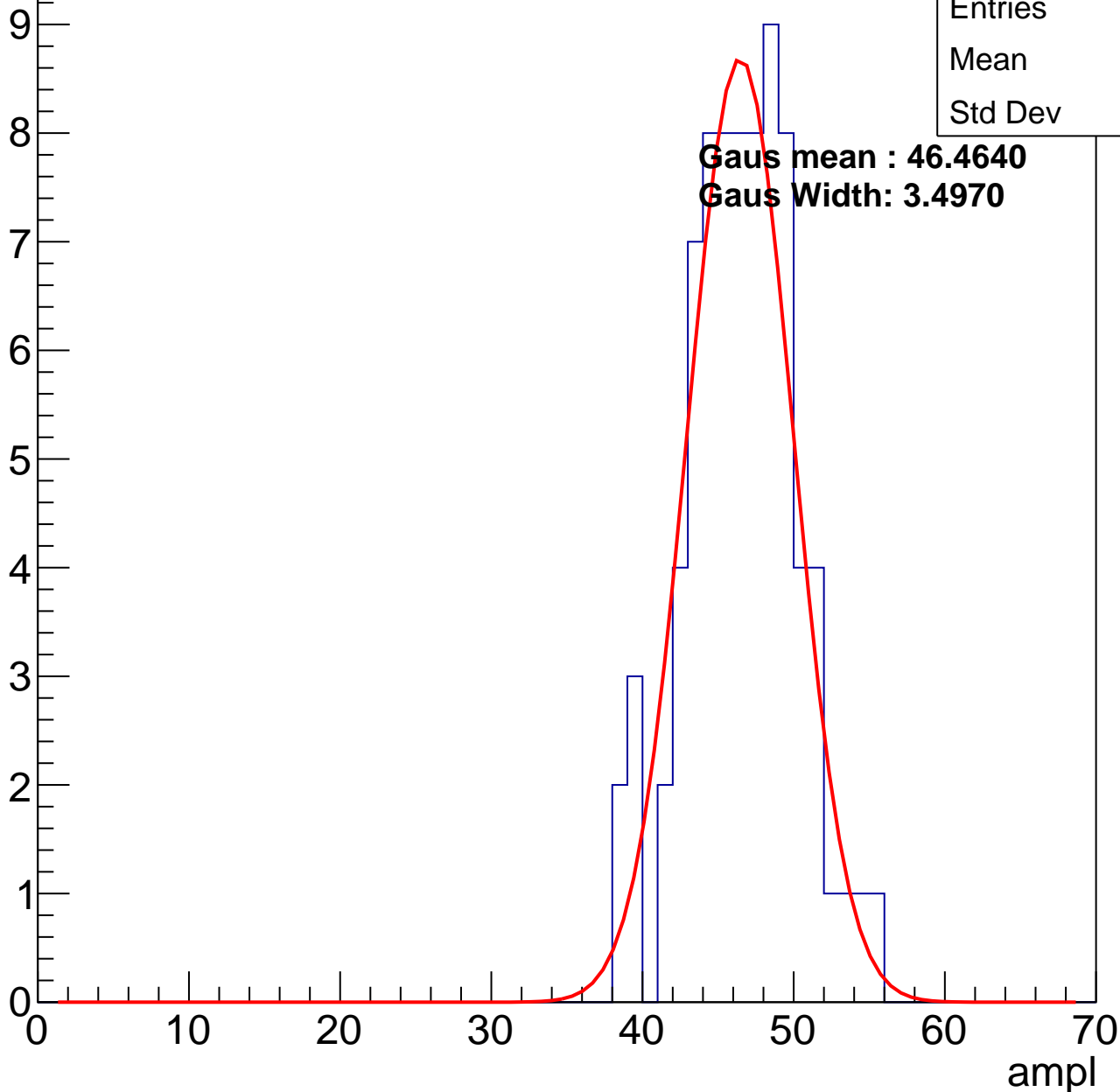
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	46.1
Std Dev	3.56

Gaus mean : 46.4640

Gaus Width: 3.4970

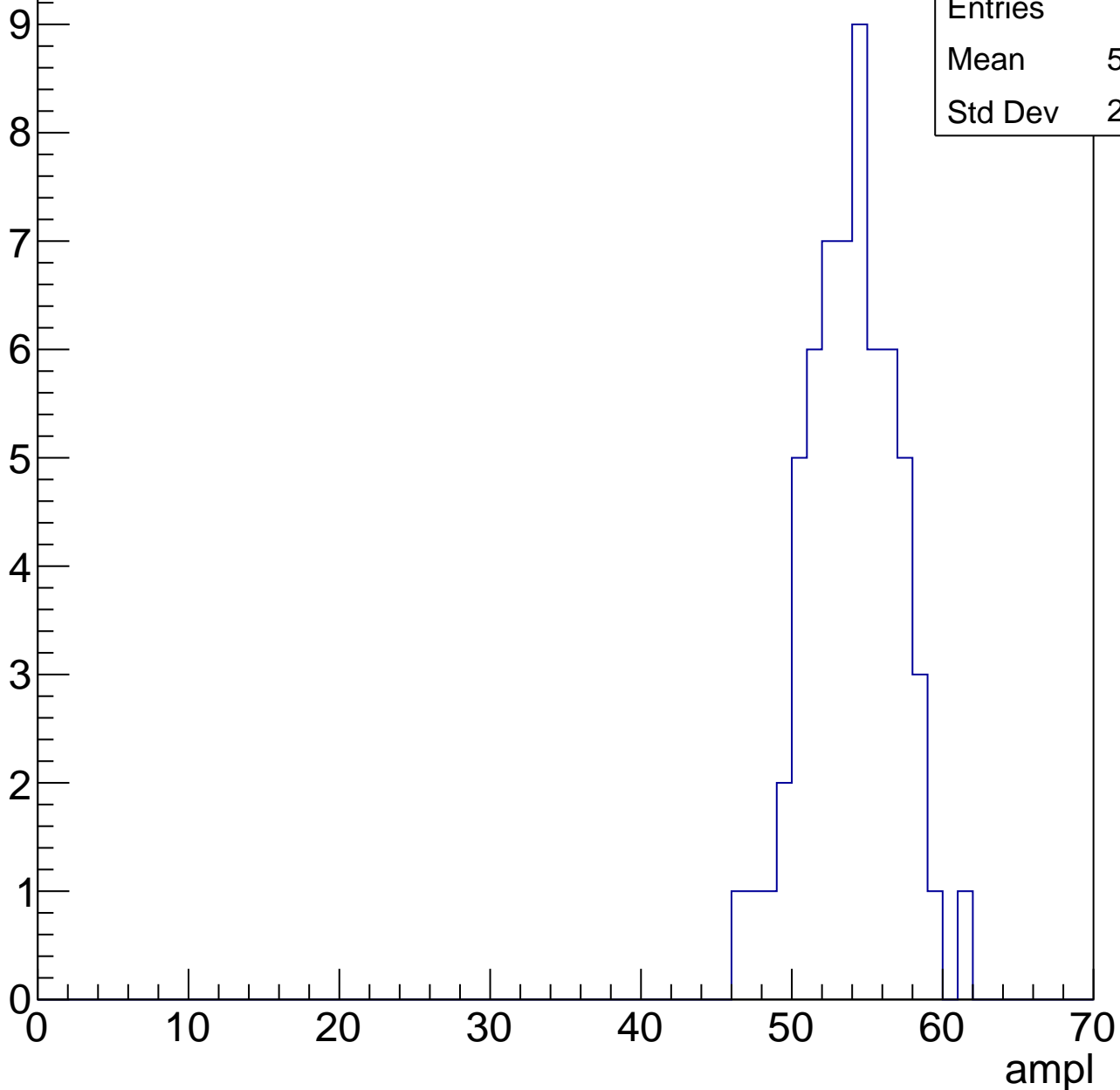


B0L001S, U25-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	53.46
Std Dev	2.995

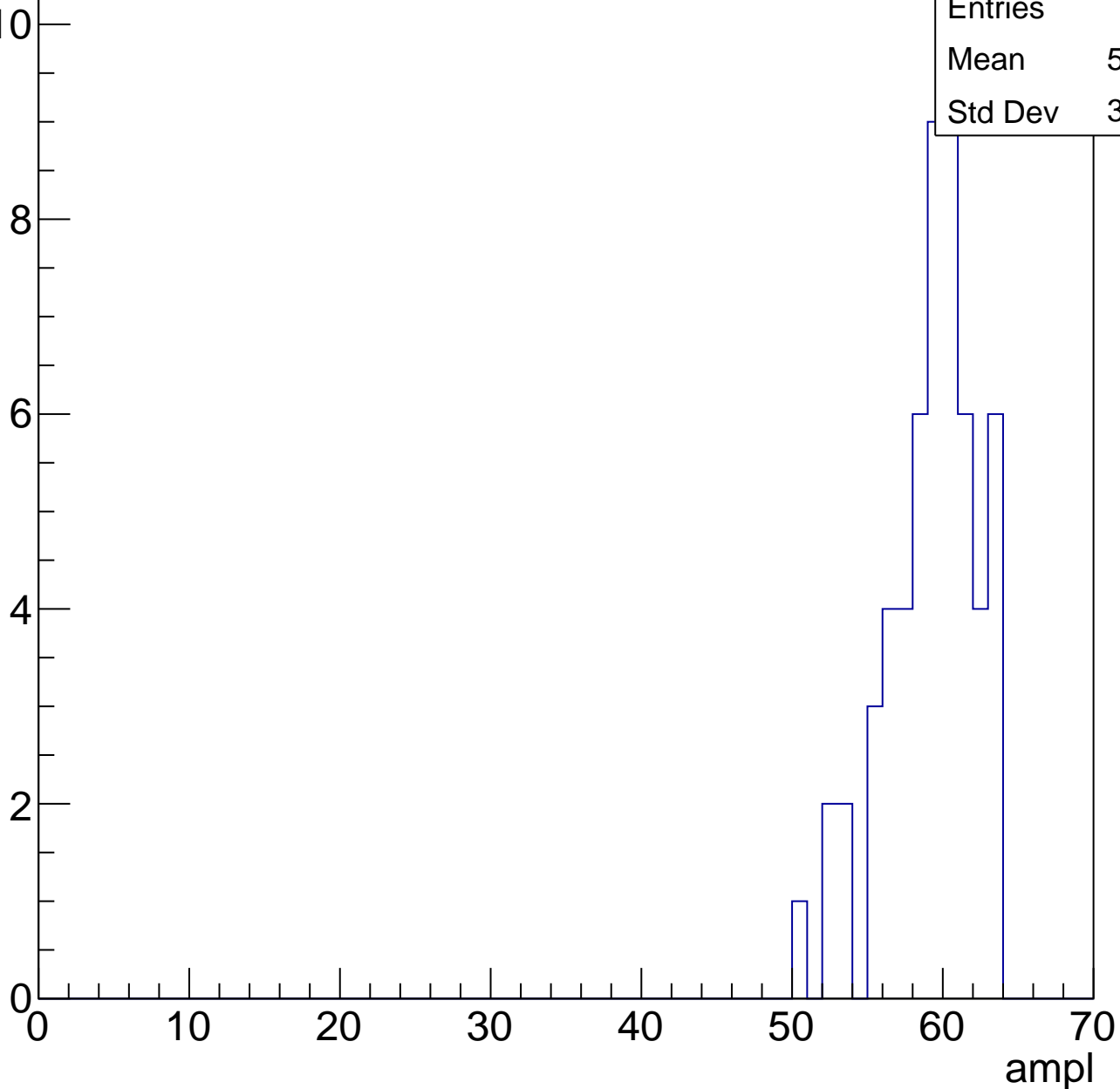


B0L001S, U25-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	58.74
Std Dev	3.018

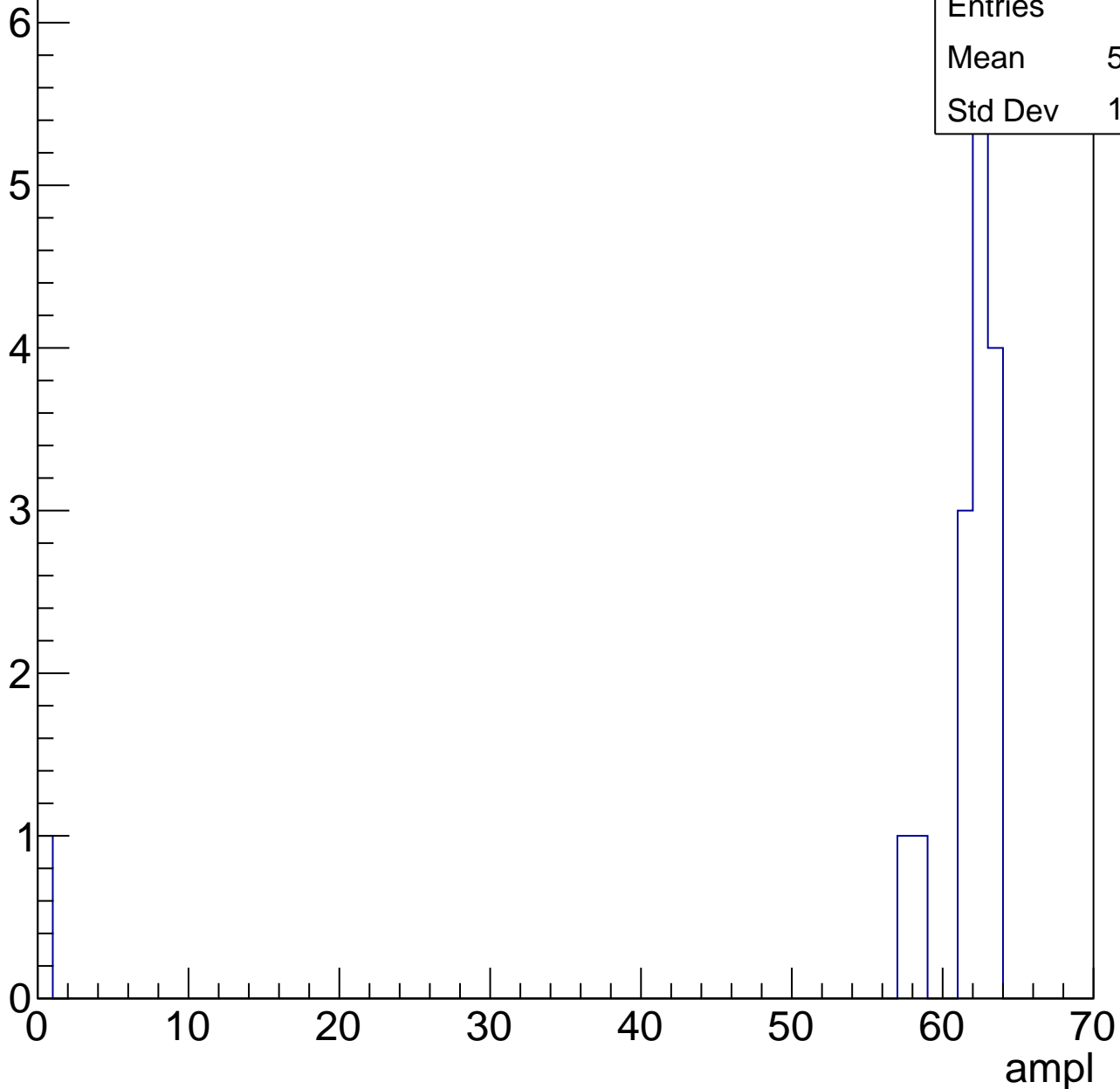


B0L001S, U25-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	57.62
Std Dev	14.97



B0L001S, U25-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch29, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	29.74
Std Dev	5.157

Gaus mean : 30.2766

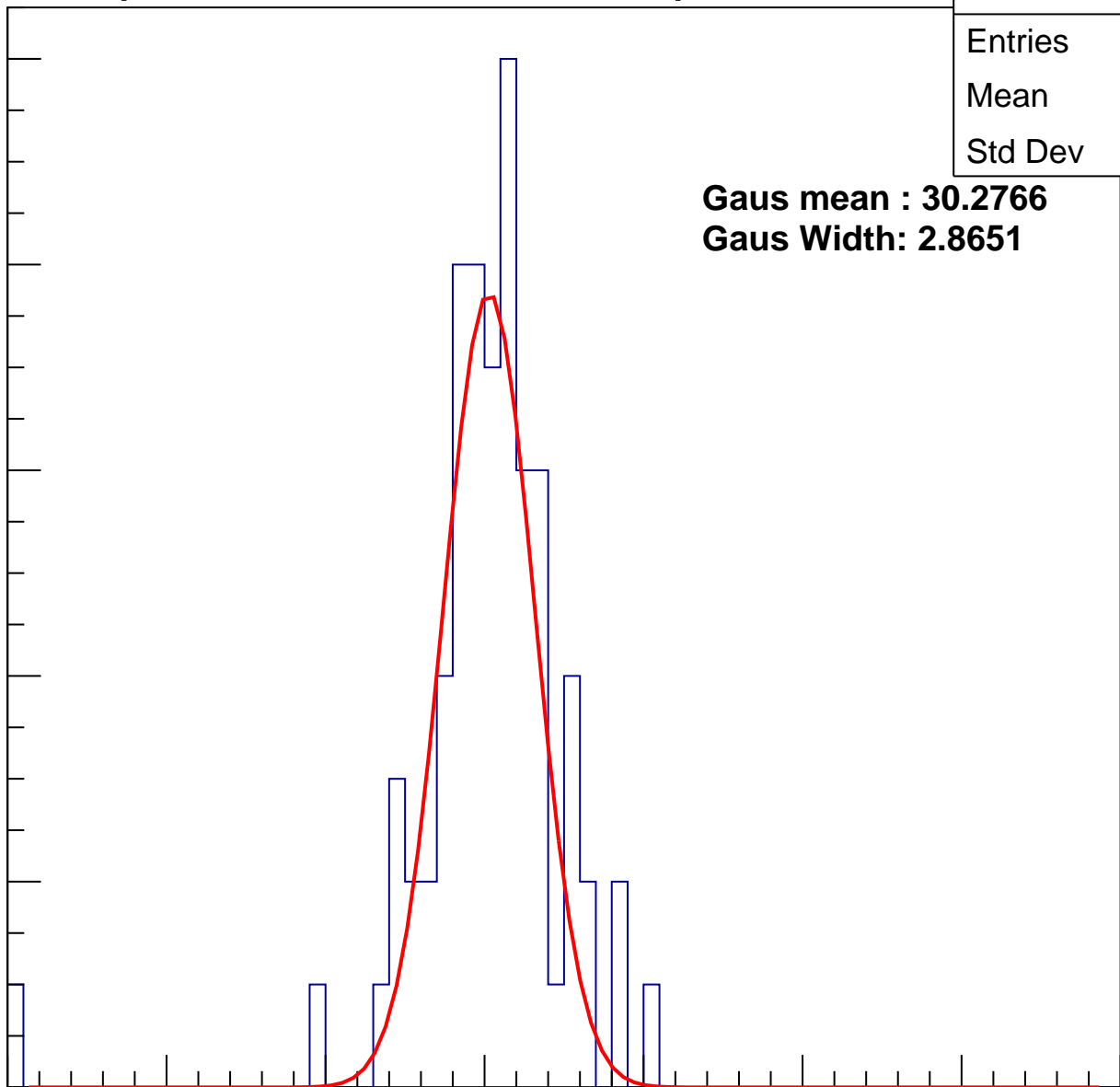
Gaus Width: 2.8651

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch29, adc1

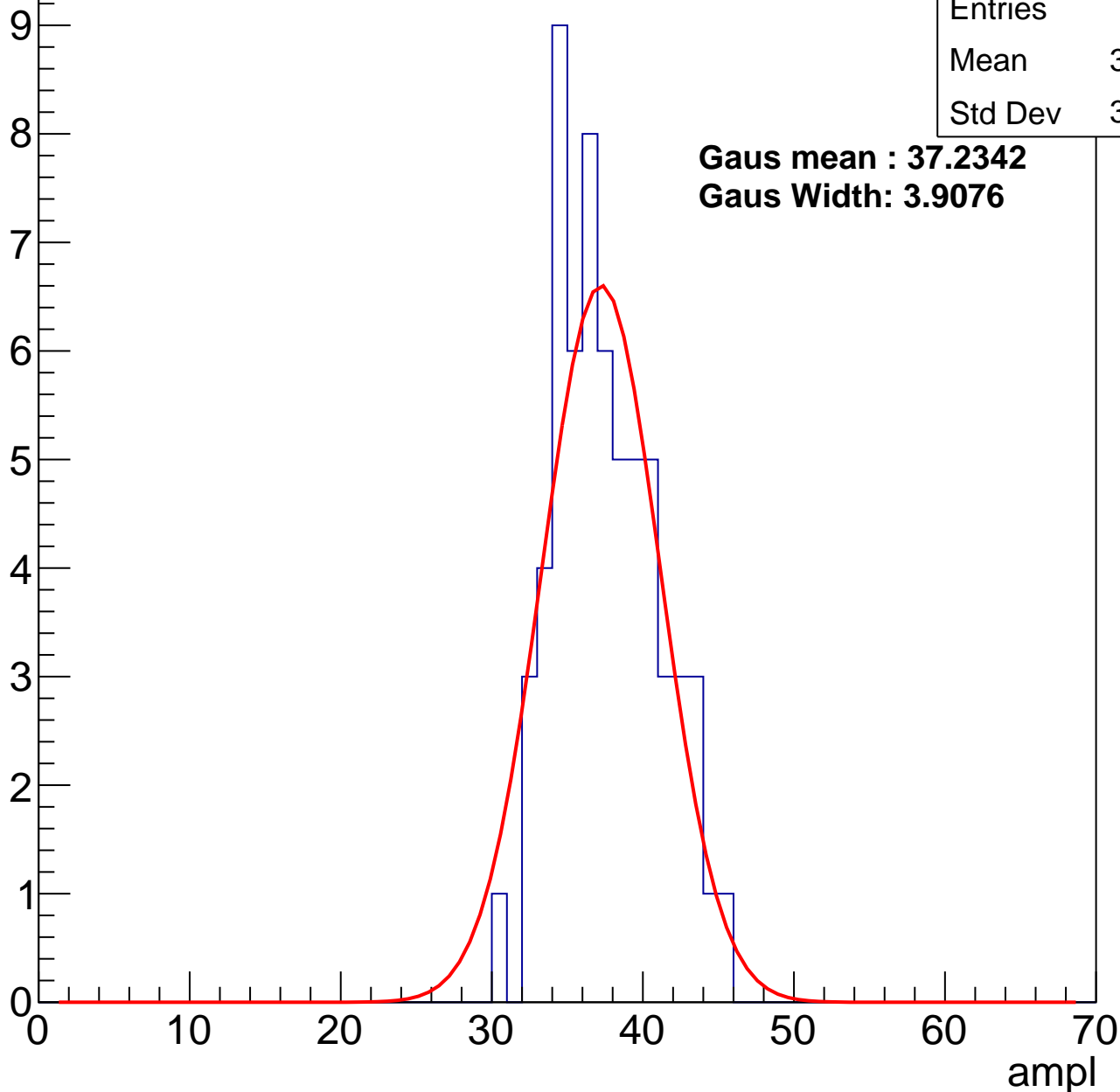
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.08
Std Dev	3.368

Gaus mean : 37.2342

Gaus Width: 3.9076



B0L001S, U25-ch29, adc2

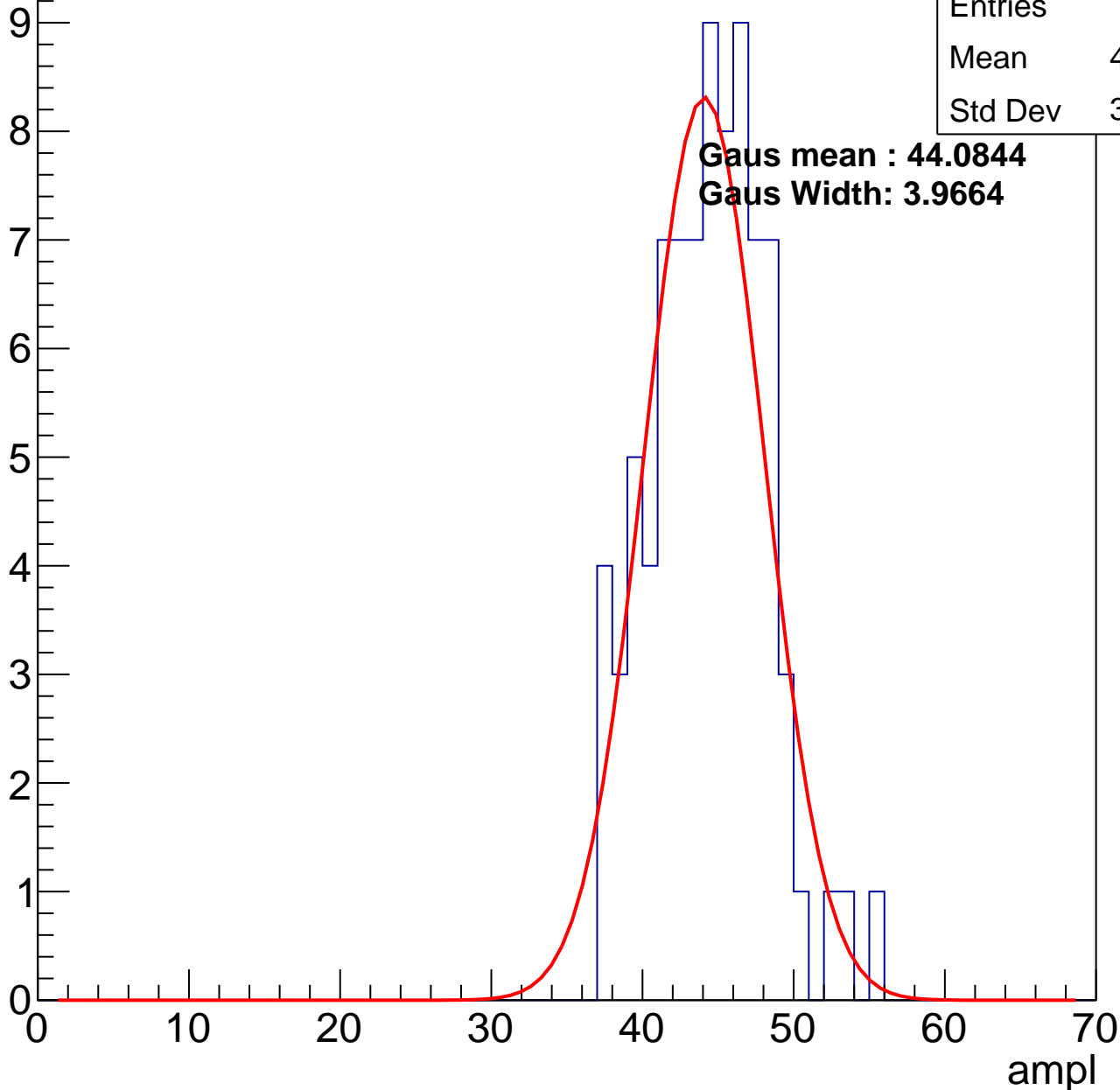
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	43.94
Std Dev	3.759

Gaus mean : 44.0844

Gaus Width: 3.9664

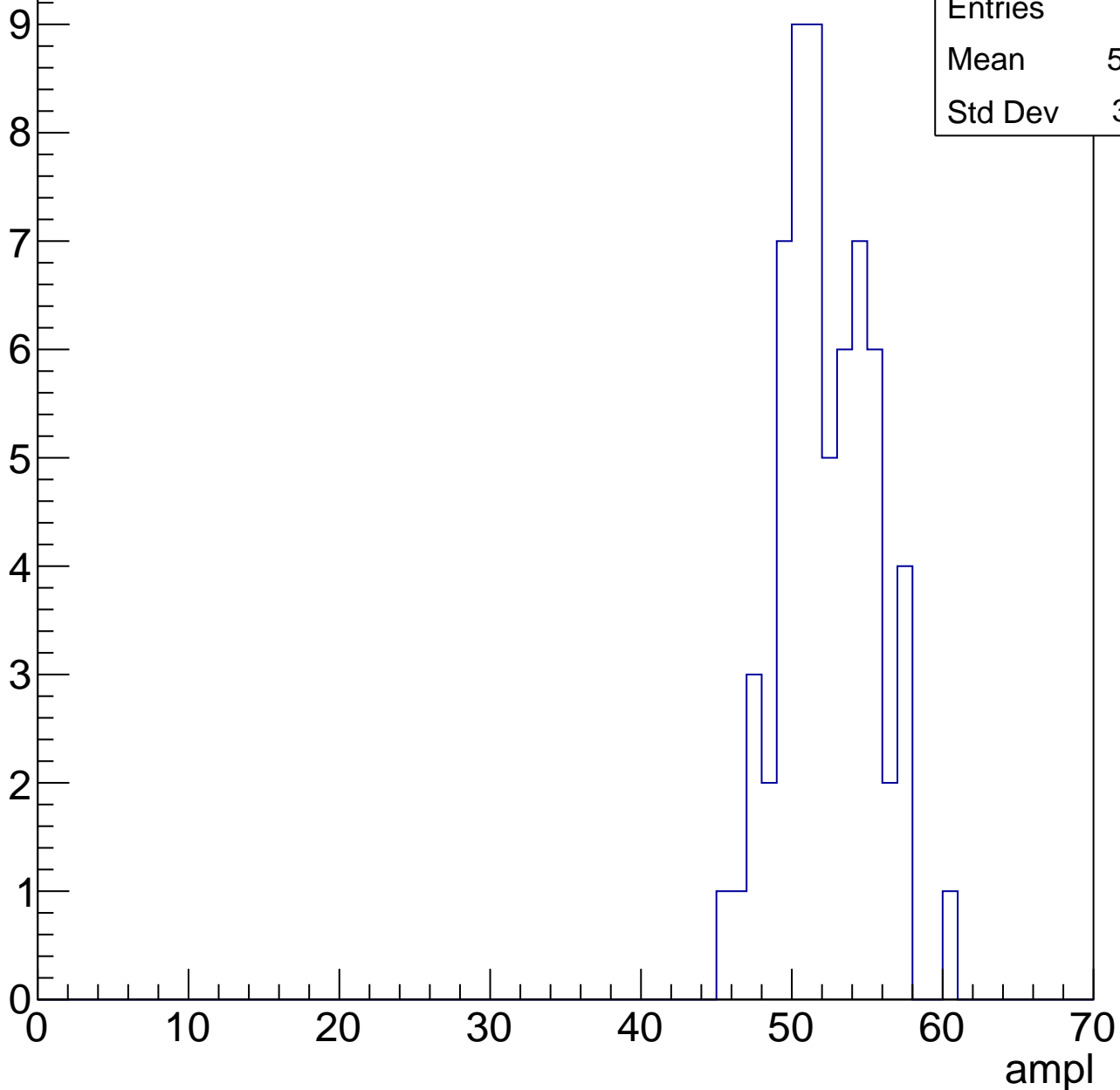


B0L001S, U25-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	51.84
Std Dev	3.041



B0L001S, U25-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

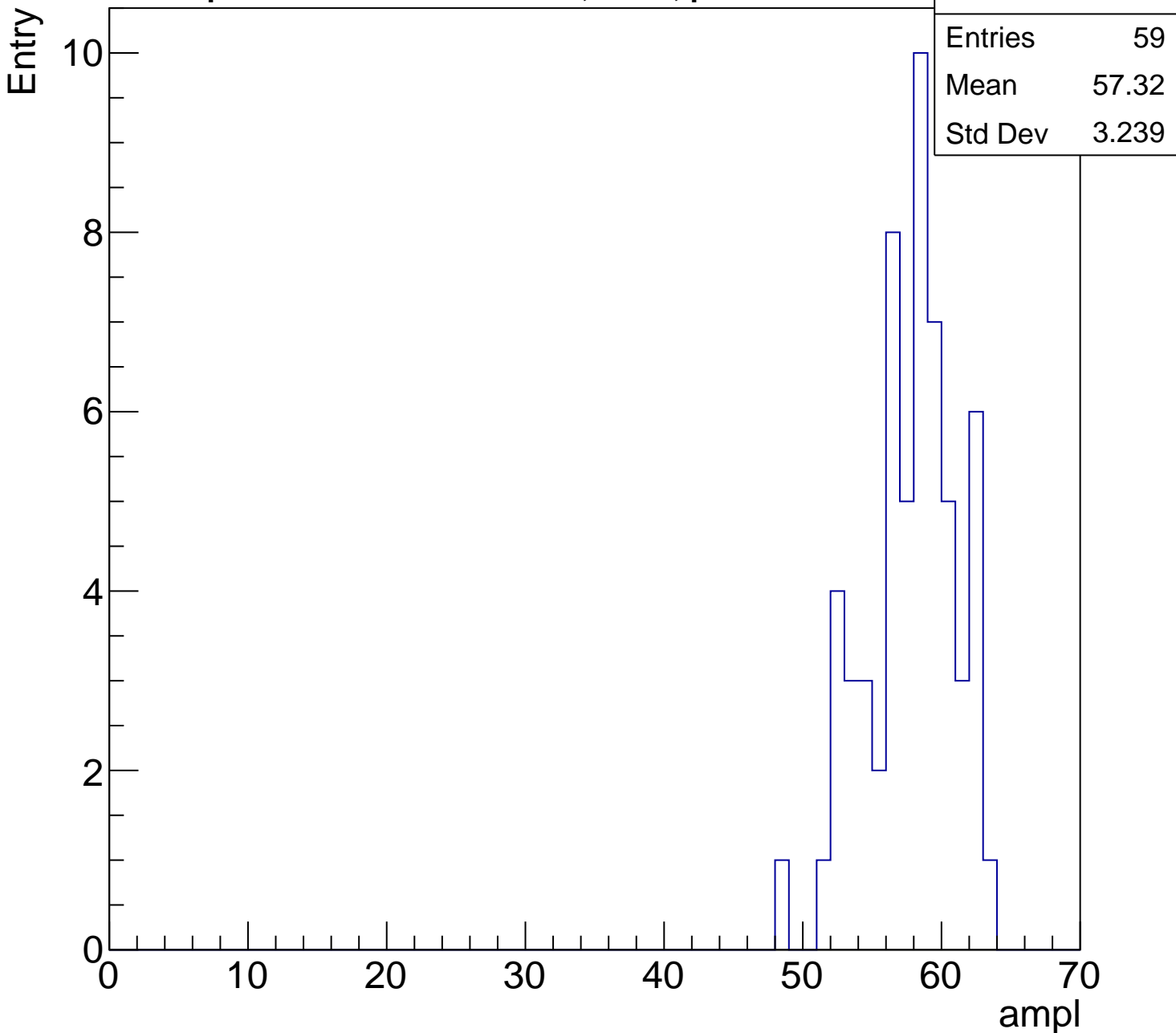
Entries	59
Mean	57.32
Std Dev	3.239

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

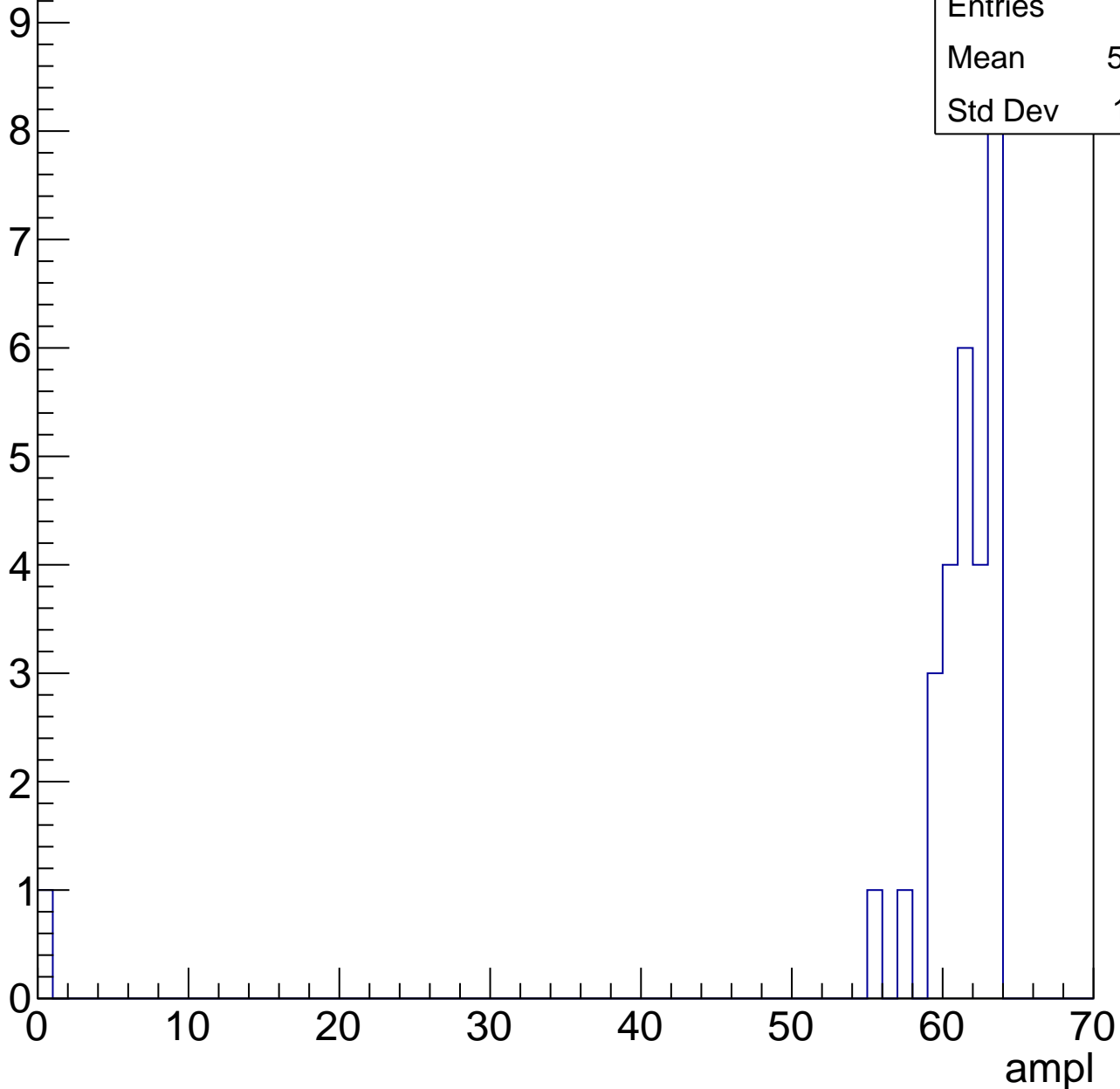


B0L001S, U25-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.97
Std Dev	11.31



B0L001S, U25-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch30, adc0

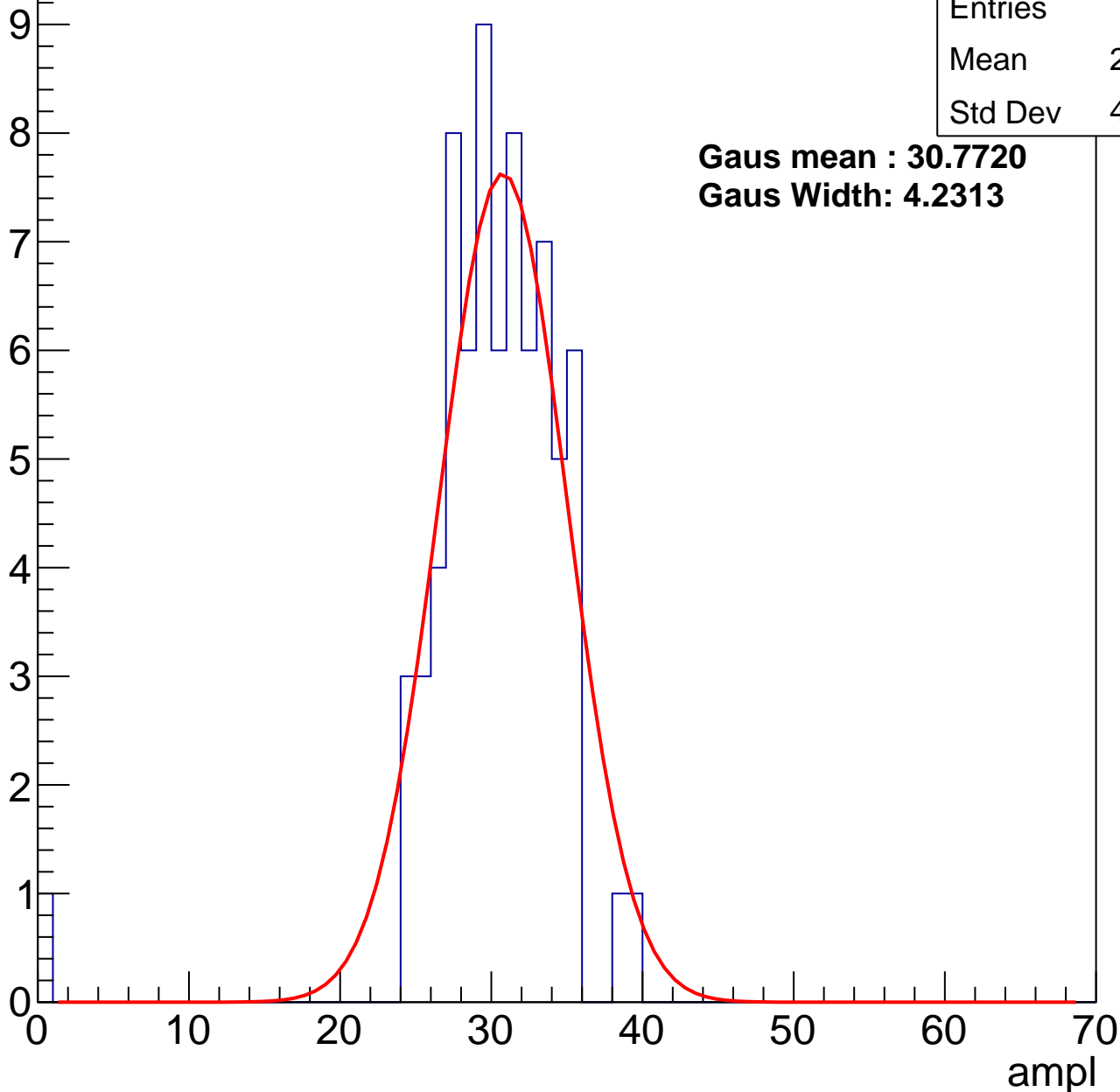
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.78
Std Dev	4.819

Gaus mean : 30.7720

Gaus Width: 4.2313



B0L001S, U25-ch30, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	37.11
Std Dev	3.235

Gaus mean : 37.9203

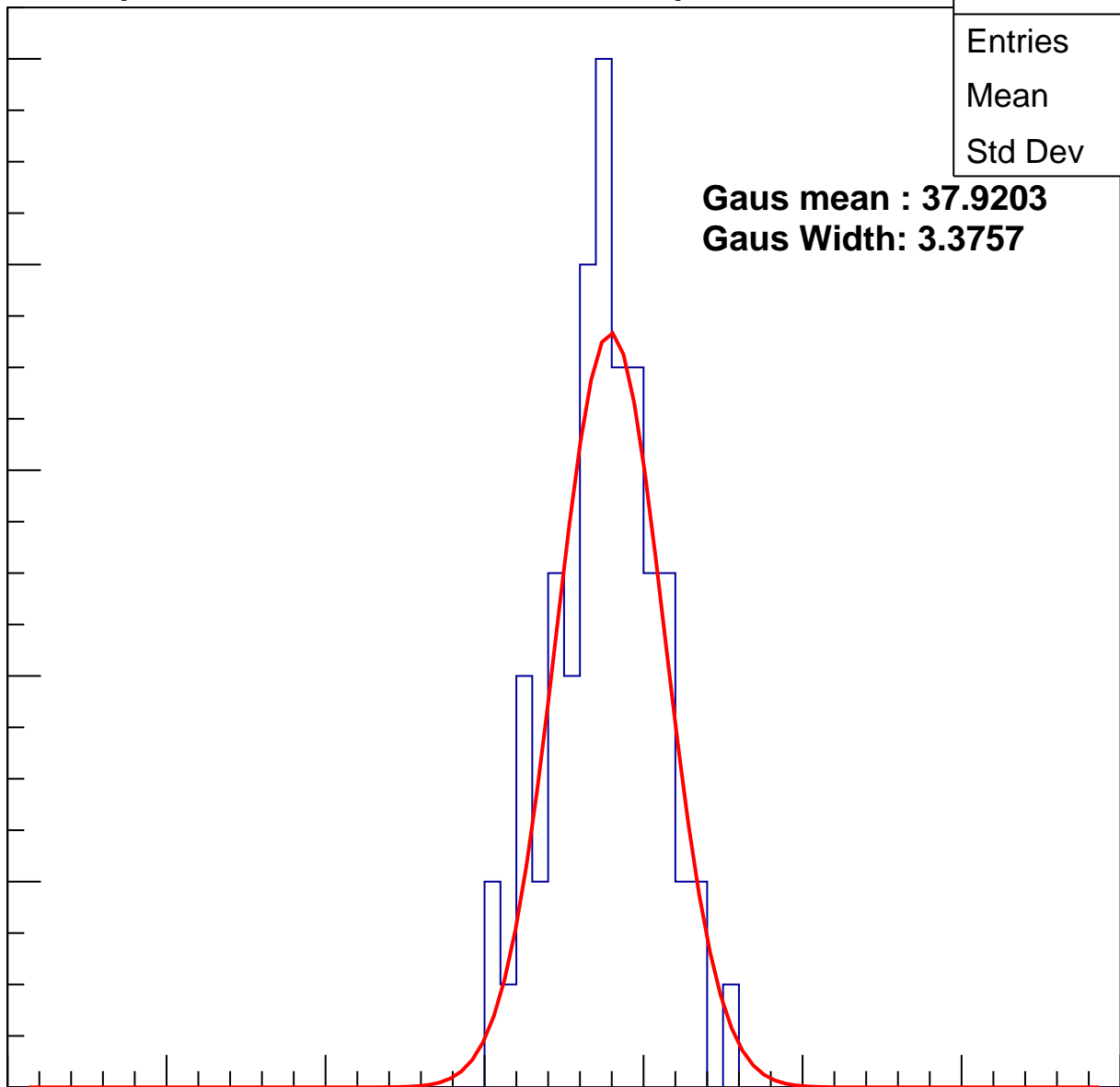
Gaus Width: 3.3757

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch30, adc2

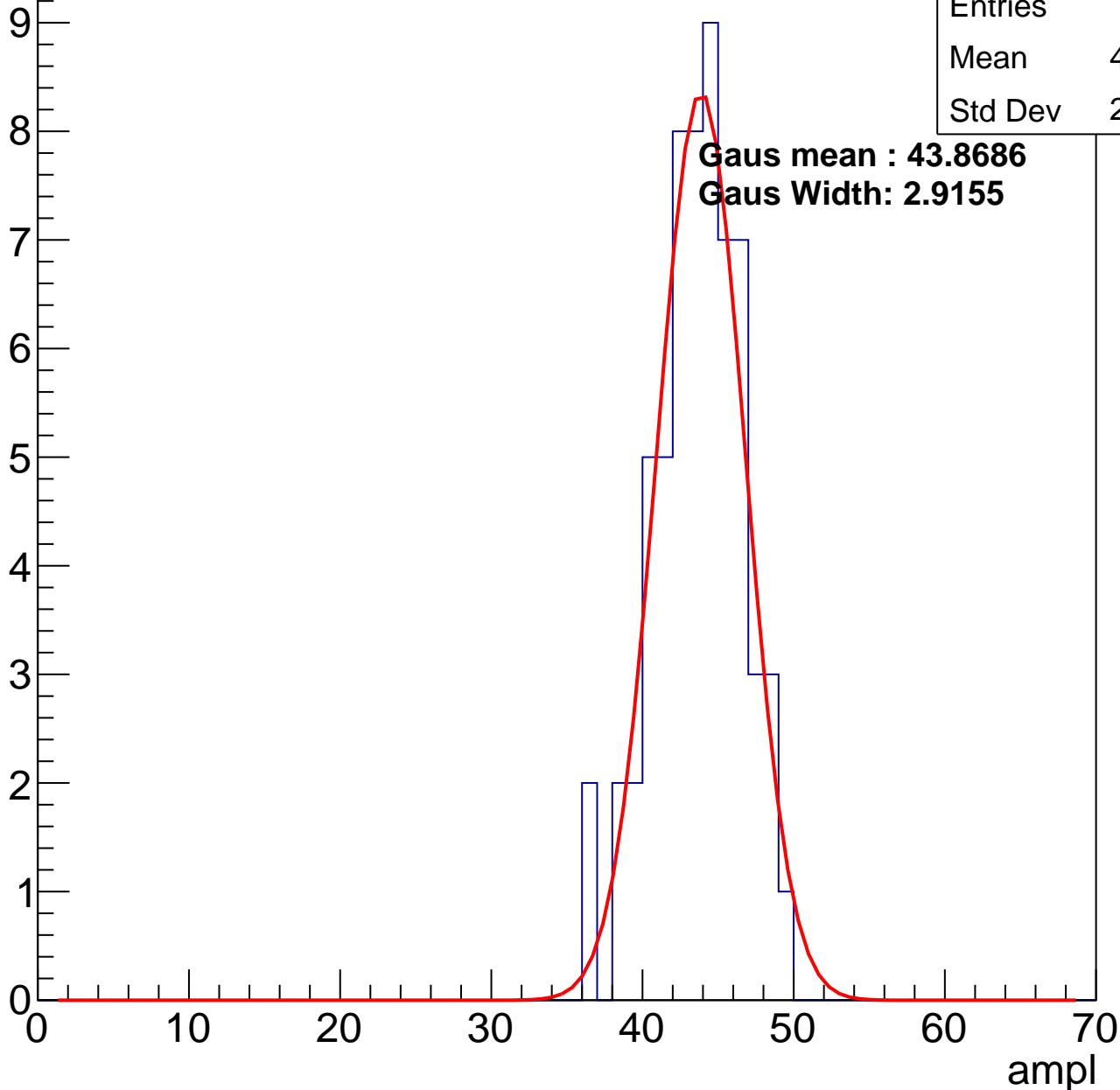
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.19
Std Dev	2.867

Gaus mean : 43.8686

Gaus Width: 2.9155

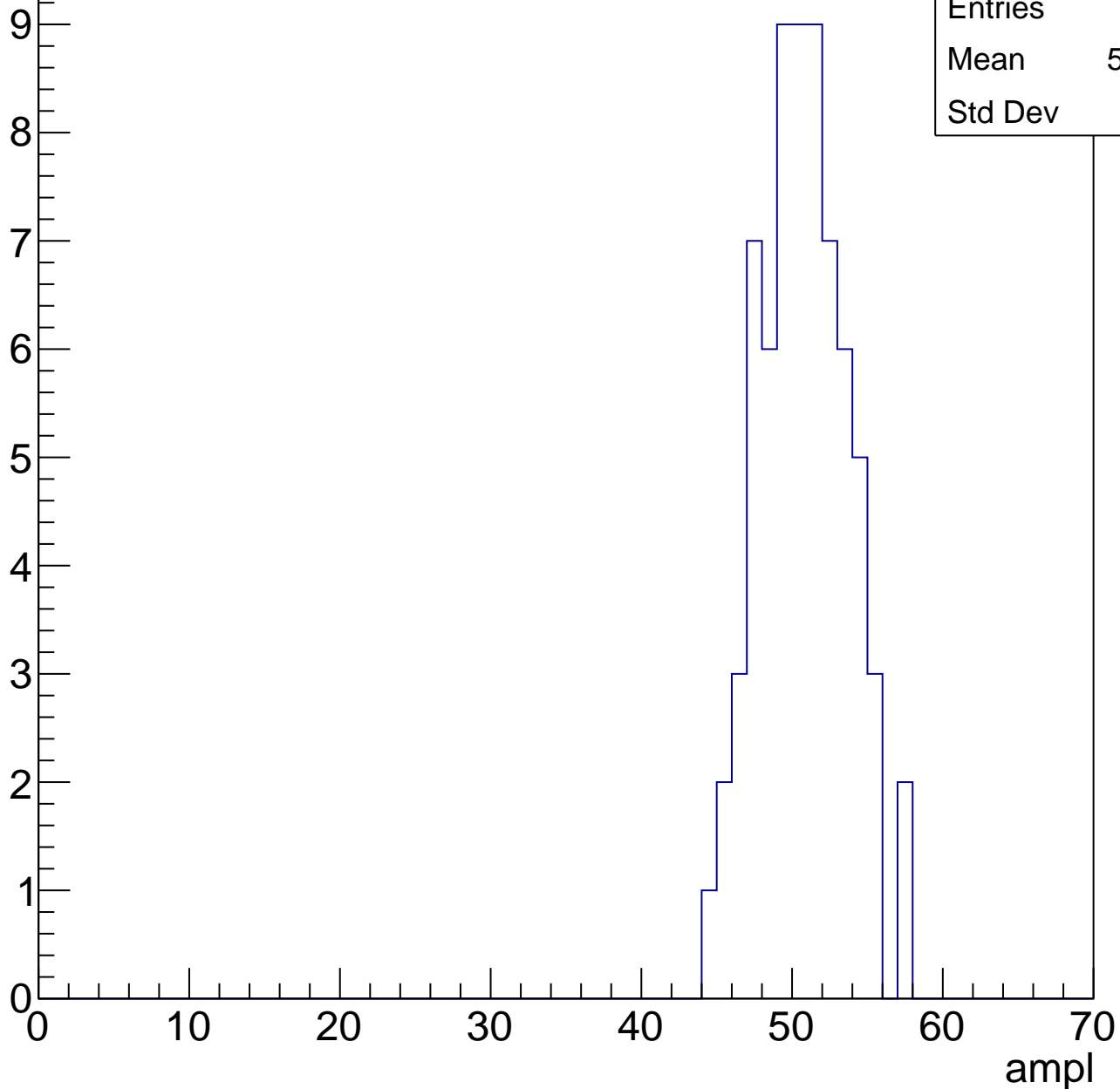


B0L001S, U25-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.29
Std Dev	2.87

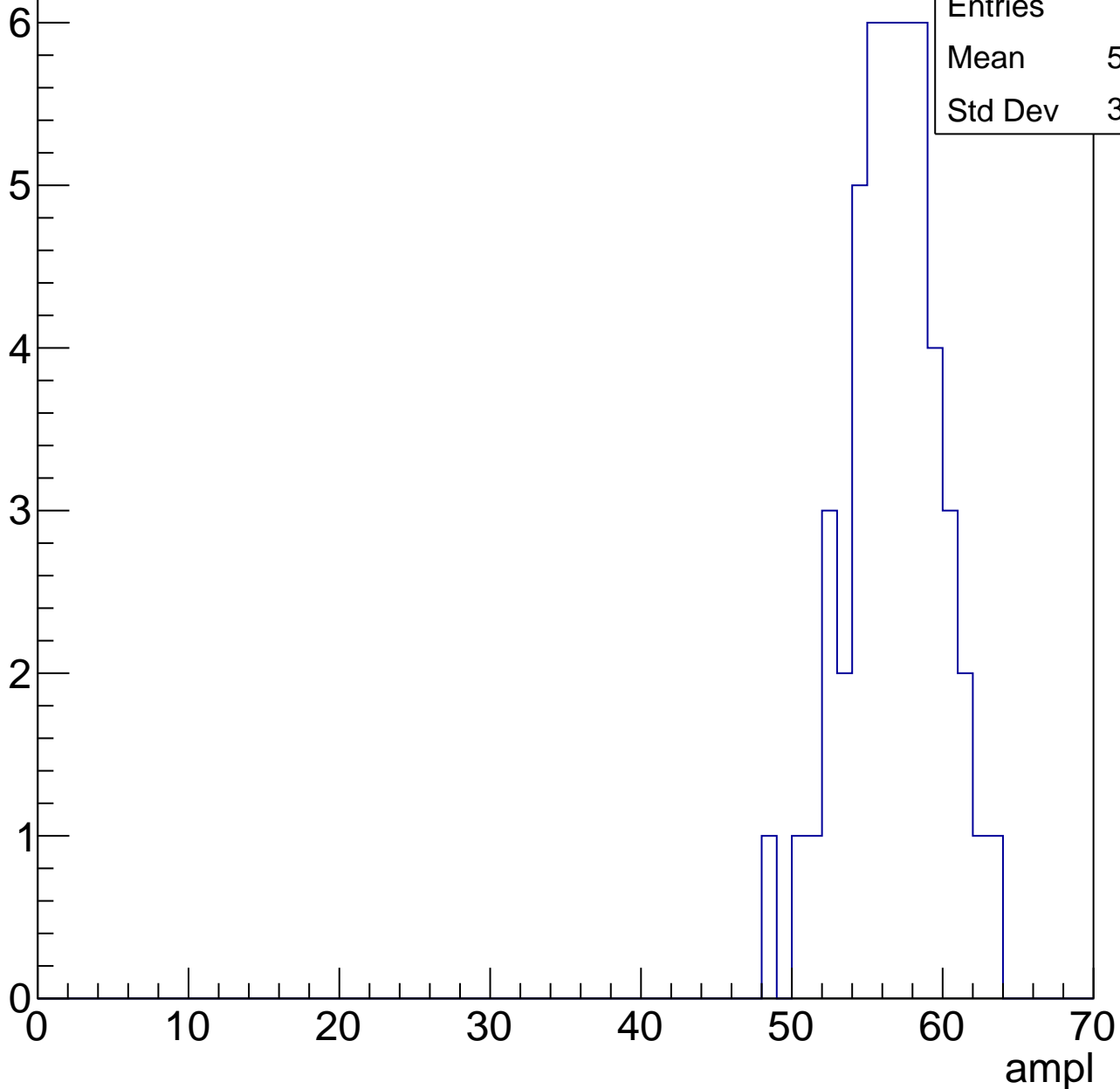


B0L001S, U25-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

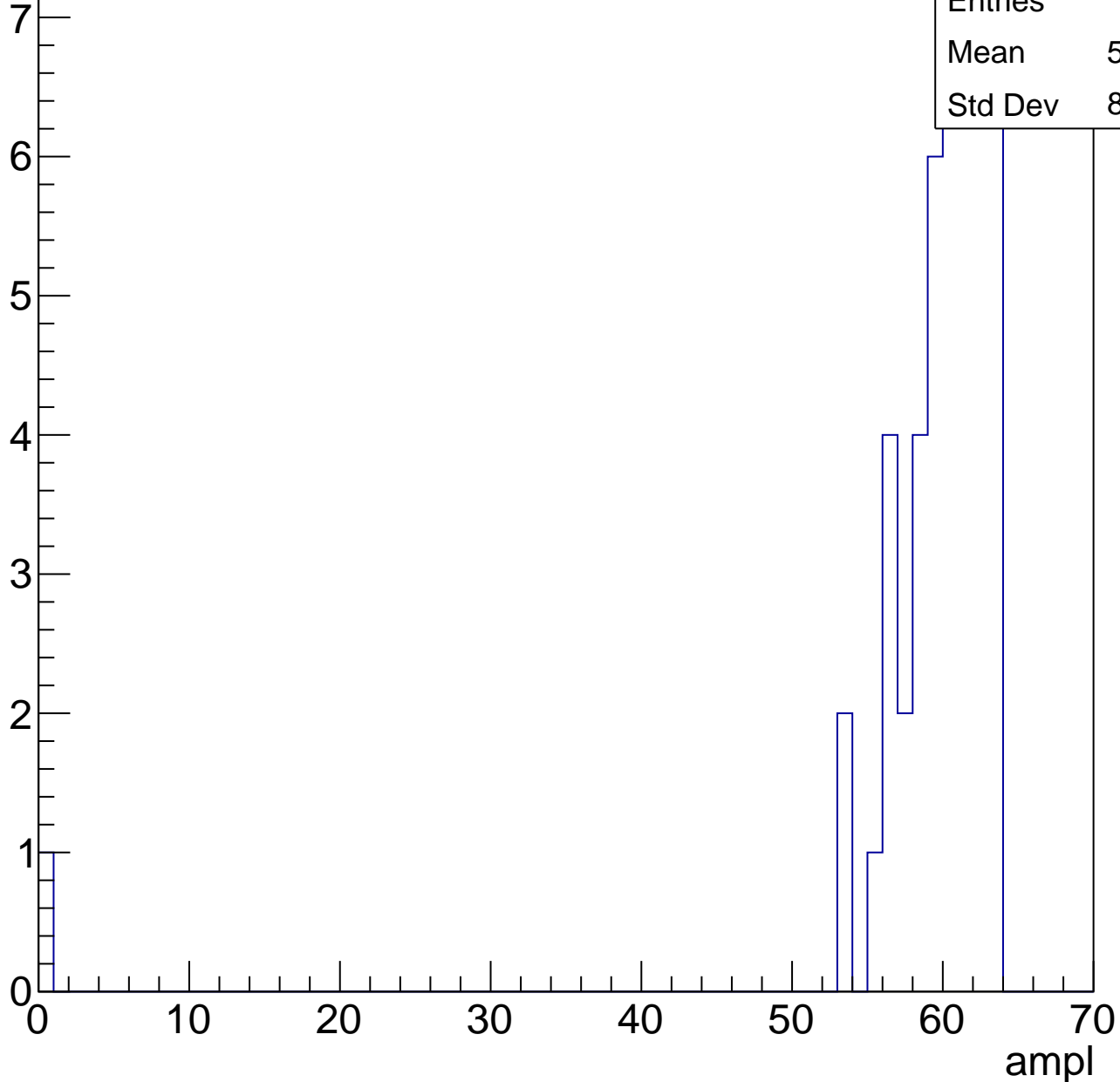
Entries	48
Mean	56.25
Std Dev	3.106



B0L001S, U25-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L001S, U25-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch31, adc0

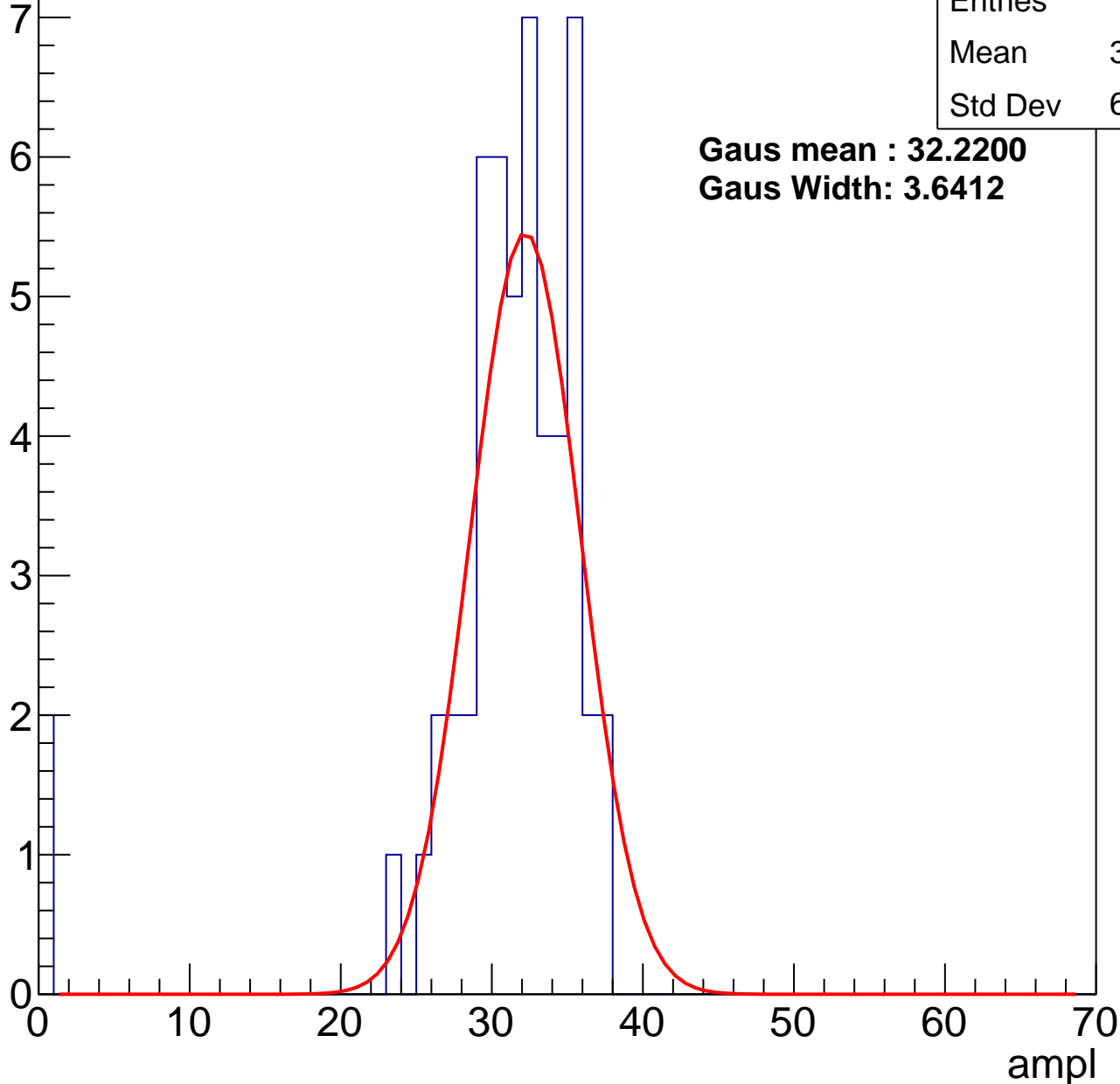
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.23
Std Dev	6.748

Gaus mean : 32.2200

Gaus Width: 3.6412



B0L001S, U25-ch31, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	37.08
Std Dev	3.627

Gaus mean : 37.6395

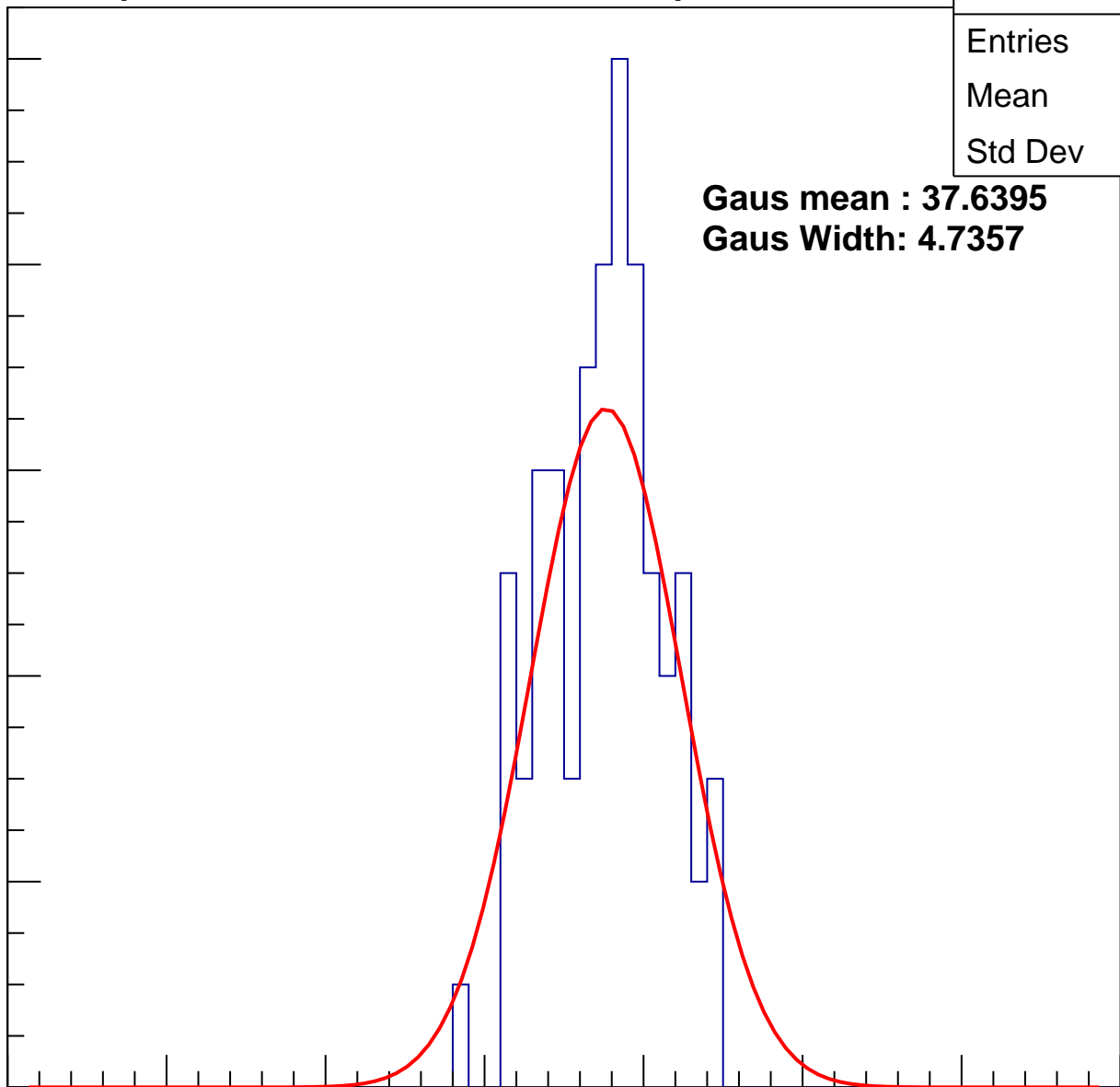
Gaus Width: 4.7357

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch31, adc2

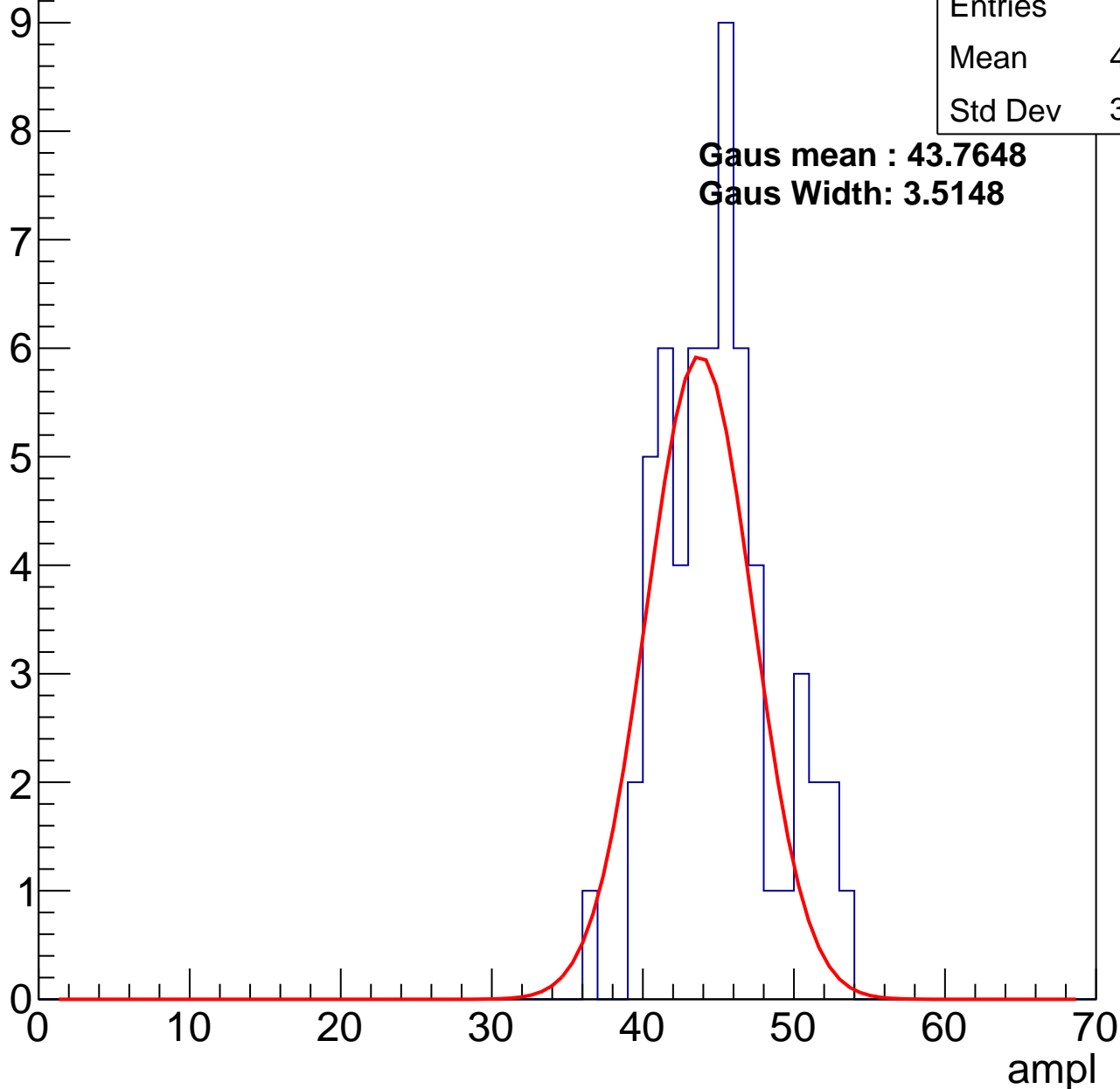
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.49
Std Dev	3.638

Gaus mean : 43.7648

Gaus Width: 3.5148

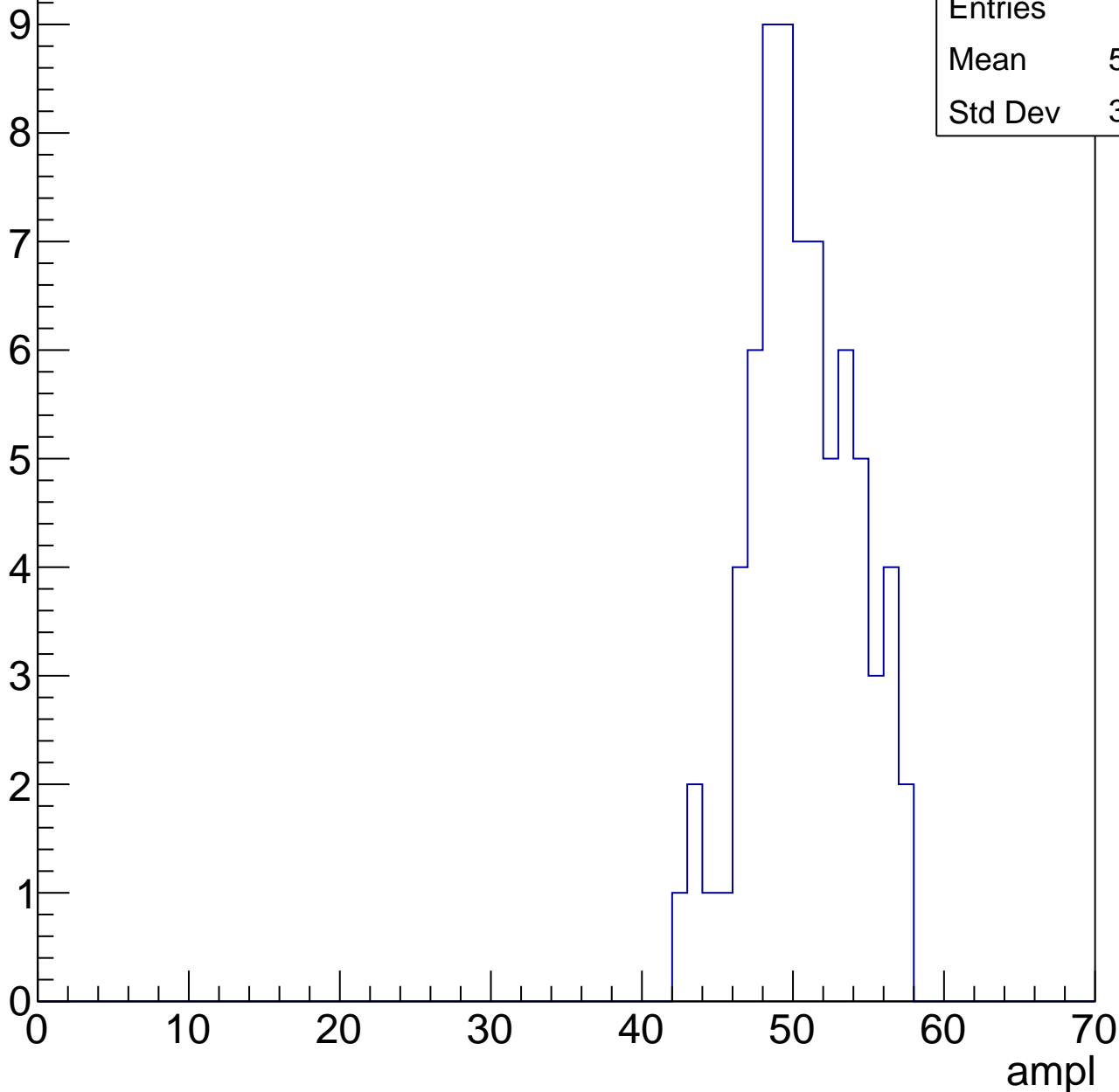


B0L001S, U25-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

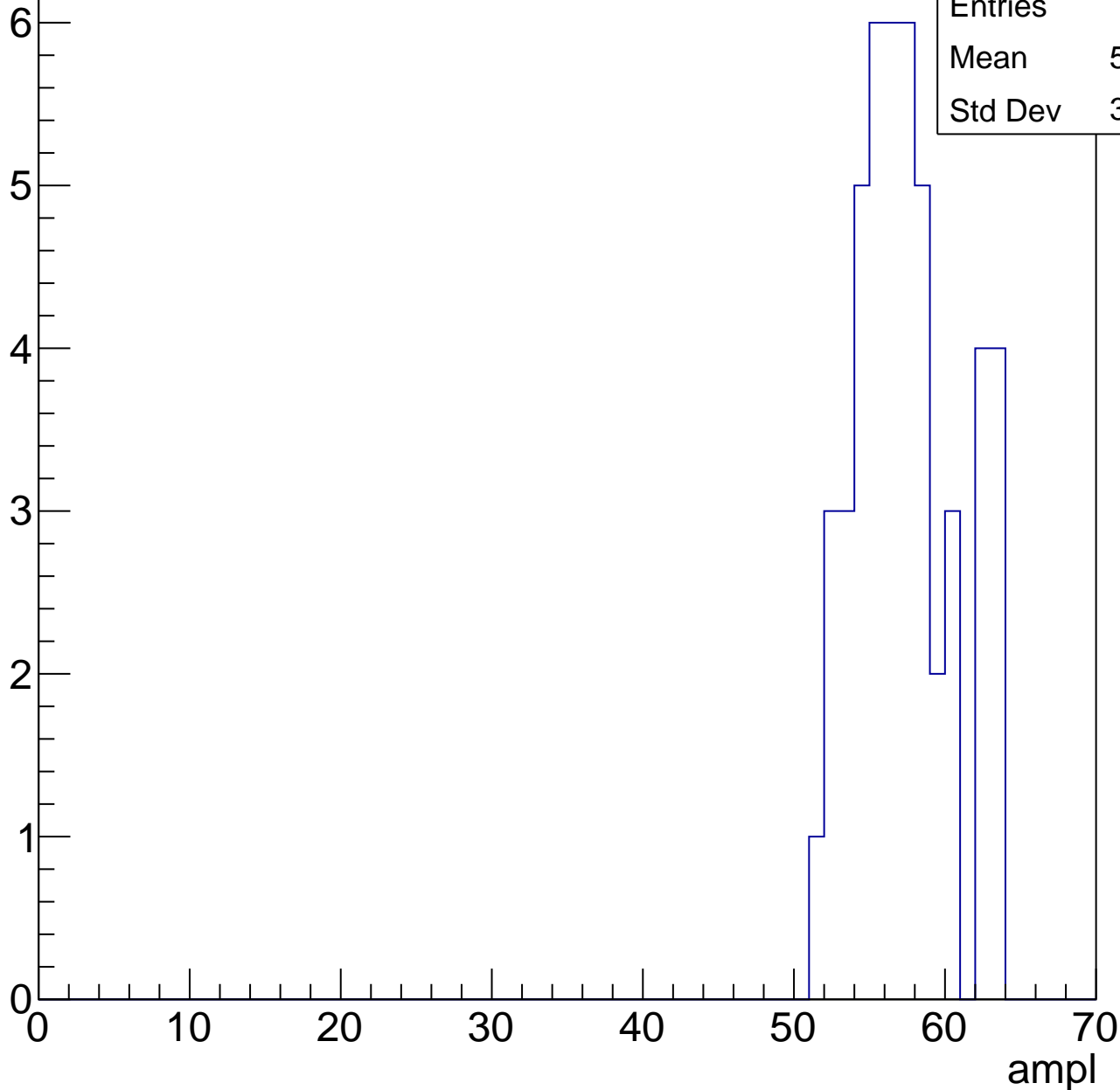
Entries	72
Mean	50.19
Std Dev	3.459



B0L001S, U25-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

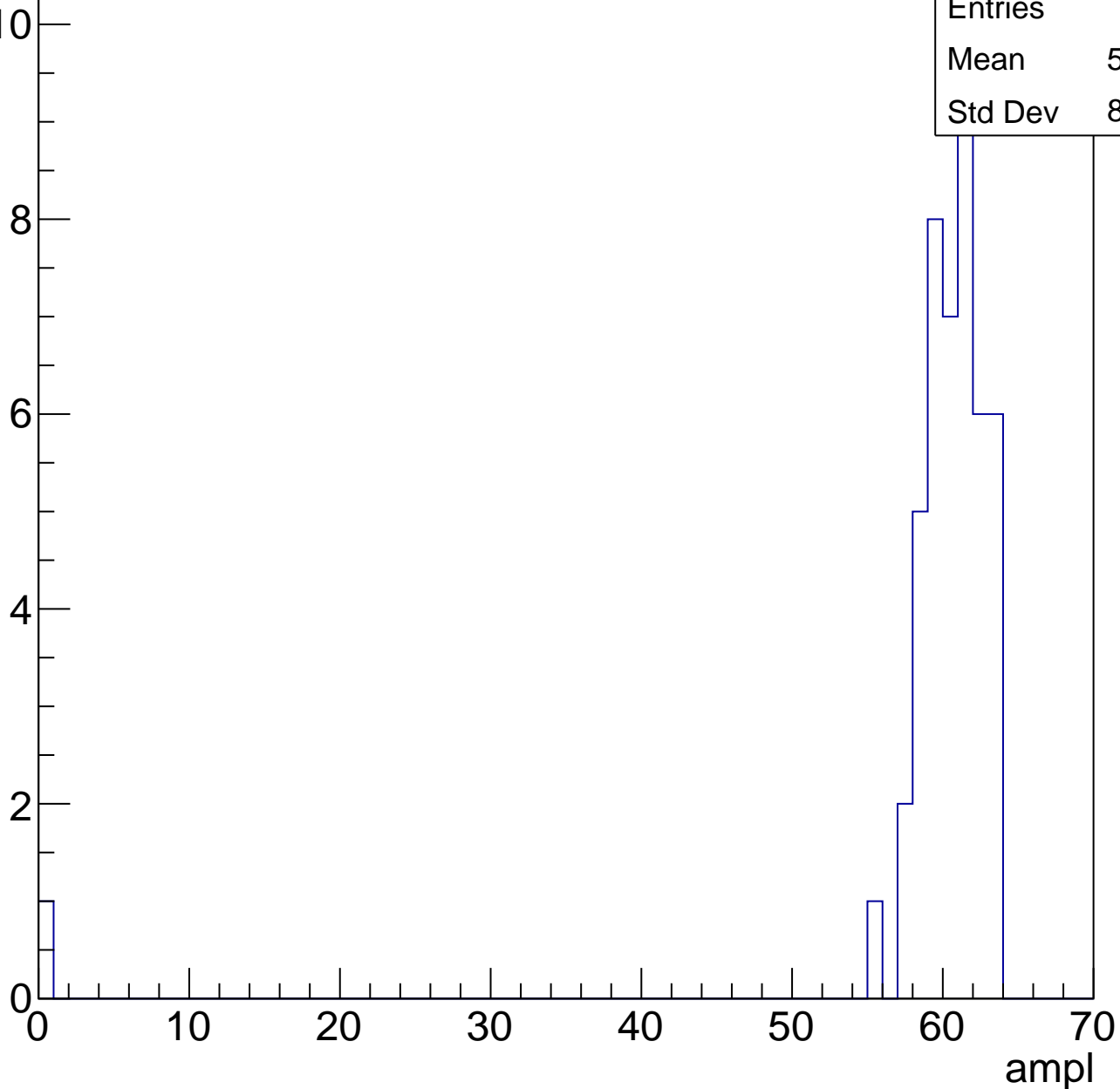


B0L001S, U25-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.93
Std Dev	8.977



B0L001S, U25-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch32, adc0

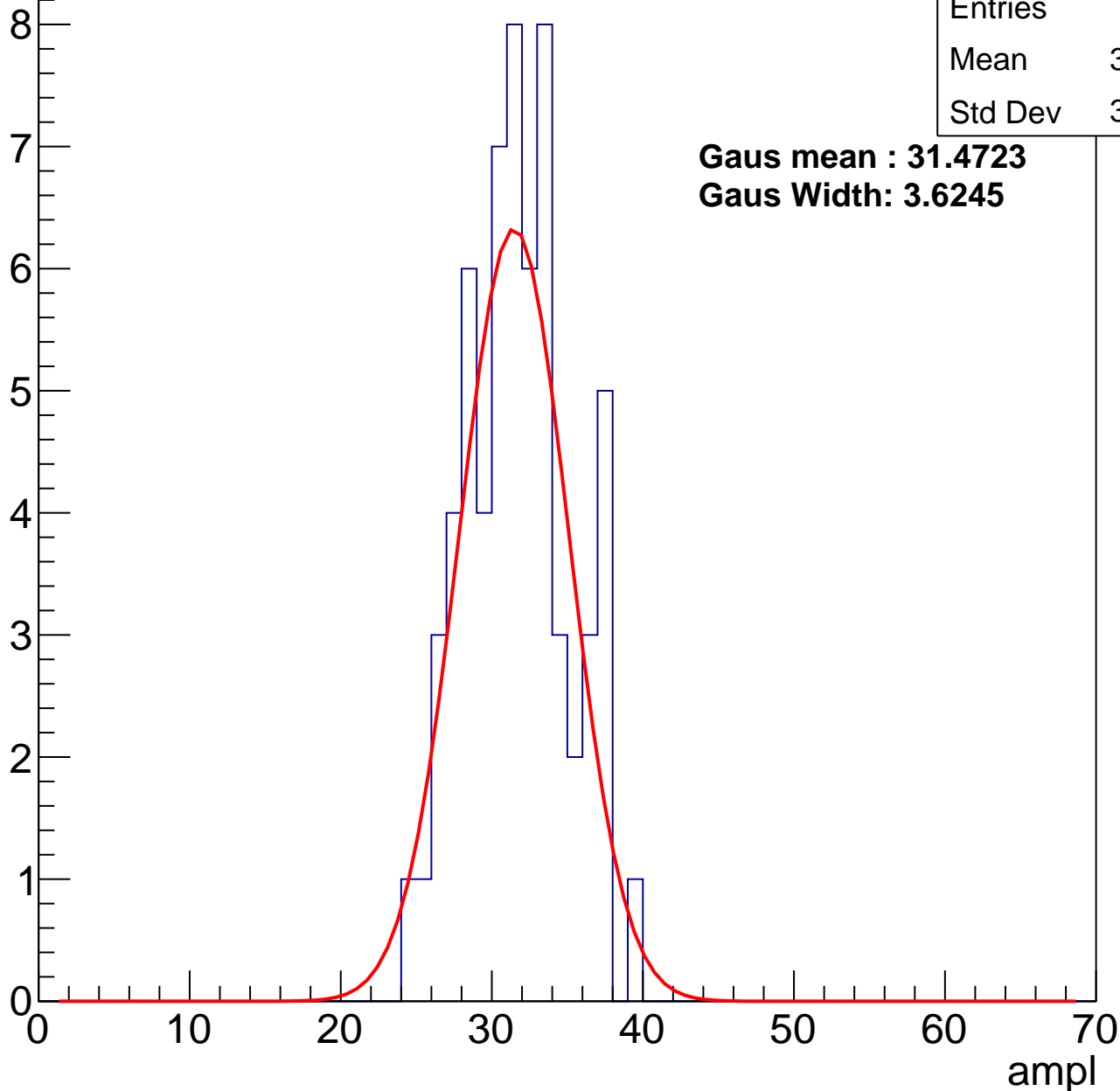
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.24
Std Dev	3.392

Gaus mean : 31.4723

Gaus Width: 3.6245



B0L001S, U25-ch32, adc1

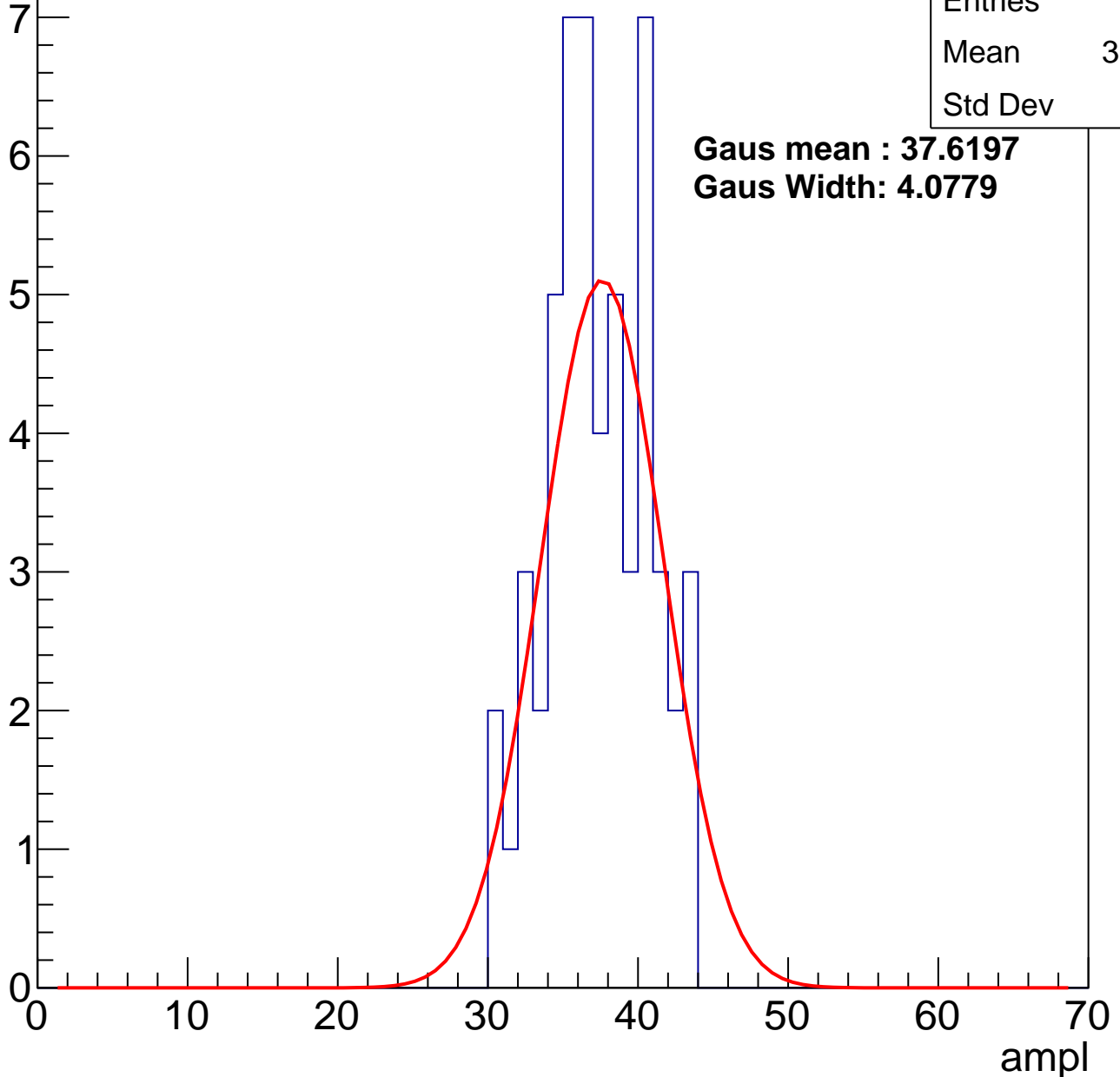
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	36.87
Std Dev	3.35

Gaus mean : 37.6197

Gaus Width: 4.0779



B0L001S, U25-ch32, adc2

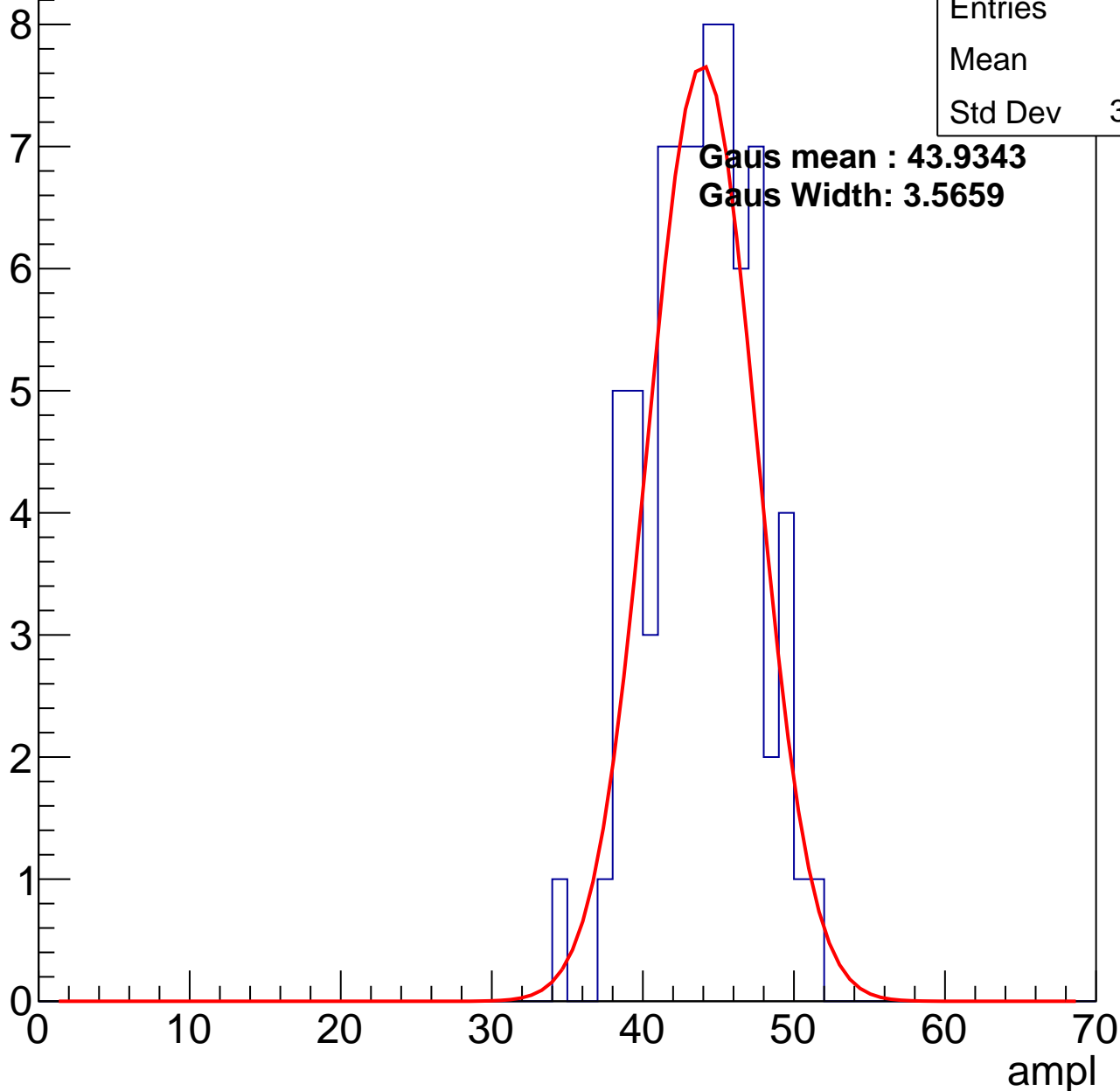
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.4
Std Dev	3.483

Gaus mean : 43.9343

Gaus Width: 3.5659

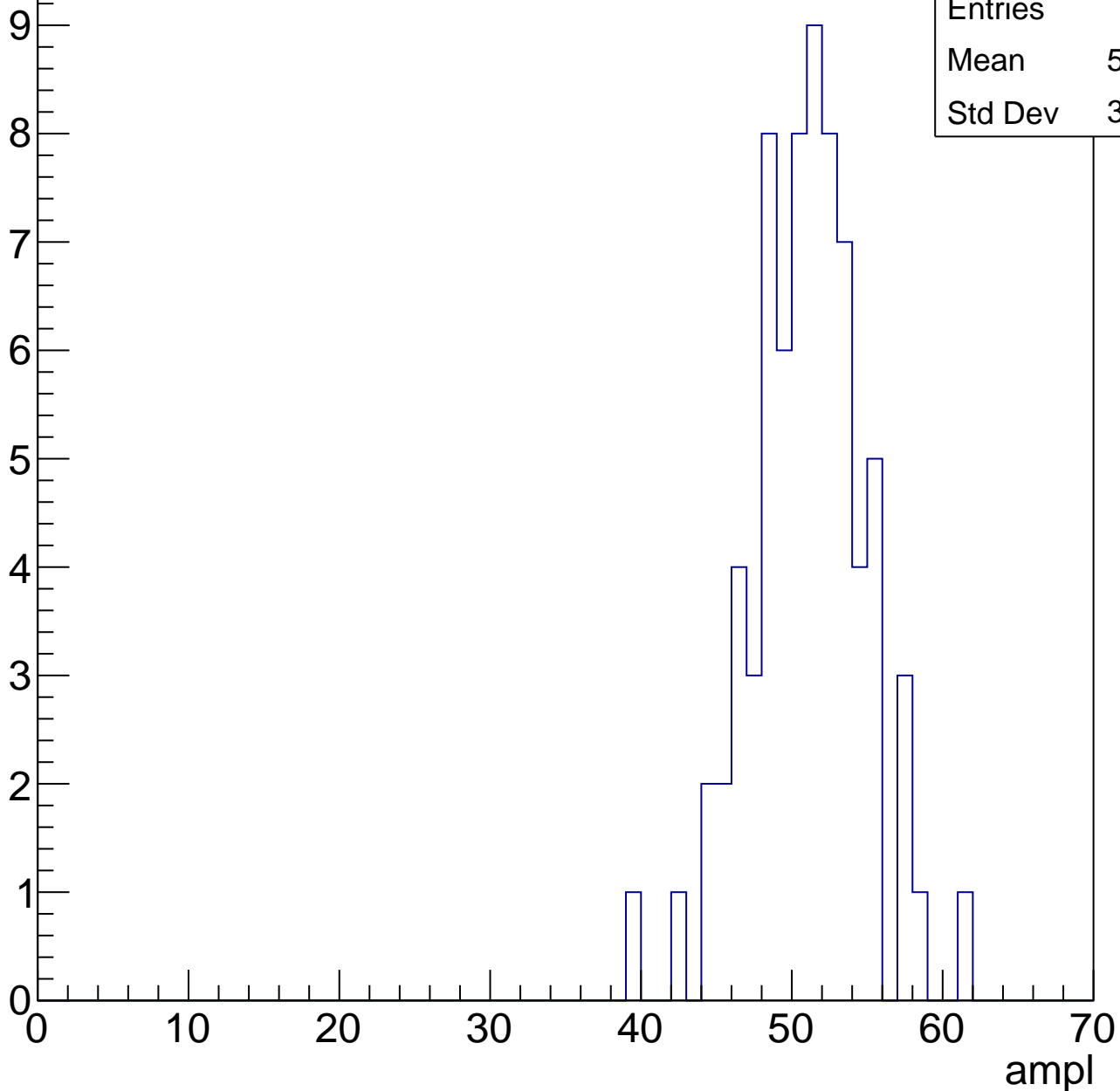


B0L001S, U25-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

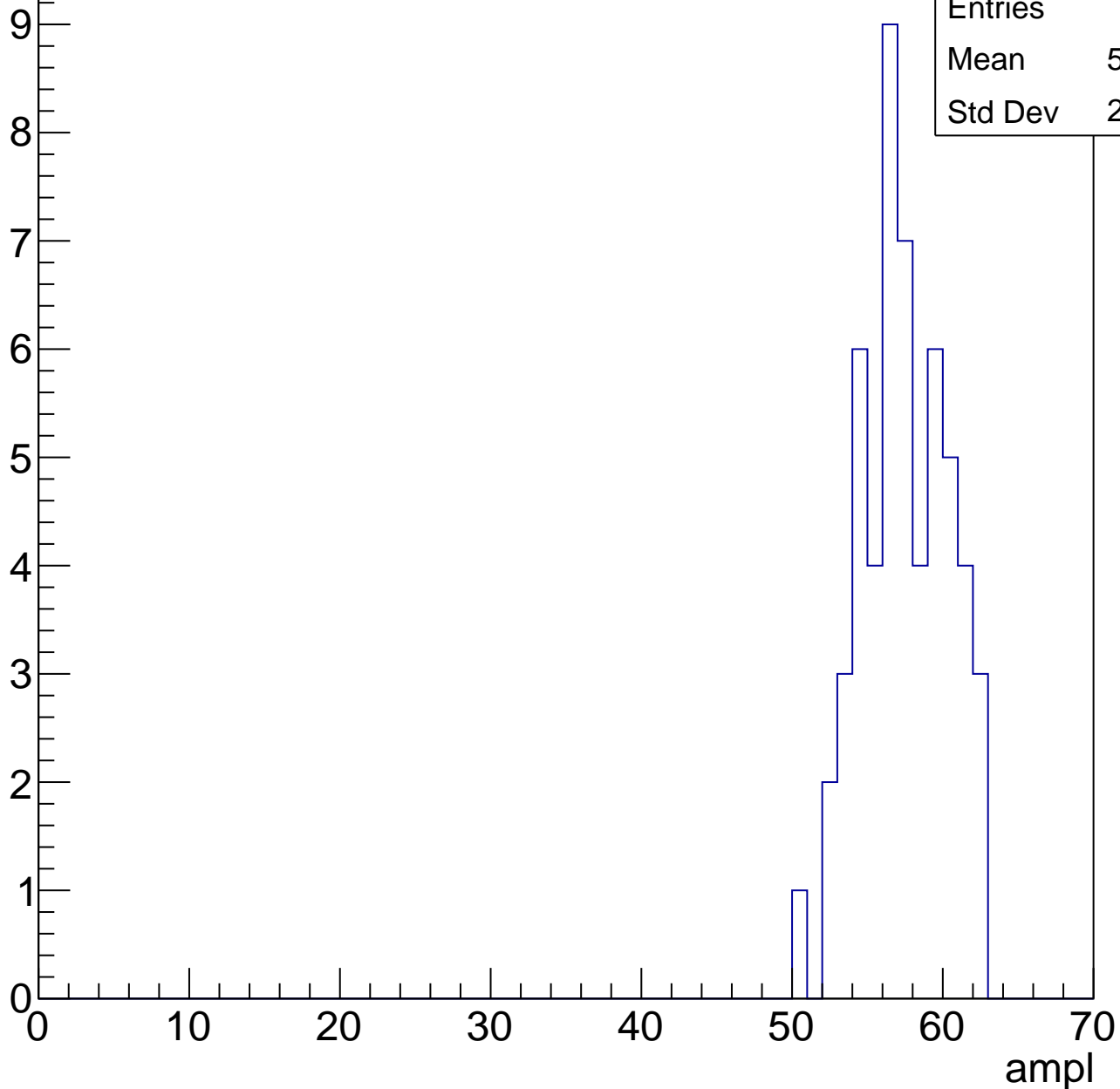
Entries	73
Mean	50.53
Std Dev	3.775



B0L001S, U25-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

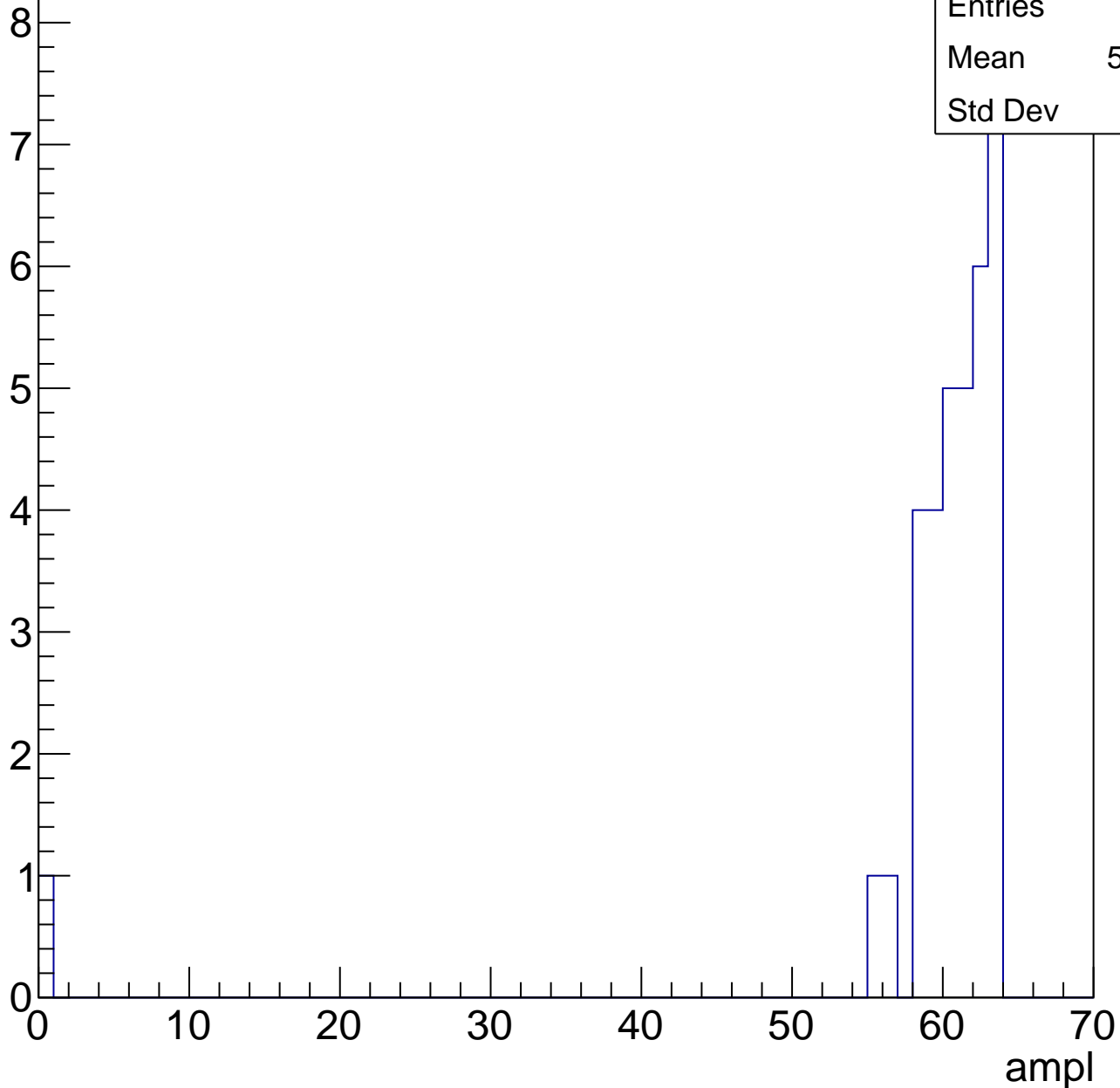


B0L001S, U25-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.86
Std Dev	10.3



B0L001S, U25-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch33, adc0

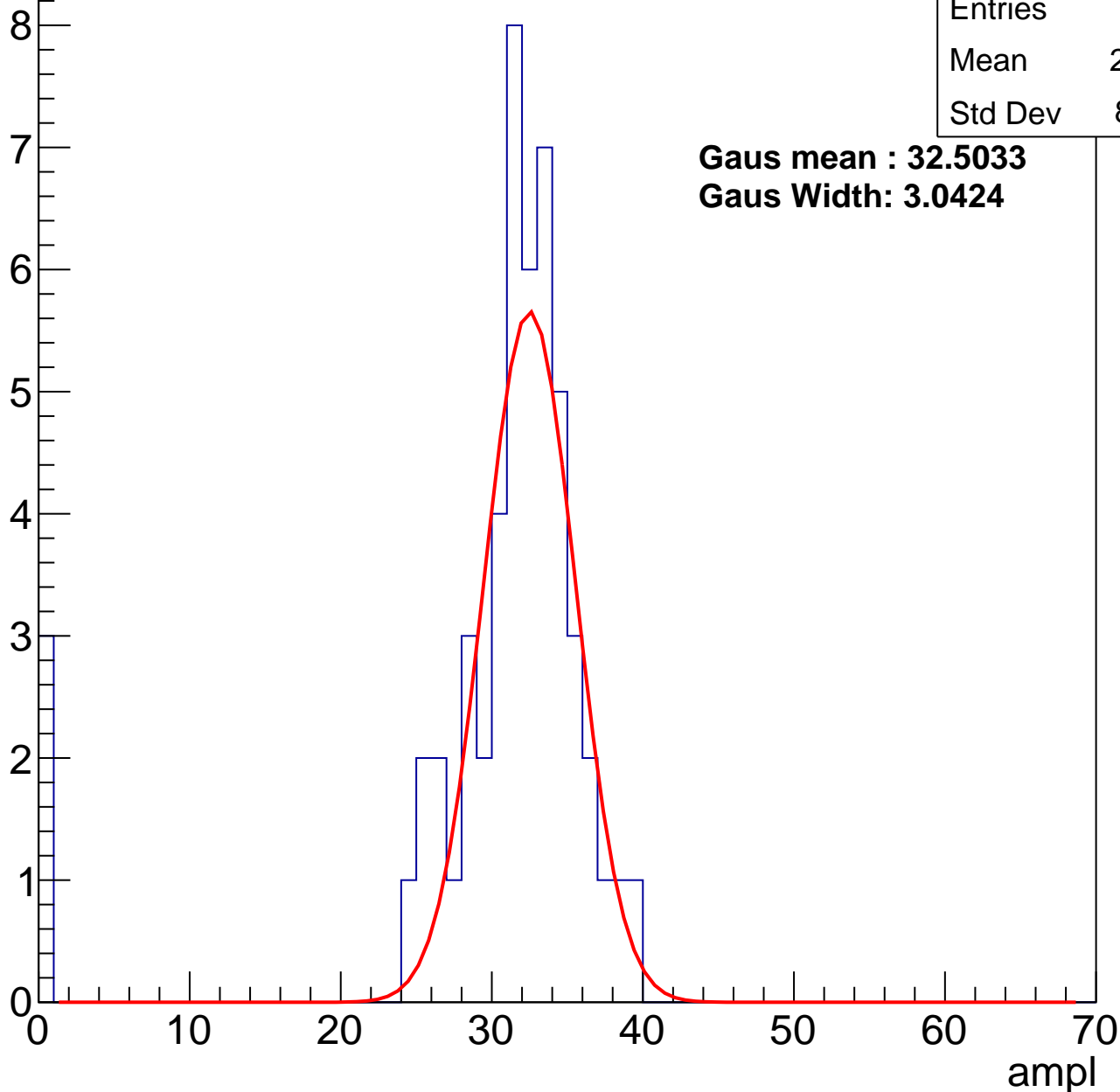
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.75
Std Dev	8.031

Gaus mean : 32.5033

Gaus Width: 3.0424



B0L001S, U25-ch33, adc1

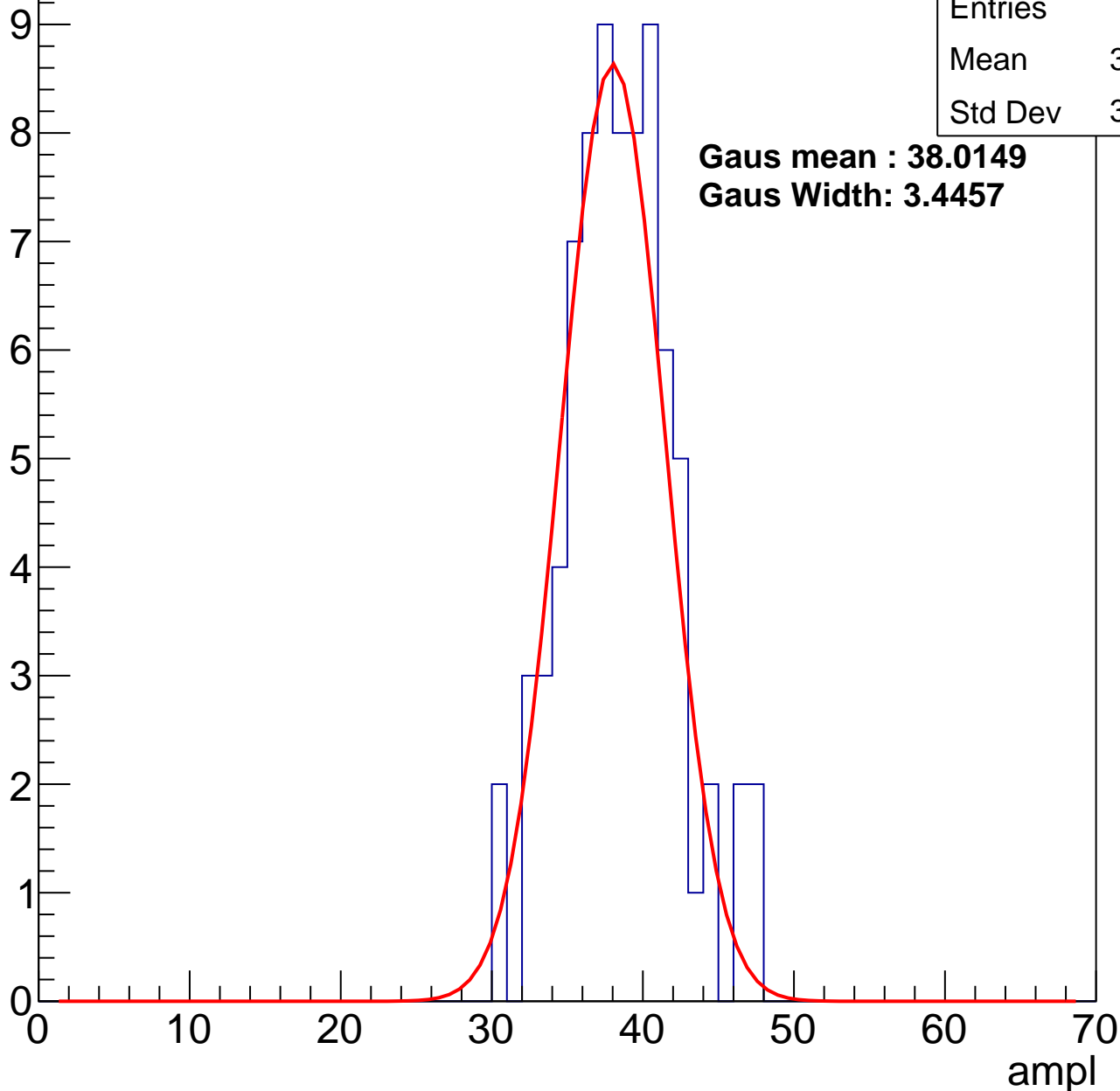
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	38.05
Std Dev	3.635

Gaus mean : 38.0149

Gaus Width: 3.4457



B0L001S, U25-ch33, adc2

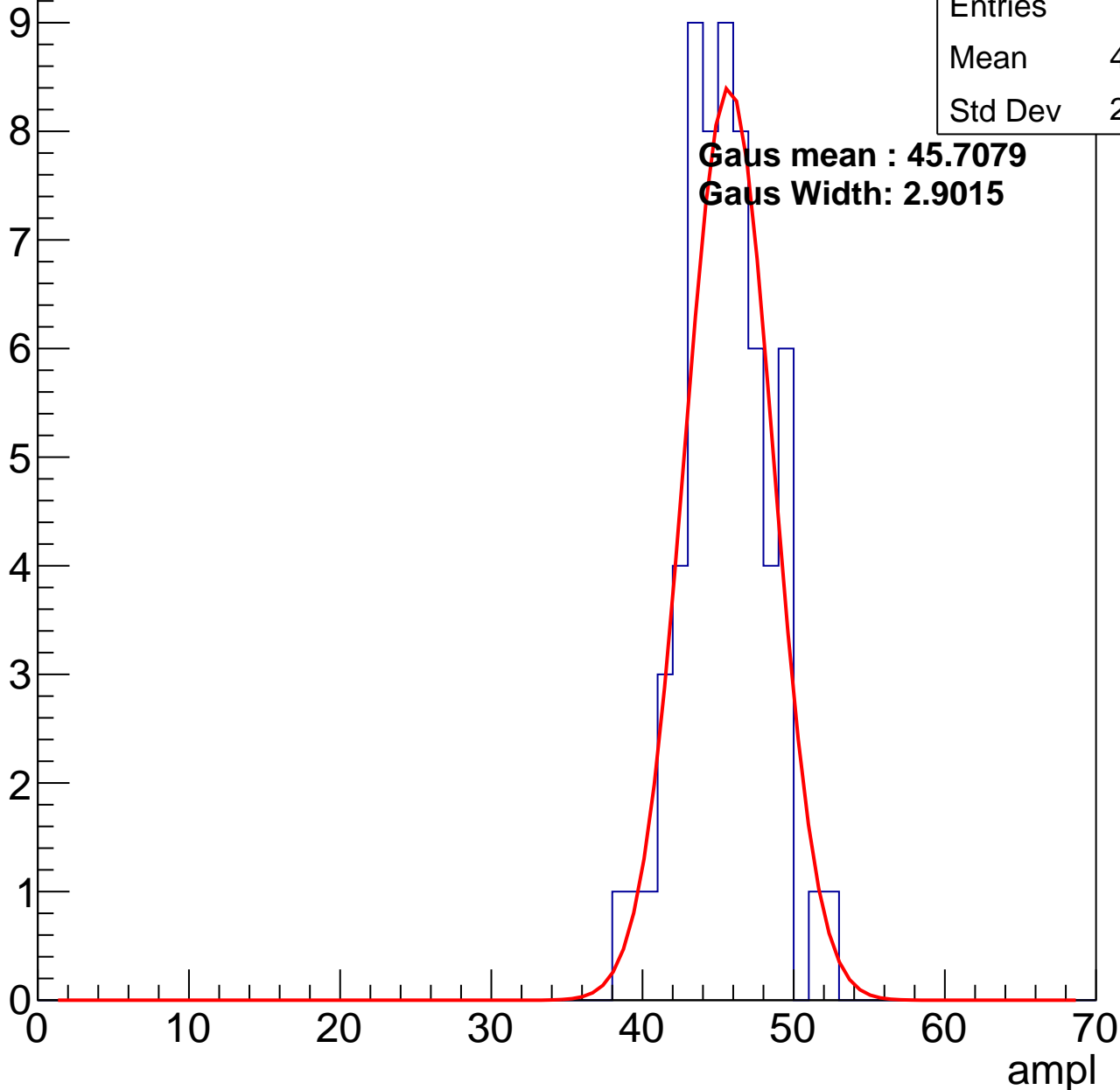
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	45.02
Std Dev	2.803

Gaus mean : 45.7079

Gaus Width: 2.9015



B0L001S, U25-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	52.1
Std Dev	3.481

Entry

10

8

6

4

2

0

0

10

20

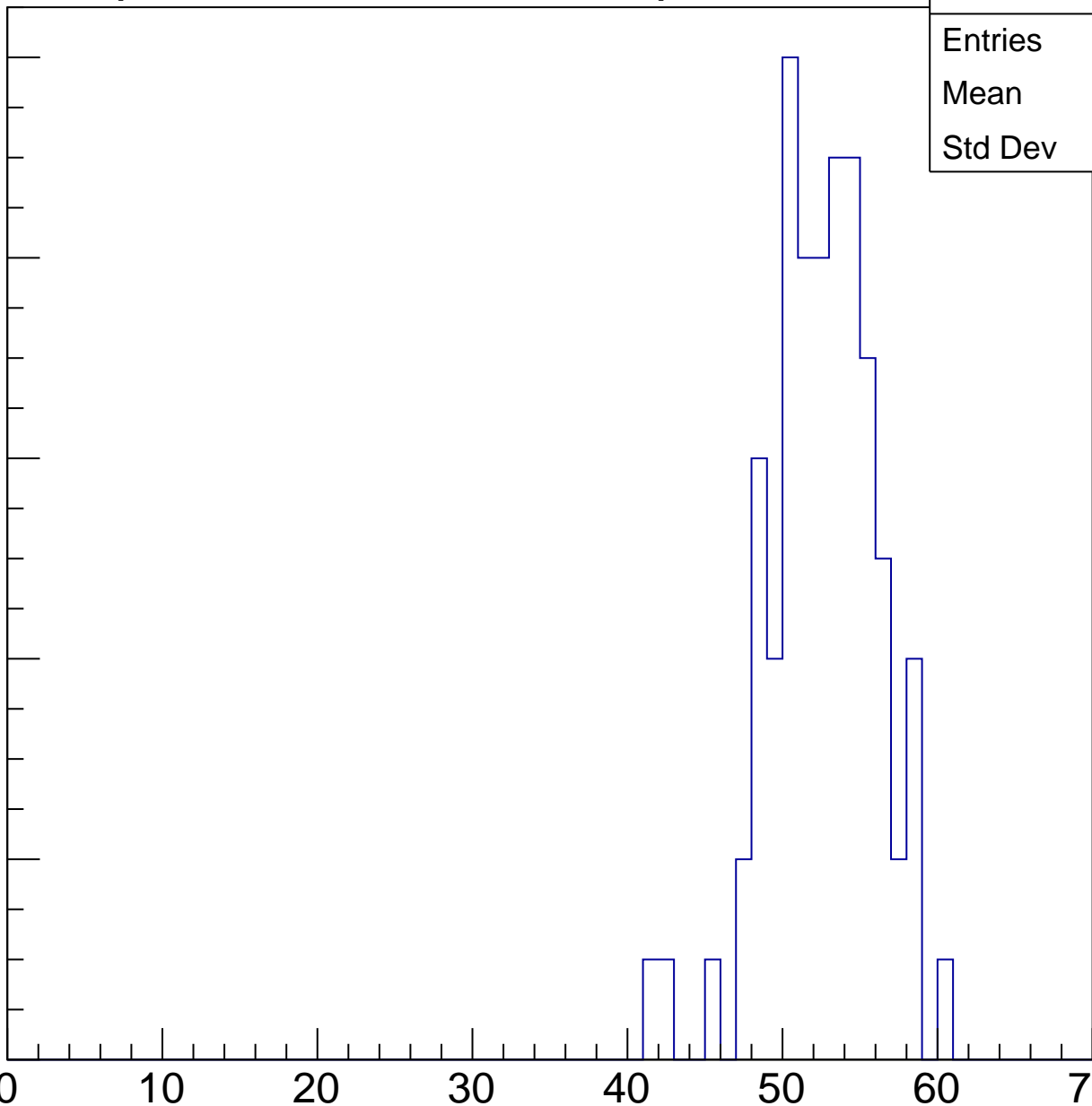
30

40

50

60

ampl

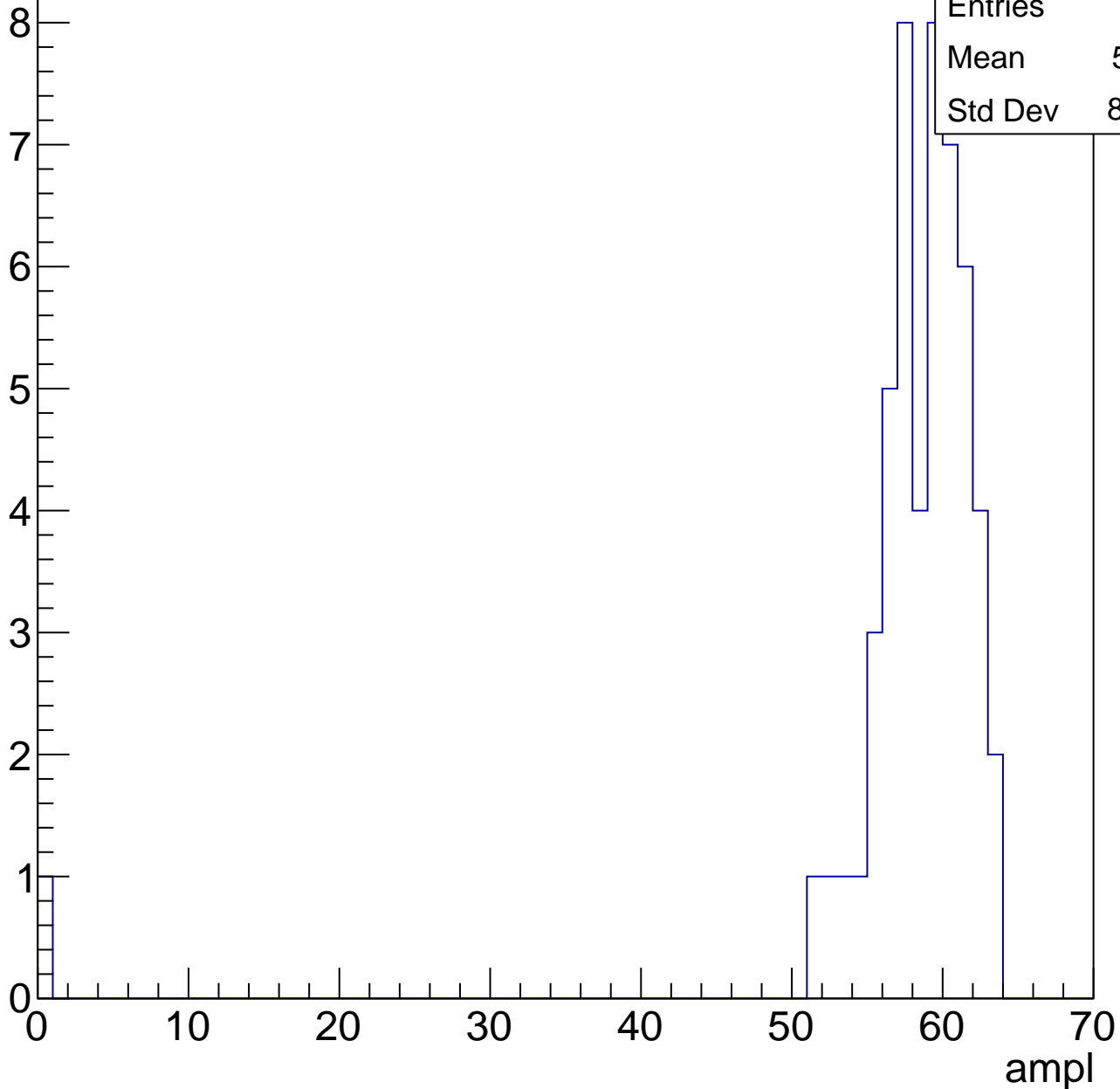


B0L001S, U25-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	57.21
Std Dev	8.454

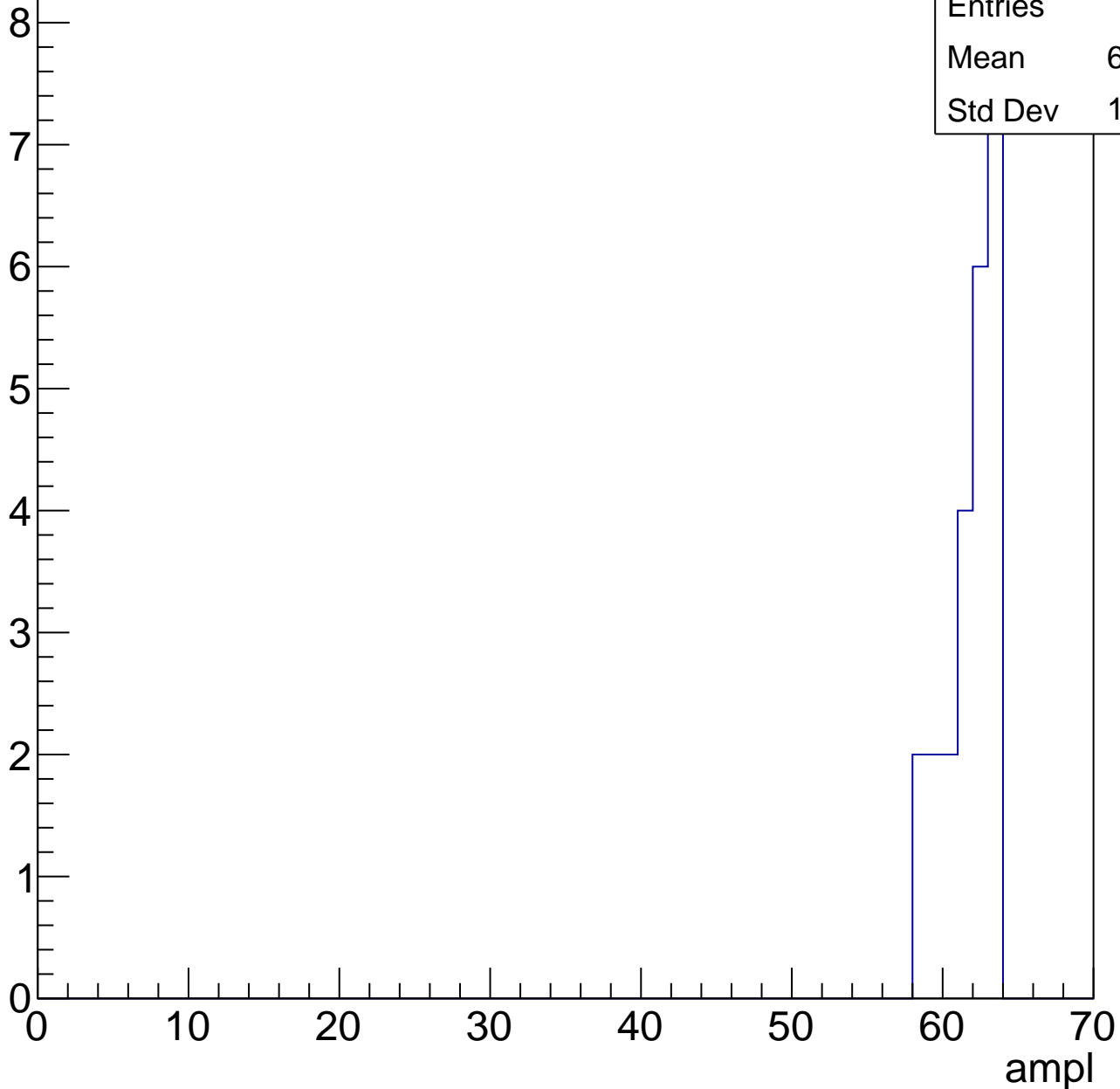


B0L001S, U25-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	61.42
Std Dev	1.605



B0L001S, U25-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch34, adc0

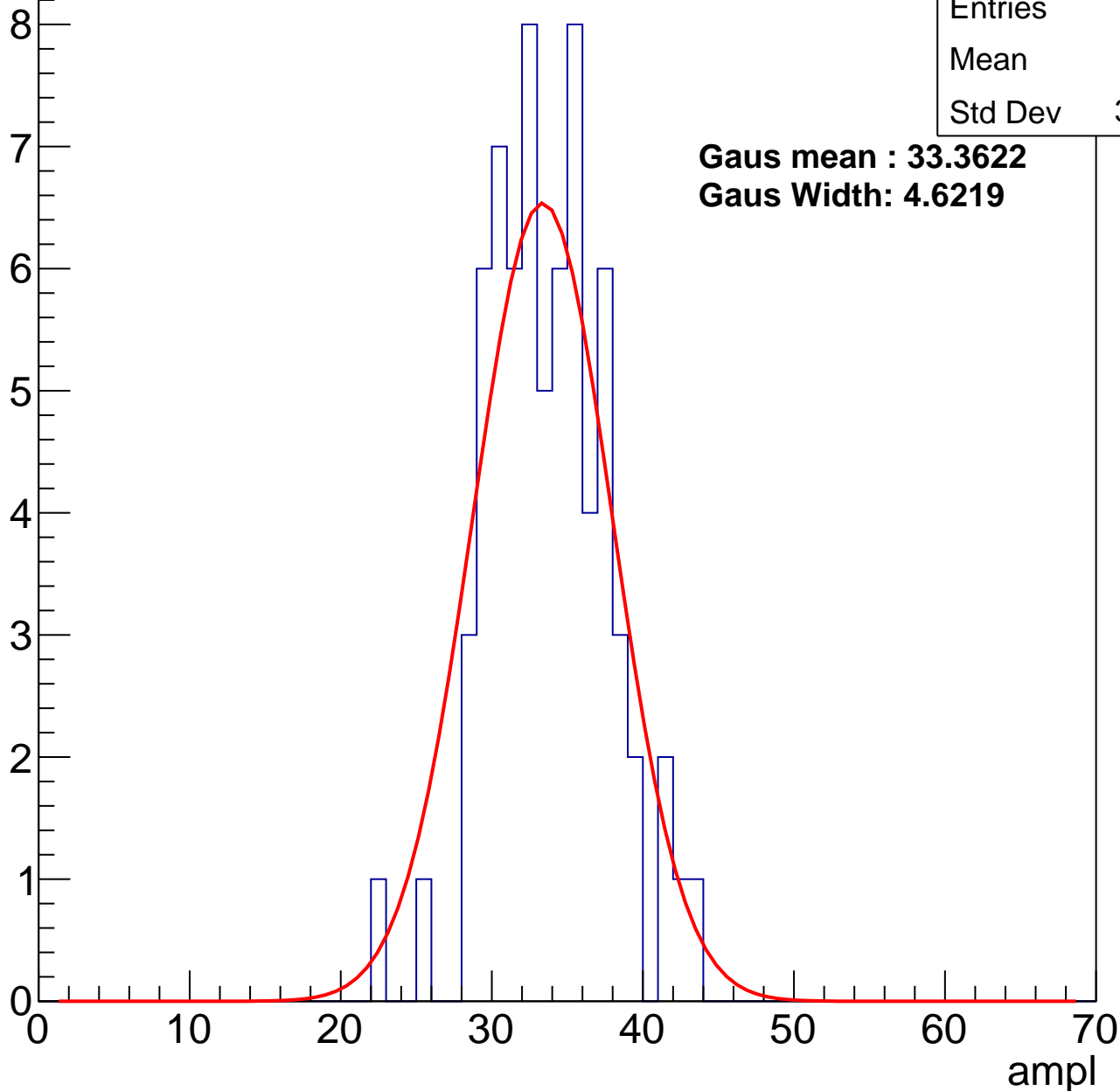
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	33.3
Std Dev	3.911

Gaus mean : 33.3622

Gaus Width: 4.6219



B0L001S, U25-ch34, adc1

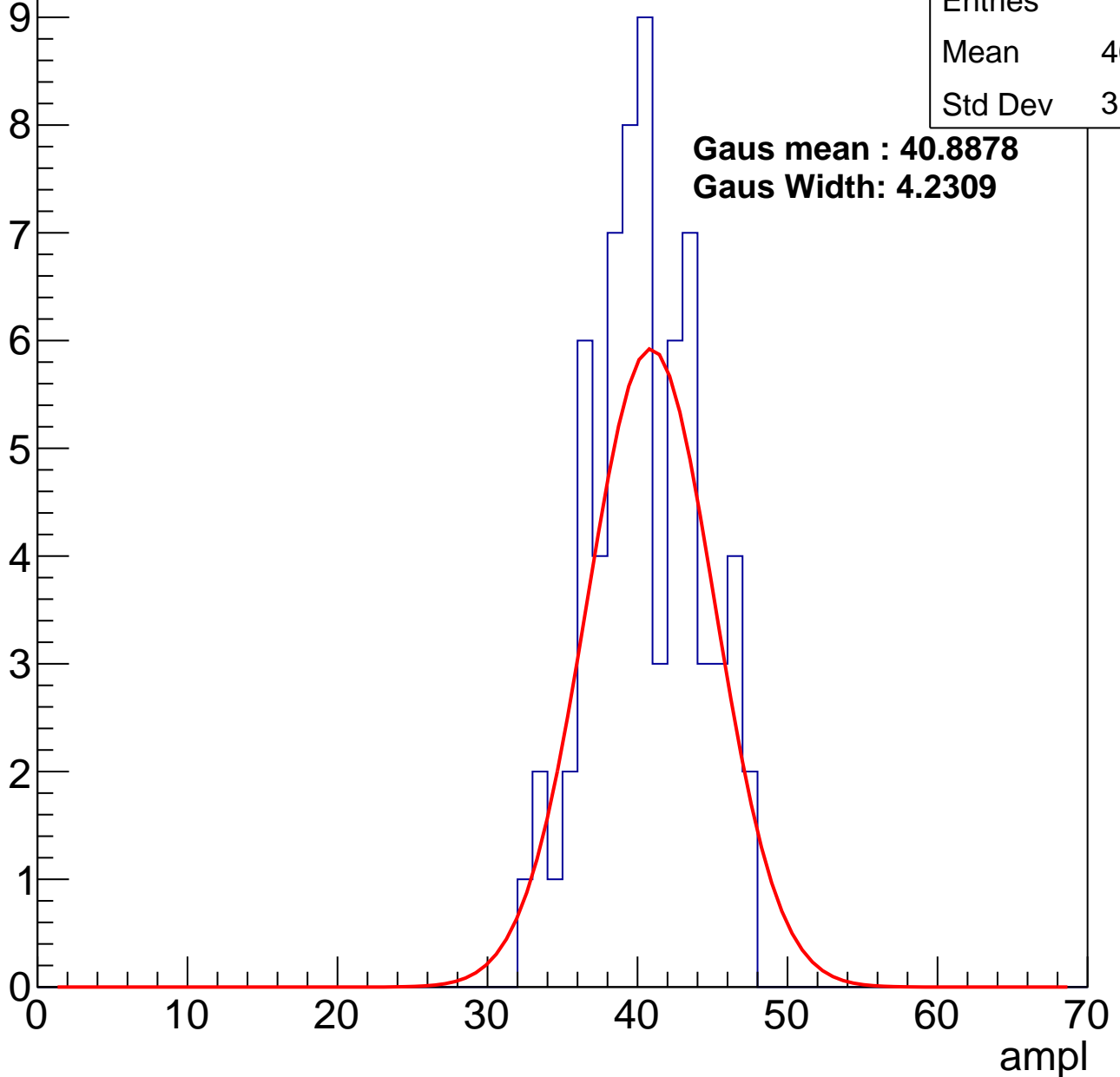
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	40.07
Std Dev	3.578

Gaus mean : 40.8878

Gaus Width: 4.2309



B0L001S, U25-ch34, adc2

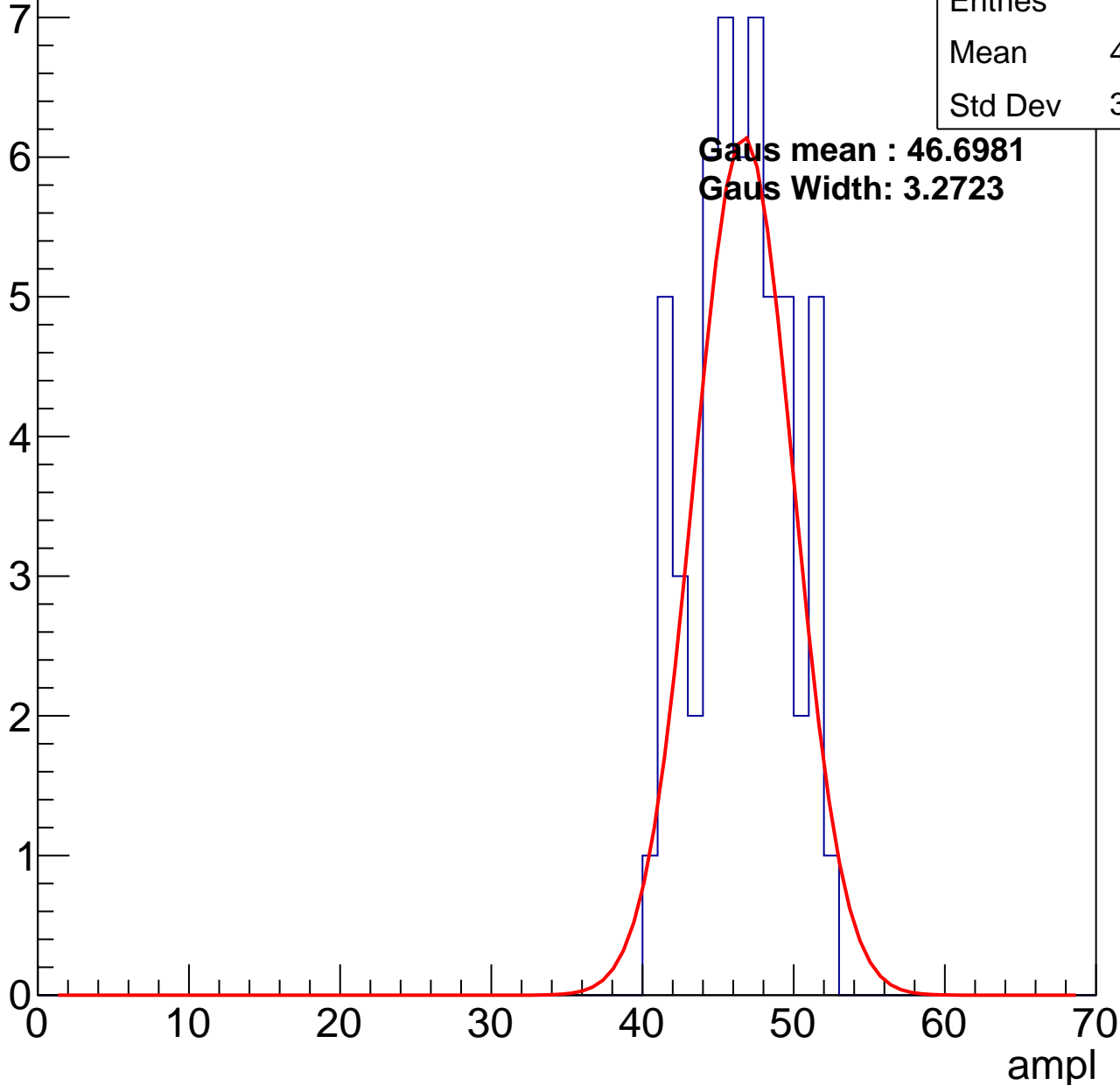
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	46.05
Std Dev	3.083

Gaus mean : 46.6981

Gaus Width: 3.2723

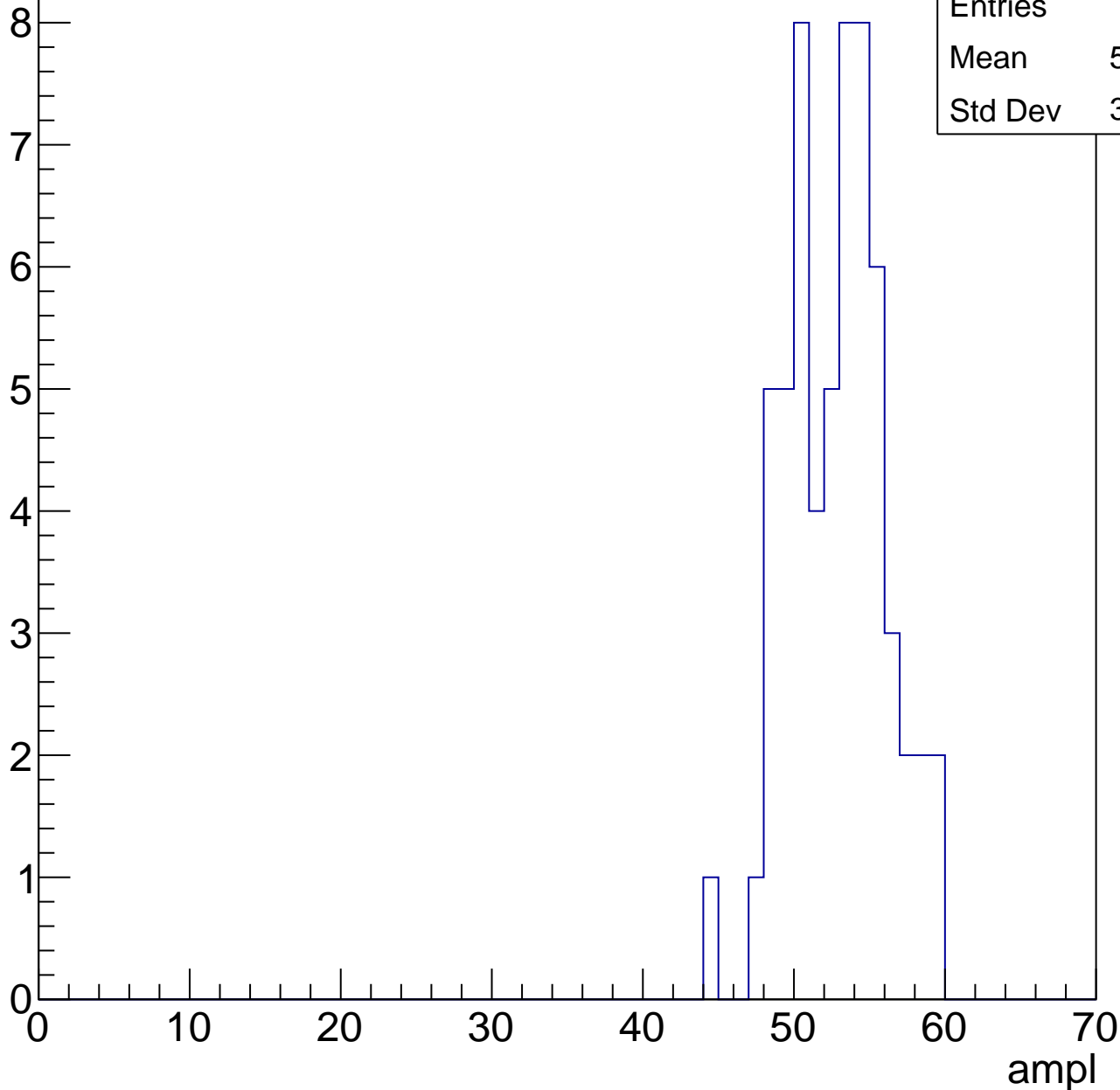


B0L001S, U25-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

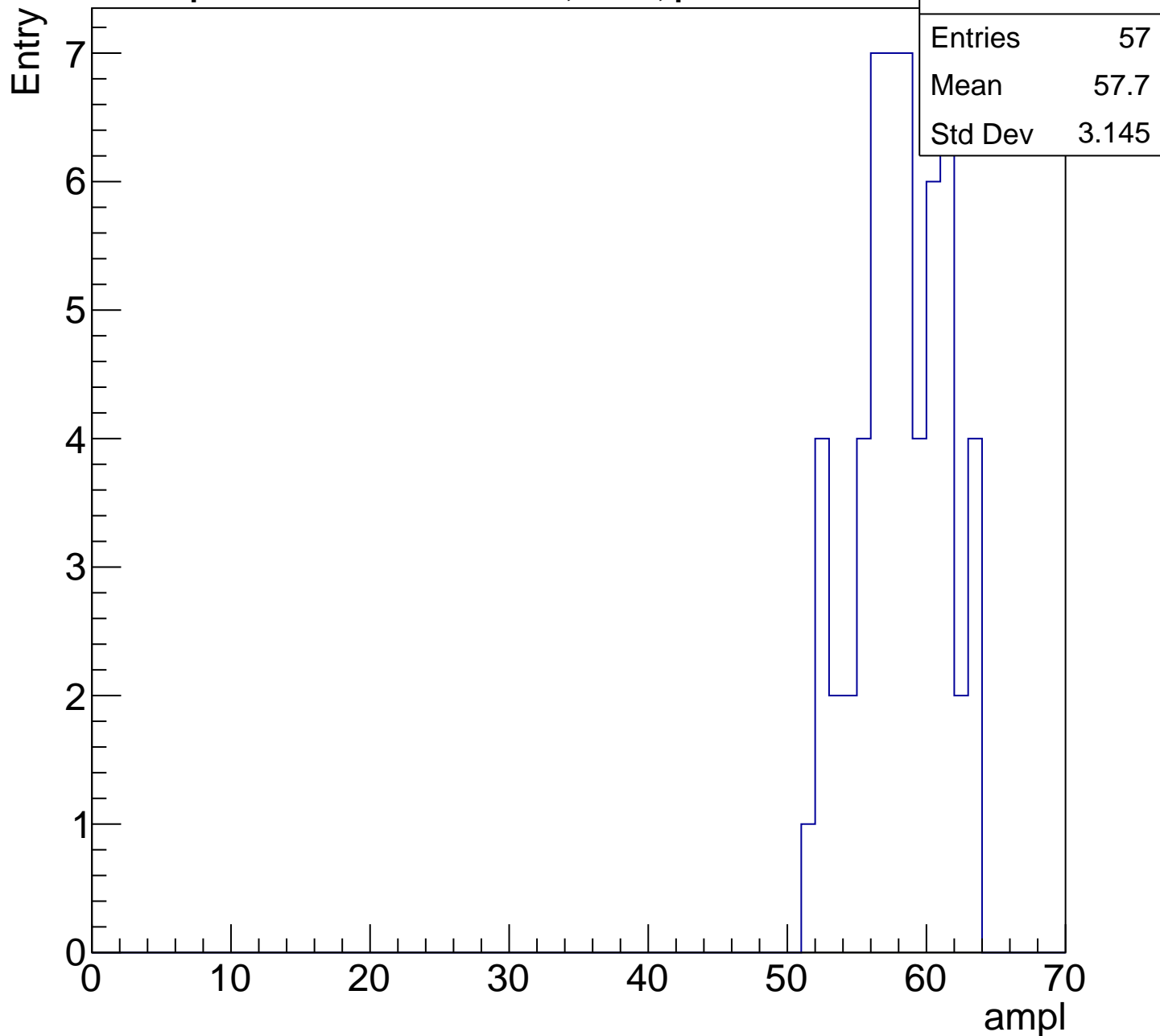
Entry

Entries	60
Mean	52.37
Std Dev	3.173



B0L001S, U25-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

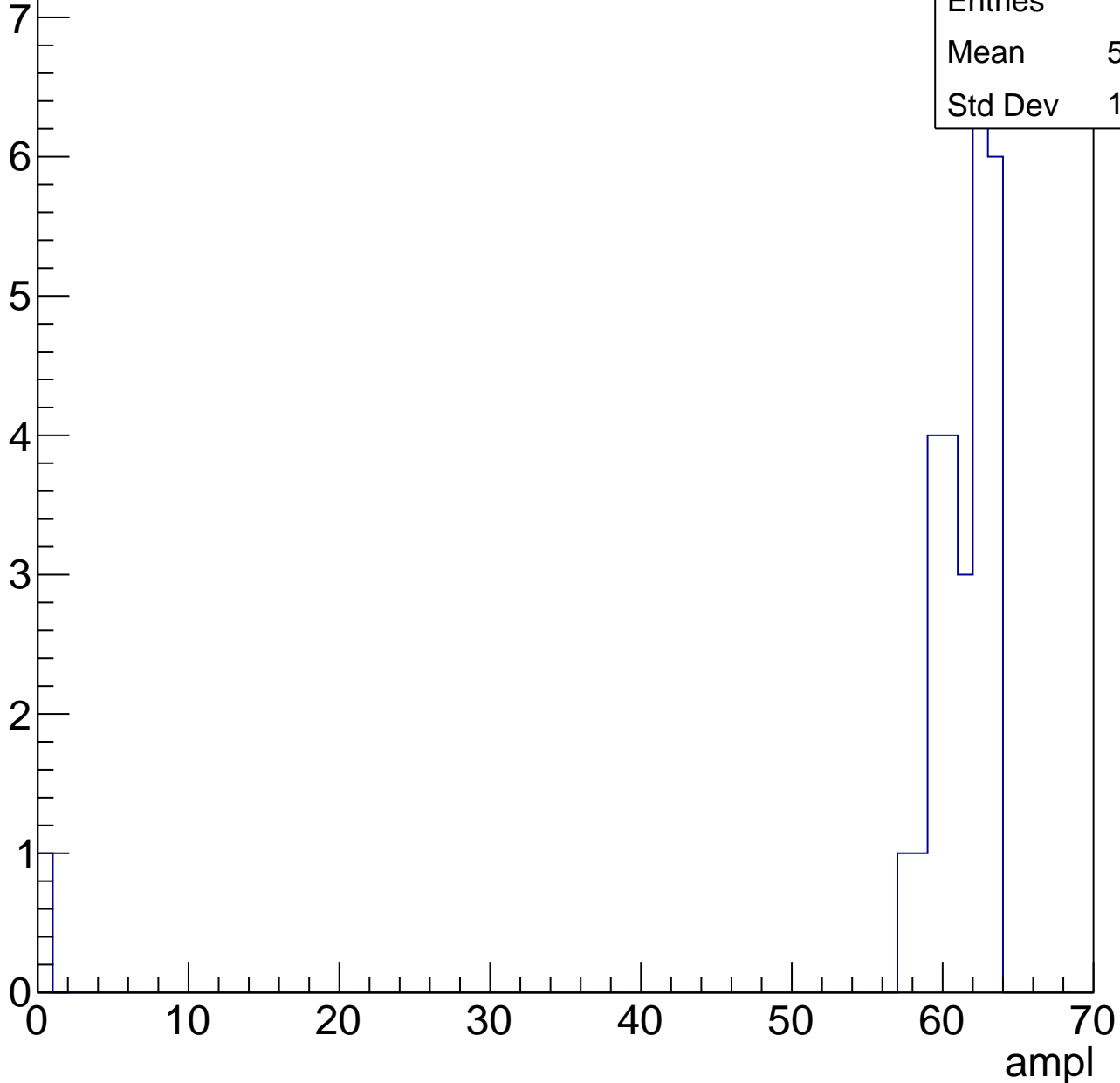


B0L001S, U25-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.74
Std Dev	11.64



B0L001S, U25-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	62
Std Dev	0

B0L001S, U25-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch35, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	30.36
Std Dev	4.84

Gaus mean : 30.8181

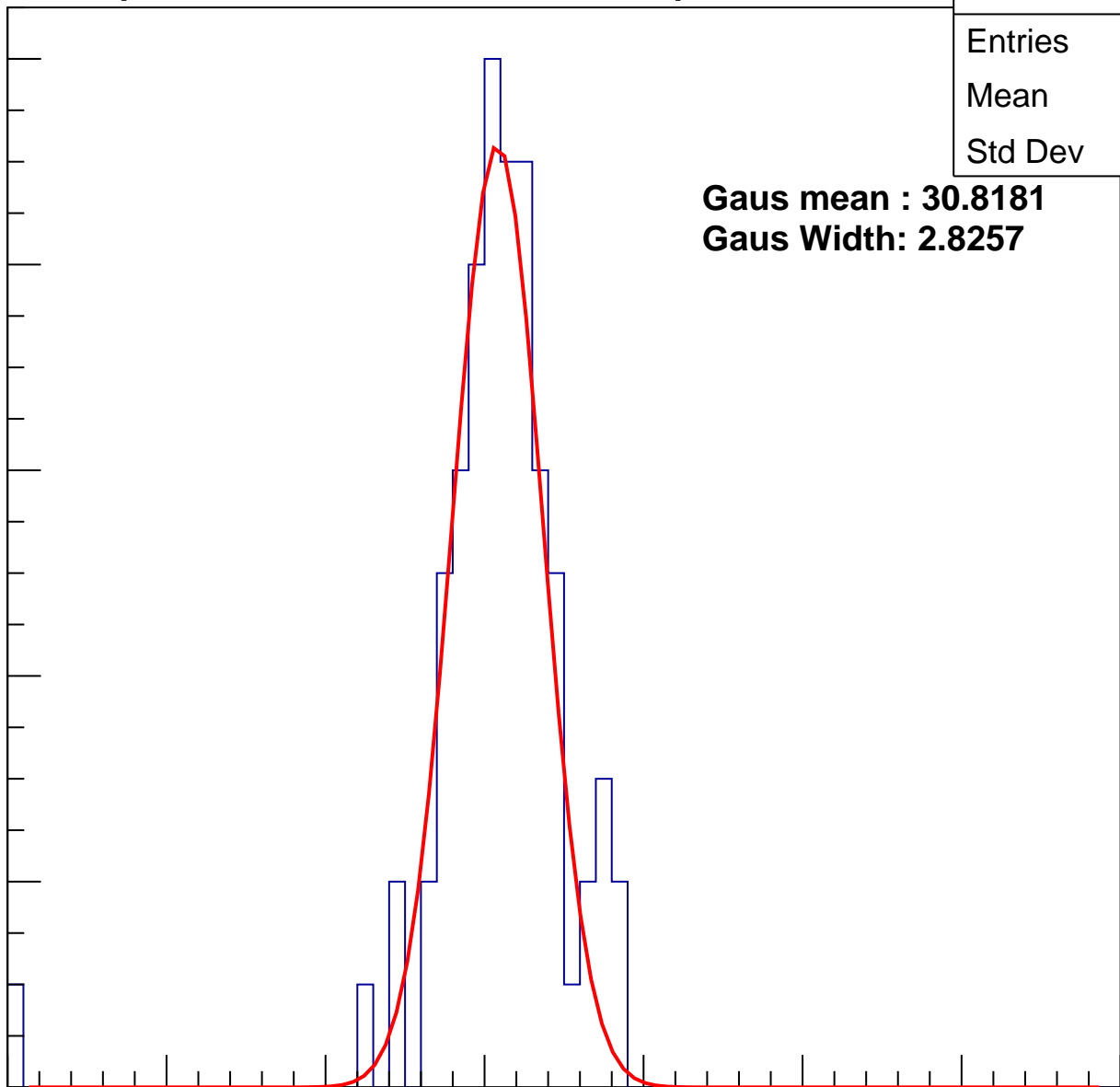
Gaus Width: 2.8257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



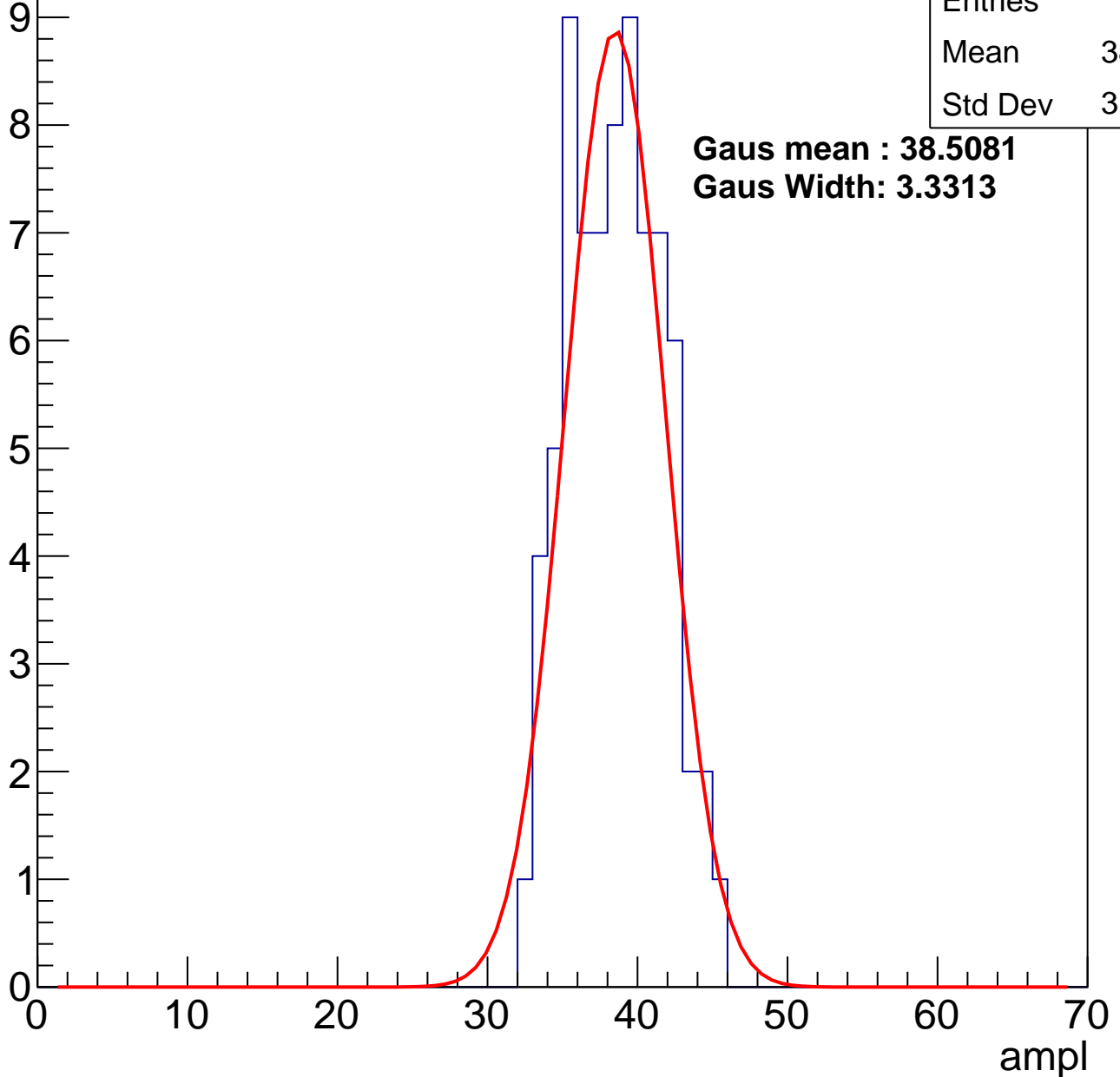
B0L001S, U25-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	38.04
Std Dev	3.053

Gaus mean : 38.5081
Gaus Width: 3.3313



B0L001S, U25-ch35, adc2

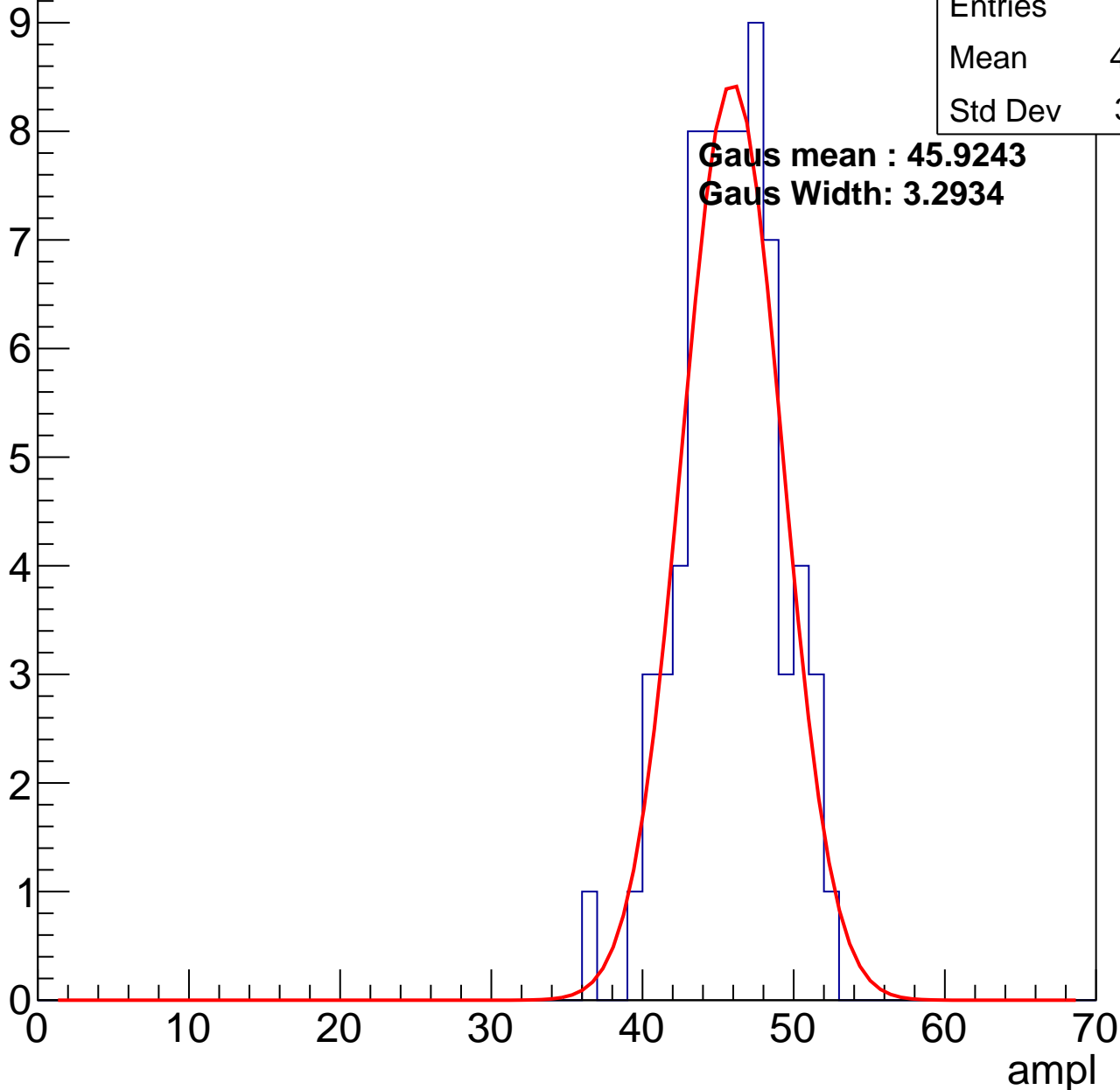
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	45.37
Std Dev	3.181

Gaus mean : 45.9243

Gaus Width: 3.2934

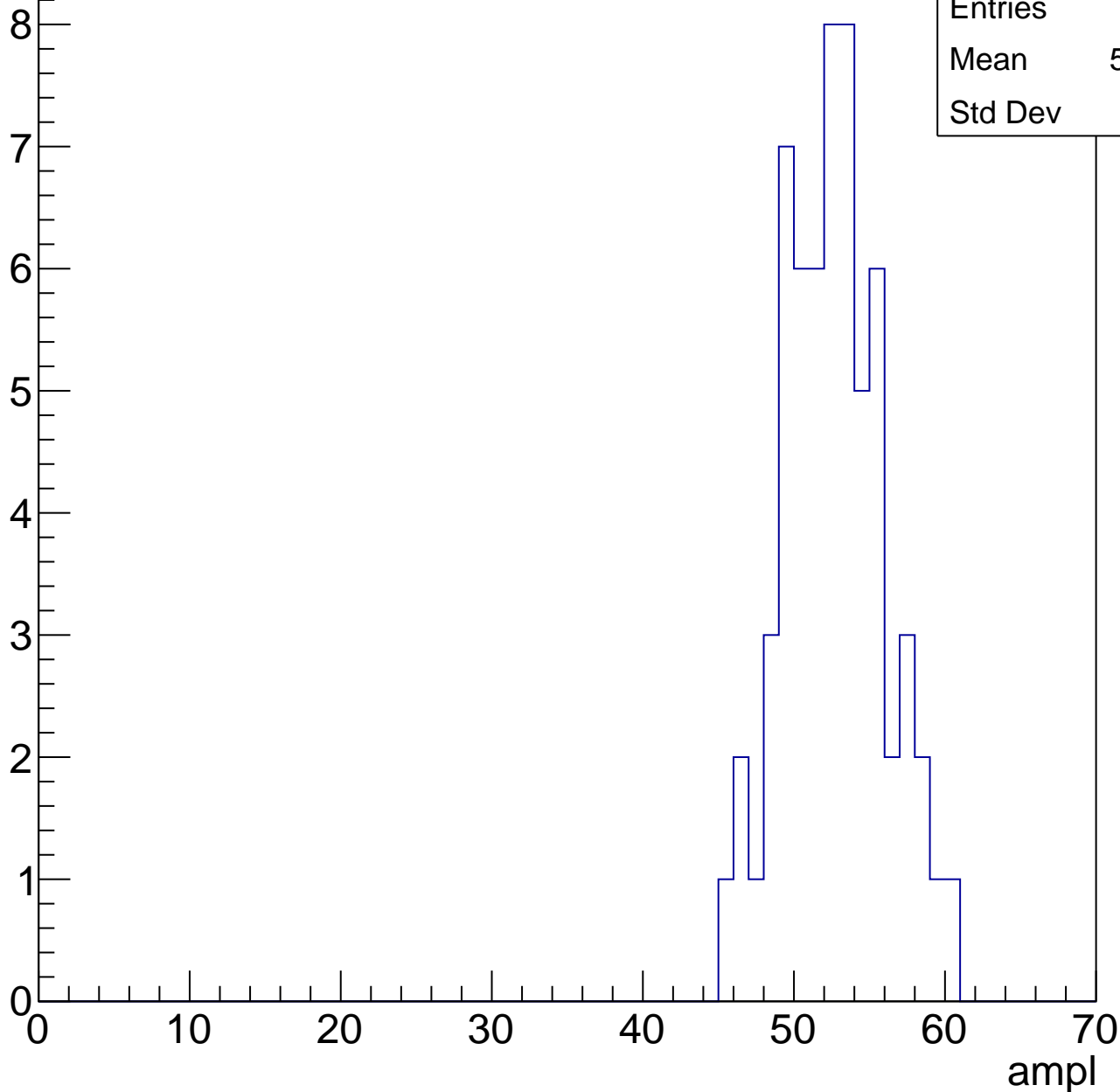


B0L001S, U25-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	52.18
Std Dev	3.26

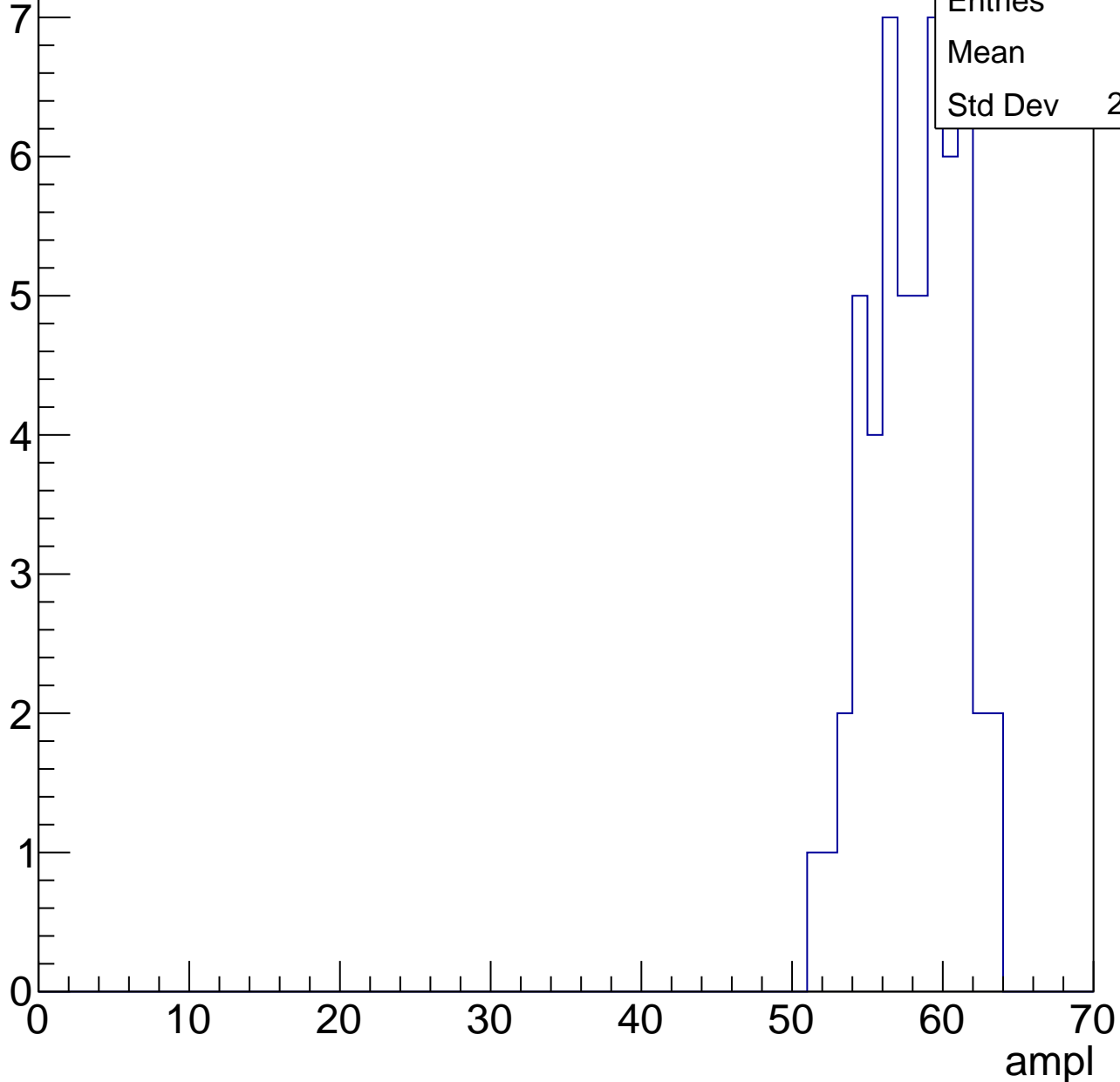


B0L001S, U25-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	57.7
Std Dev	2.904

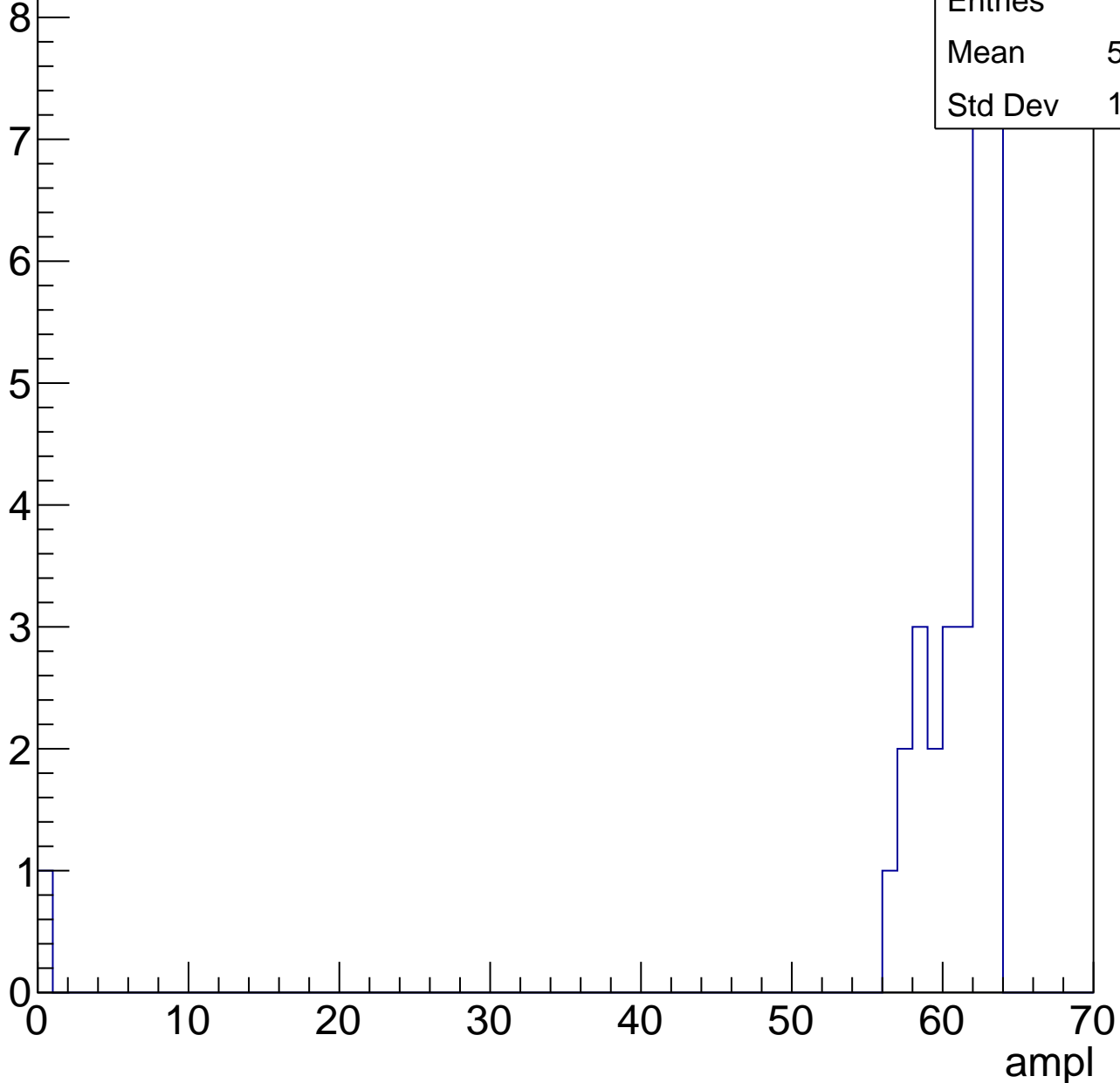


B0L001S, U25-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.87
Std Dev	10.95



B0L001S, U25-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch36, adc0

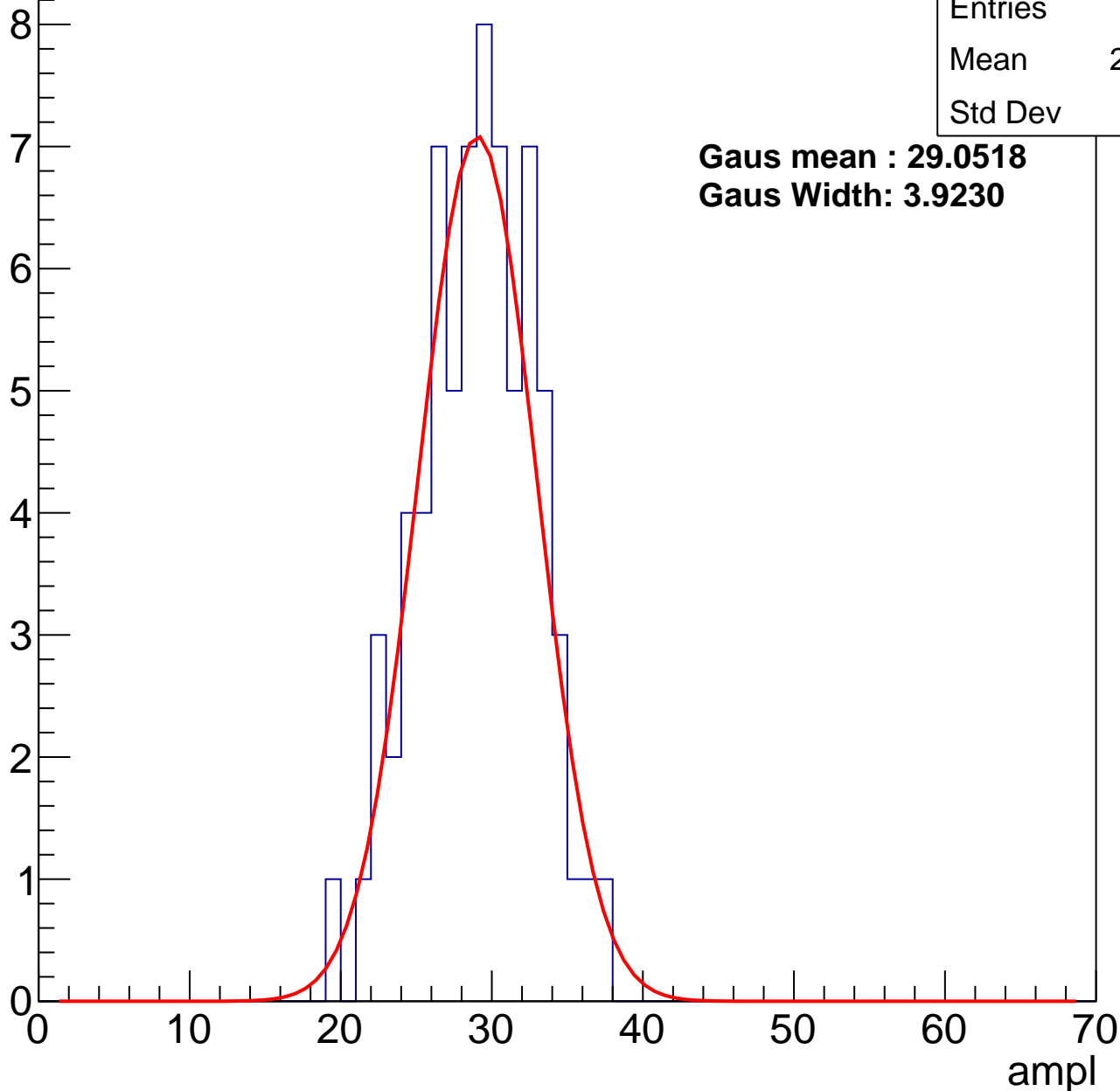
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	28.57
Std Dev	3.76

Gaus mean : 29.0518

Gaus Width: 3.9230



B0L001S, U25-ch36, adc1

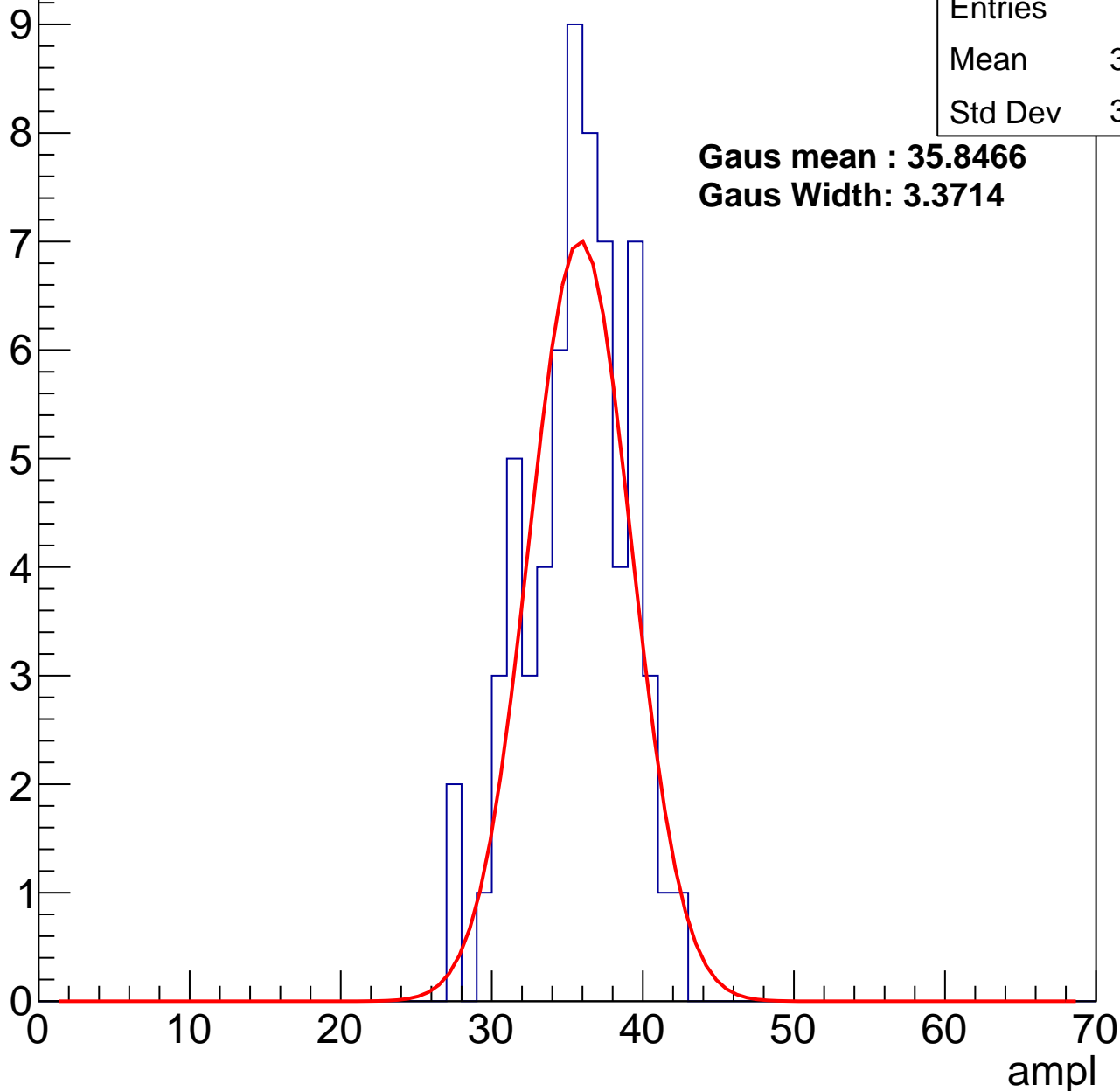
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	35.16
Std Dev	3.332

Gaus mean : 35.8466

Gaus Width: 3.3714



B0L001S, U25-ch36, adc2

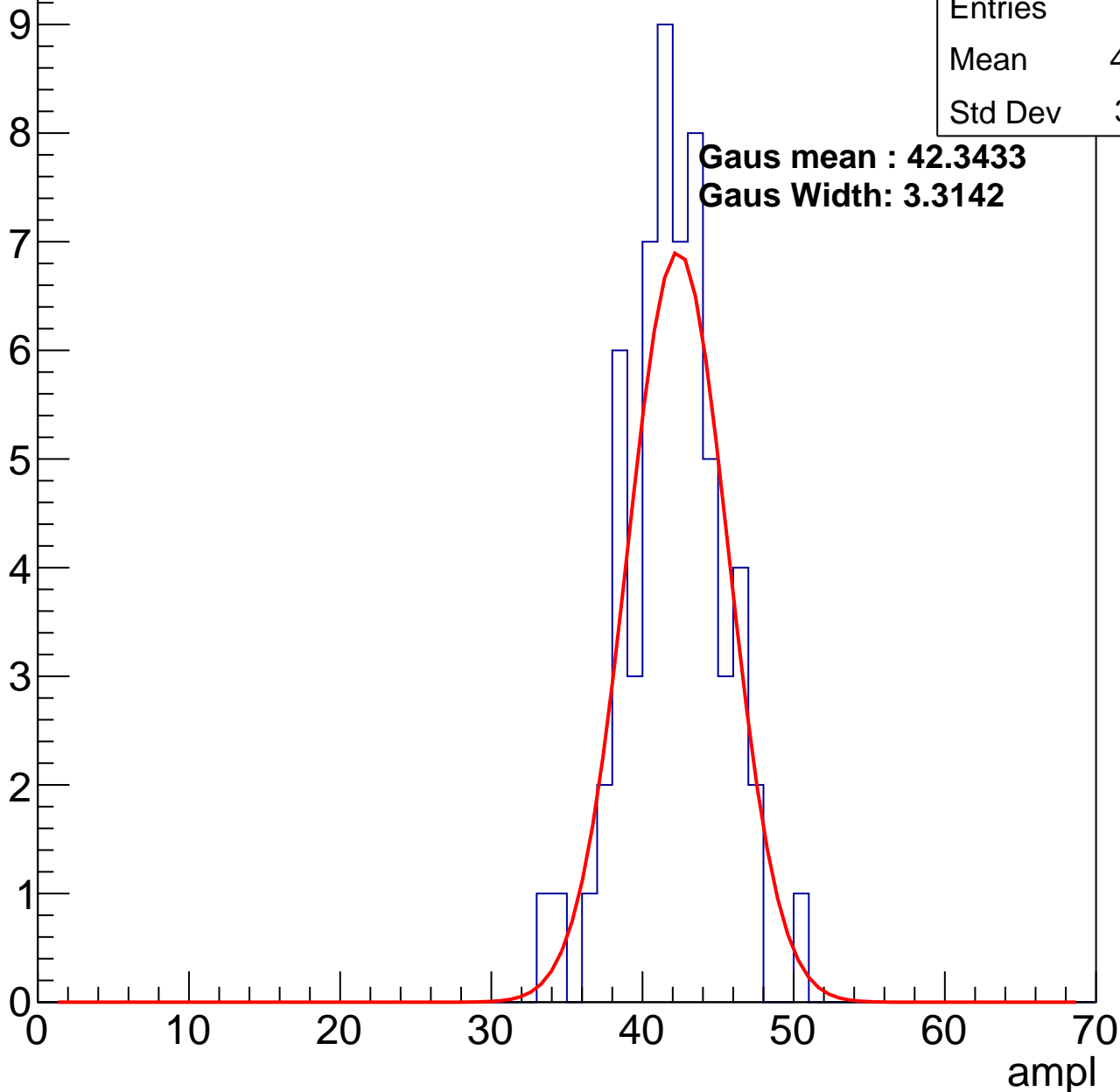
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	41.53
Std Dev	3.191

Gaus mean : 42.3433

Gaus Width: 3.3142



B0L001S, U25-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	48.8
Std Dev	3.579

Entry

10

8

6

4

2

0

0

10

20

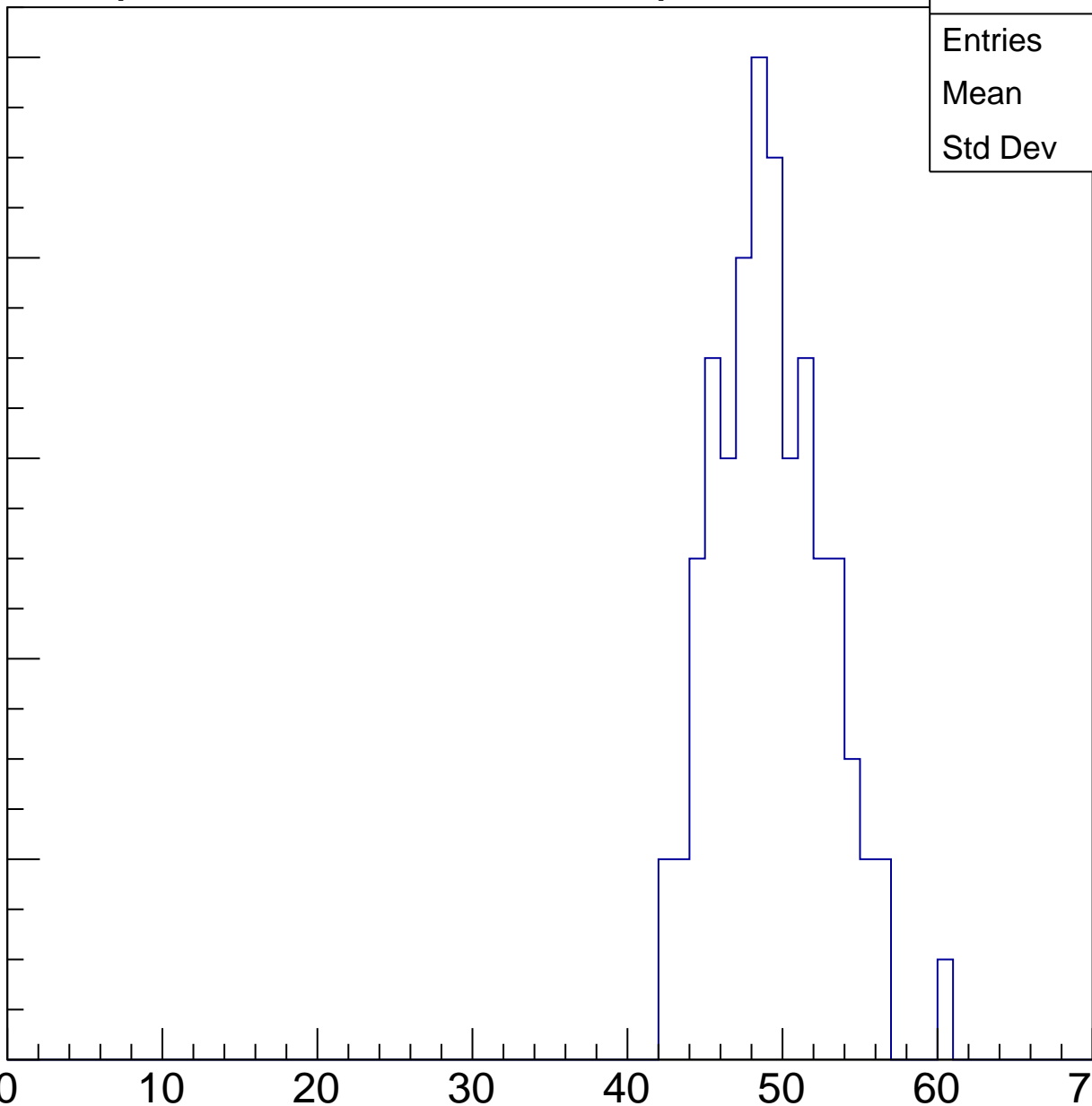
30

40

50

60

ampl

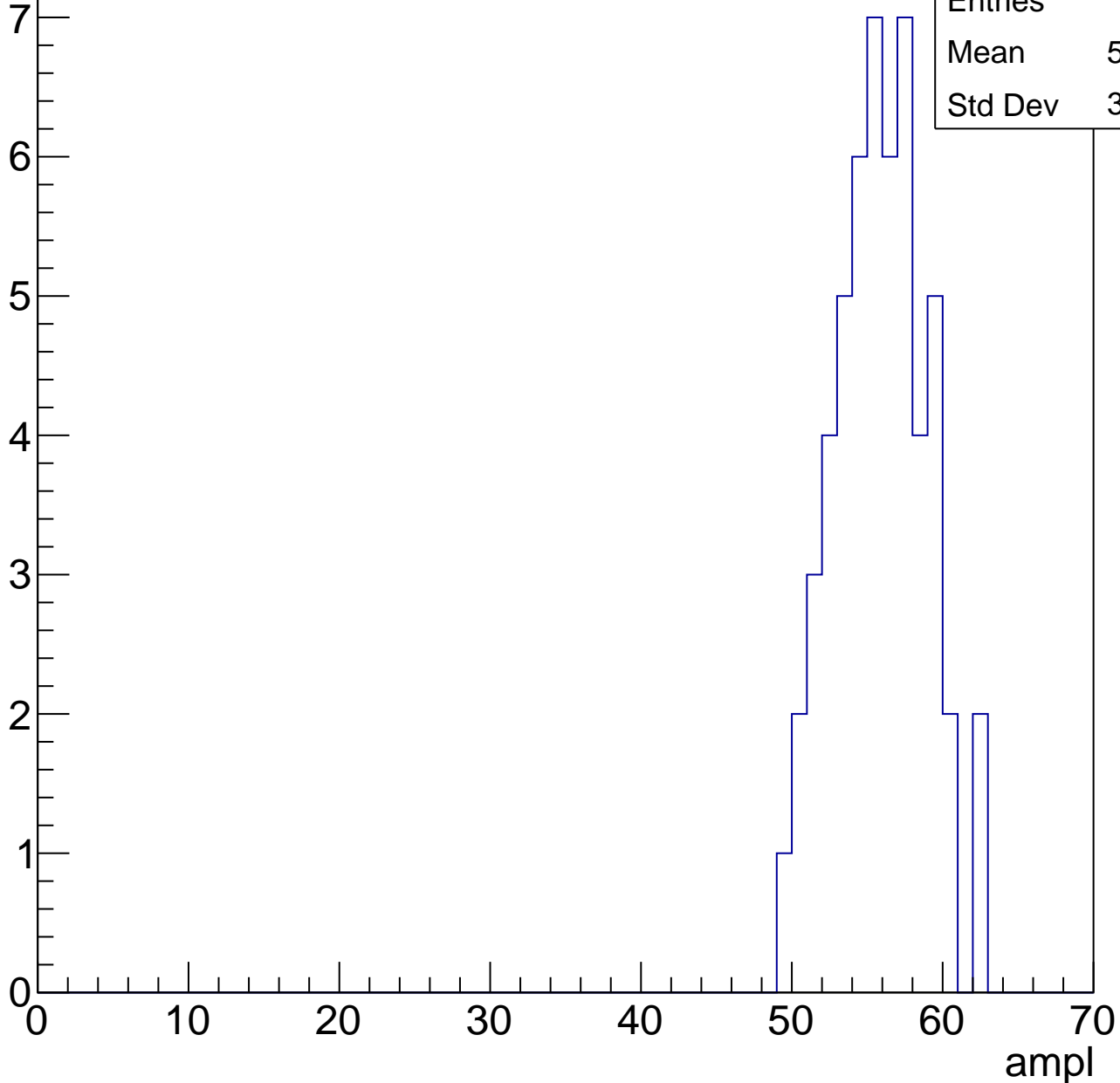


B0L001S, U25-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.37
Std Dev	3.002

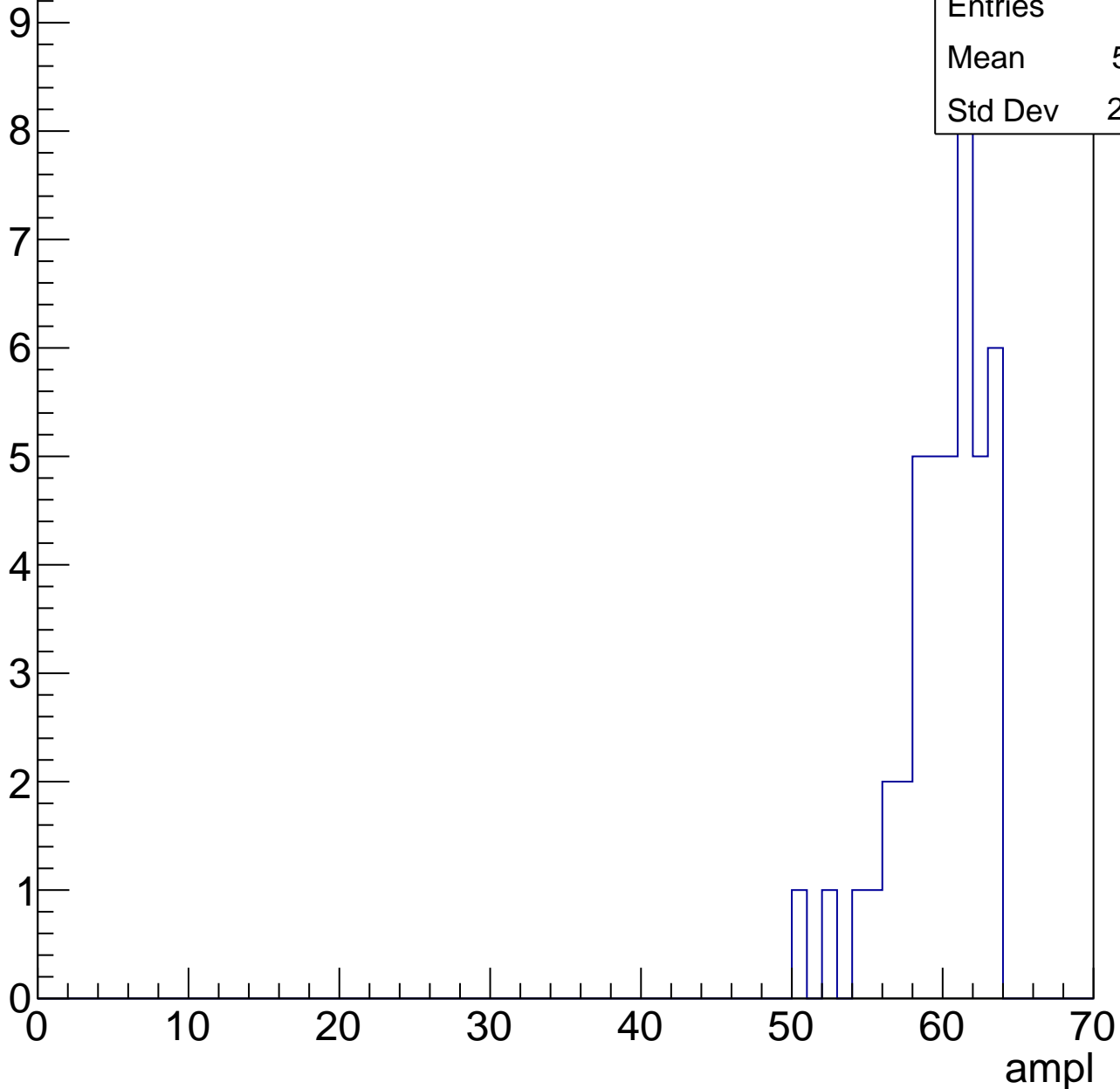


B0L001S, U25-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

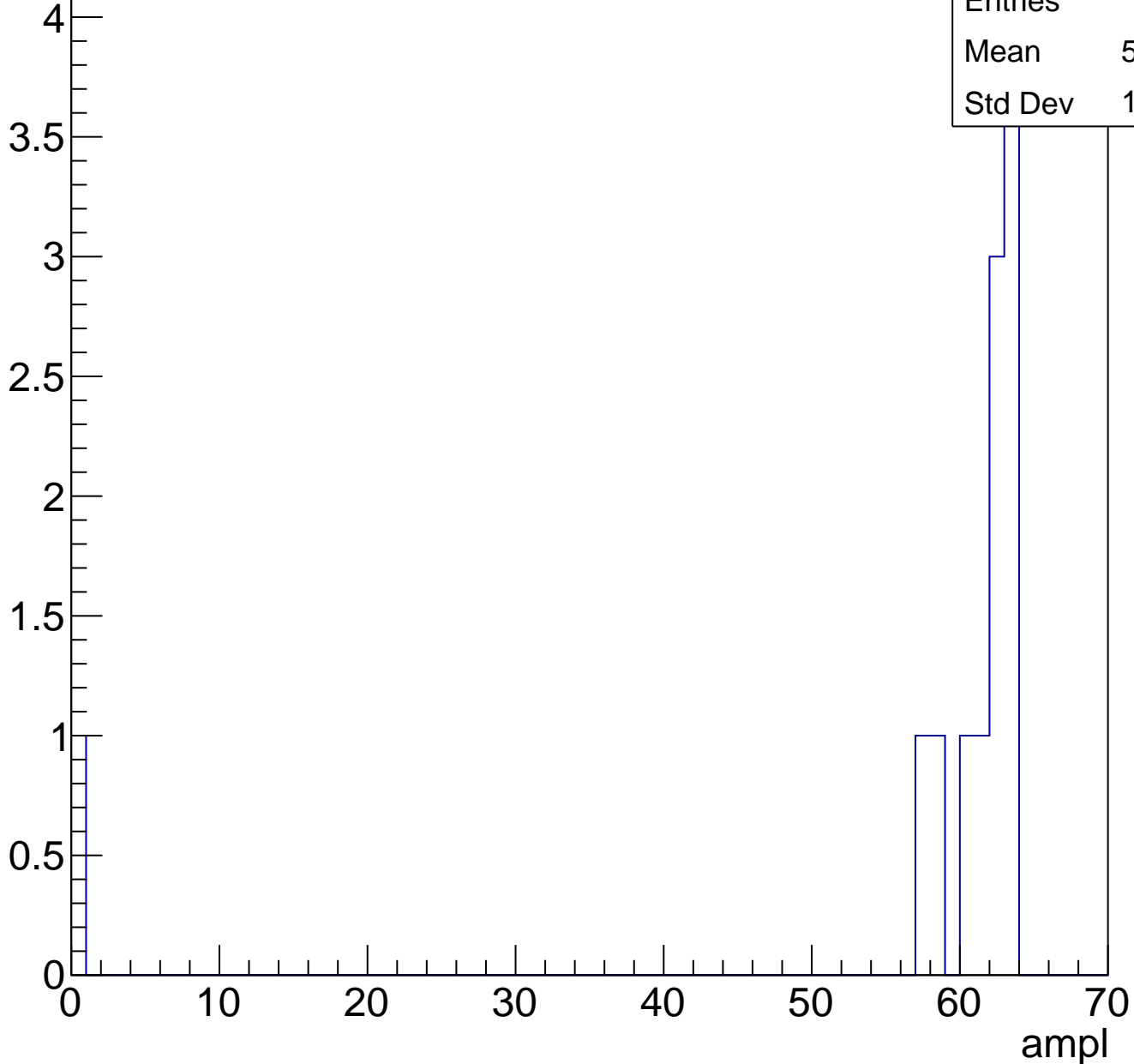
Entries	43
Mean	59.51
Std Dev	2.944



B0L001S, U25-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch37, adc0

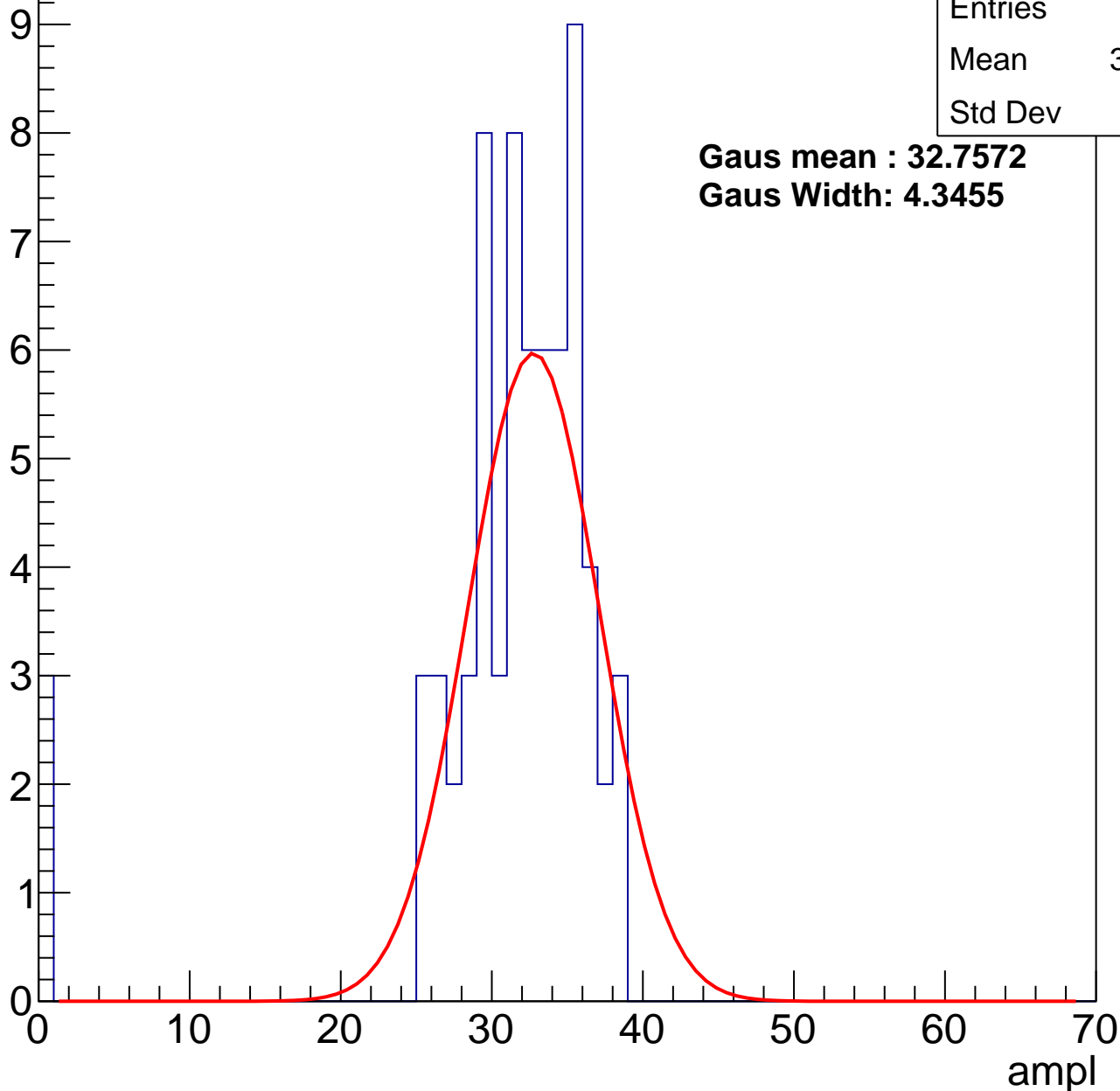
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.46
Std Dev	7.31

Gaus mean : 32.7572

Gaus Width: 4.3455



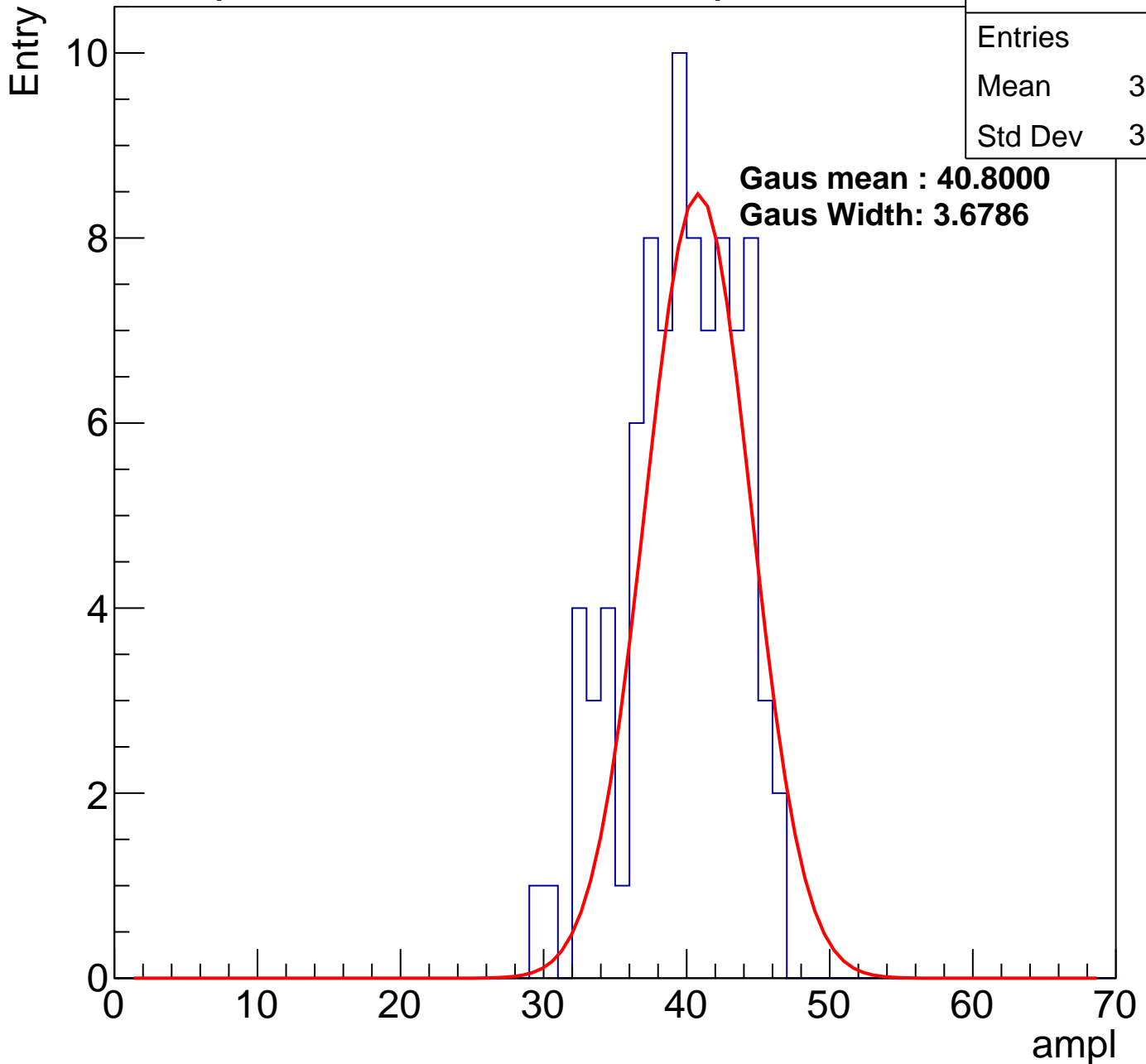
B0L001S, U25-ch37, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	39.18
Std Dev	3.869

Gaus mean : 40.8000

Gaus Width: 3.6786



B0L001S, U25-ch37, adc2

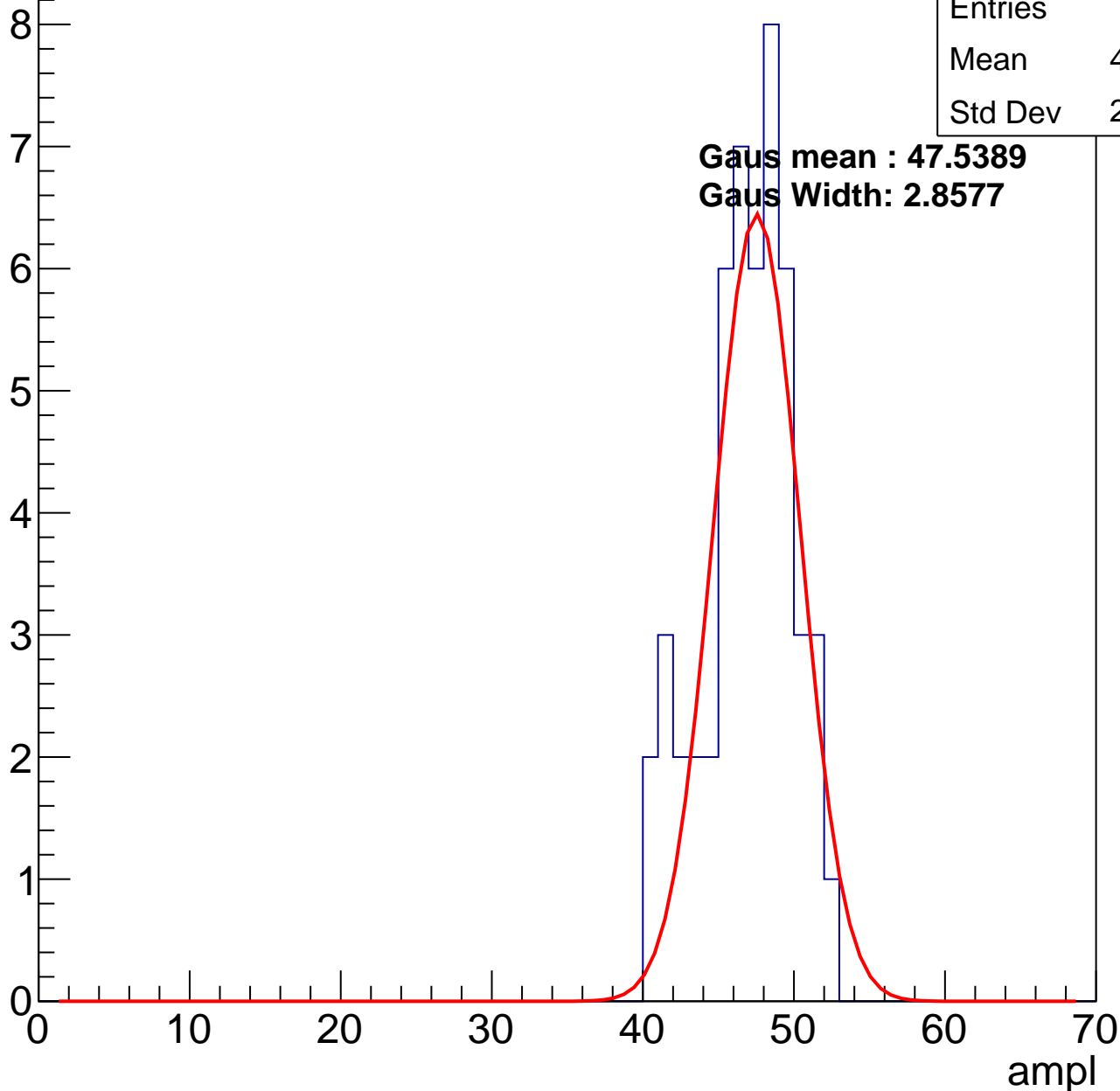
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	46.43
Std Dev	2.979

Gaus mean : 47.5389

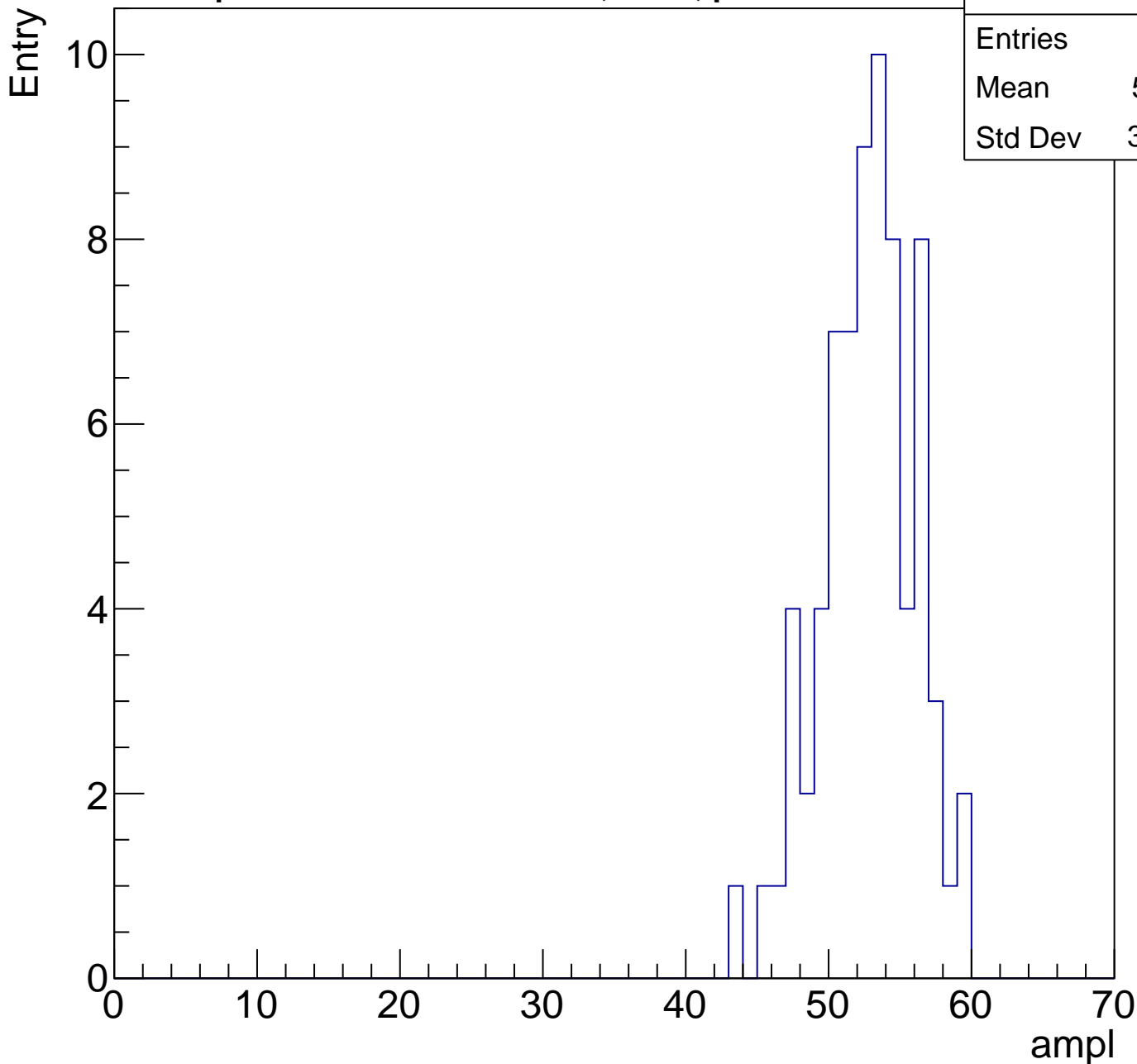
Gaus Width: 2.8577



B0L001S, U25-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

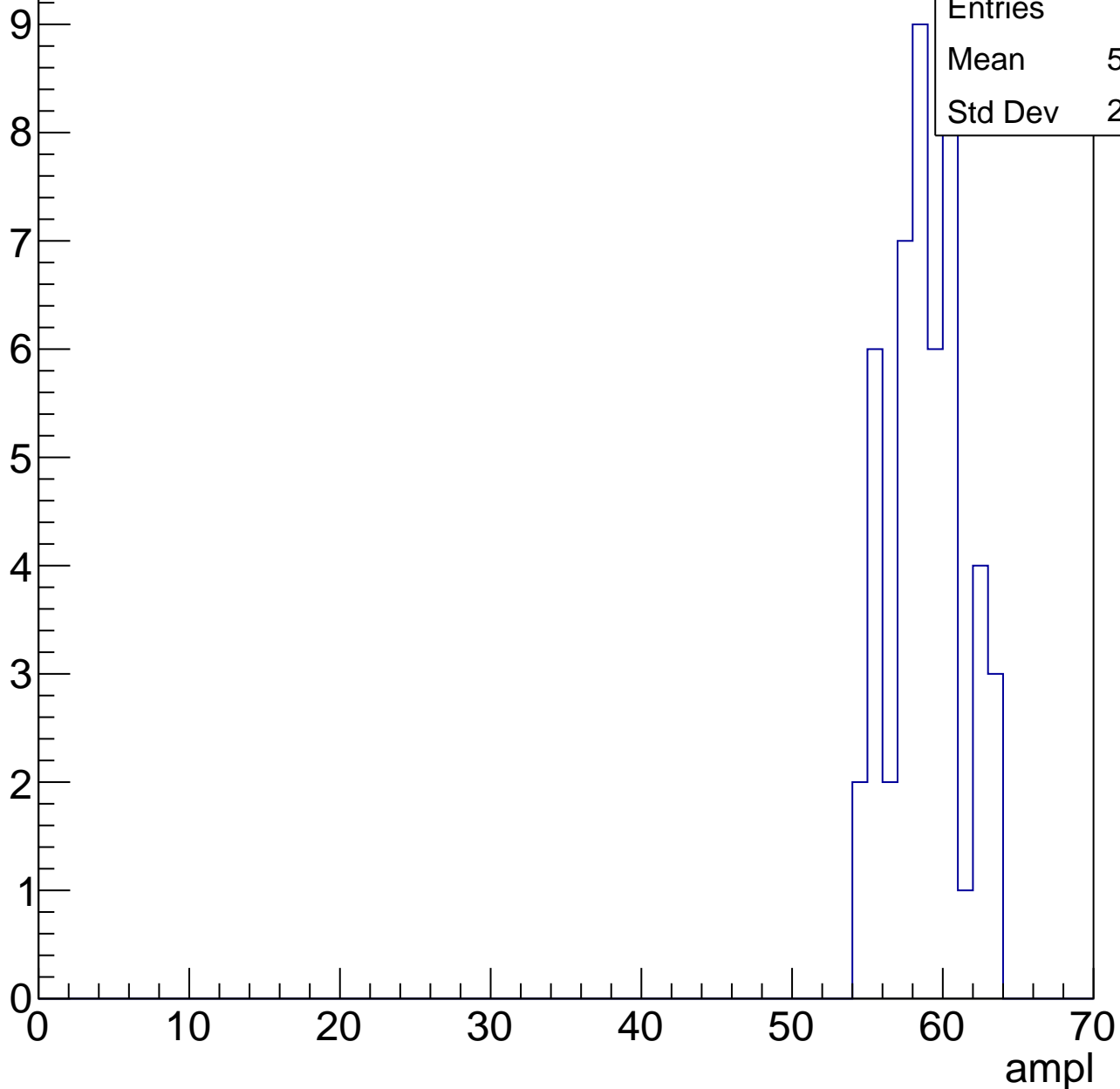
Entries	72
Mean	52.31
Std Dev	3.286



B0L001S, U25-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

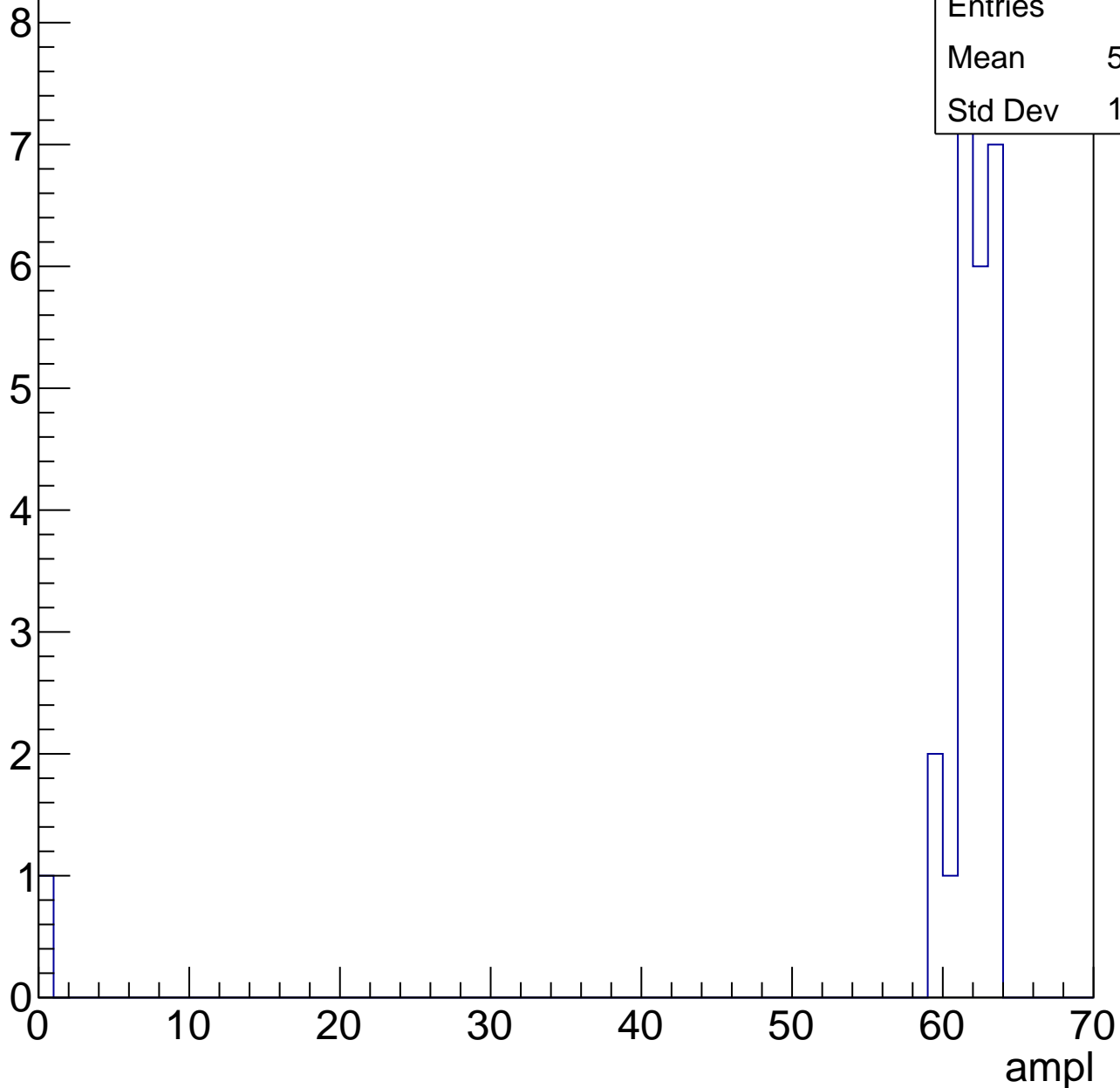
Entry



B0L001S, U25-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch38, adc0

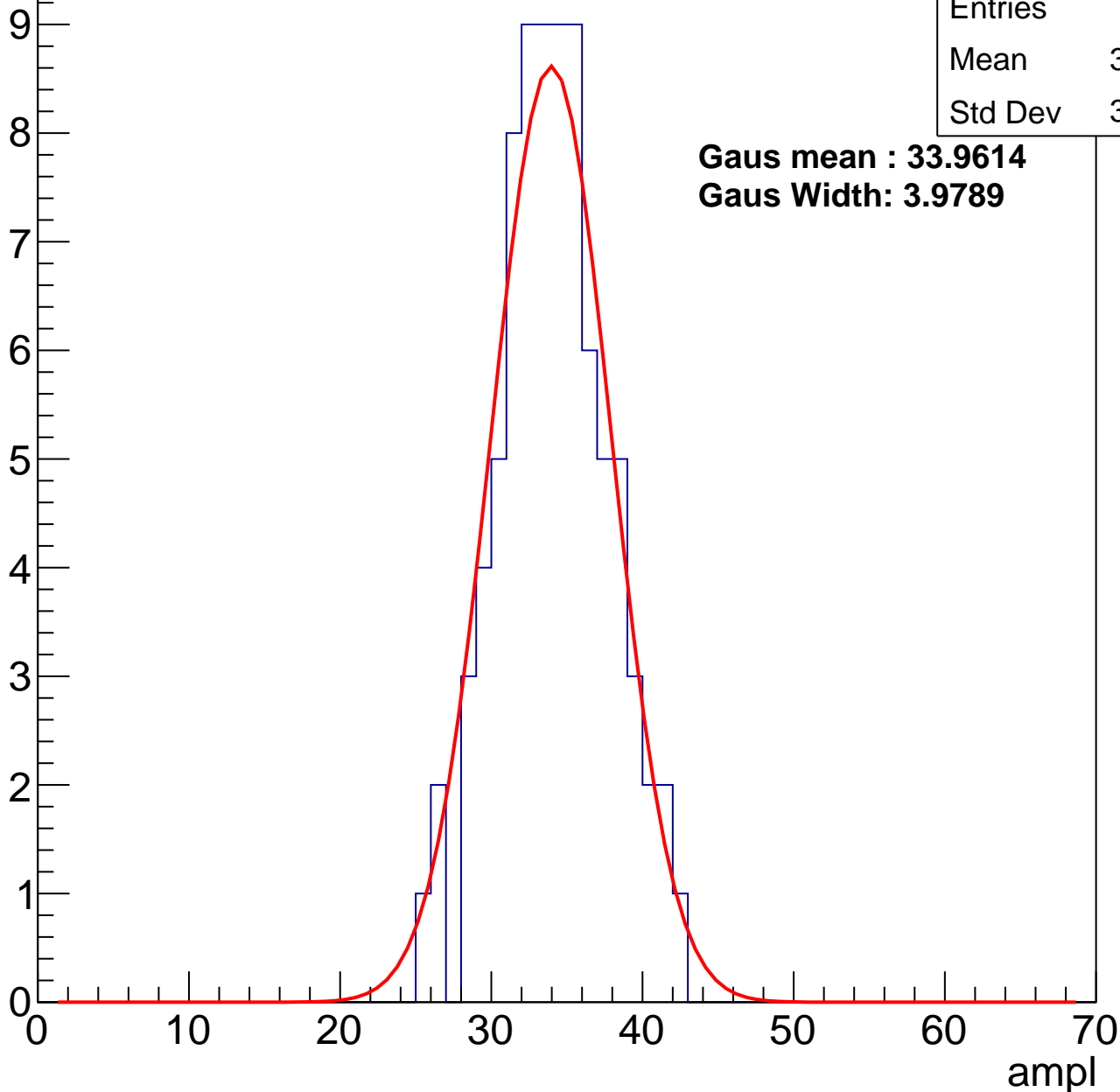
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	33.65
Std Dev	3.592

Gaus mean : 33.9614

Gaus Width: 3.9789



B0L001S, U25-ch38, adc1

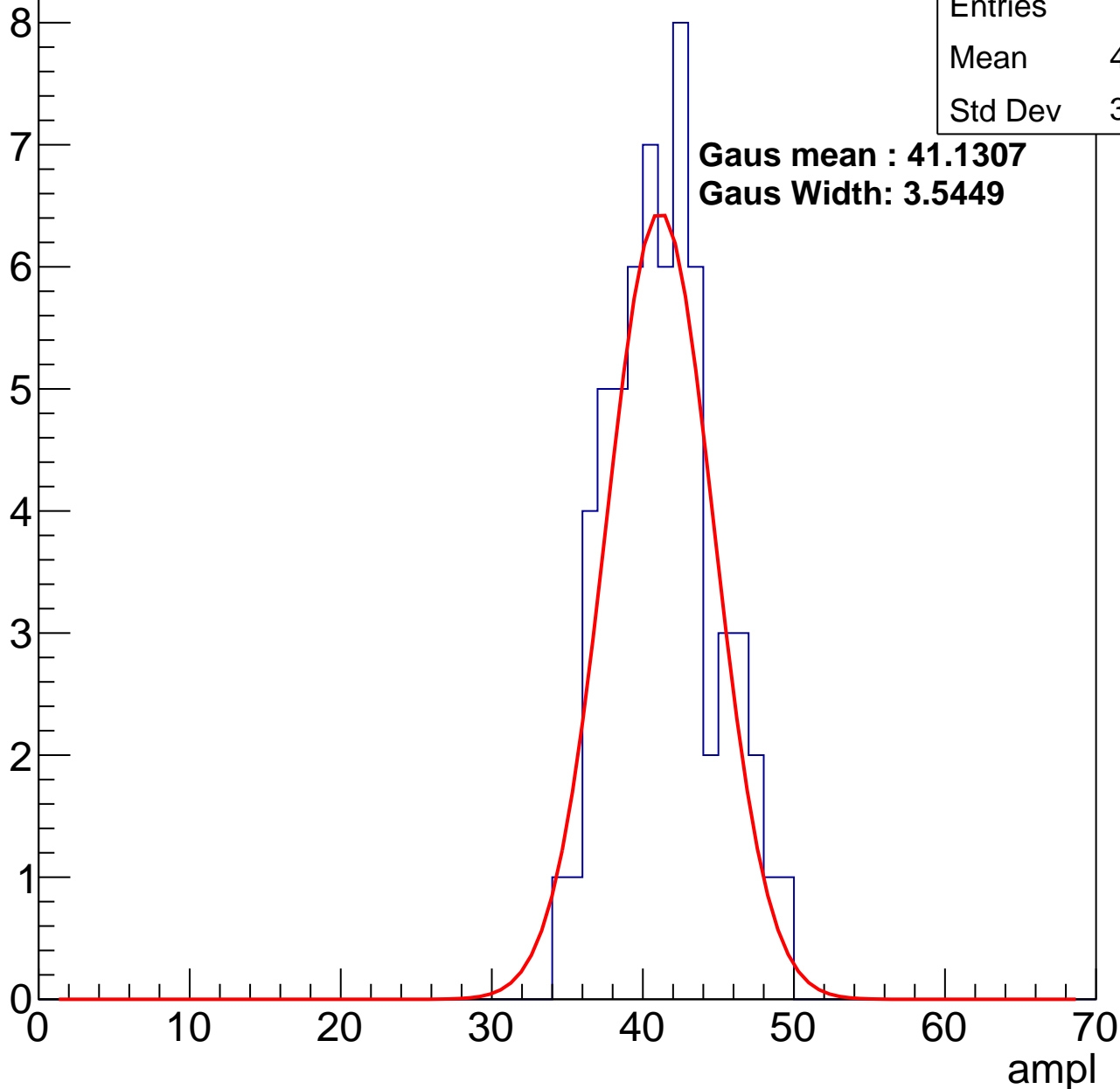
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	40.89
Std Dev	3.383

Gaus mean : 41.1307

Gaus Width: 3.5449



B0L001S, U25-ch38, adc2

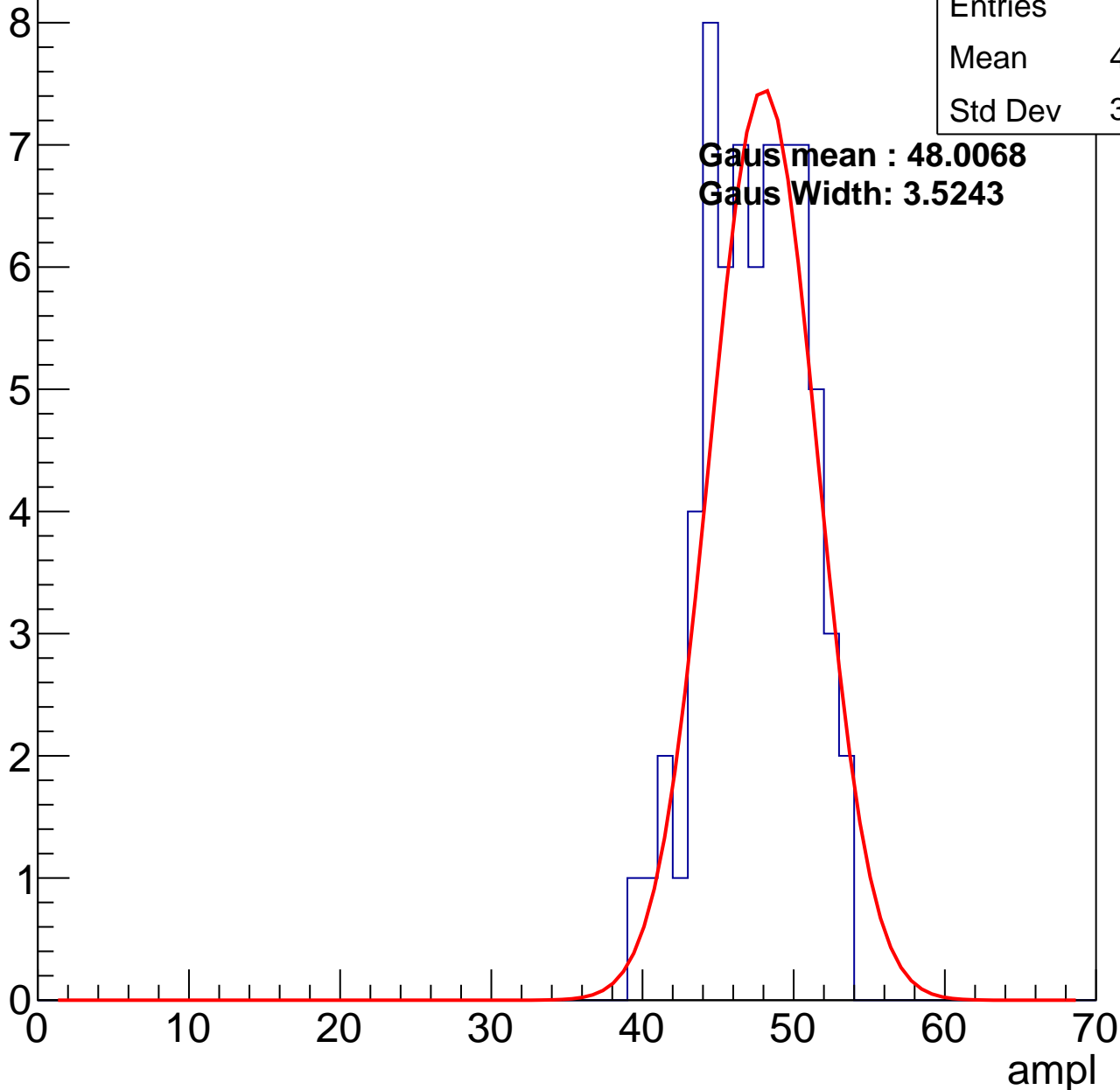
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	46.97
Std Dev	3.237

Gaus mean : 48.0068

Gaus Width: 3.5243

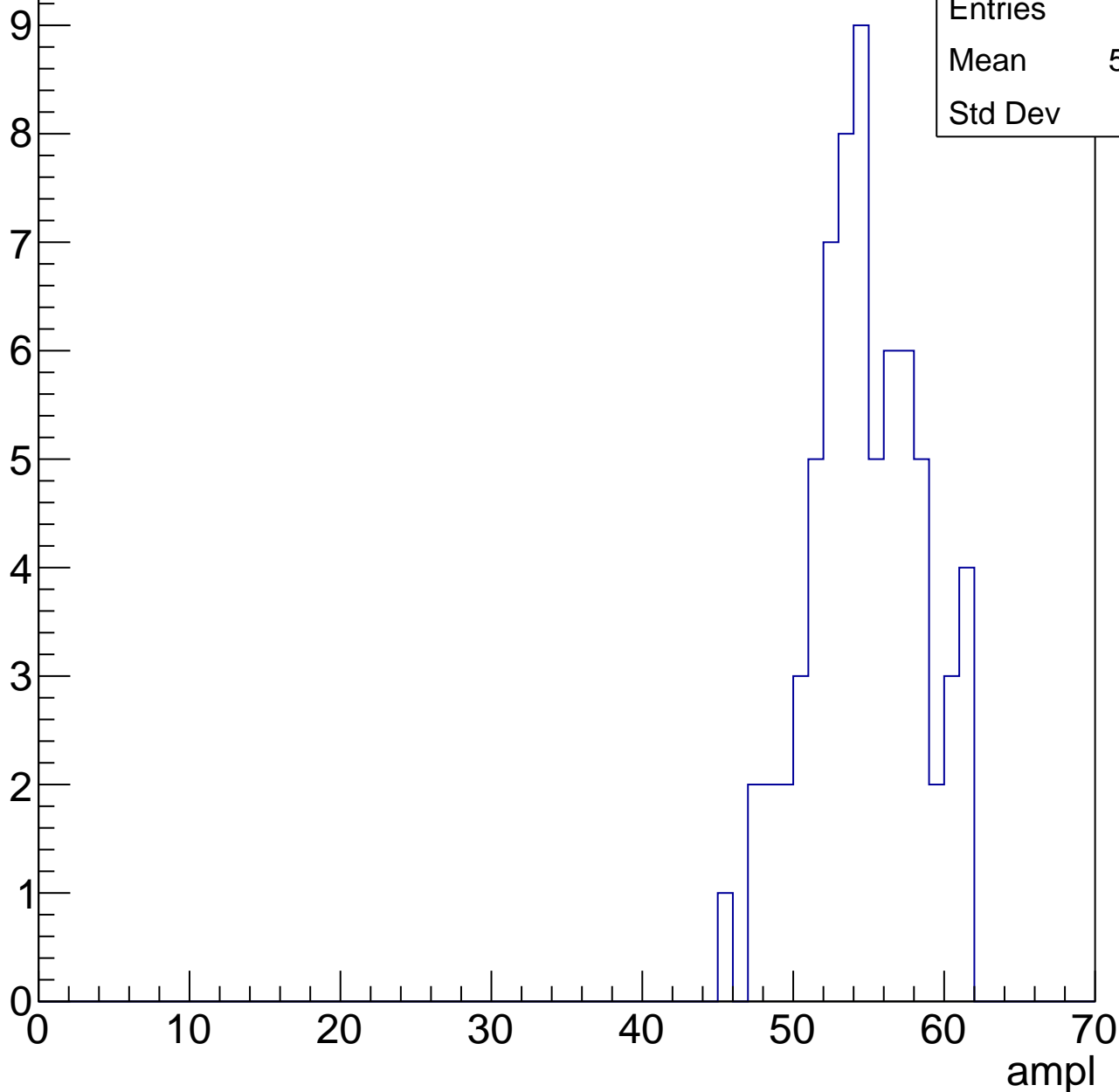


B0L001S, U25-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	54.24
Std Dev	3.67

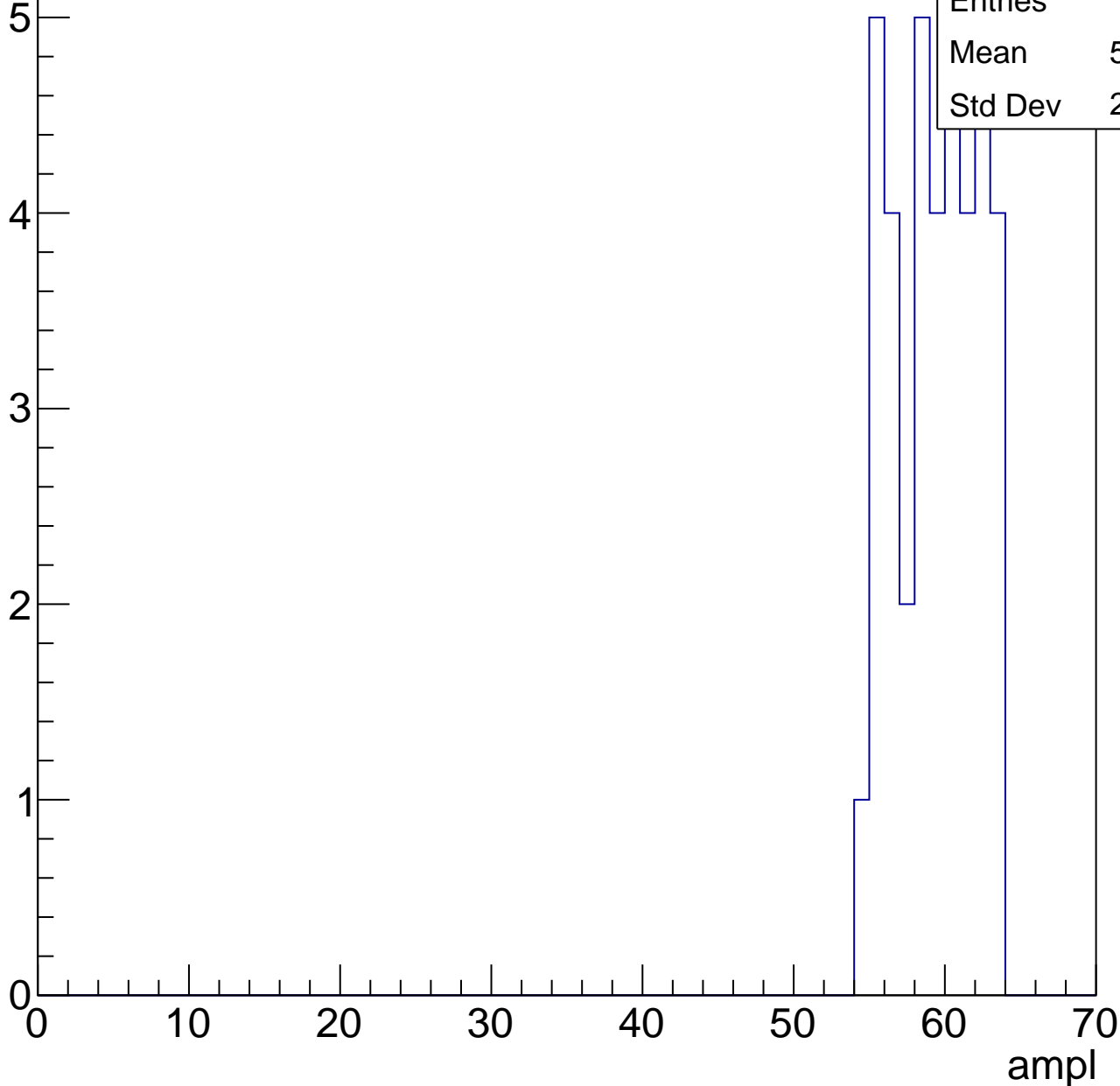


B0L001S, U25-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

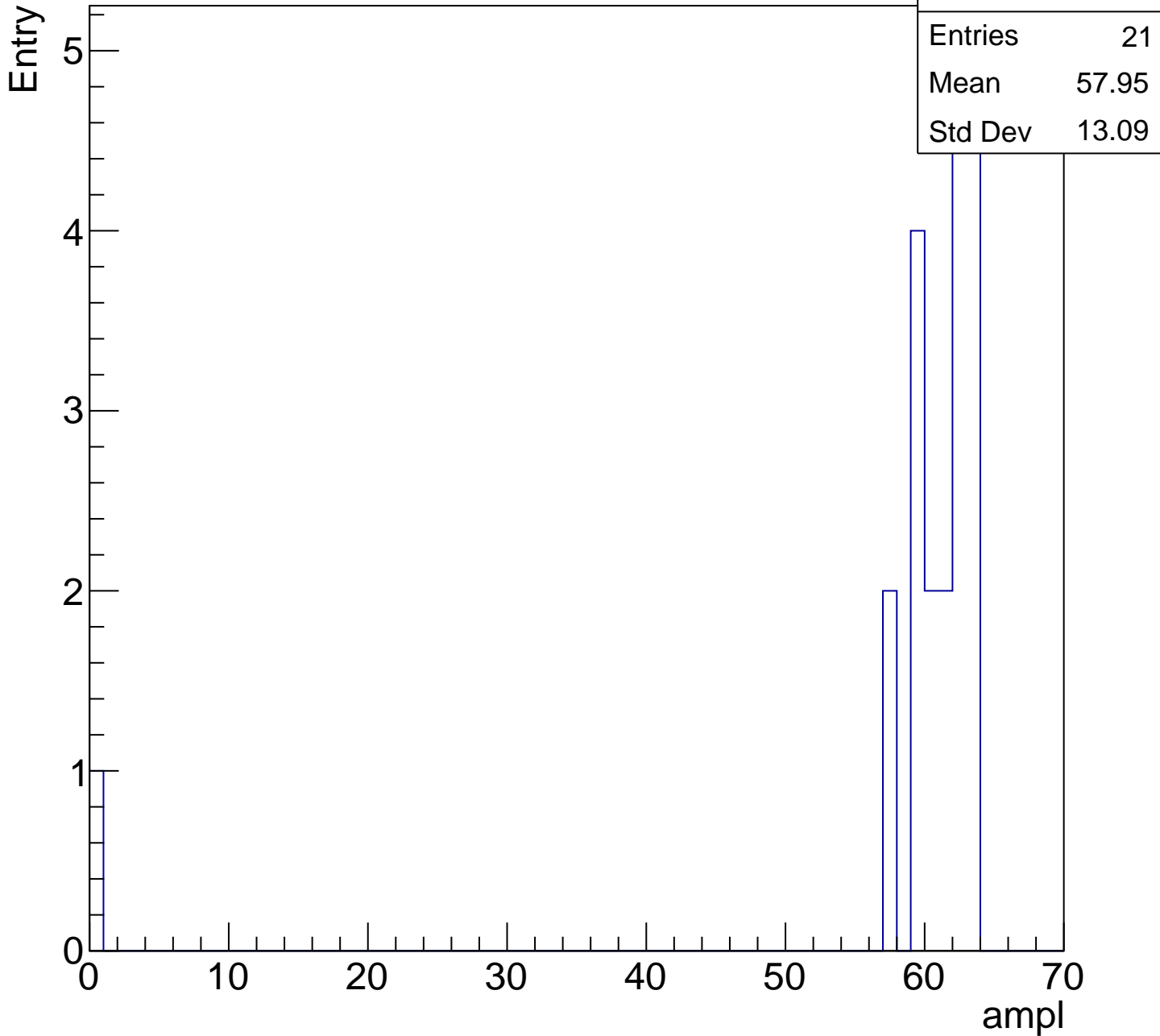
Entry

Entries	39
Mean	58.95
Std Dev	2.698



B0L001S, U25-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch39, adc0

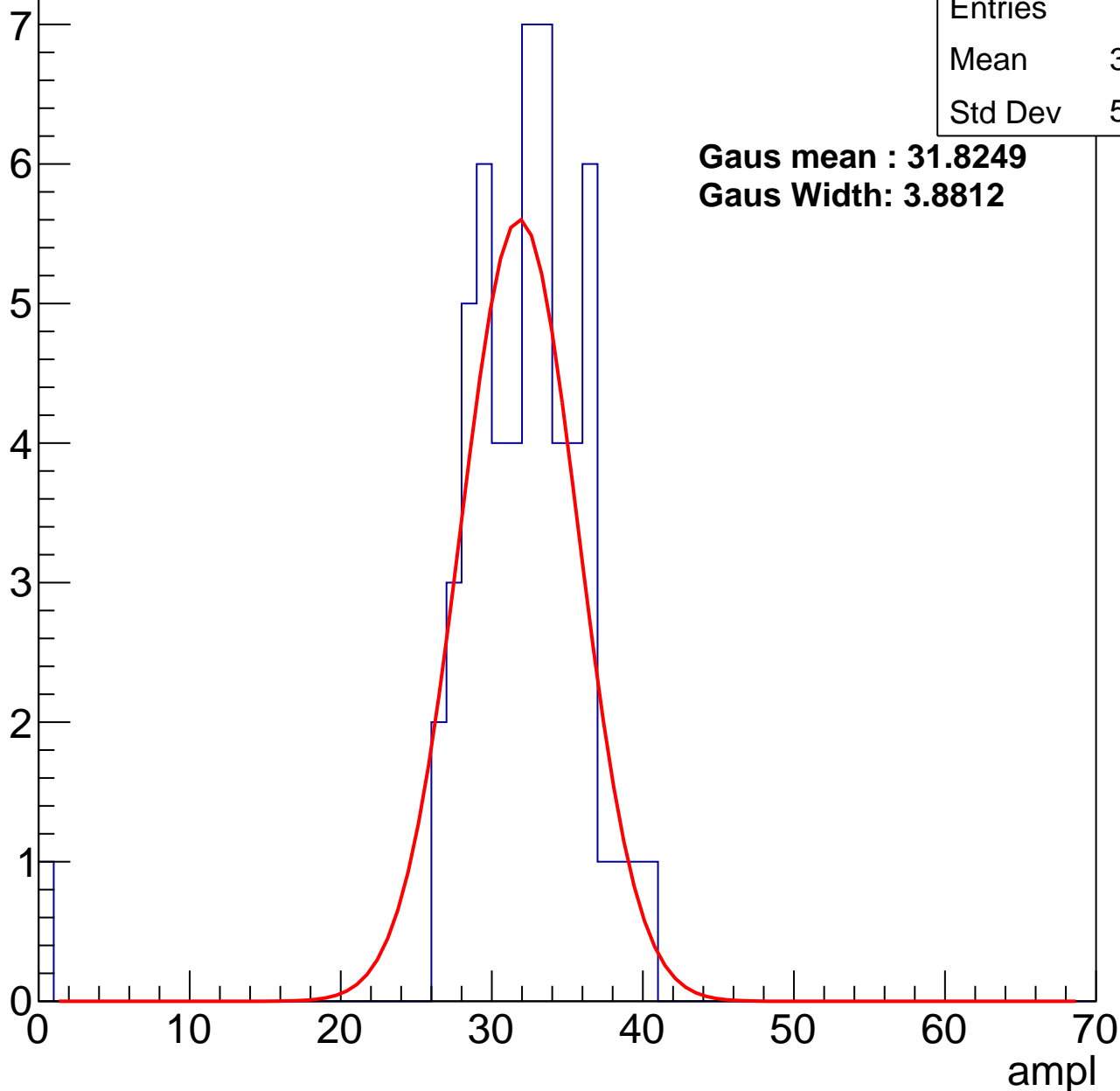
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	31.44
Std Dev	5.357

Gaus mean : 31.8249

Gaus Width: 3.8812



B0L001S, U25-ch39, adc1

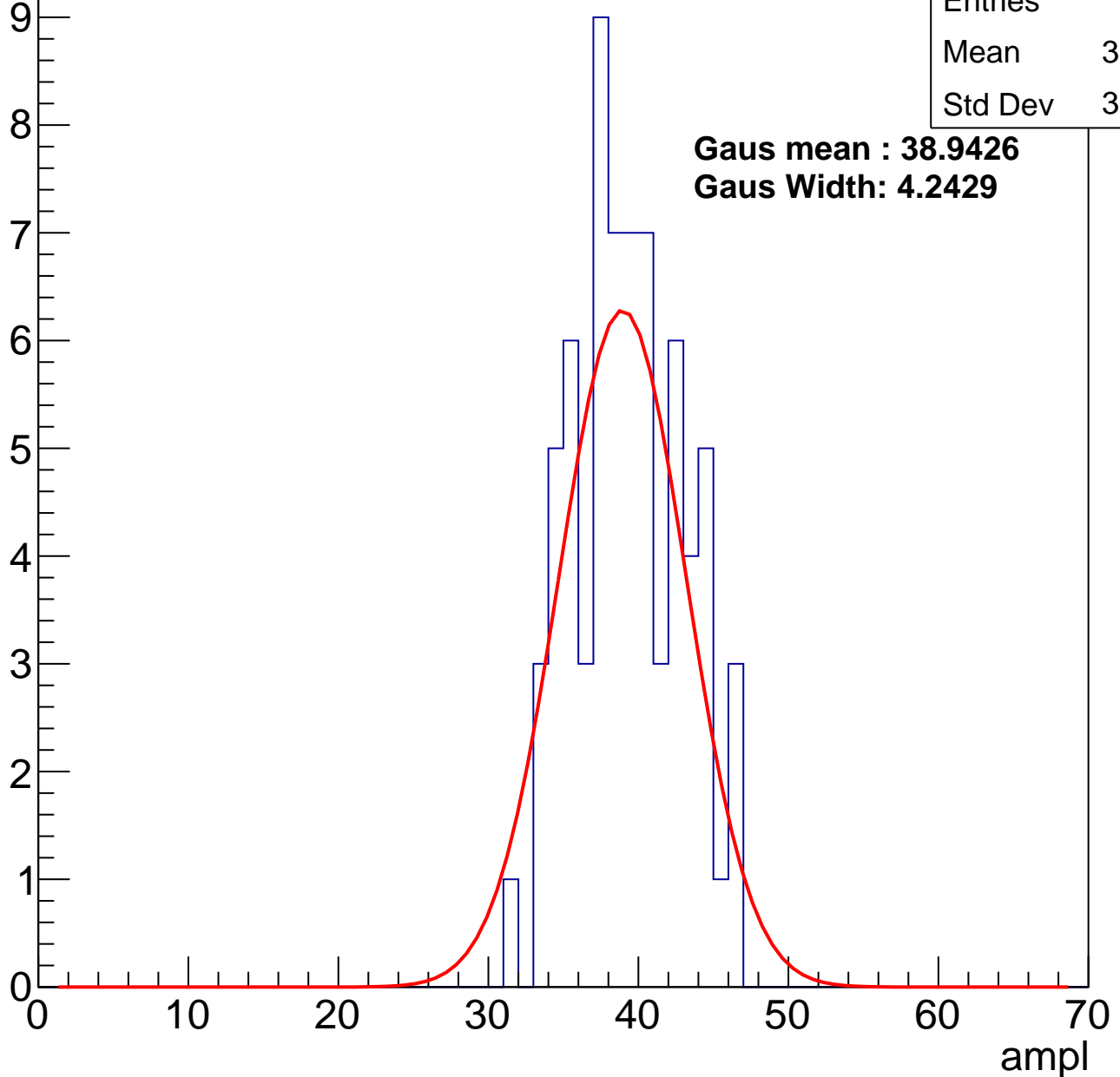
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.86
Std Dev	3.599

Gaus mean : 38.9426

Gaus Width: 4.2429



B0L001S, U25-ch39, adc2

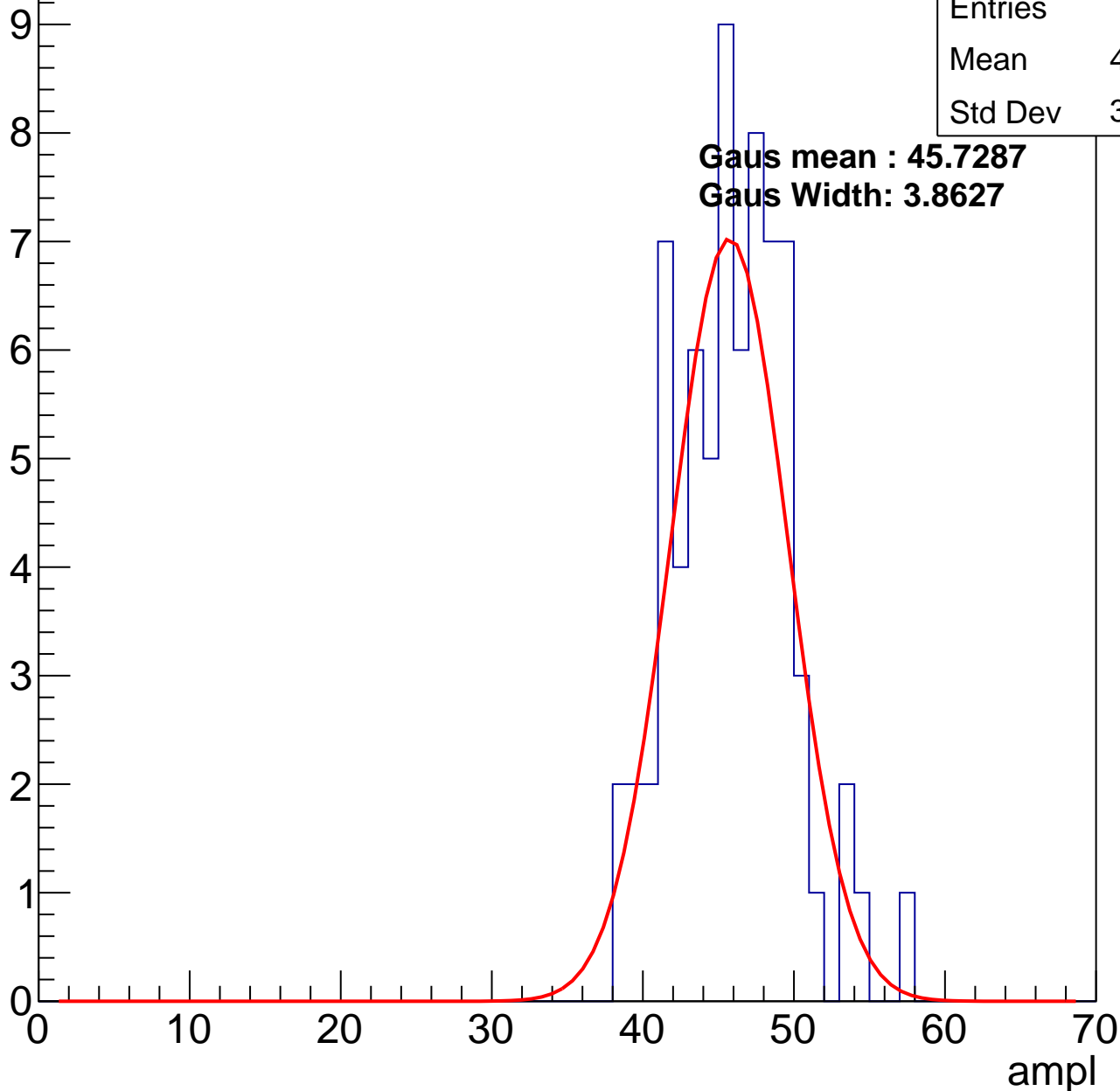
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.49
Std Dev	3.804

Gaus mean : 45.7287

Gaus Width: 3.8627

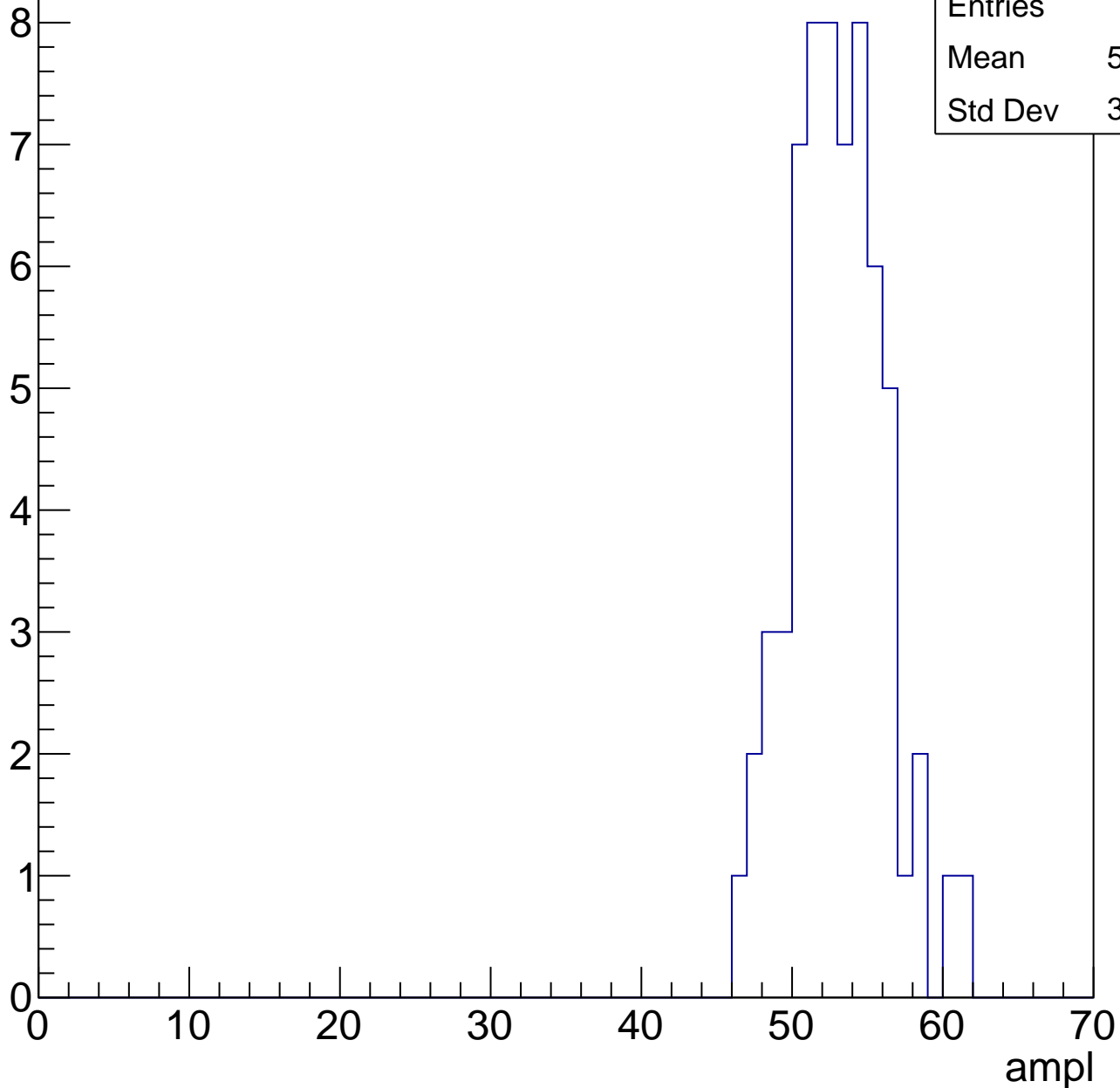


B0L001S, U25-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.57
Std Dev	3.064

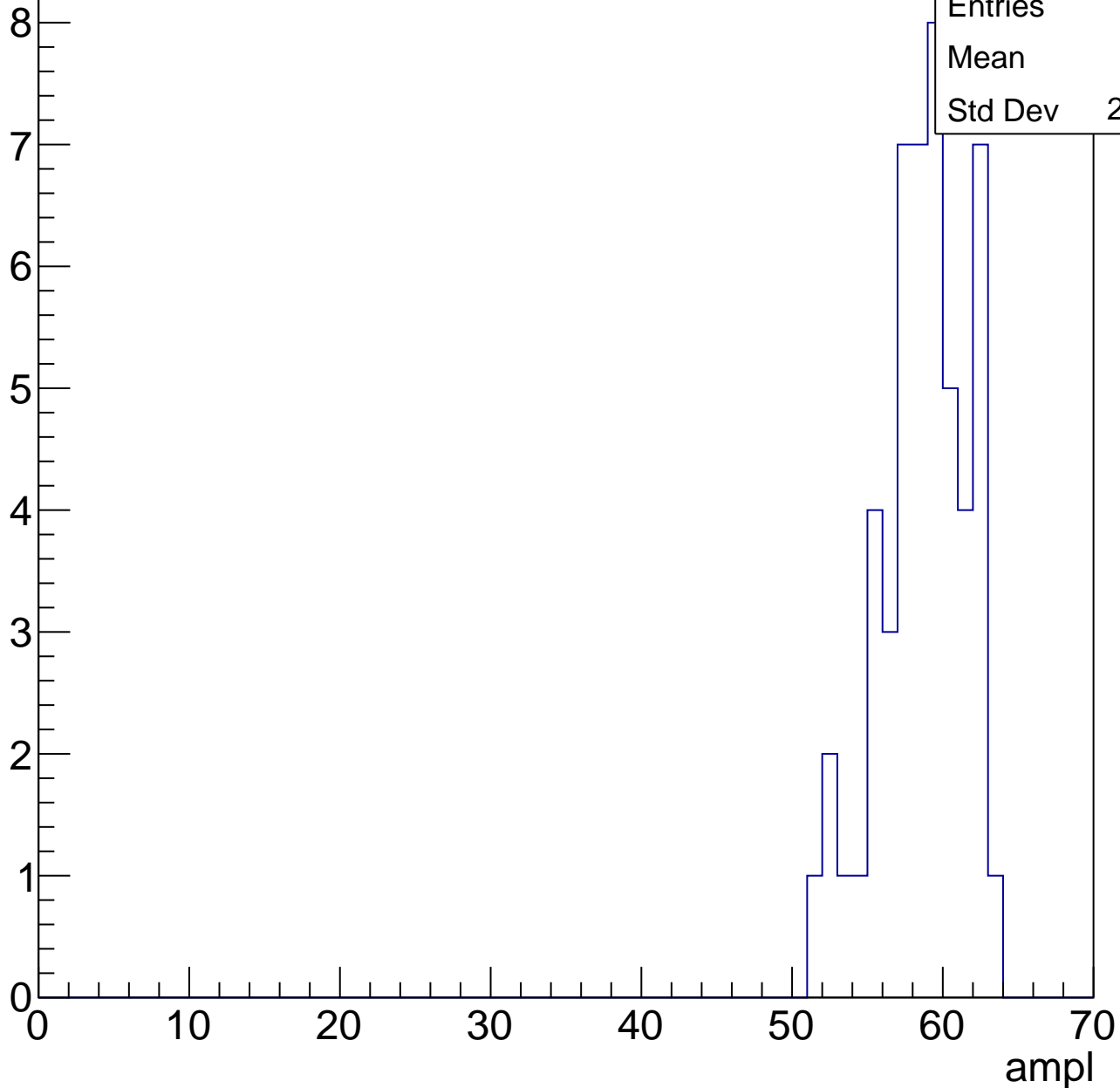


B0L001S, U25-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	58.2
Std Dev	2.856

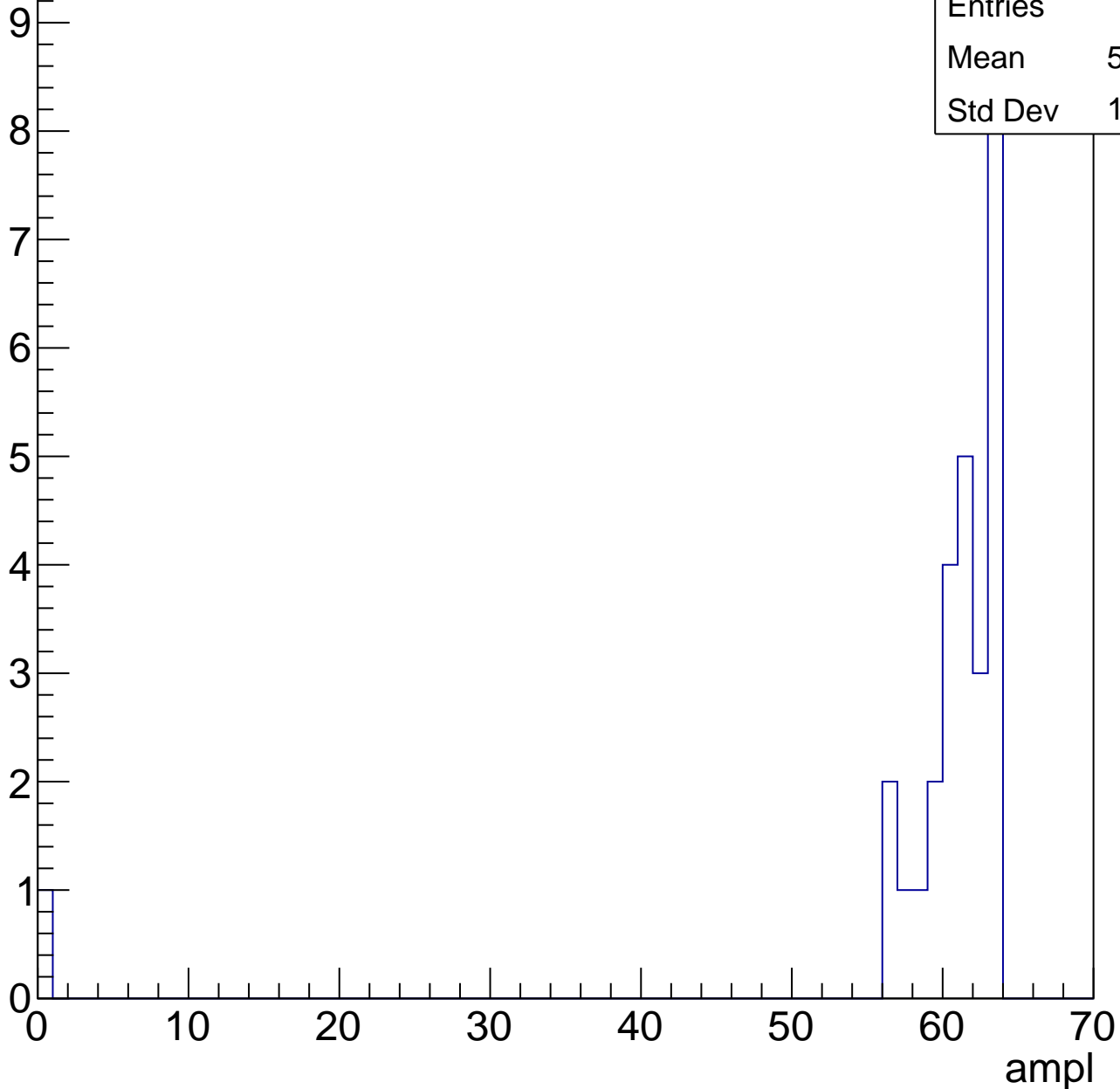


B0L001S, U25-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.68
Std Dev	11.49



B0L001S, U25-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch40, adc0

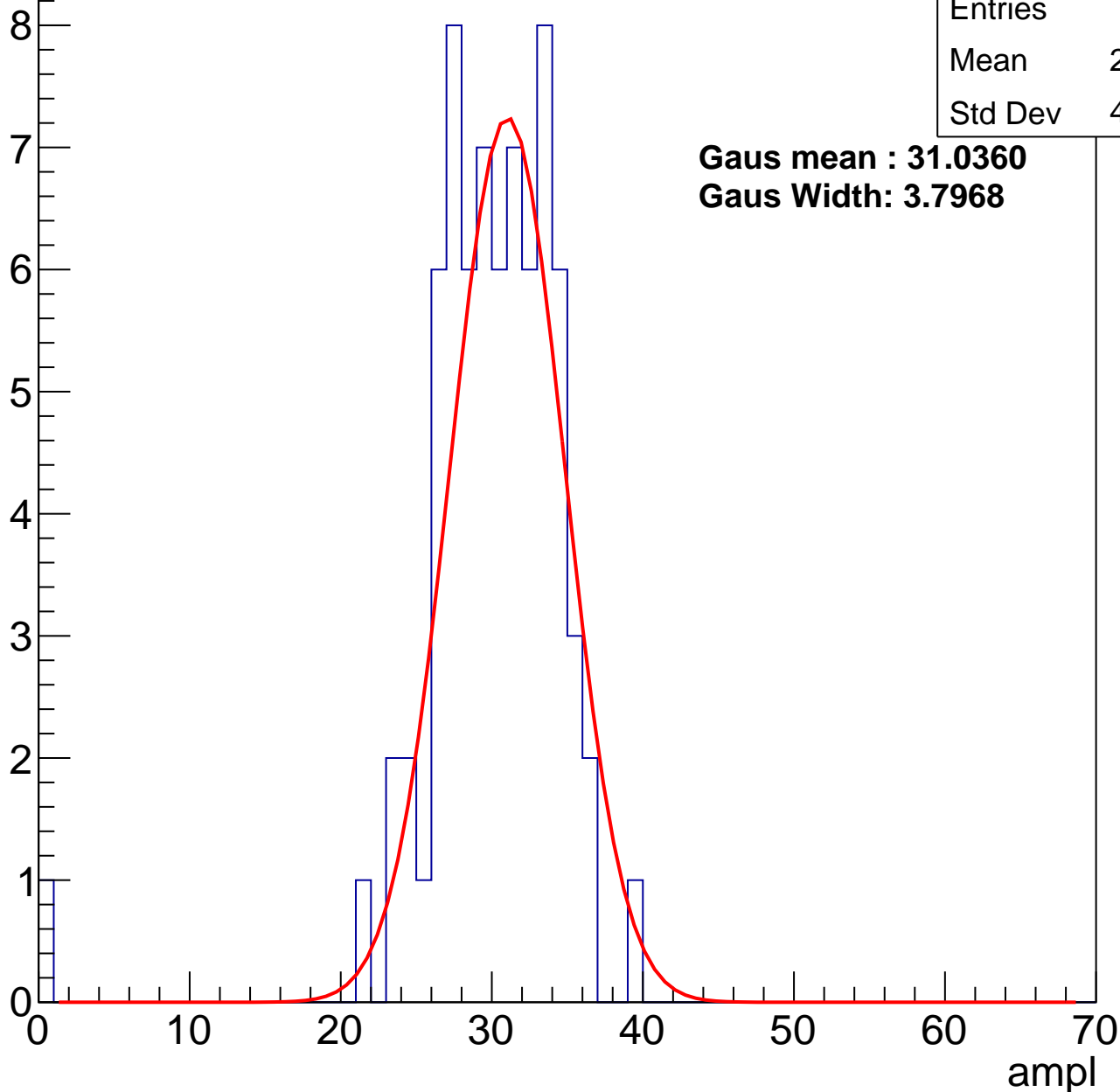
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.53
Std Dev	4.947

Gaus mean : 31.0360

Gaus Width: 3.7968



B0L001S, U25-ch40, adc1

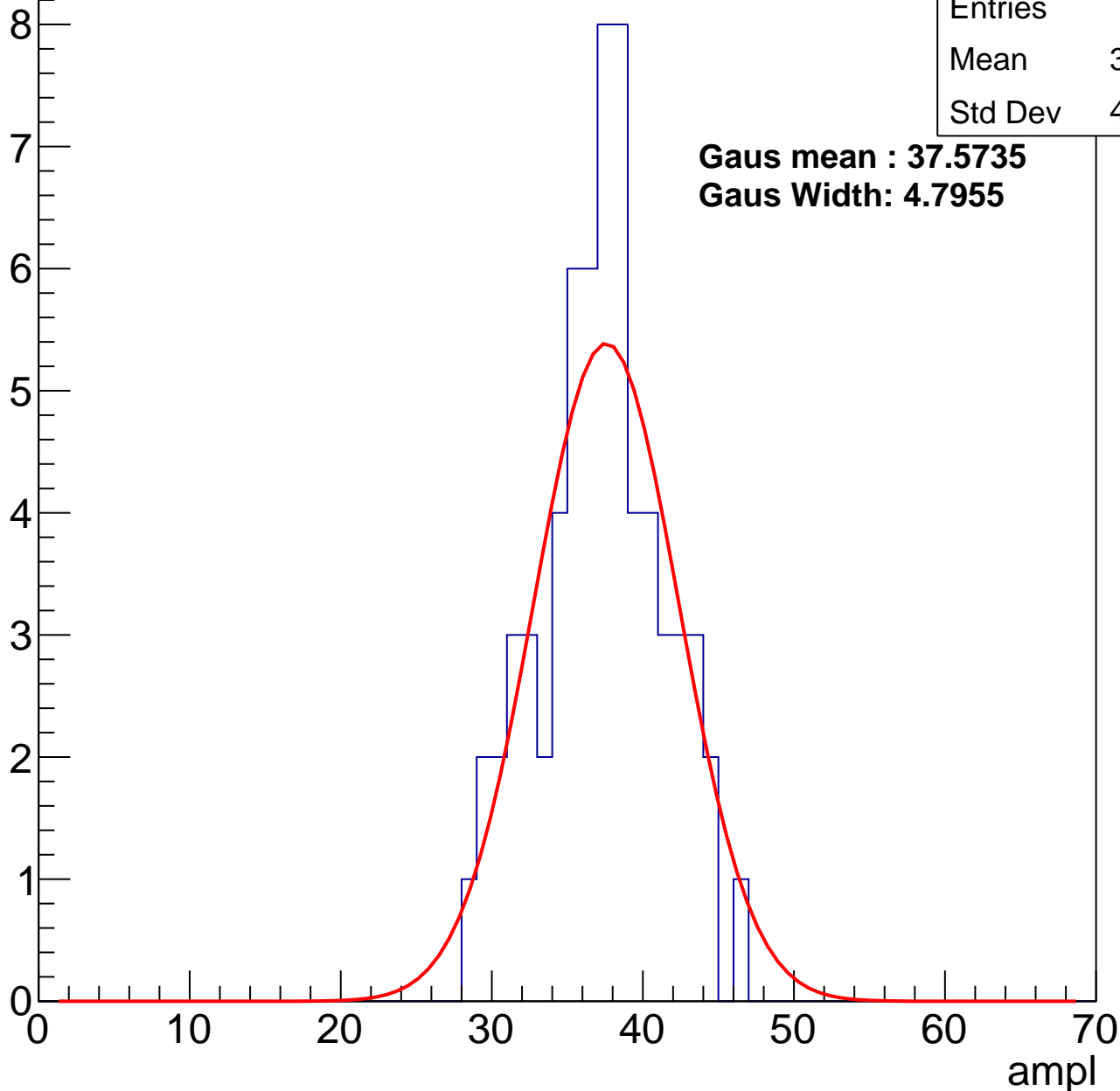
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.78
Std Dev	4.029

Gaus mean : 37.5735

Gaus Width: 4.7955



B0L001S, U25-ch40, adc2

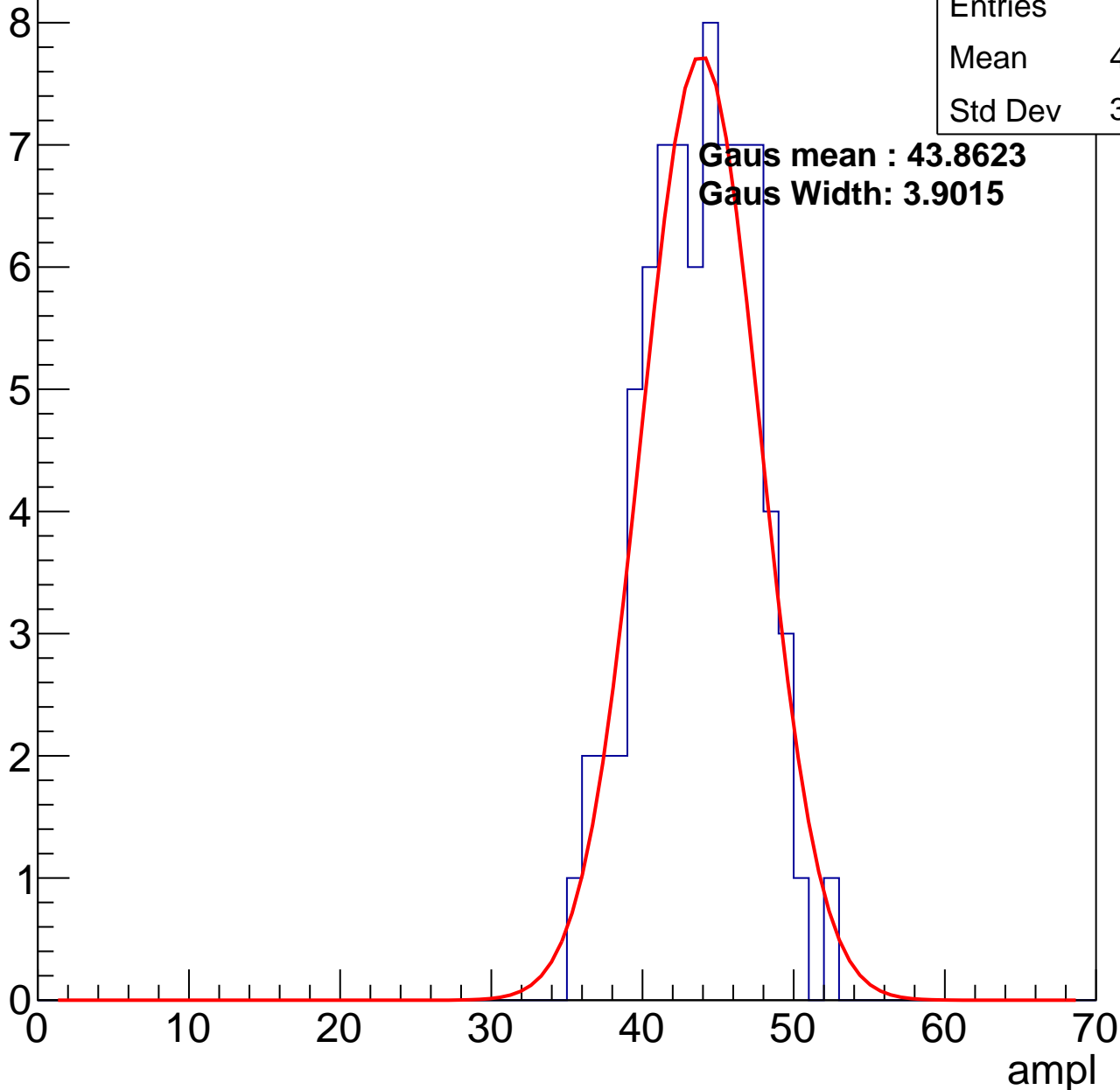
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	43.29
Std Dev	3.619

Gaus mean : 43.8623

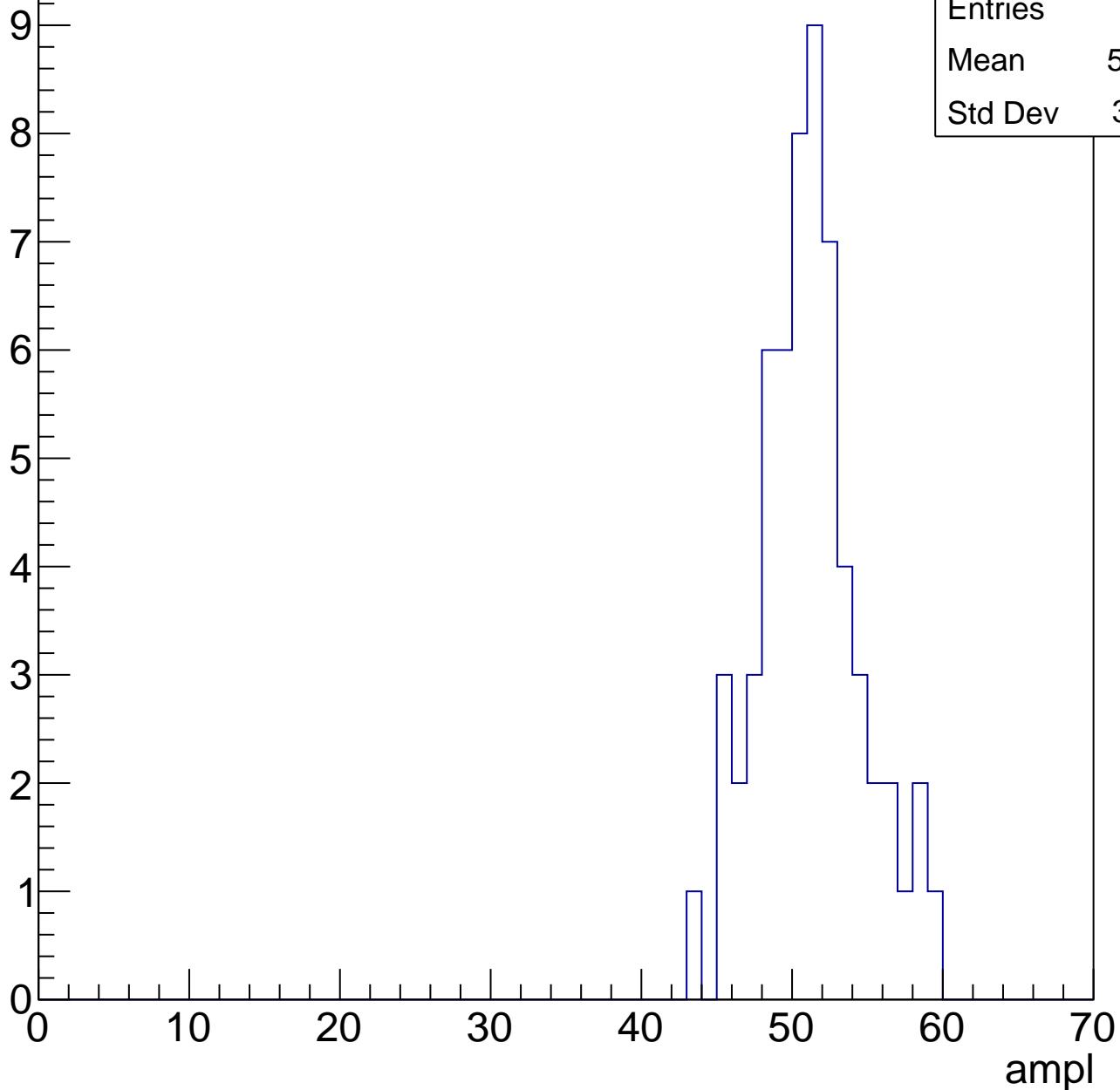
Gaus Width: 3.9015



B0L001S, U25-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



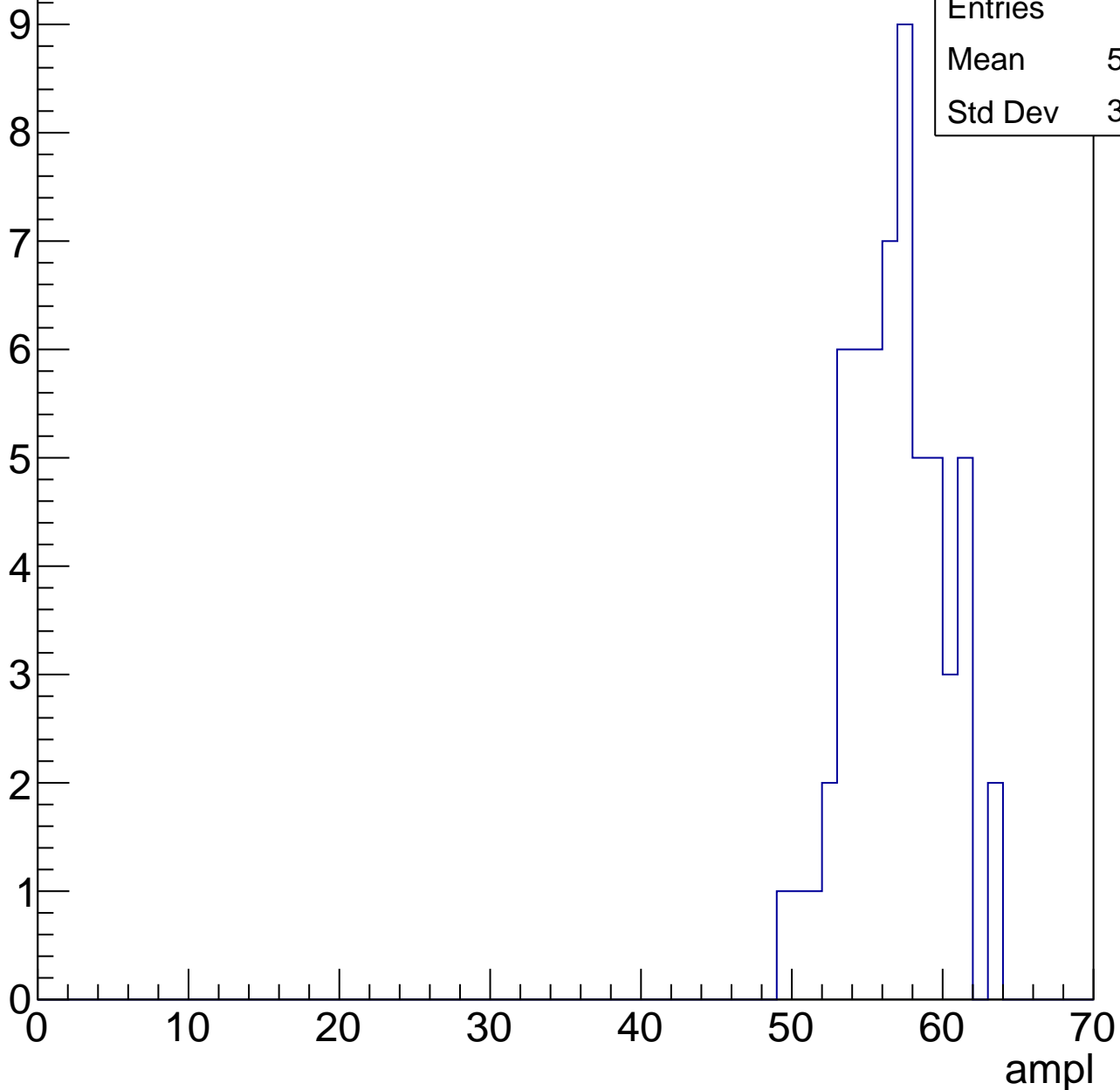
Entries	60
Mean	50.73
Std Dev	3.371

B0L001S, U25-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.39
Std Dev	3.086

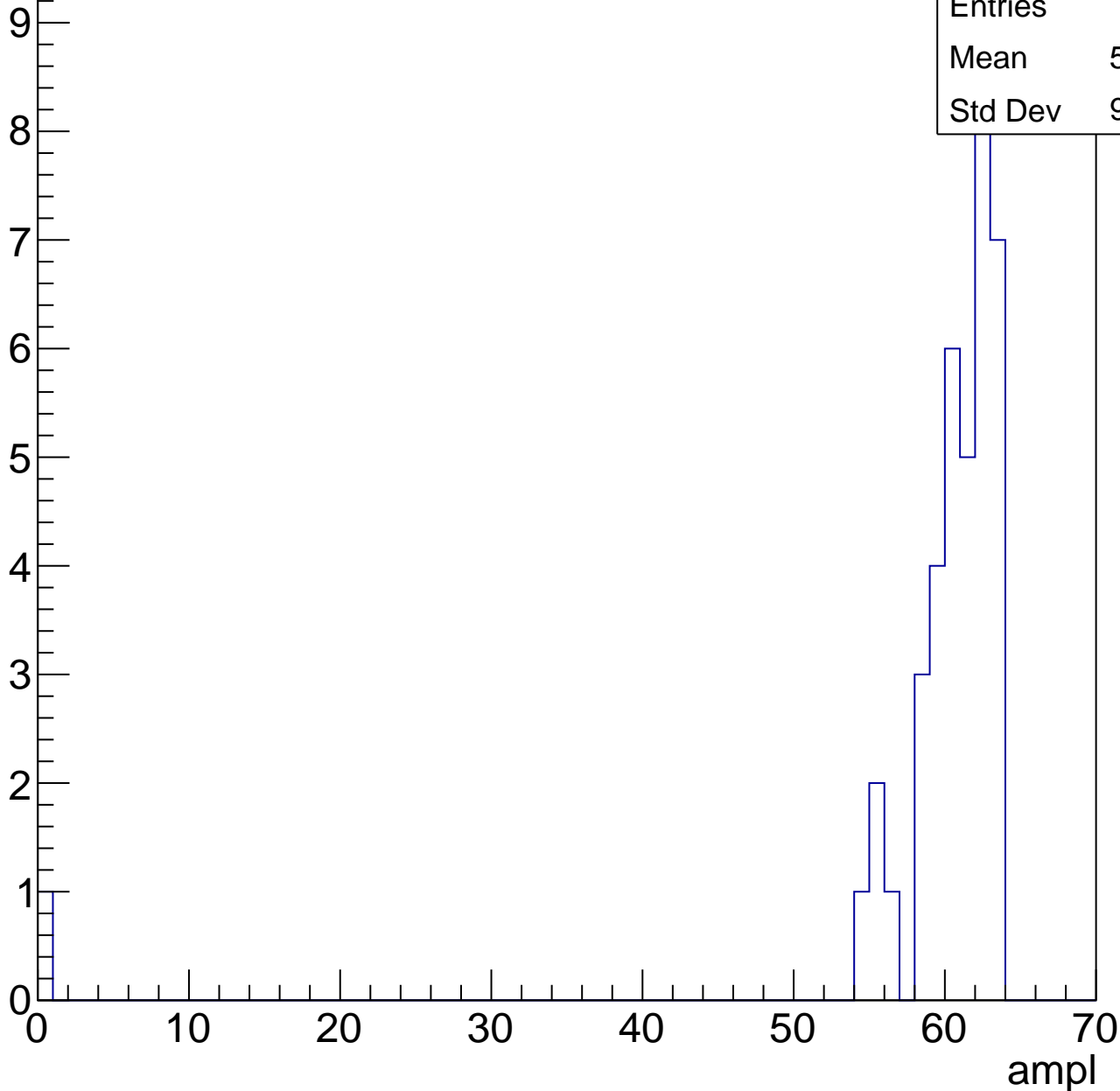


B0L001S, U25-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.82
Std Dev	9.829



B0L001S, U25-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	9
Std Dev	9

B0L001S, U25-ch41, adc0

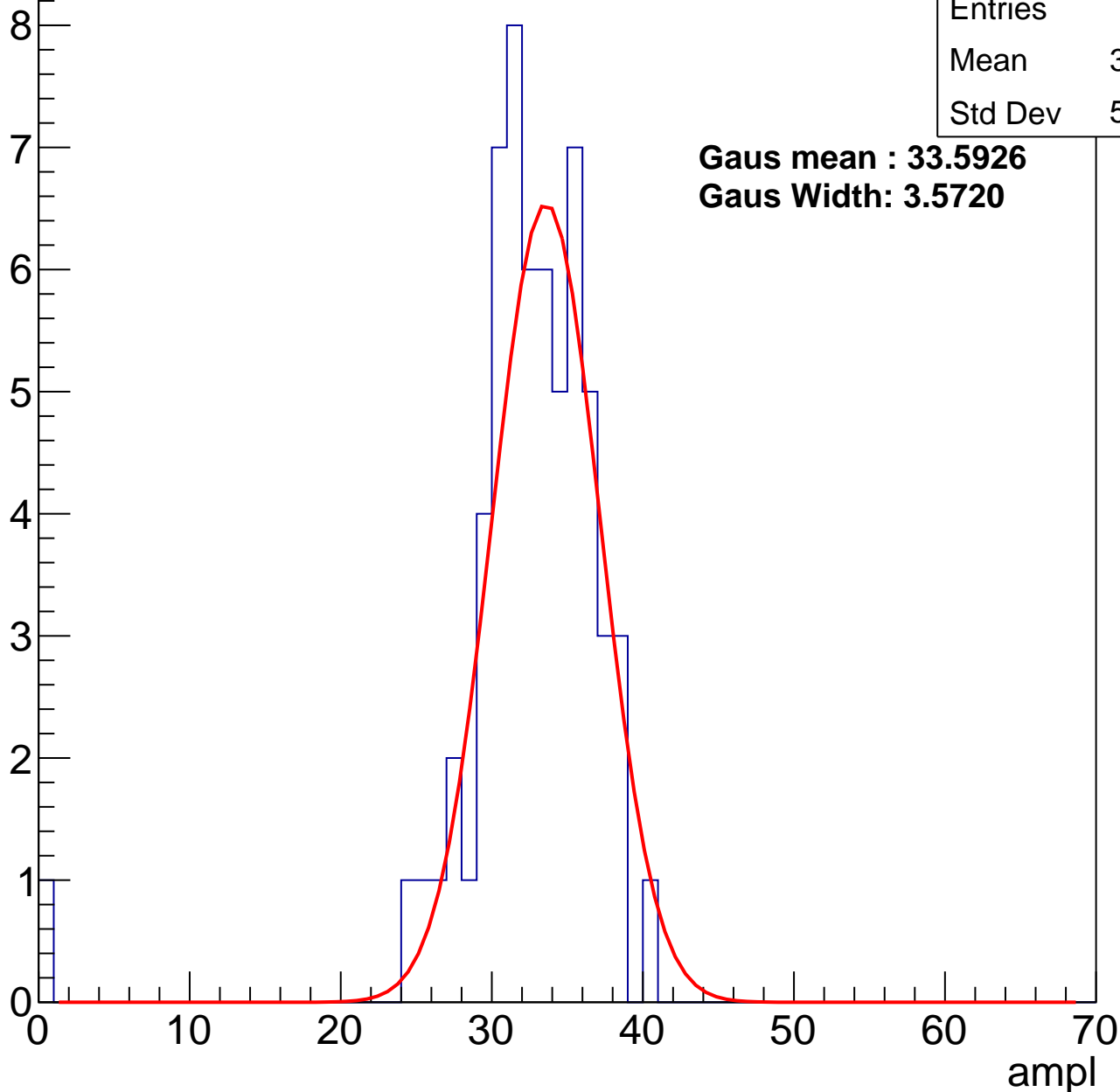
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.95
Std Dev	5.278

Gaus mean : 33.5926

Gaus Width: 3.5720



B0L001S, U25-ch41, adc1

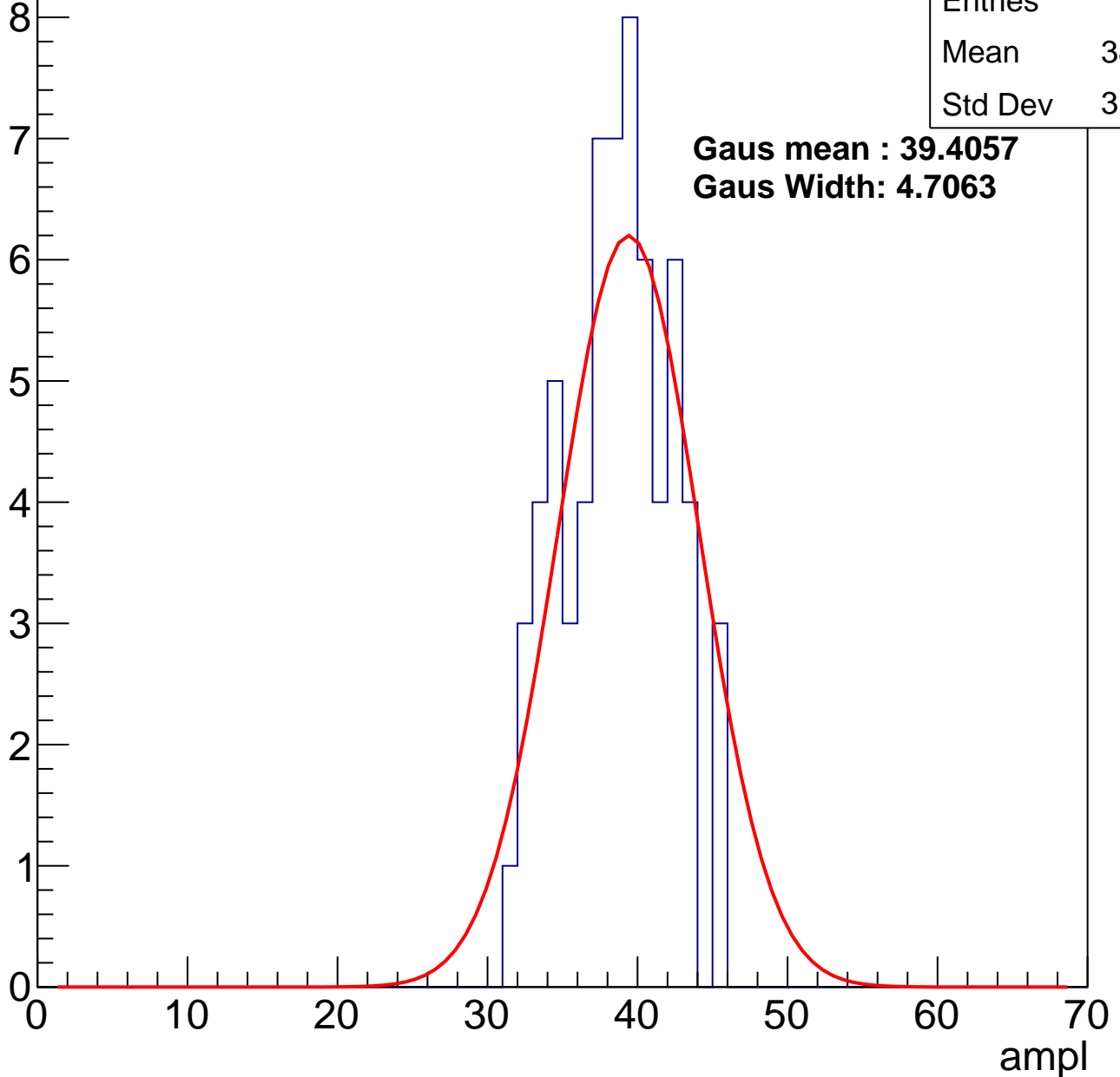
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.12
Std Dev	3.502

Gaus mean : 39.4057

Gaus Width: 4.7063



B0L001S, U25-ch41, adc2

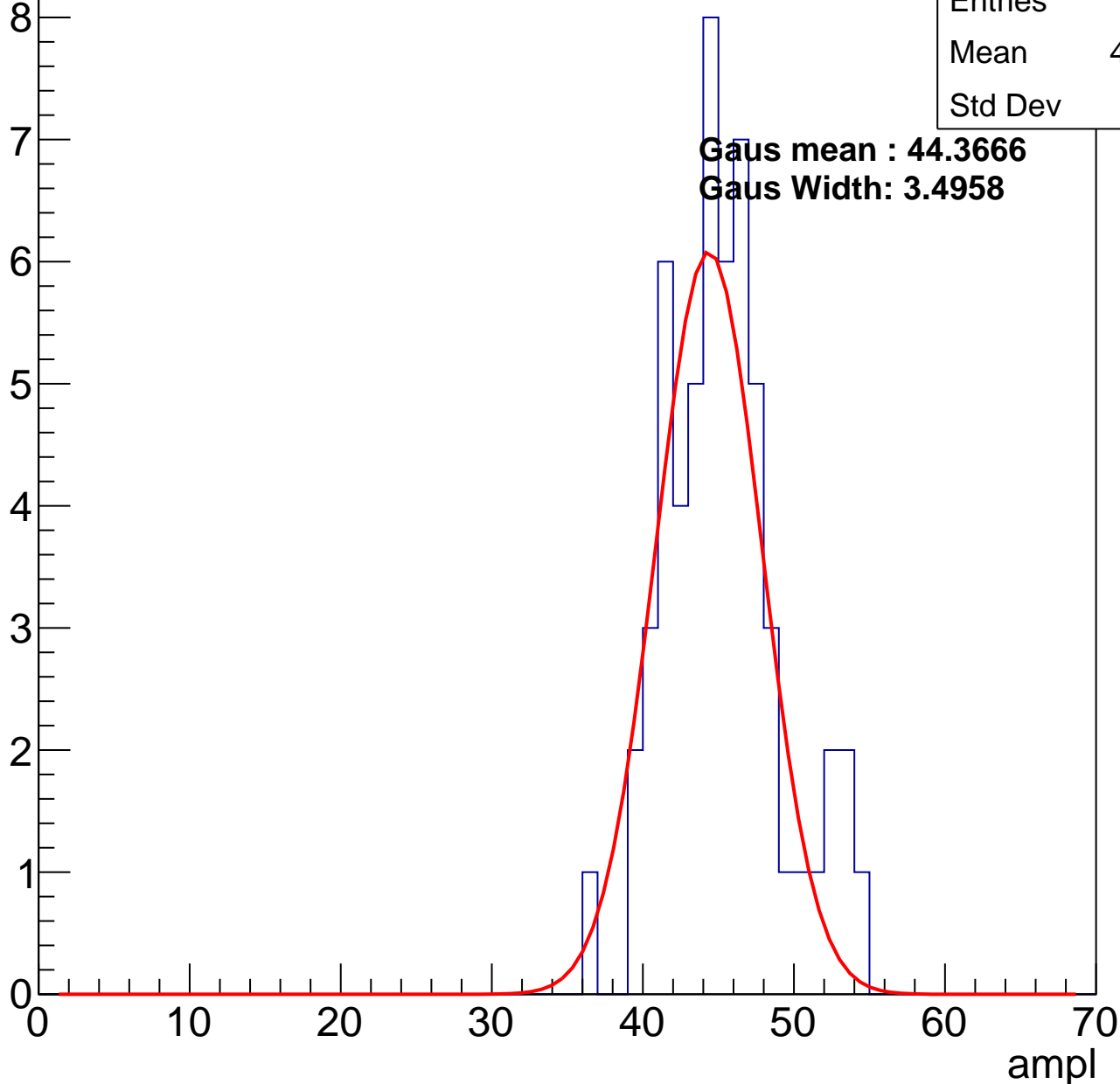
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.83
Std Dev	3.77

Gaus mean : 44.3666

Gaus Width: 3.4958

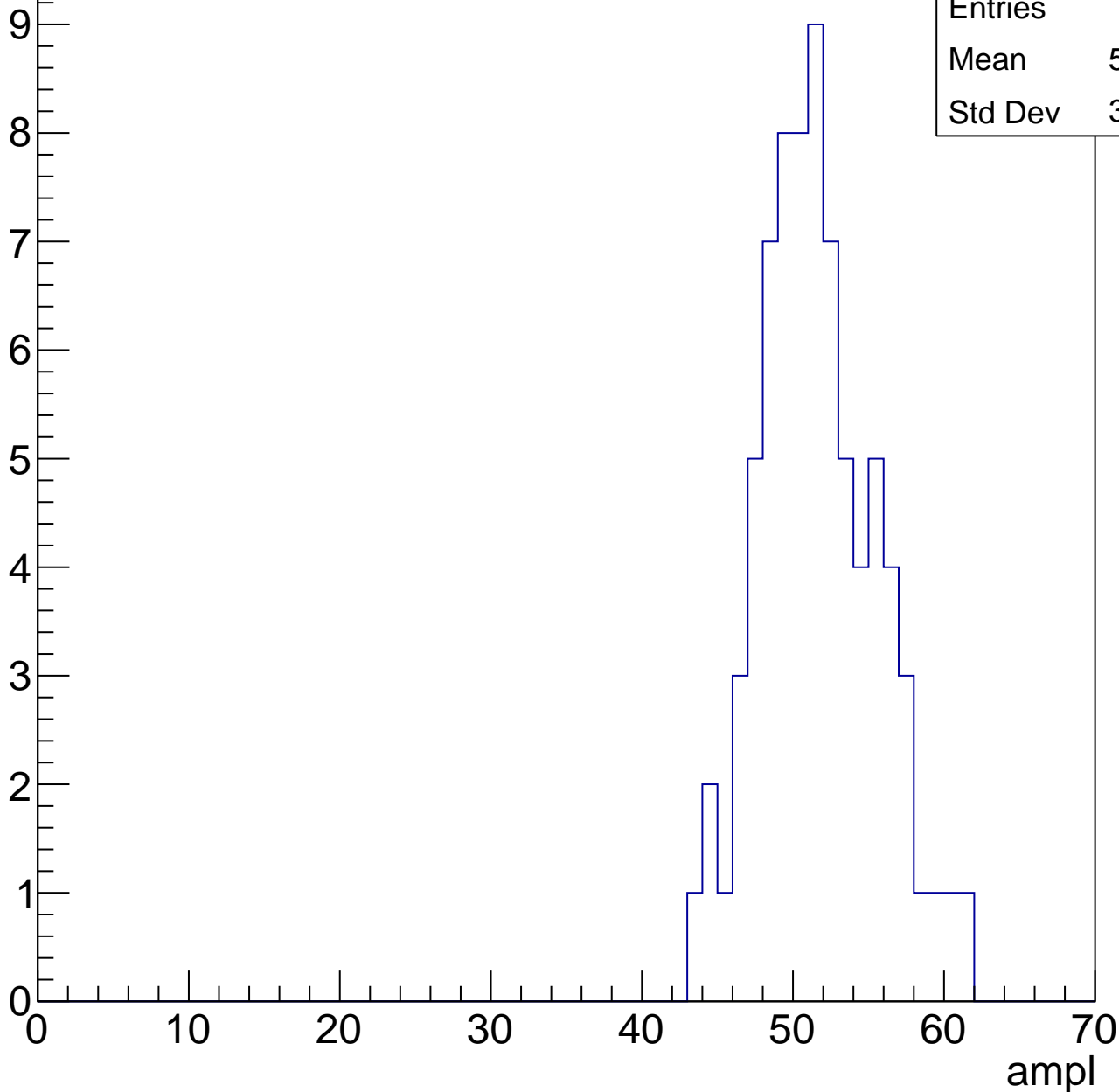


B0L001S, U25-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.17
Std Dev	3.802

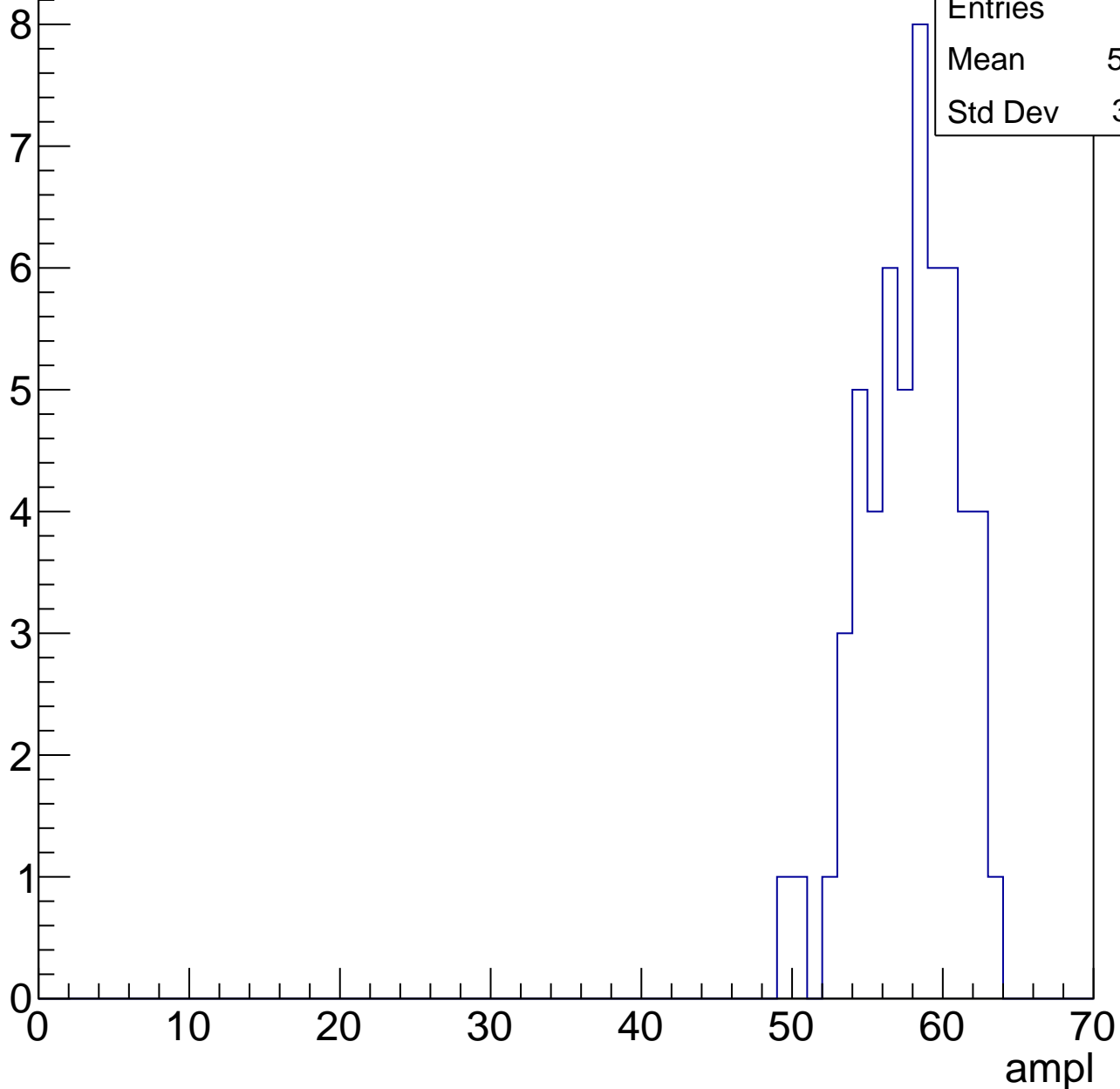


B0L001S, U25-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.35
Std Dev	3.111

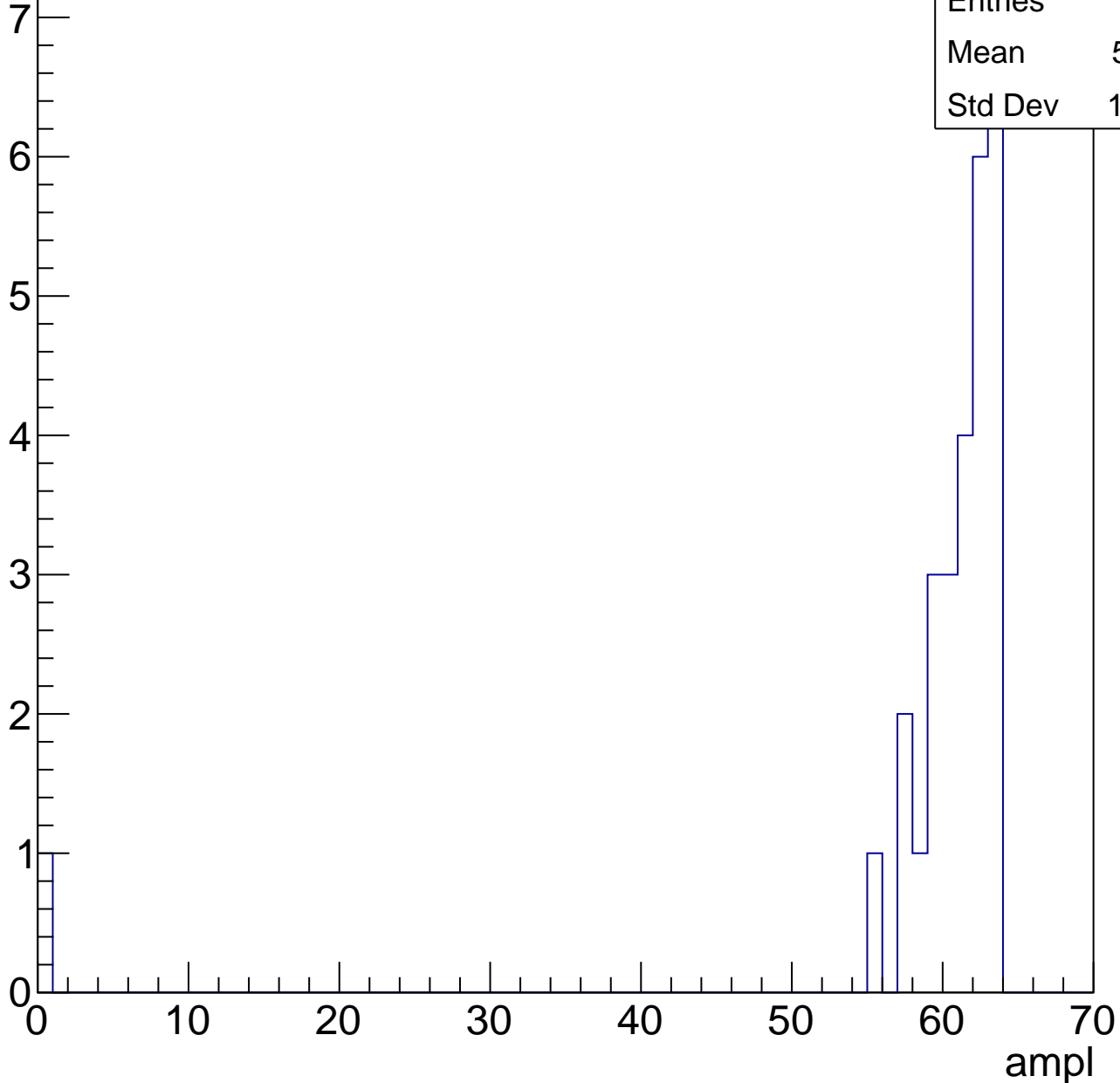


B0L001S, U25-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.61
Std Dev	11.47



B0L001S, U25-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

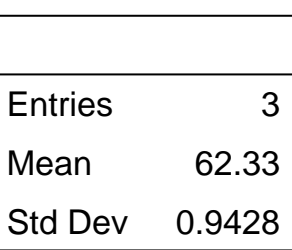
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch42, adc0

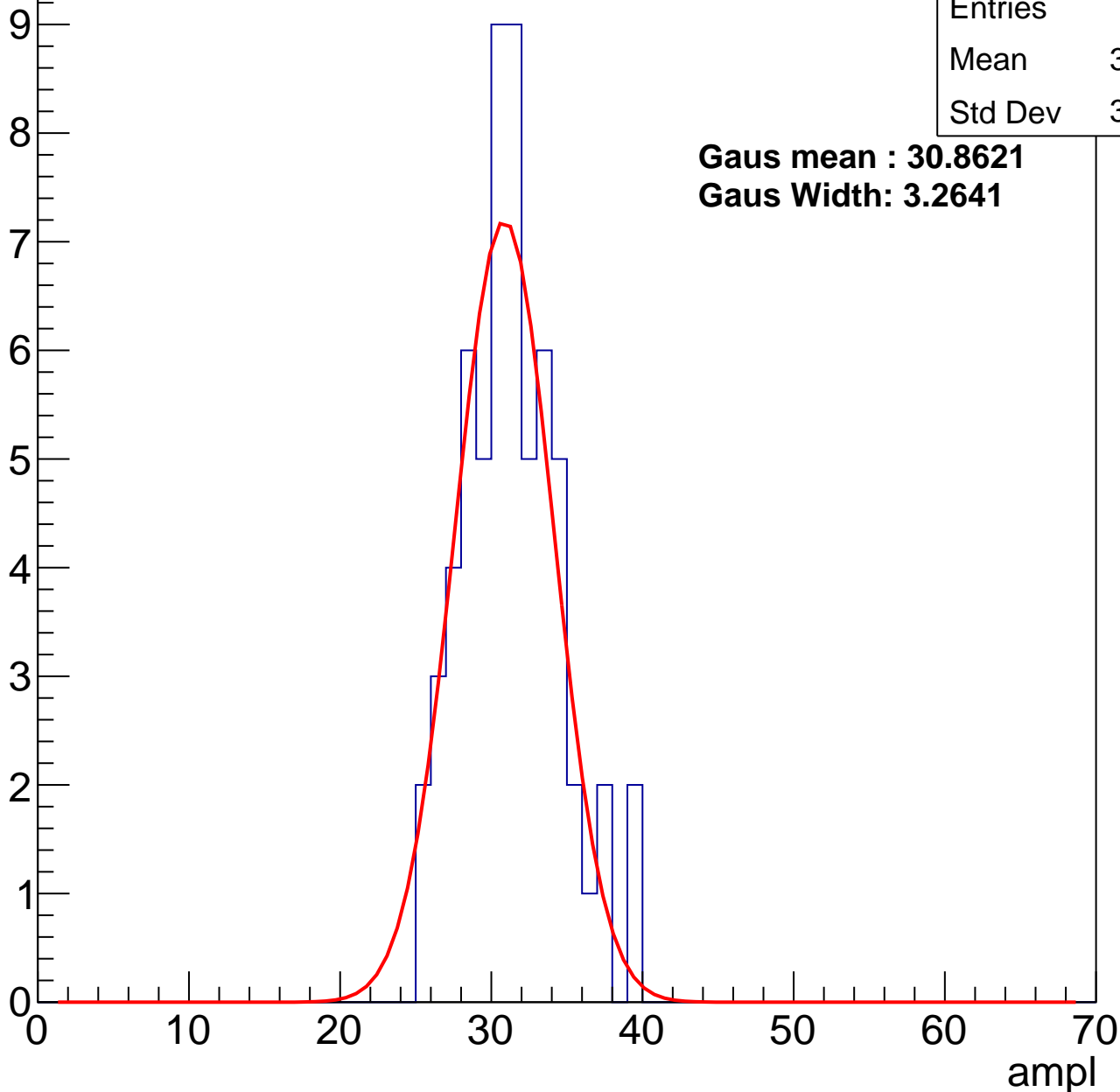
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.89
Std Dev	3.199

Gaus mean : 30.8621

Gaus Width: 3.2641



B0L001S, U25-ch42, adc1

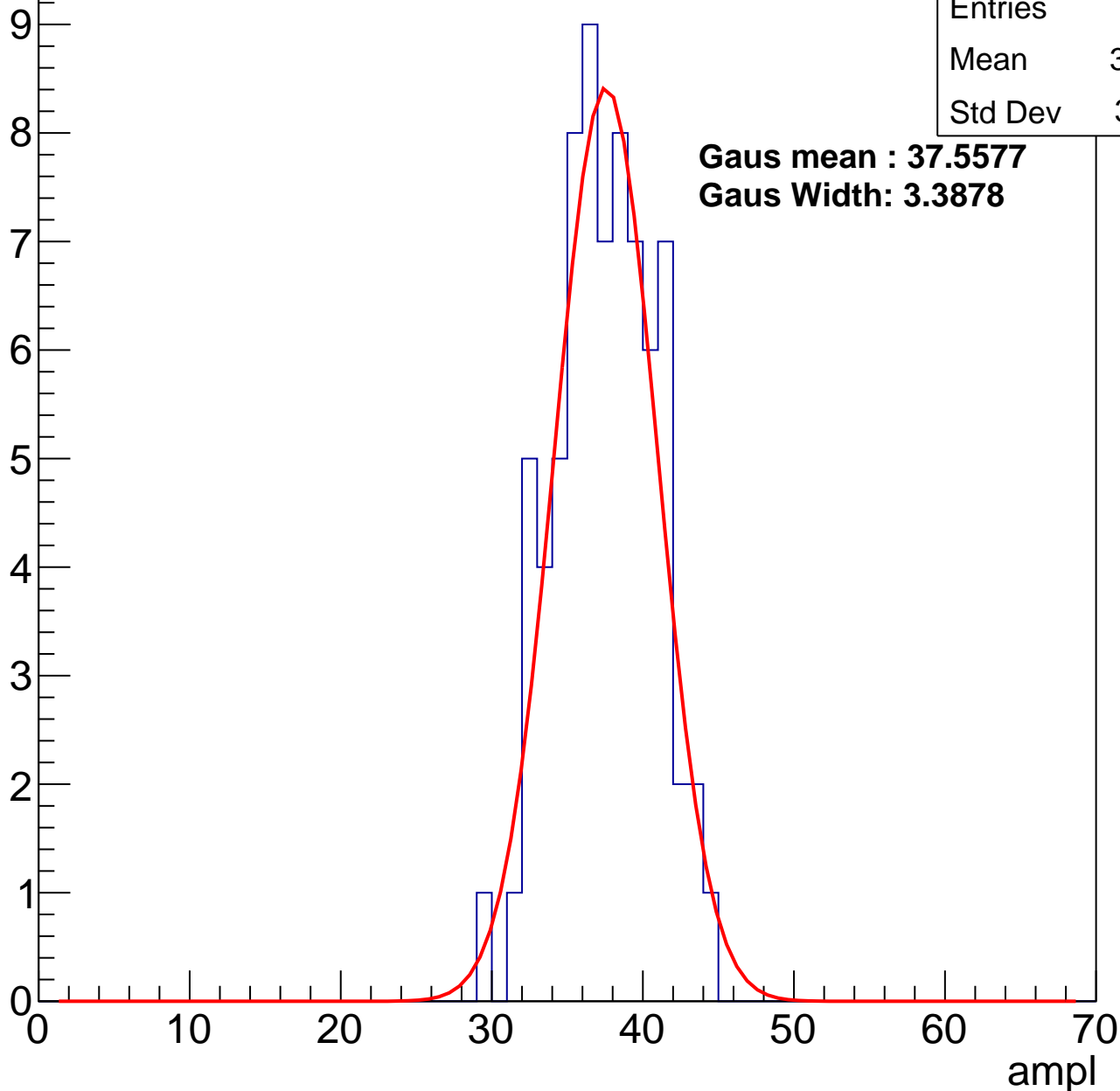
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.03
Std Dev	3.201

Gaus mean : 37.5577

Gaus Width: 3.3878



B0L001S, U25-ch42, adc2

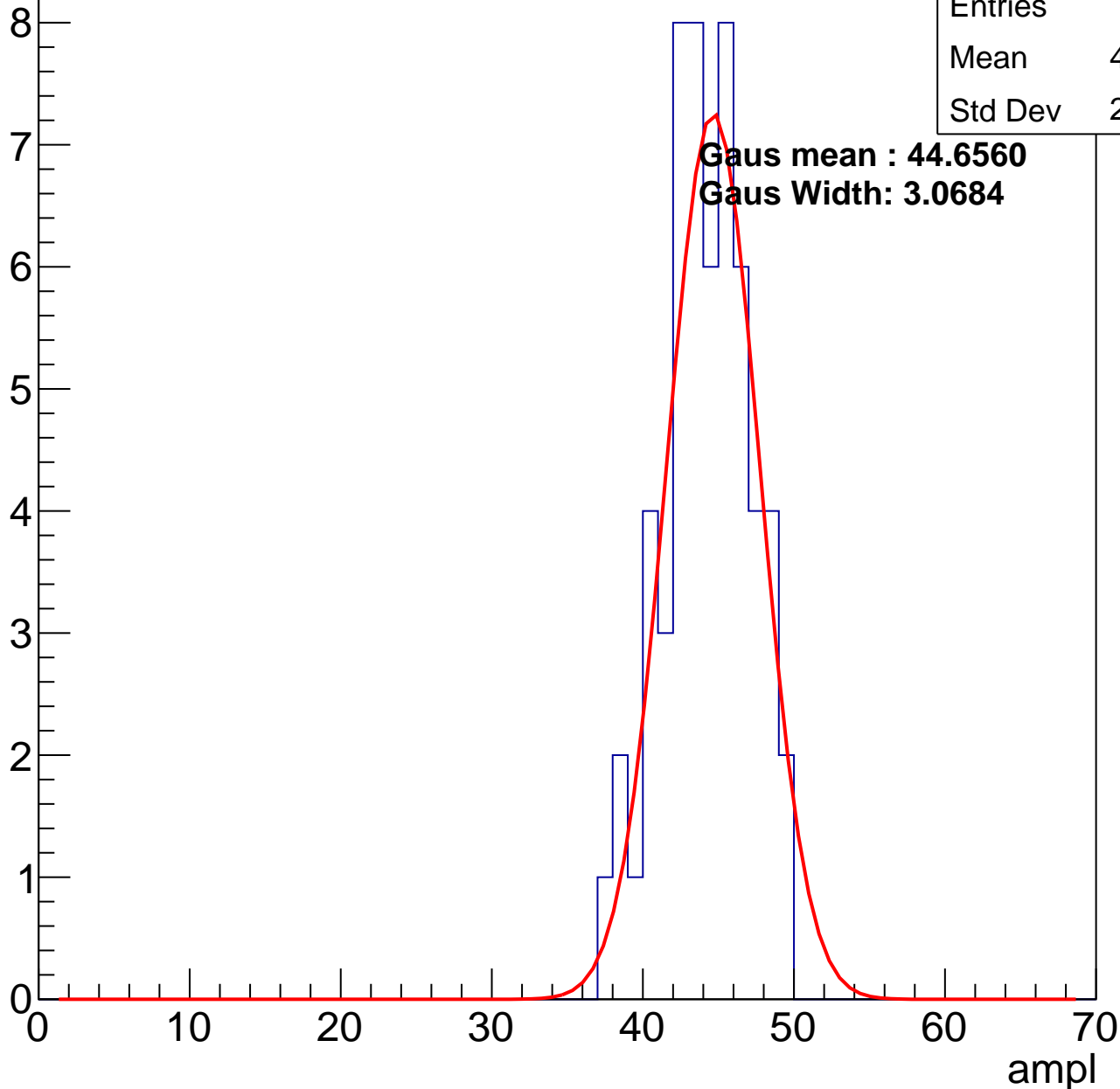
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.74
Std Dev	2.825

Gaus mean : 44.6560

Gaus Width: 3.0684



B0L001S, U25-ch42, adc3

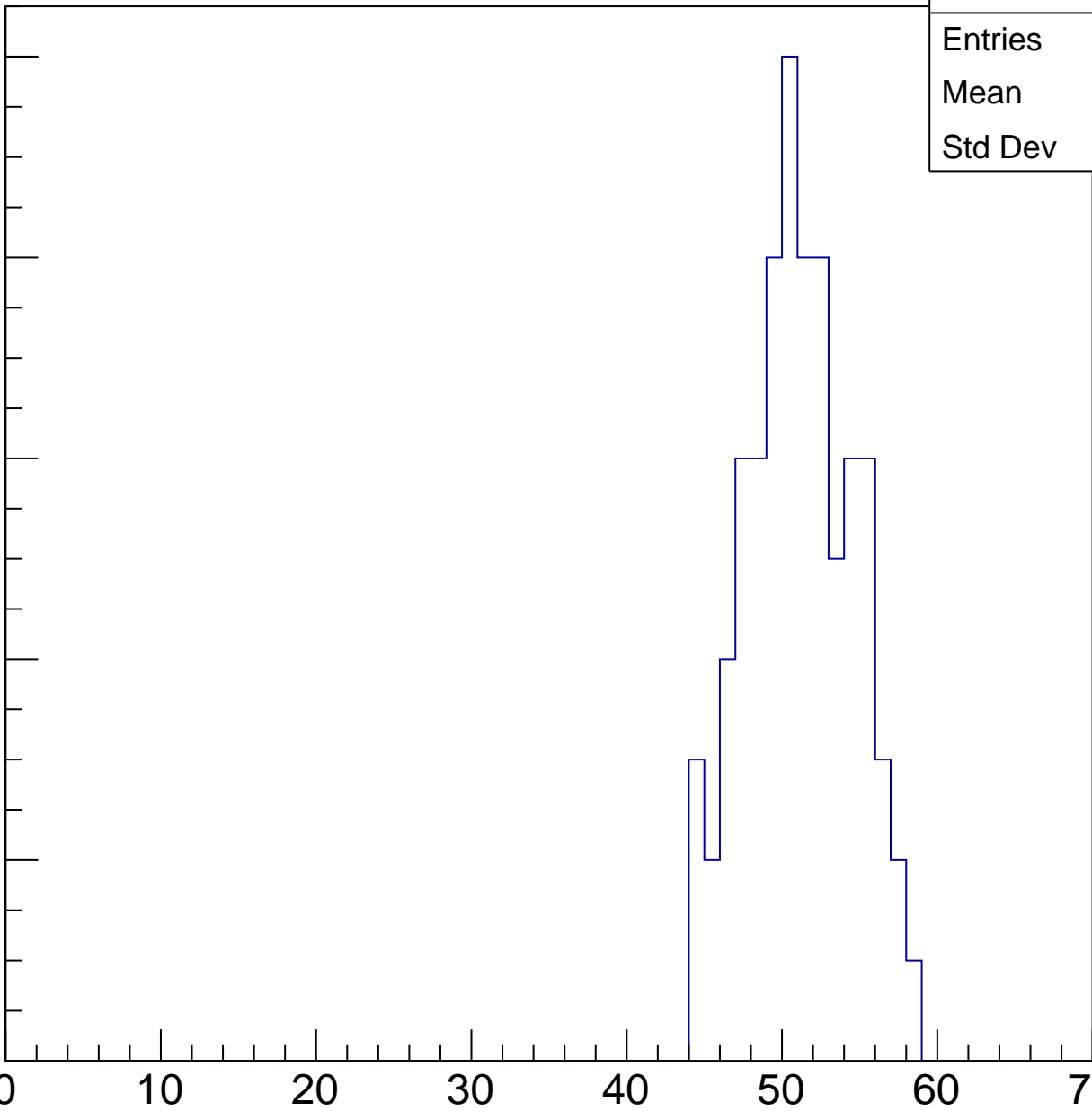
calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	50.65
Std Dev	3.351

Entry

10
8
6
4
2
0

ampl

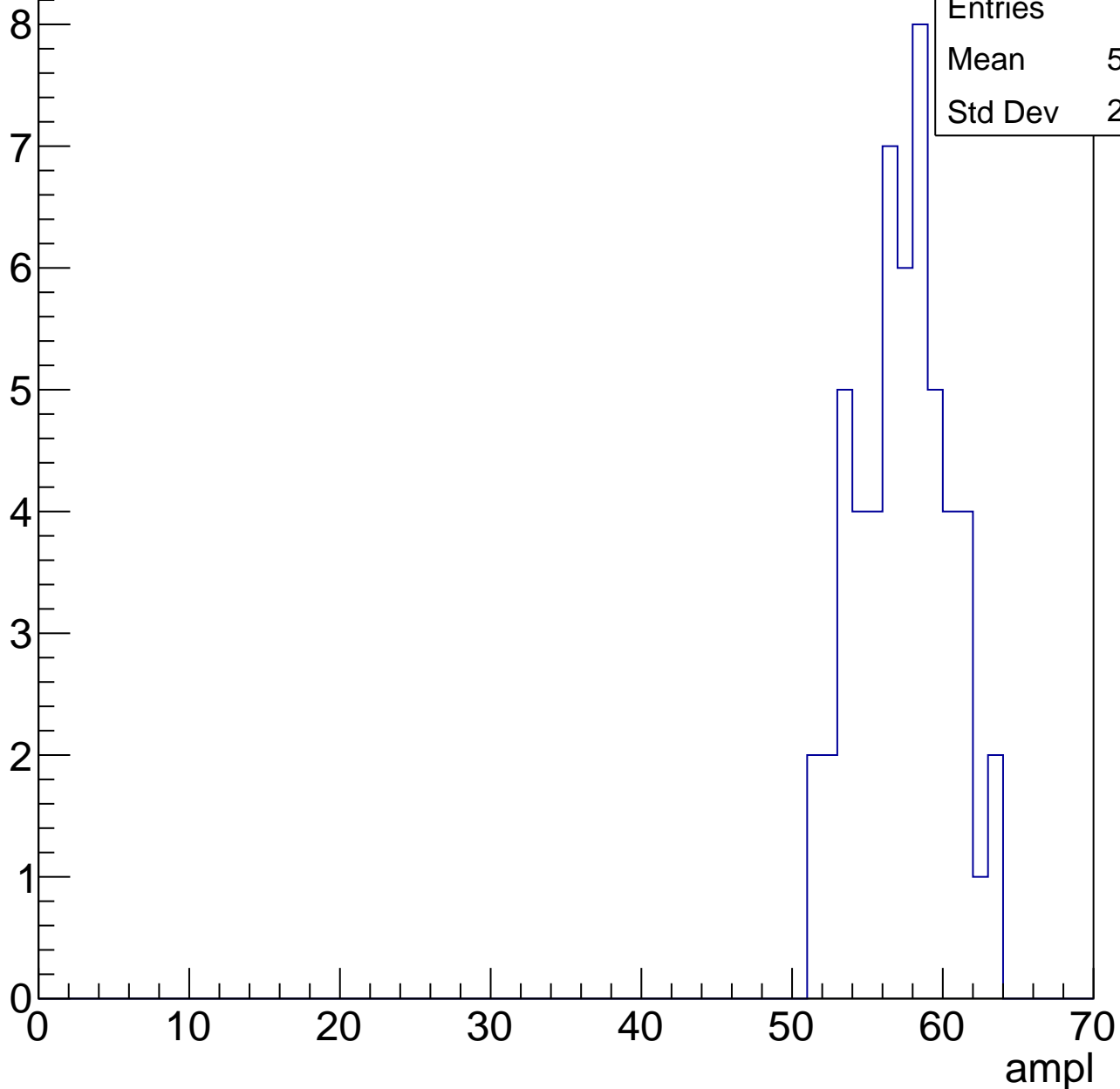


B0L001S, U25-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

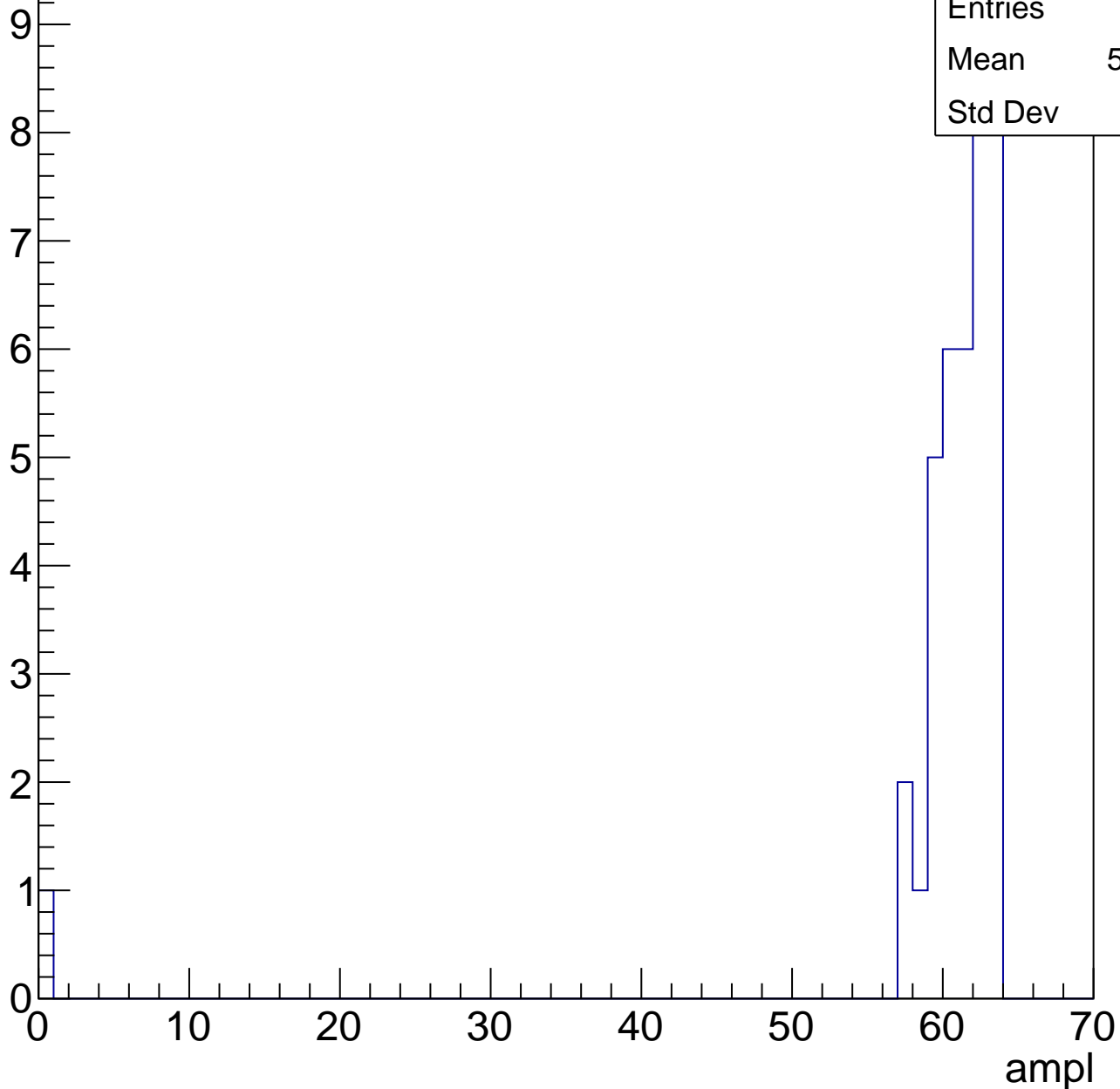
Entries	54
Mean	56.89
Std Dev	2.998



B0L001S, U25-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch43, adc0

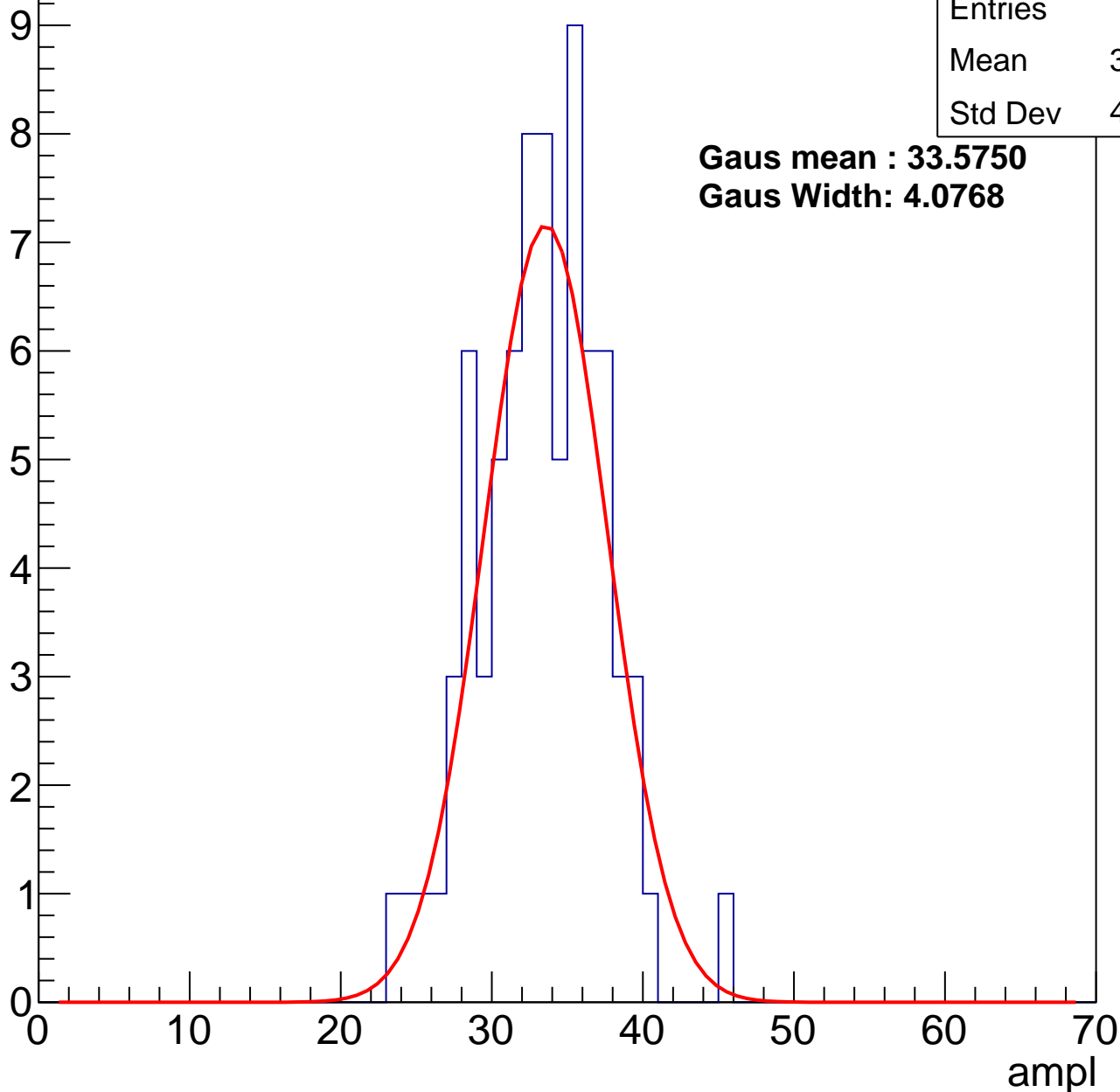
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	32.84
Std Dev	4.016

Gaus mean : 33.5750

Gaus Width: 4.0768



B0L001S, U25-ch43, adc1

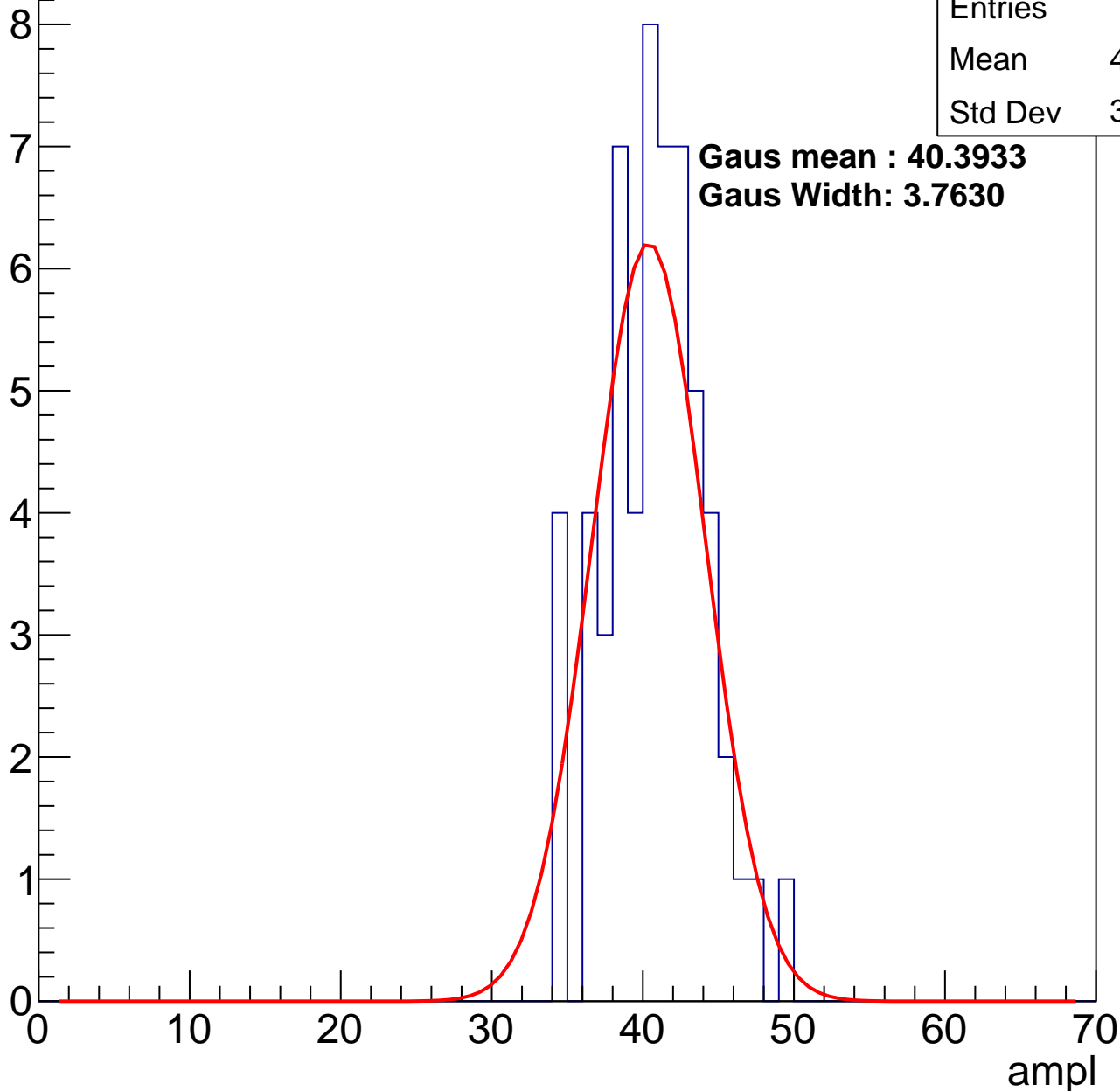
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	40.29
Std Dev	3.275

Gaus mean : 40.3933

Gaus Width: 3.7630



B0L001S, U25-ch43, adc2

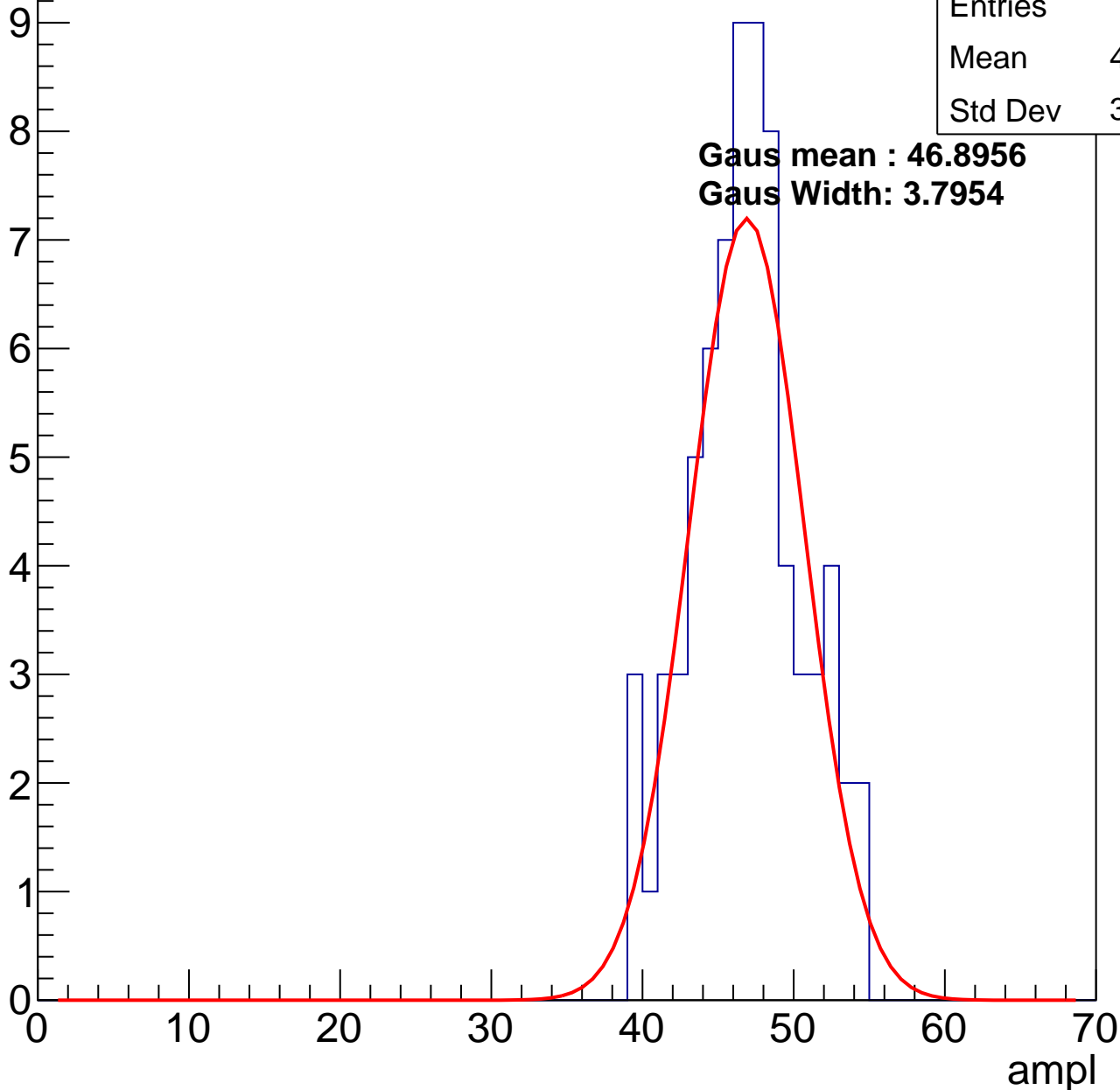
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	46.42
Std Dev	3.612

Gaus mean : 46.8956

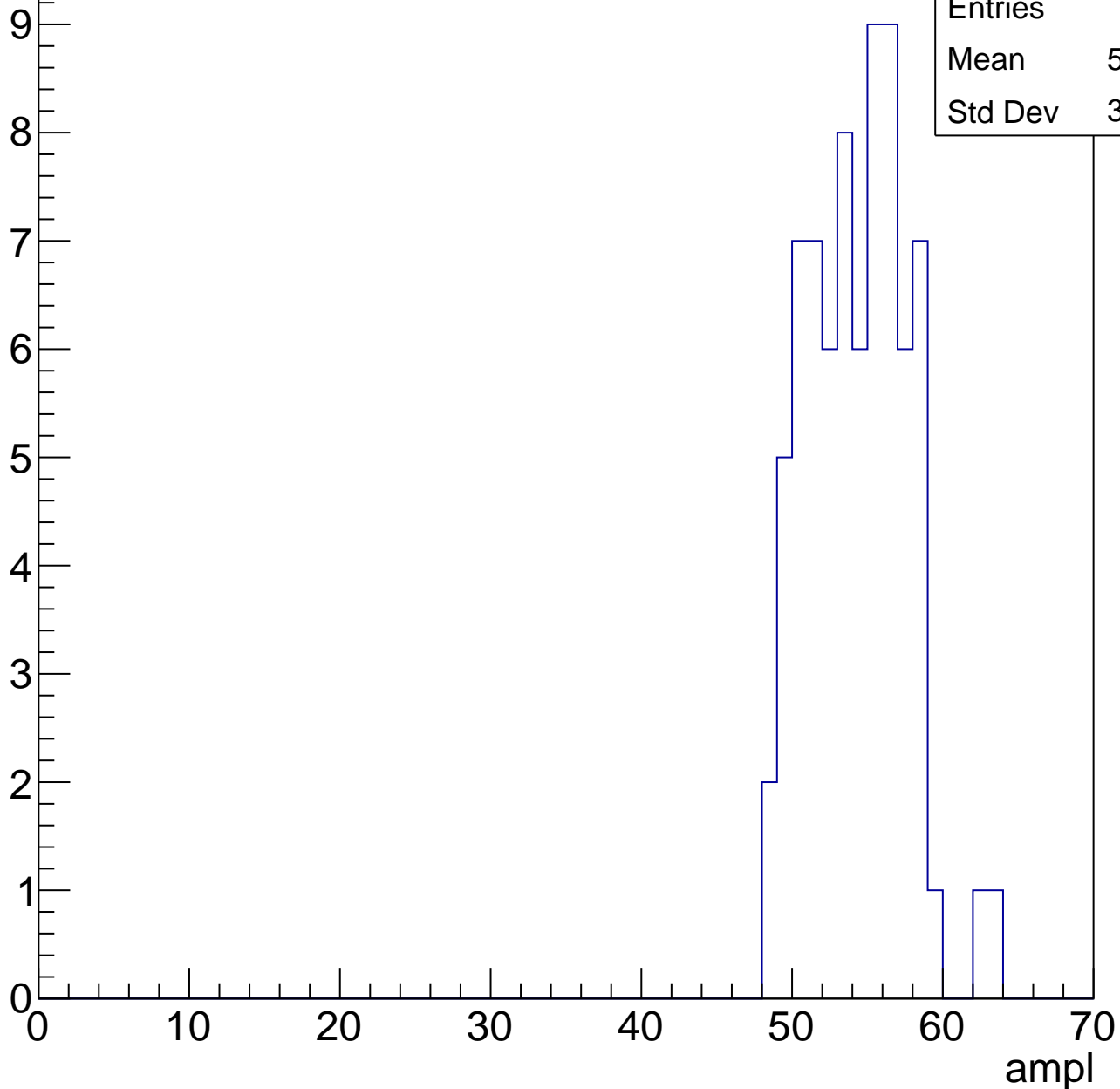
Gaus Width: 3.7954



B0L001S, U25-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

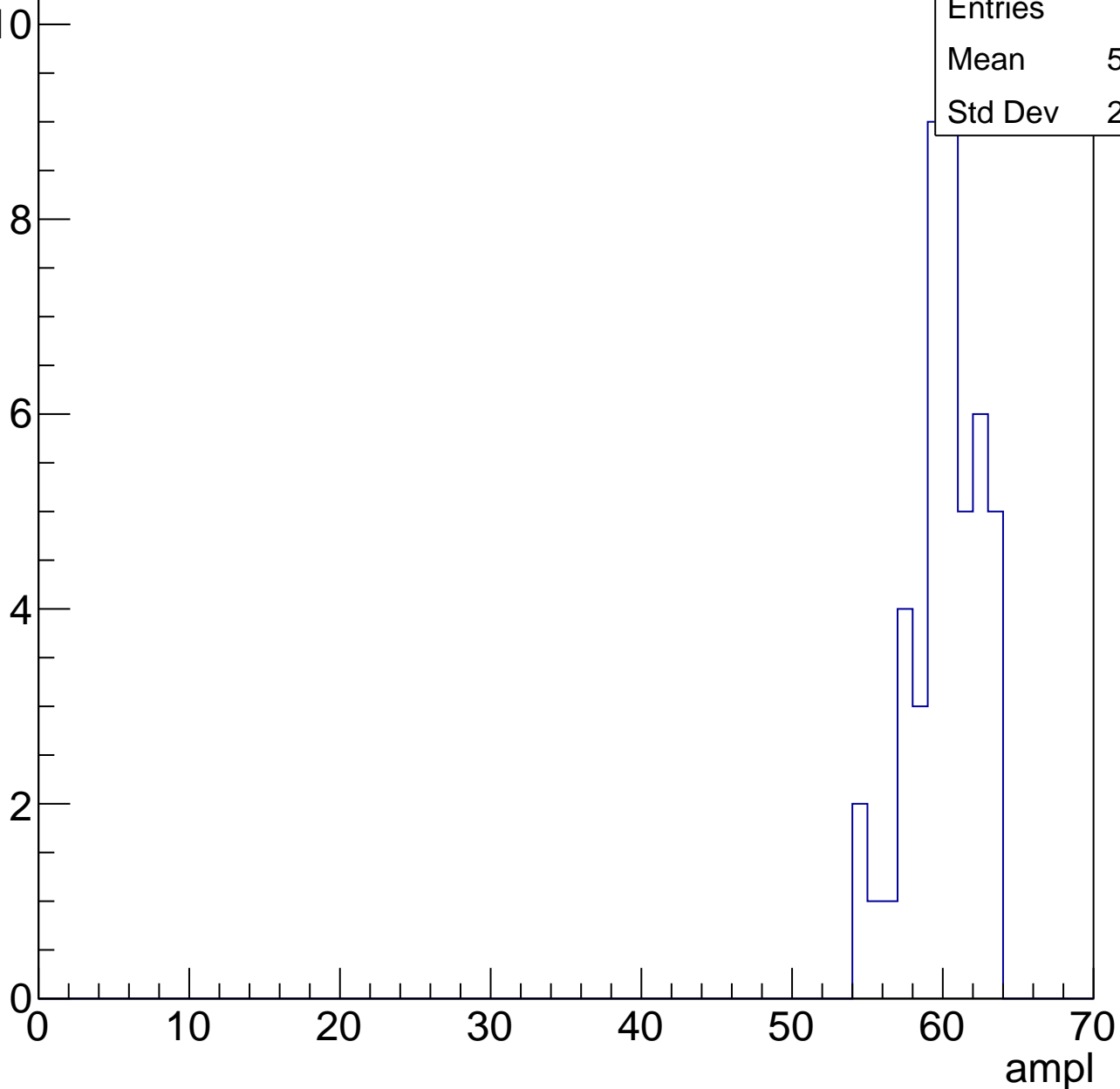


B0L001S, U25-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	59.65
Std Dev	2.277

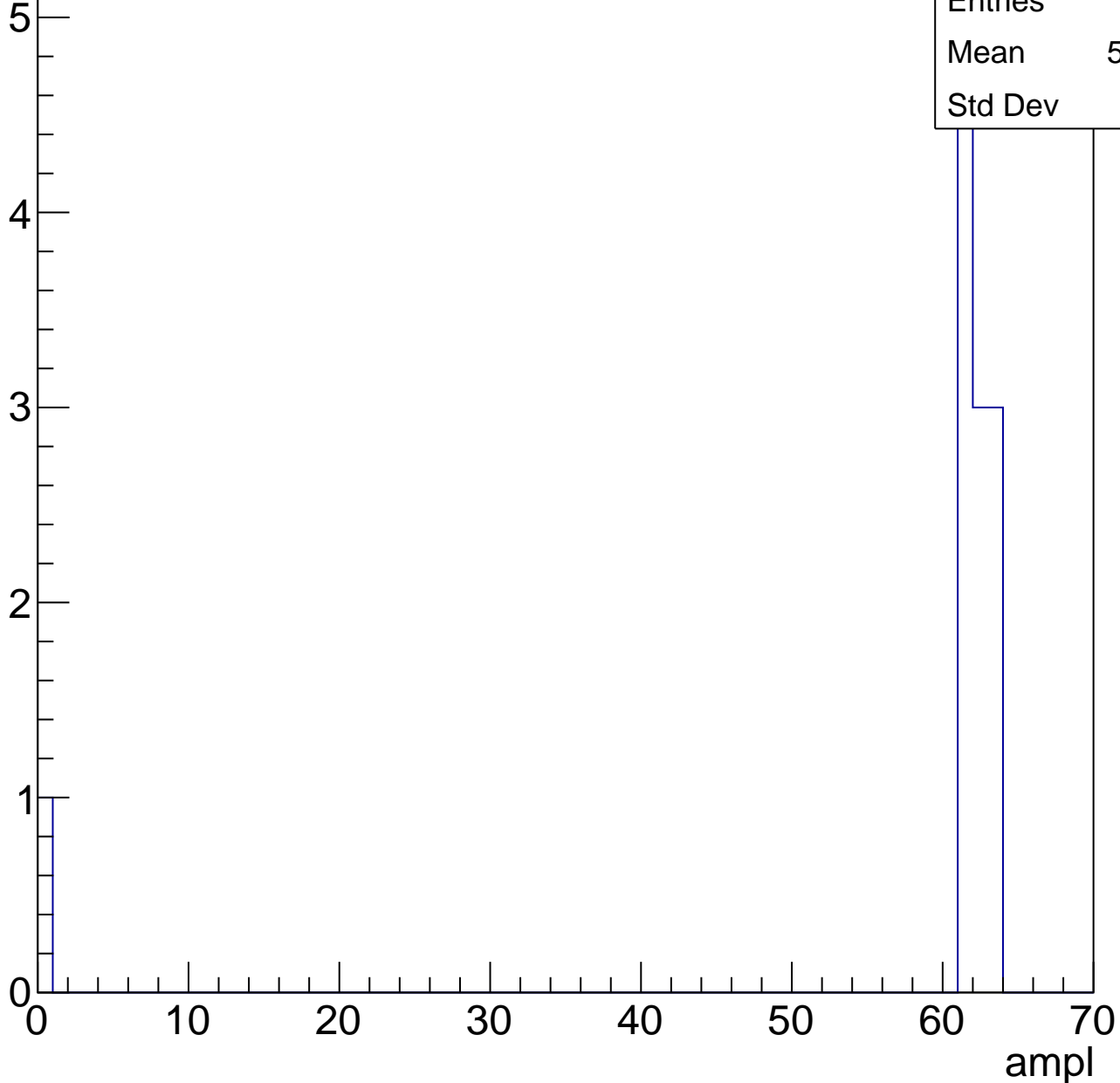


B0L001S, U25-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	12
Mean	56.67
Std Dev	17.1



B0L001S, U25-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch44, adc0

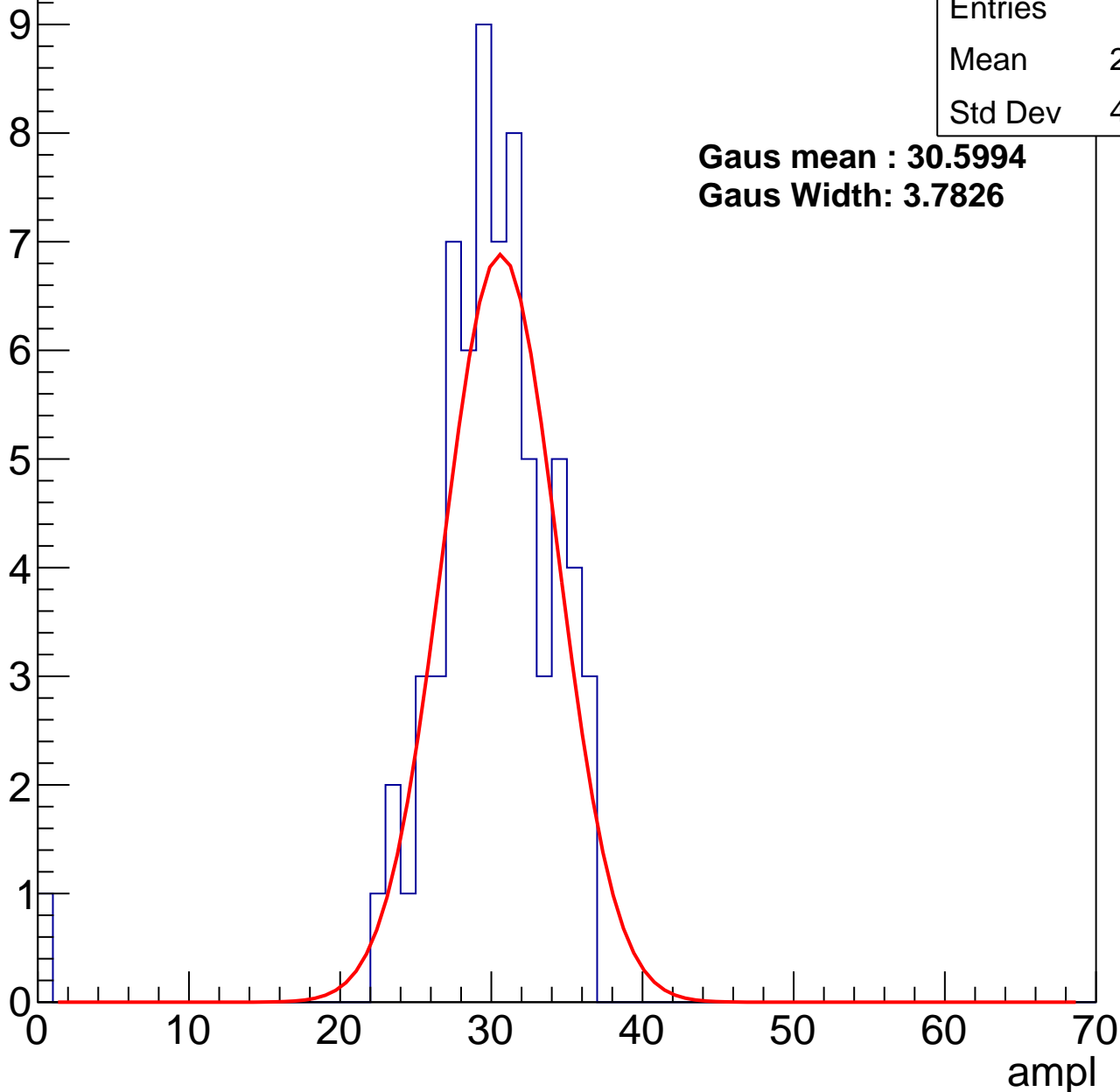
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.38
Std Dev	4.902

Gaus mean : 30.5994

Gaus Width: 3.7826



B0L001S, U25-ch44, adc1

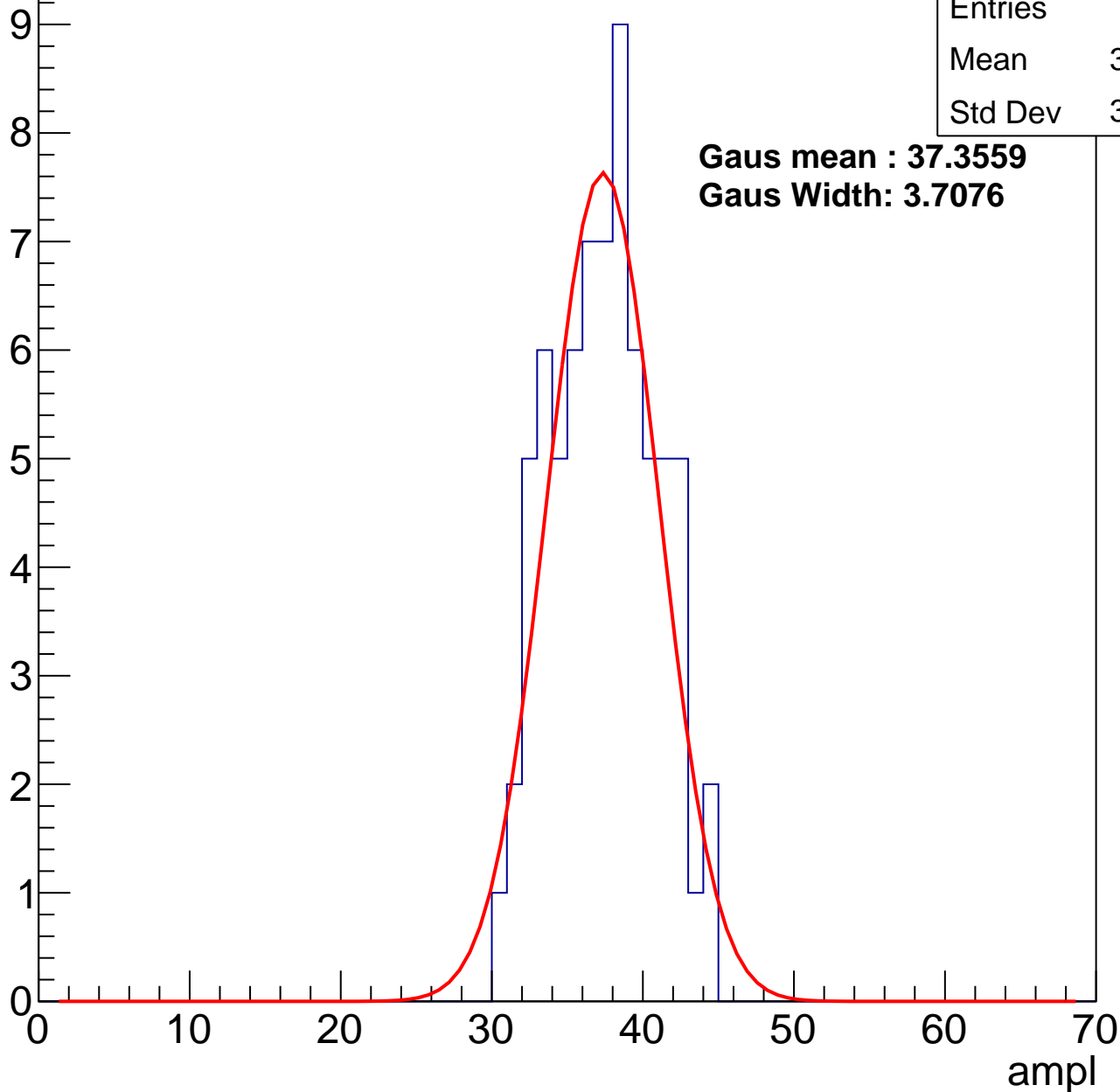
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.99
Std Dev	3.405

Gaus mean : 37.3559

Gaus Width: 3.7076



B0L001S, U25-ch44, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

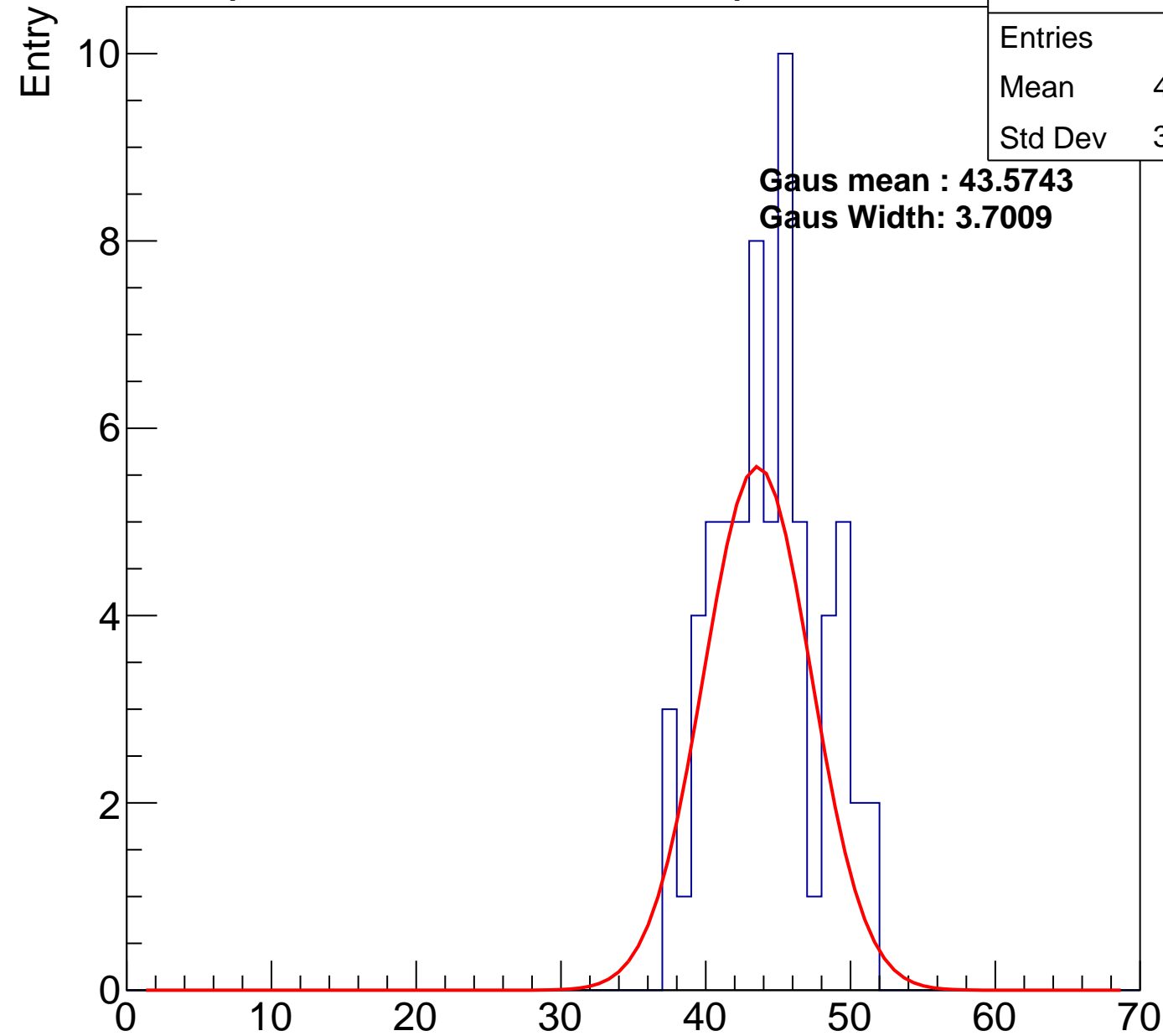
Entries	65
Mean	43.85
Std Dev	3.579

Gaus mean : 43.5743

Gaus Width: 3.7009

10
8
6
4
2
0

ampl

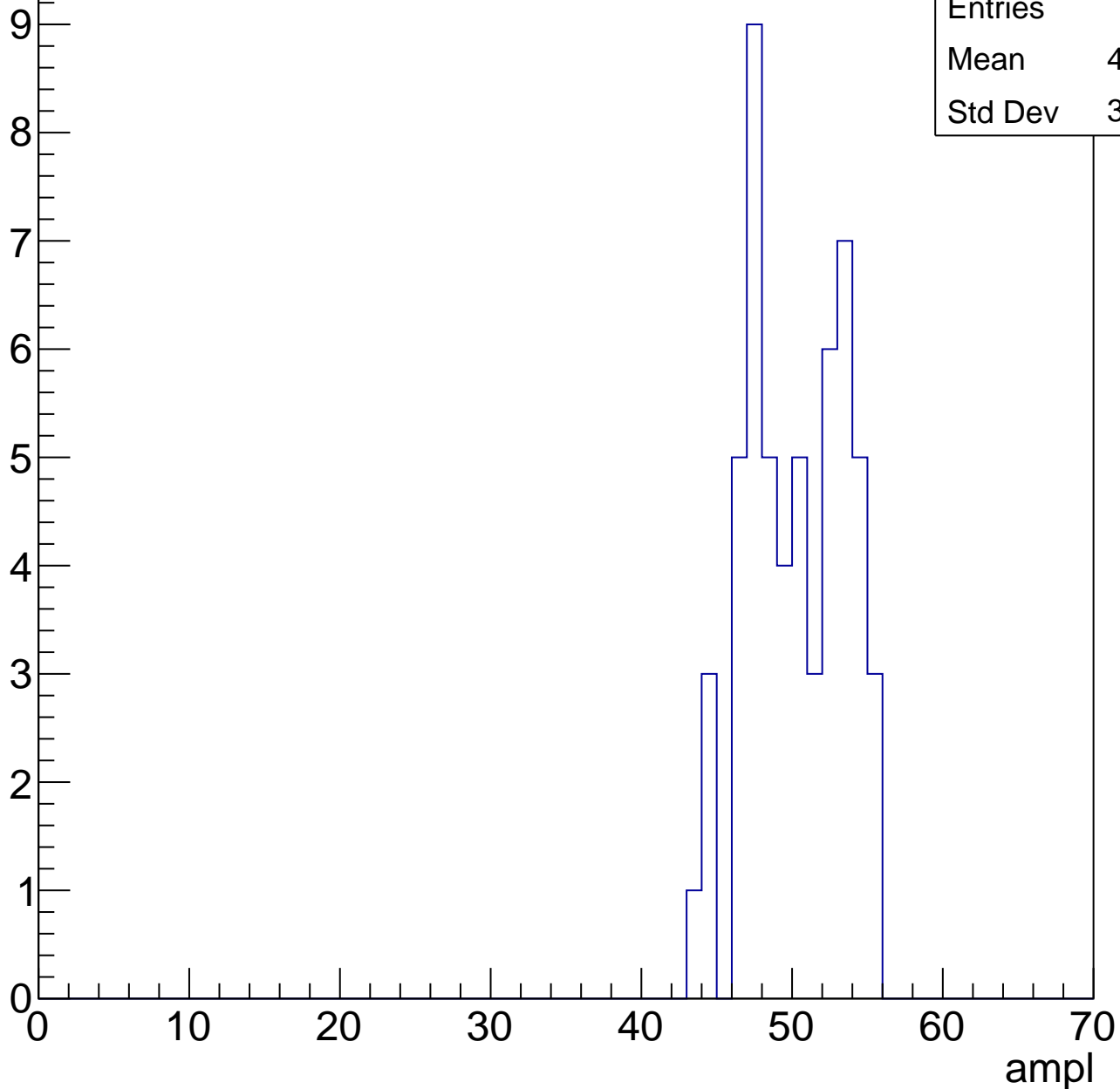


B0L001S, U25-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	49.73
Std Dev	3.227

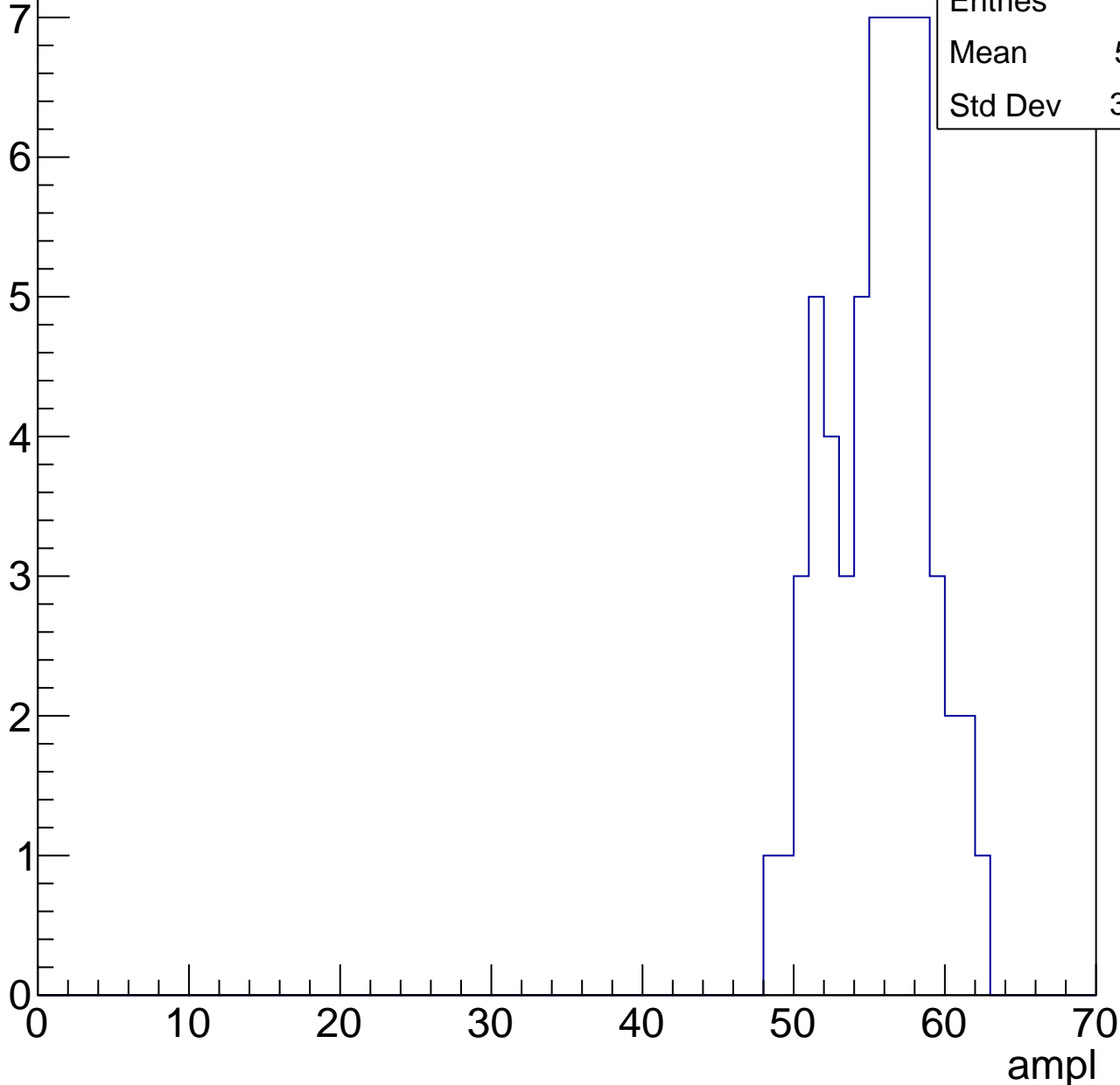


B0L001S, U25-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	55.21
Std Dev	3.236



B0L001S, U25-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59
Std Dev	8.841

ampl

0

10

20

30

40

50

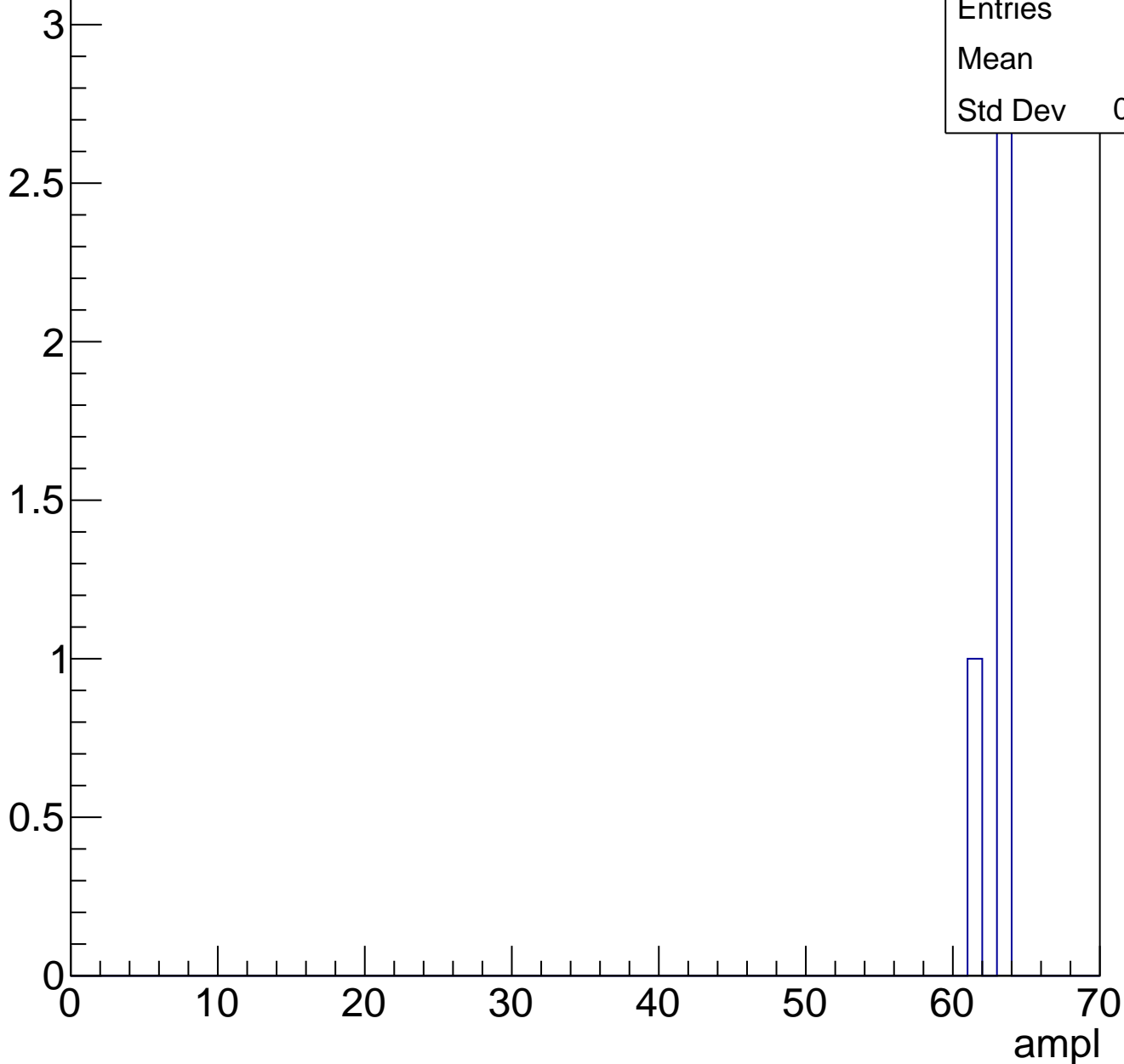
60

70

B0L001S, U25-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch45, adc0

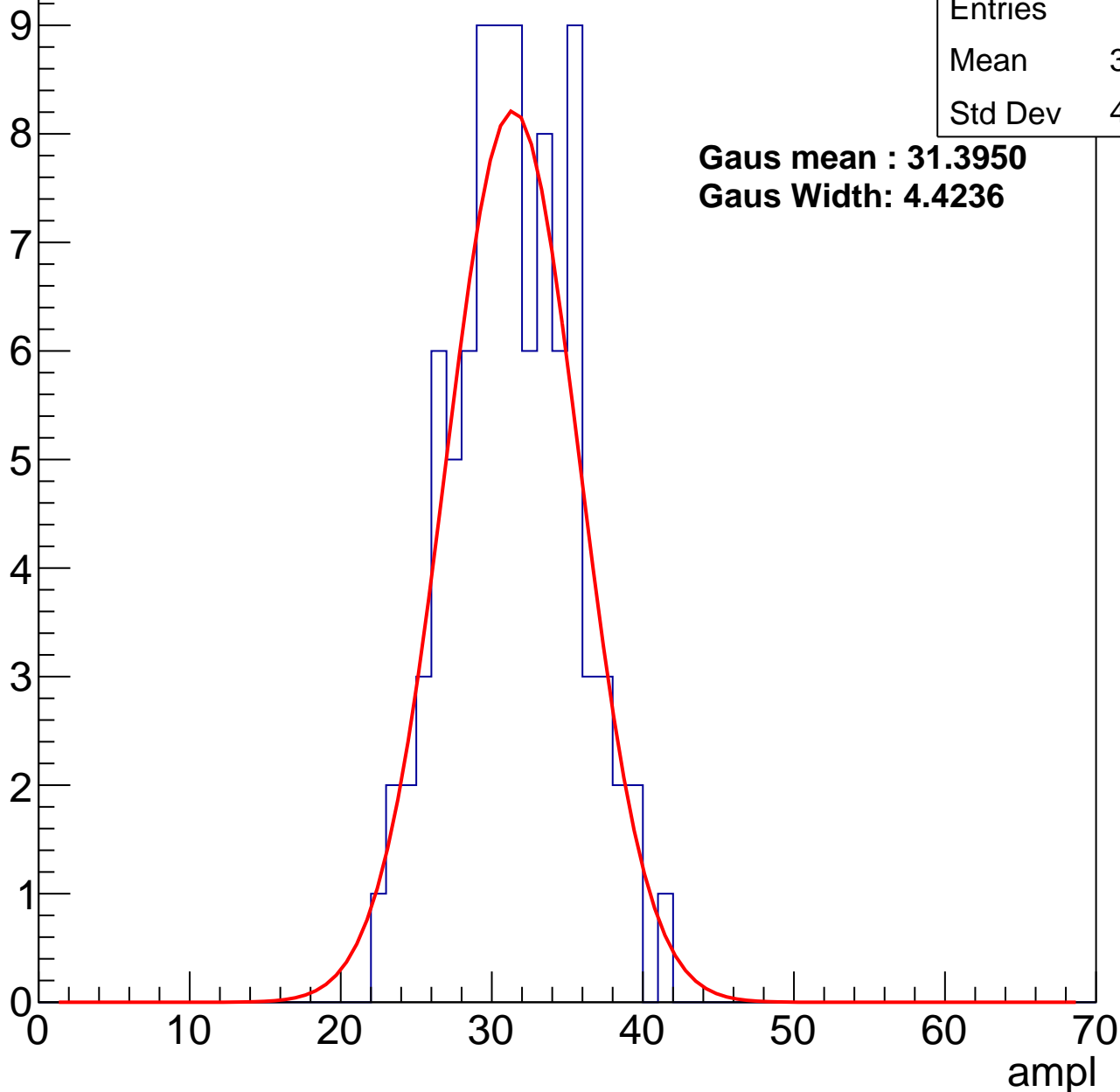
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	92
Mean	30.97
Std Dev	4.023

Gaus mean : 31.3950

Gaus Width: 4.4236



B0L001S, U25-ch45, adc1

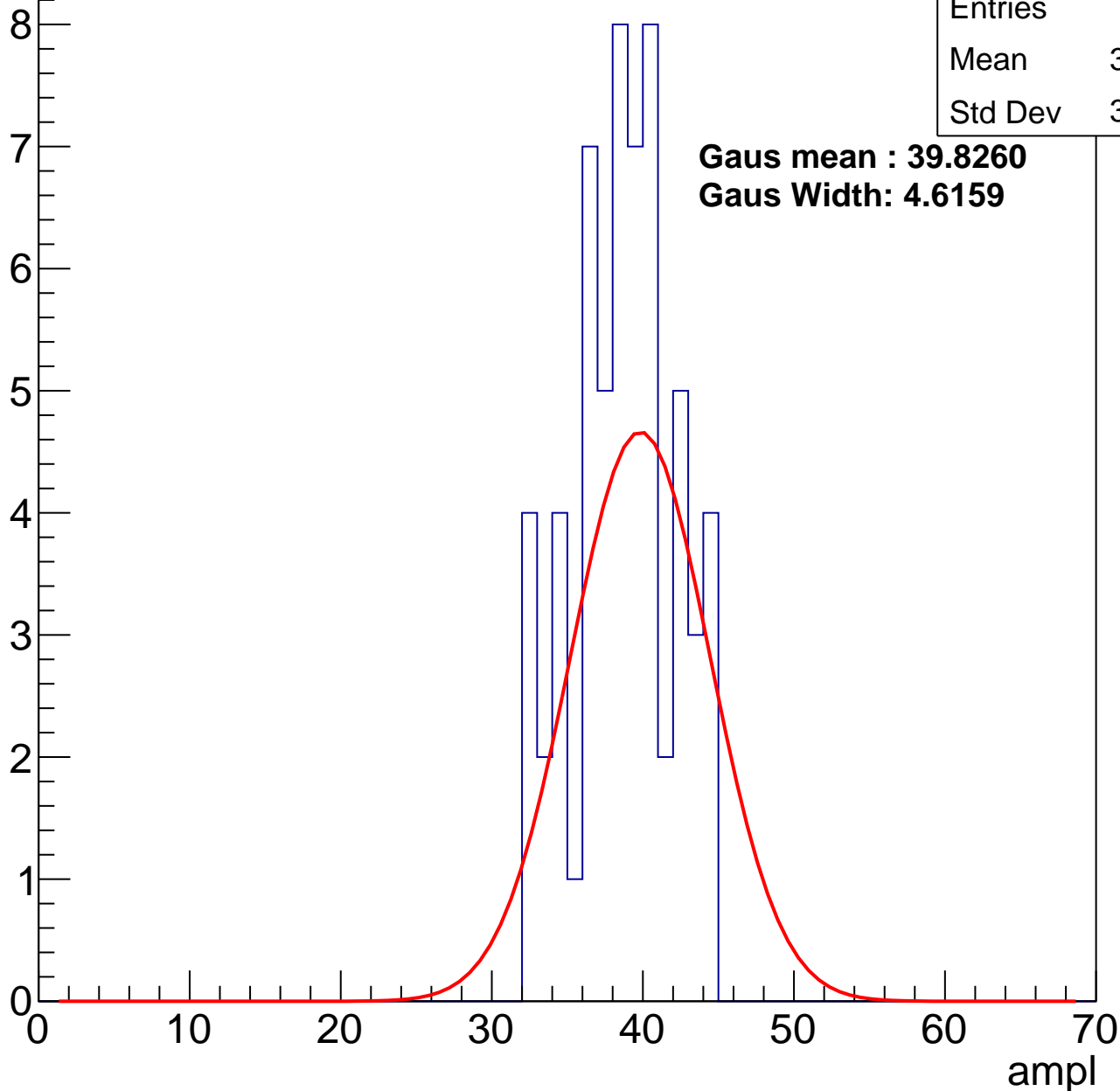
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.27
Std Dev	3.296

Gaus mean : 39.8260

Gaus Width: 4.6159



B0L001S, U25-ch45, adc2

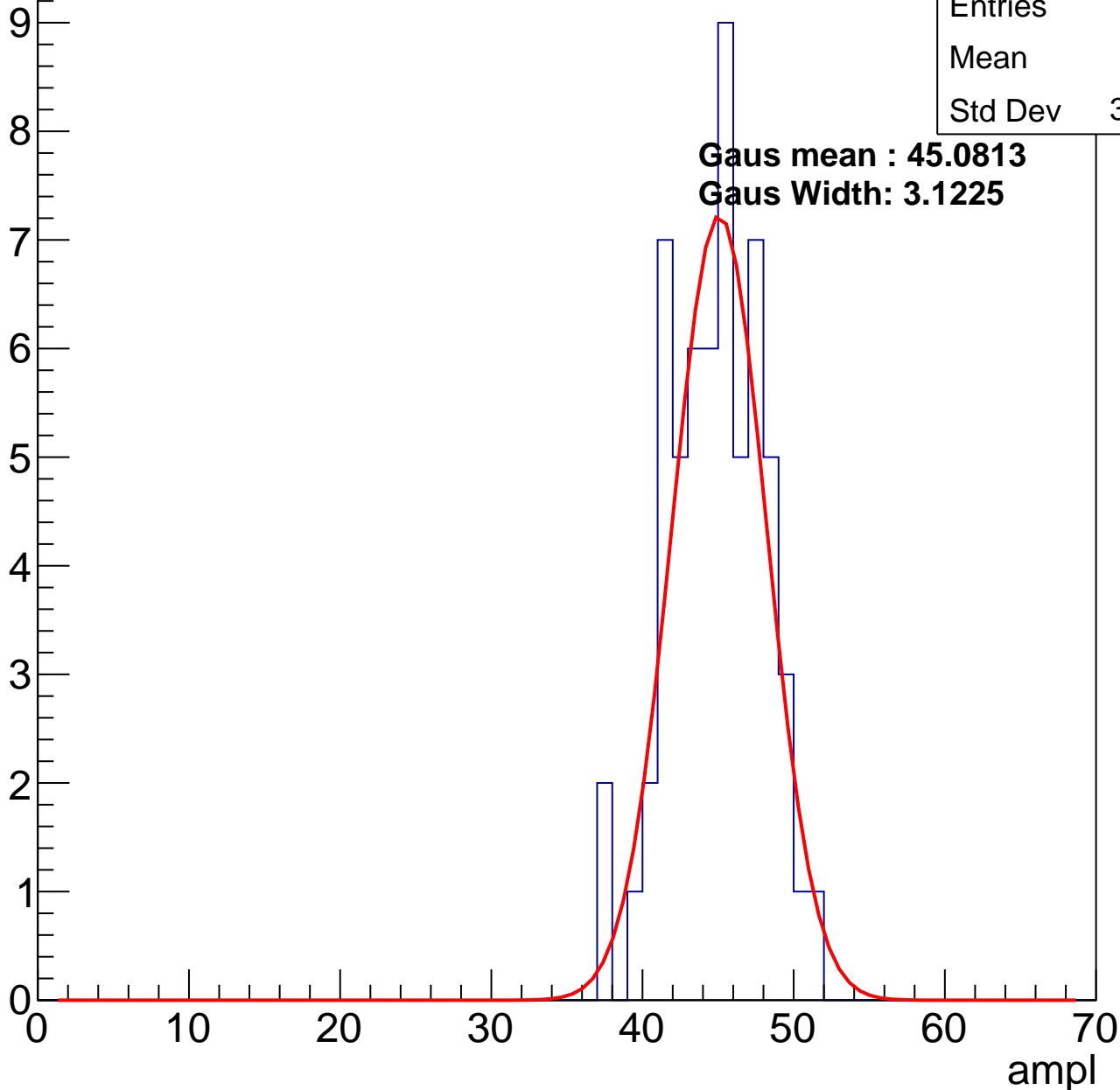
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.4
Std Dev	3.072

Gaus mean : 45.0813

Gaus Width: 3.1225

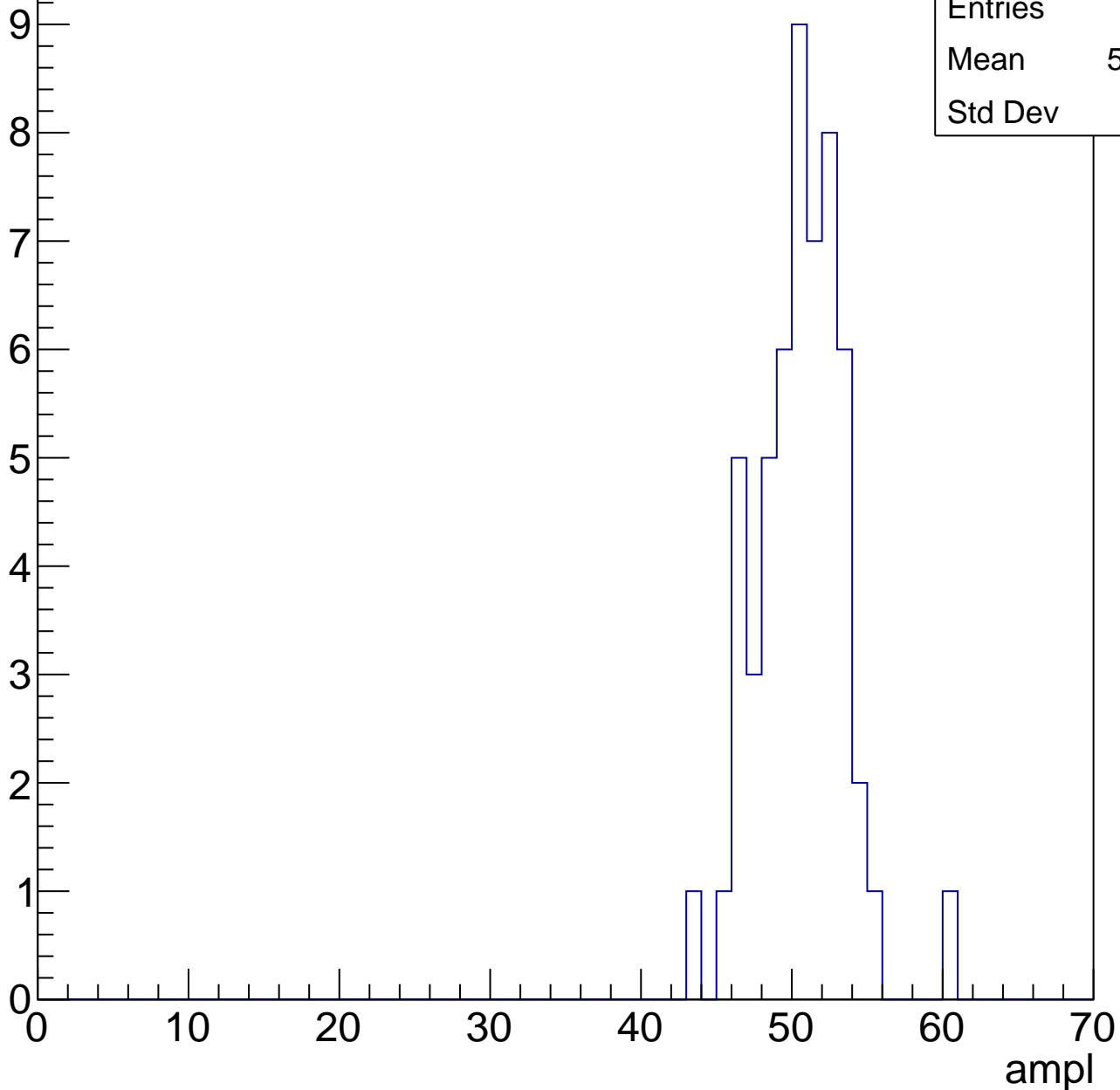


B0L001S, U25-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.13
Std Dev	2.88

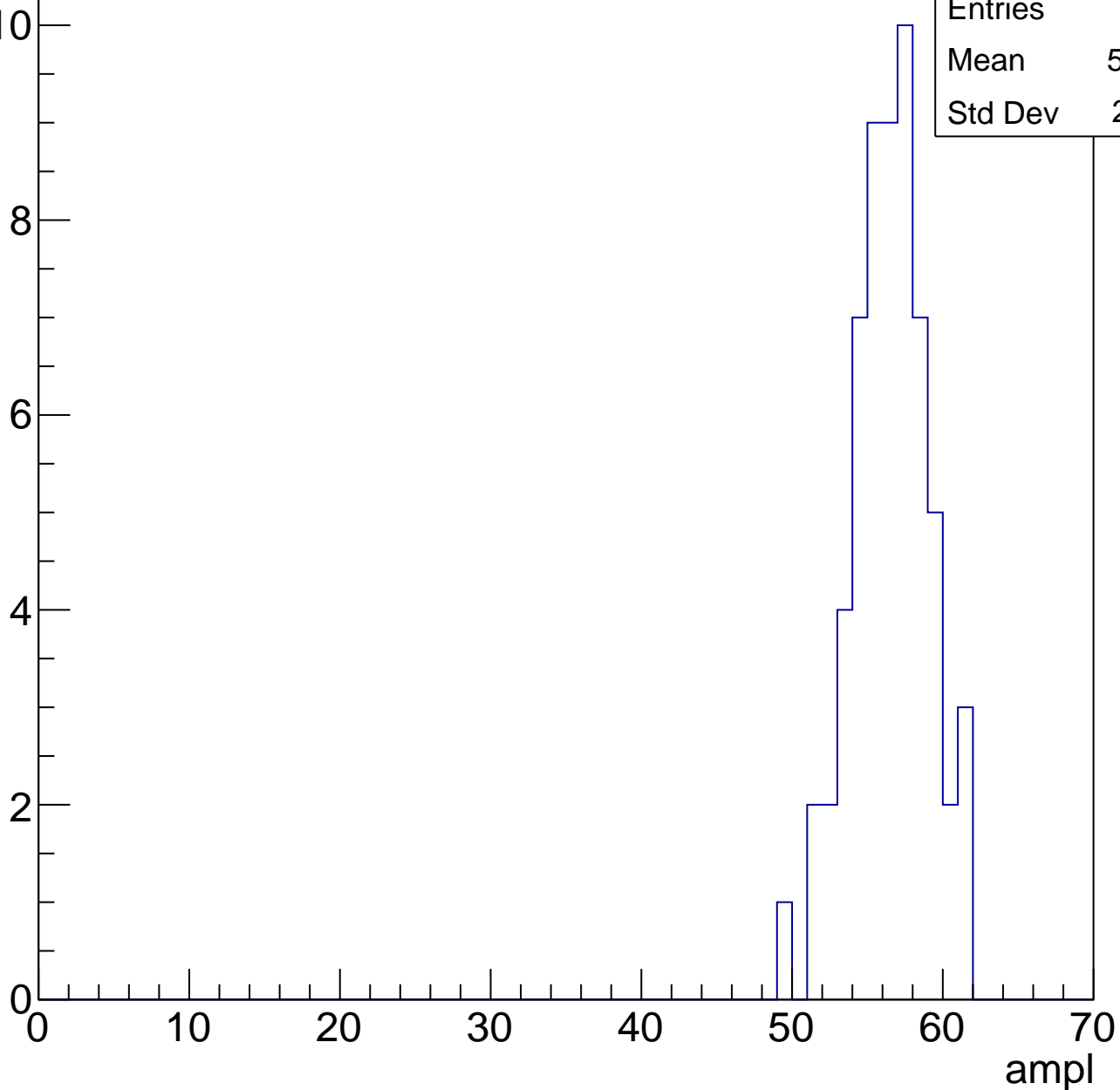


B0L001S, U25-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.03
Std Dev	2.541

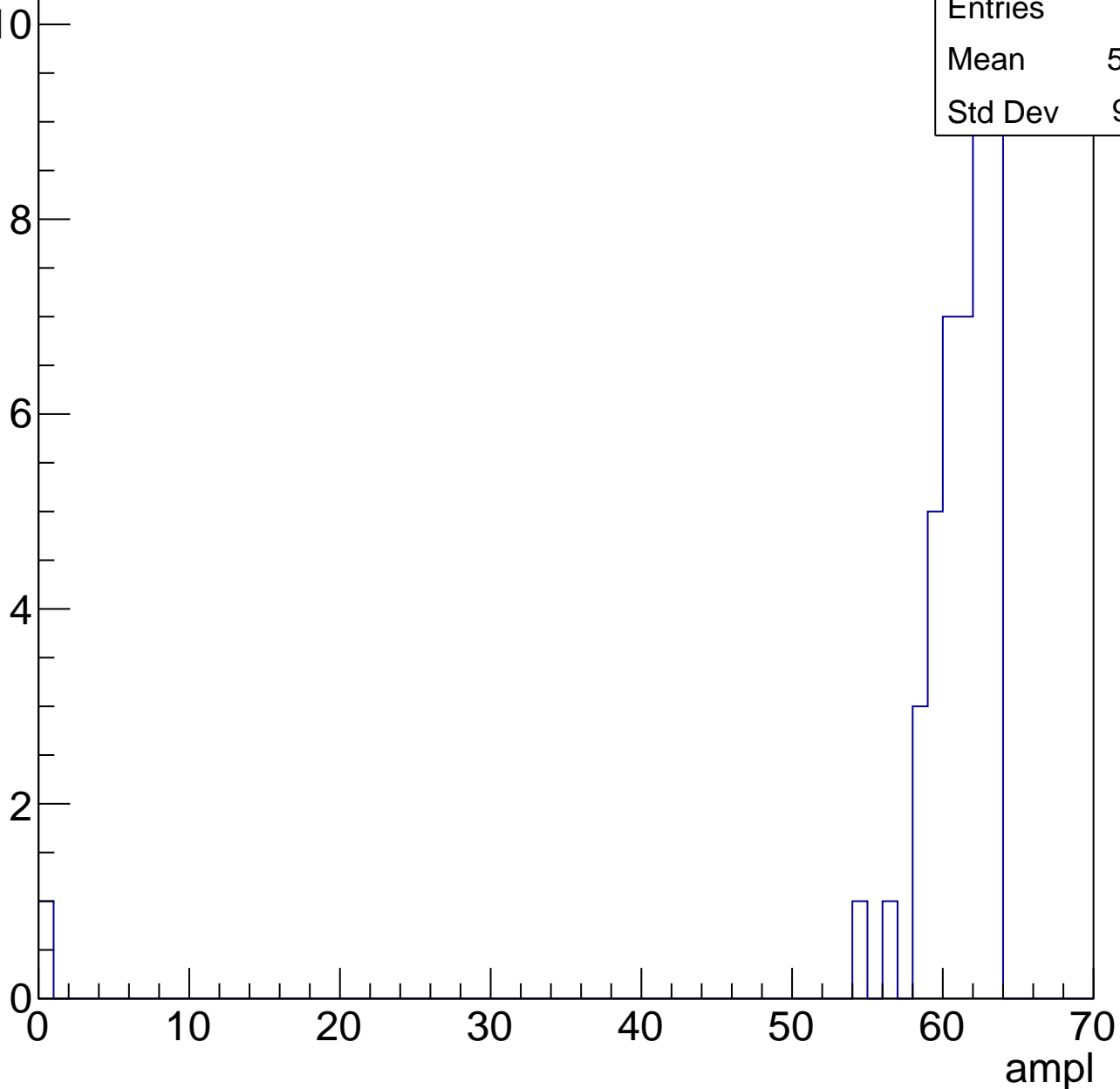


B0L001S, U25-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	59.47
Std Dev	9.181



B0L001S, U25-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch46, adc0

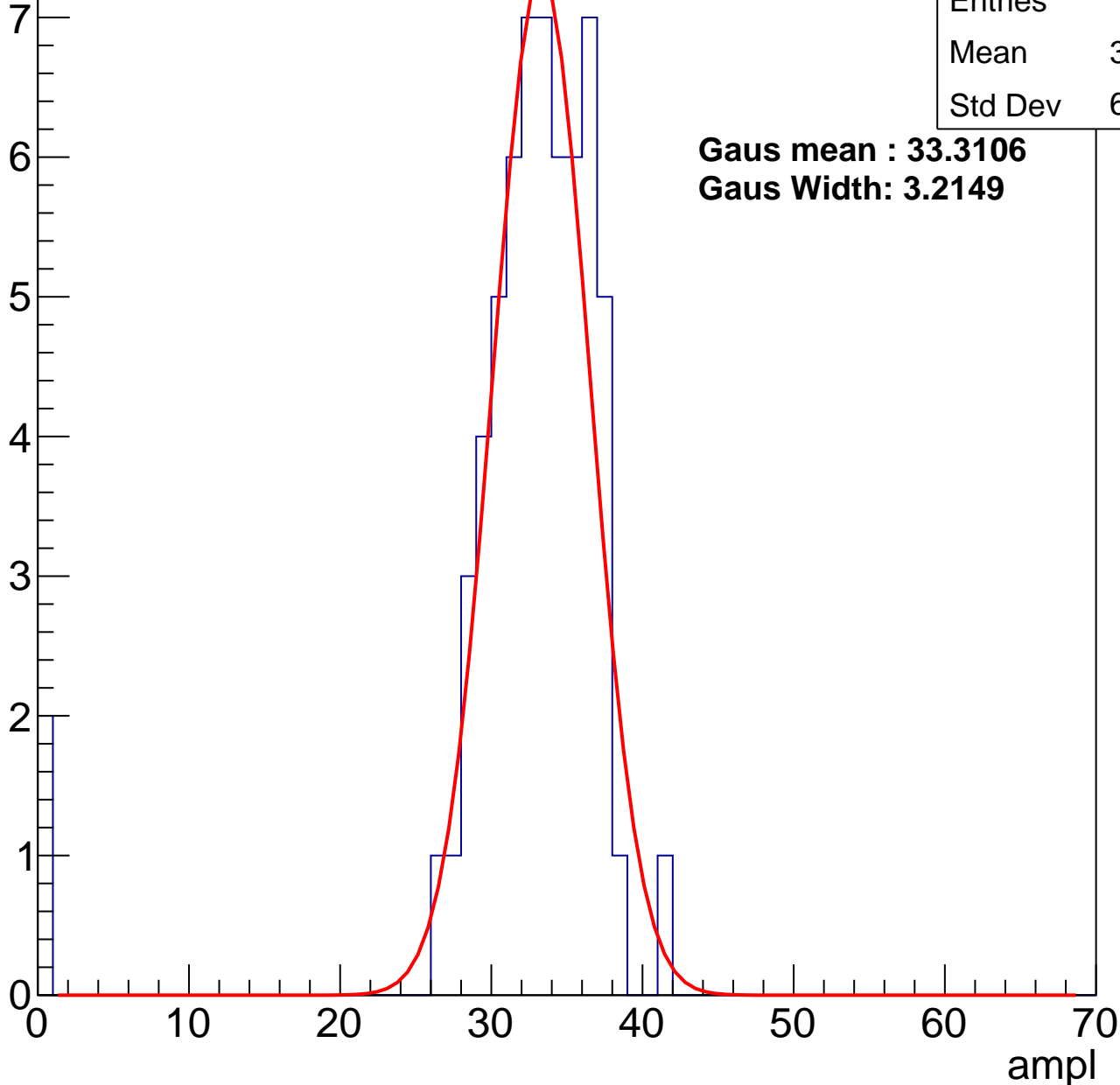
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.84
Std Dev	6.546

Gaus mean : 33.3106

Gaus Width: 3.2149



B0L001S, U25-ch46, adc1

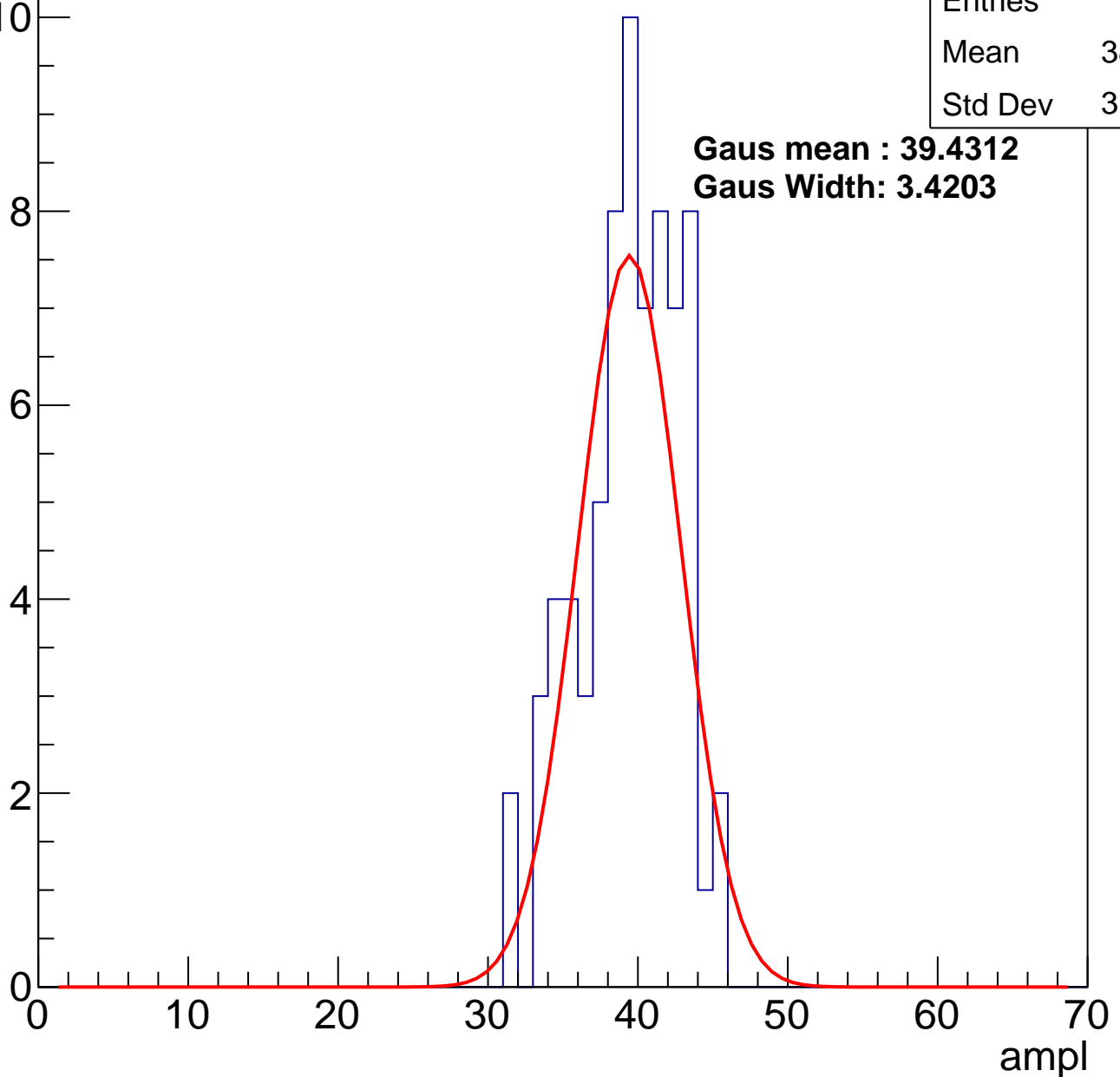
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.94
Std Dev	3.295

Gaus mean : 39.4312

Gaus Width: 3.4203



B0L001S, U25-ch46, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	46.15
Std Dev	2.978

Gaus mean : 47.1424

Gaus Width: 2.7827

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B0L001S, U25-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	52.97
Std Dev	3.472

Entry

10

8

6

4

2

0

0

10

20

30

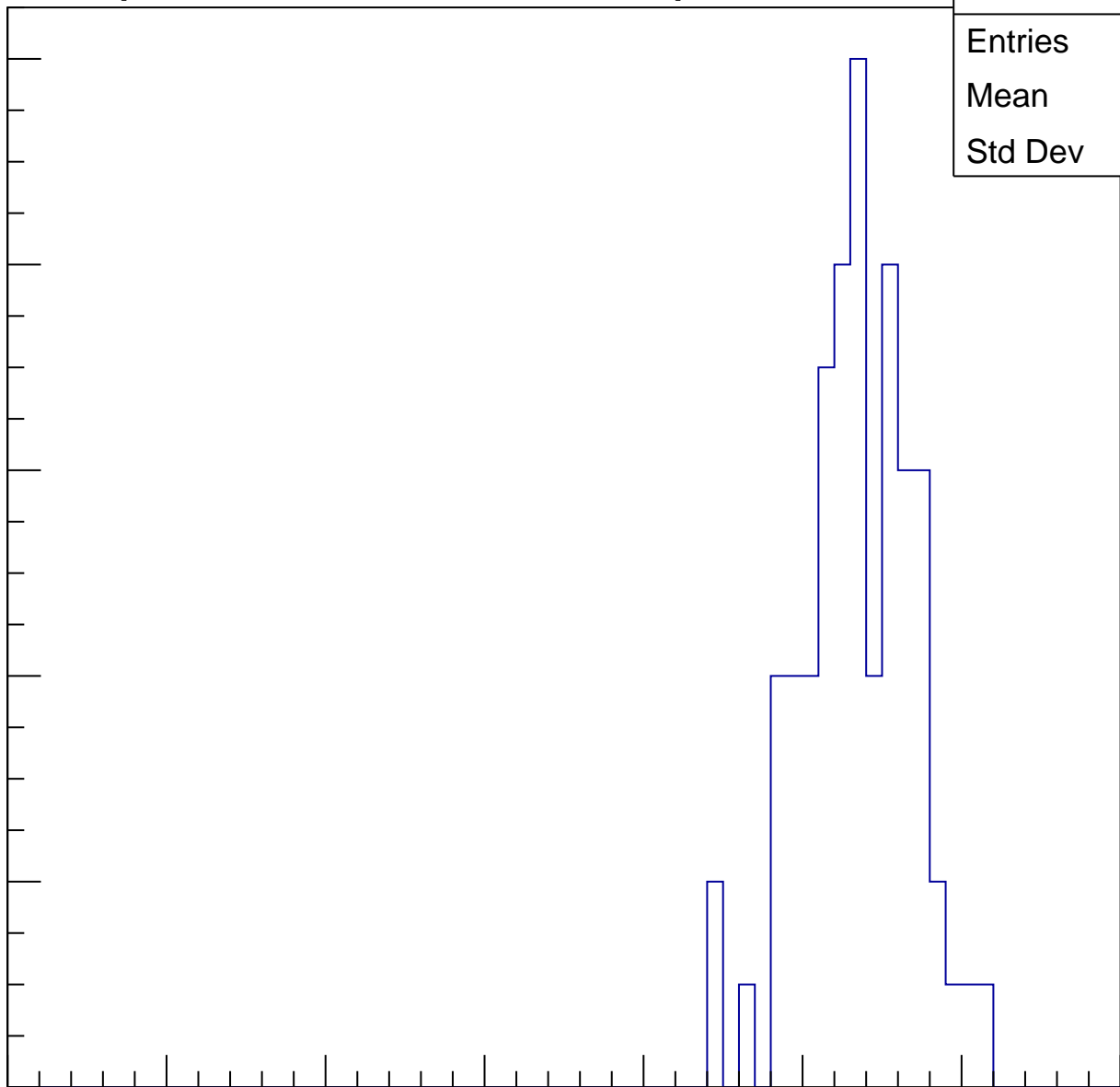
40

50

60

70

ampl

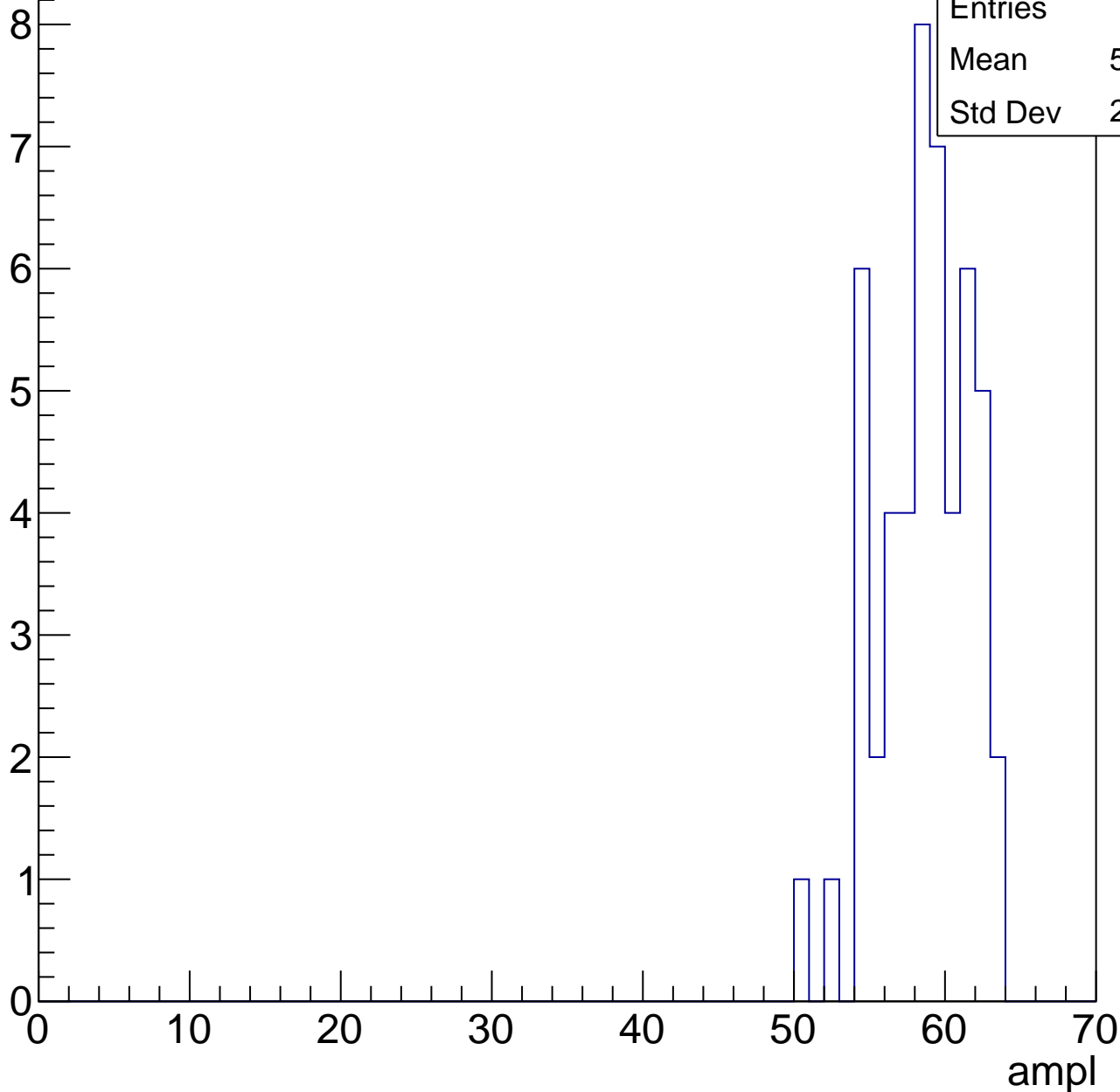


B0L001S, U25-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	58.14
Std Dev	2.967

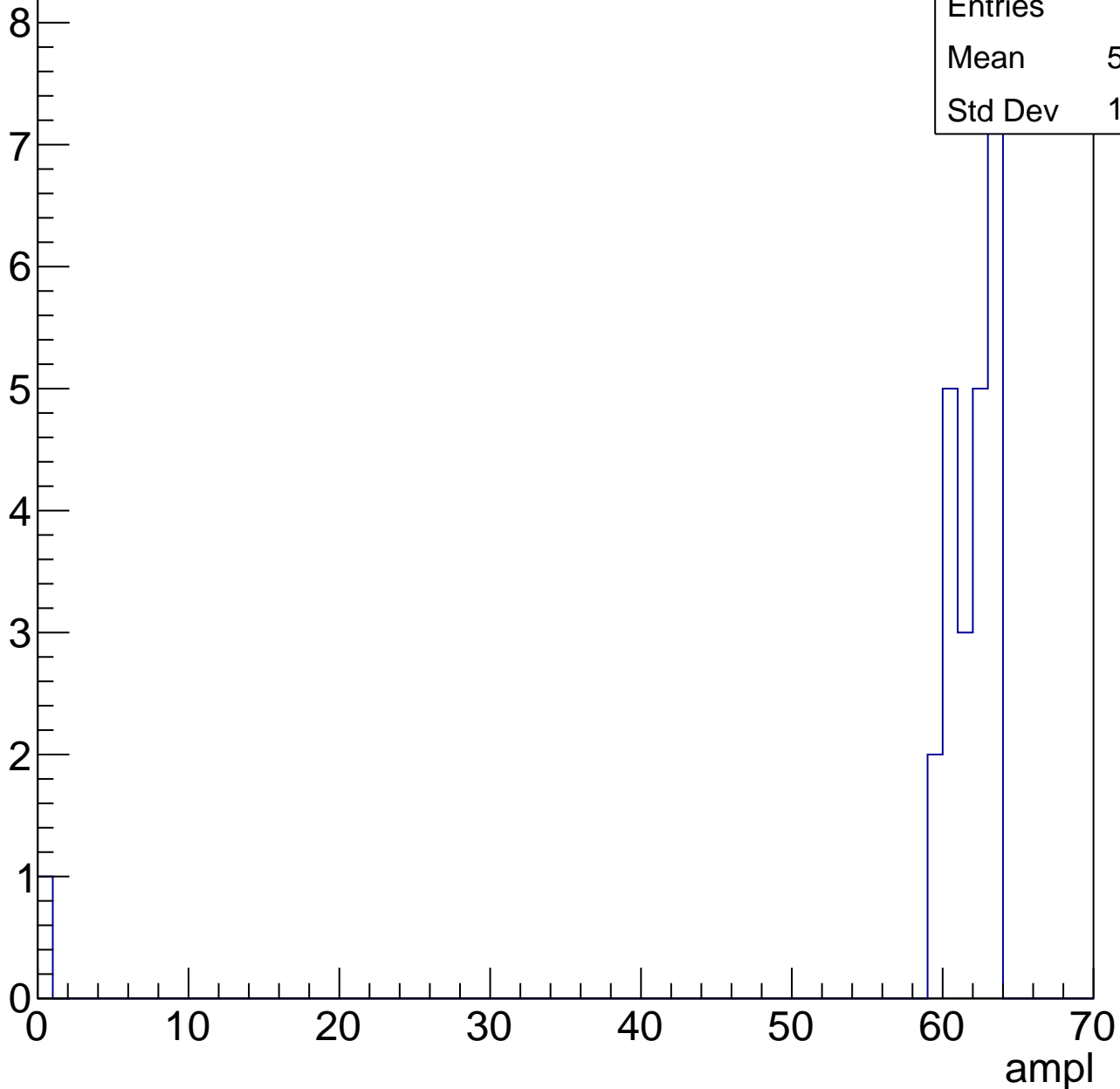


B0L001S, U25-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.96
Std Dev	12.37



B0L001S, U25-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U25-ch47, adc0

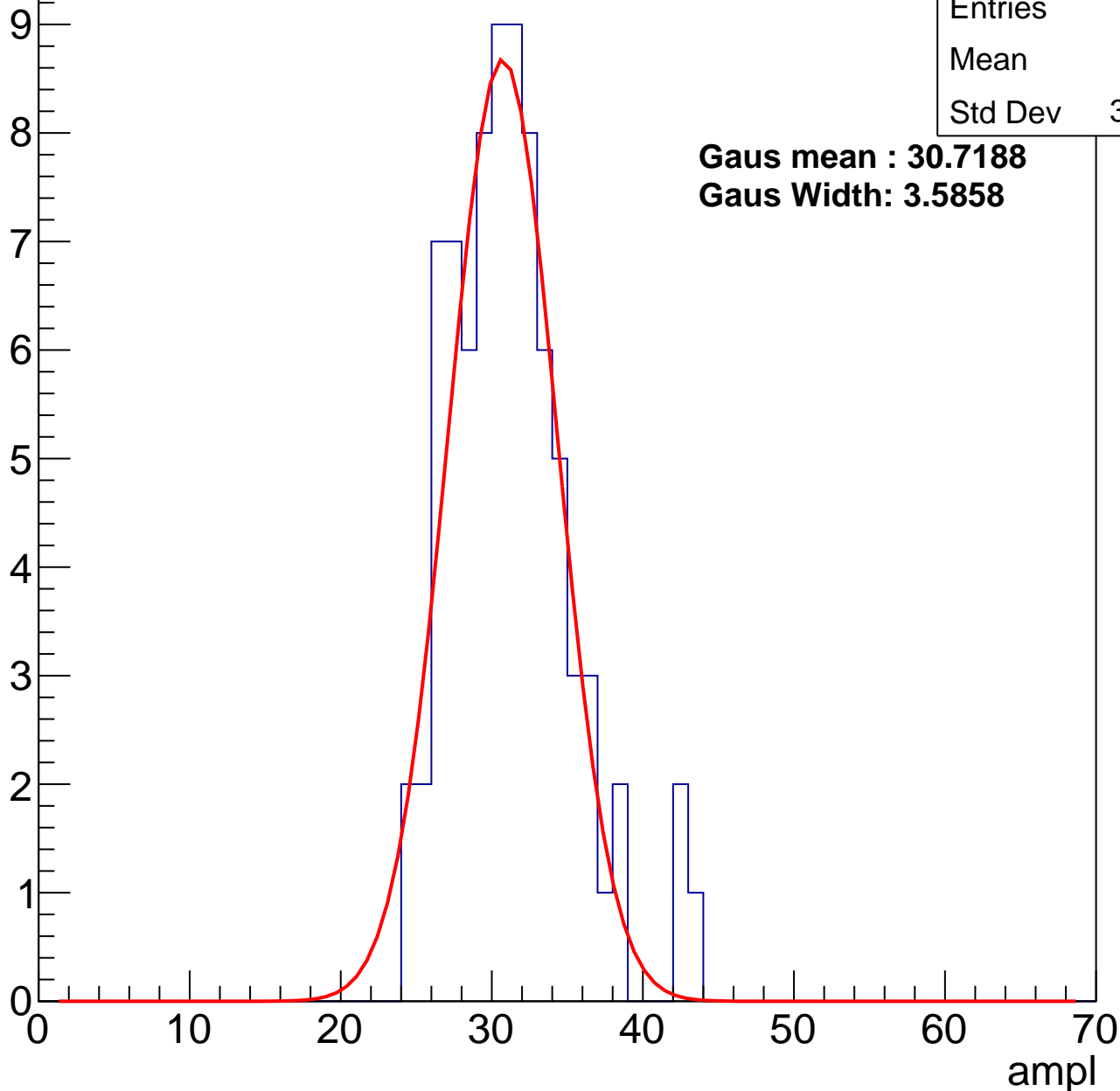
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.8
Std Dev	3.958

Gaus mean : 30.7188

Gaus Width: 3.5858



B0L001S, U25-ch47, adc1

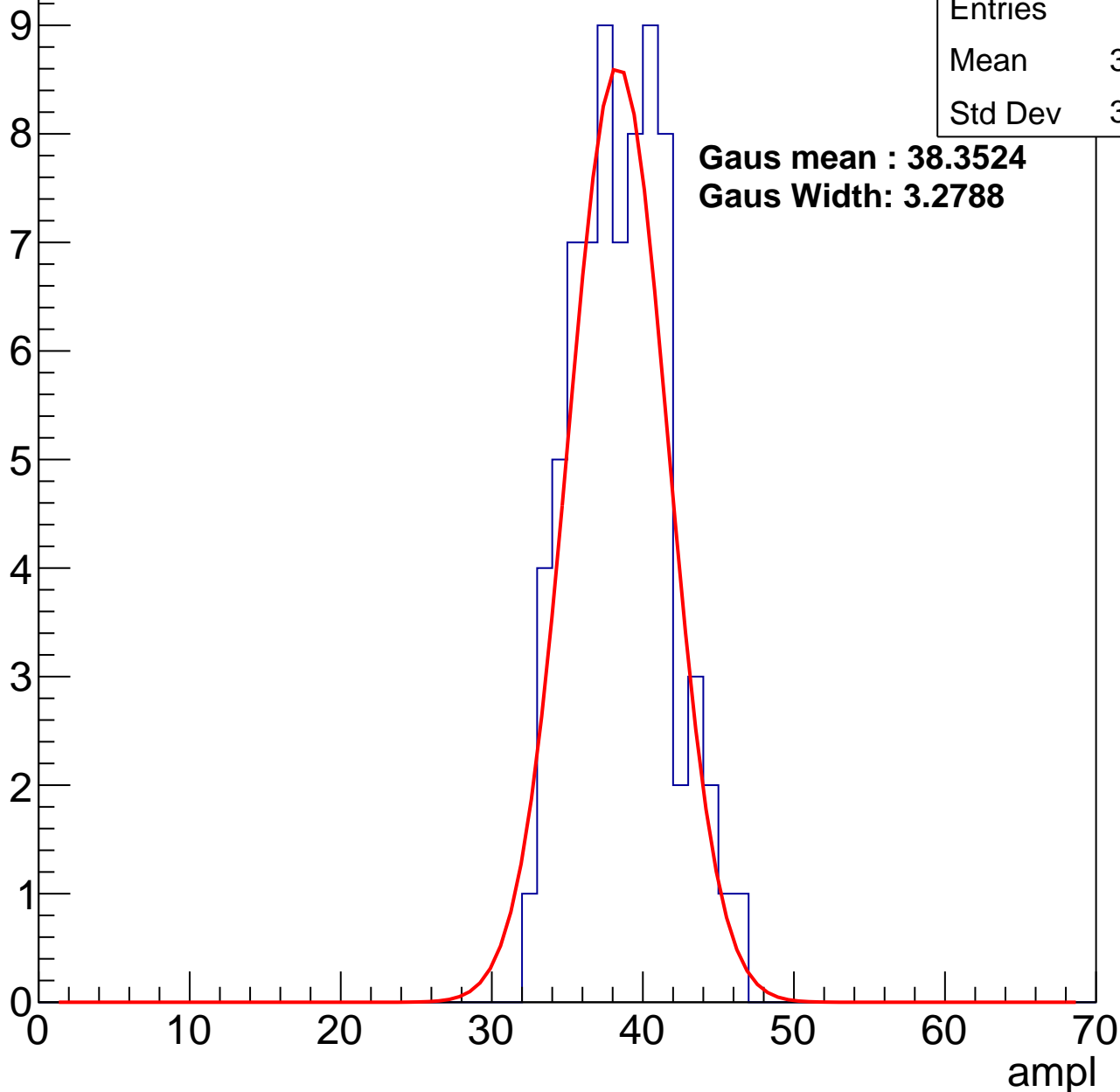
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.14
Std Dev	3.125

Gaus mean : 38.3524

Gaus Width: 3.2788



B0L001S, U25-ch47, adc2

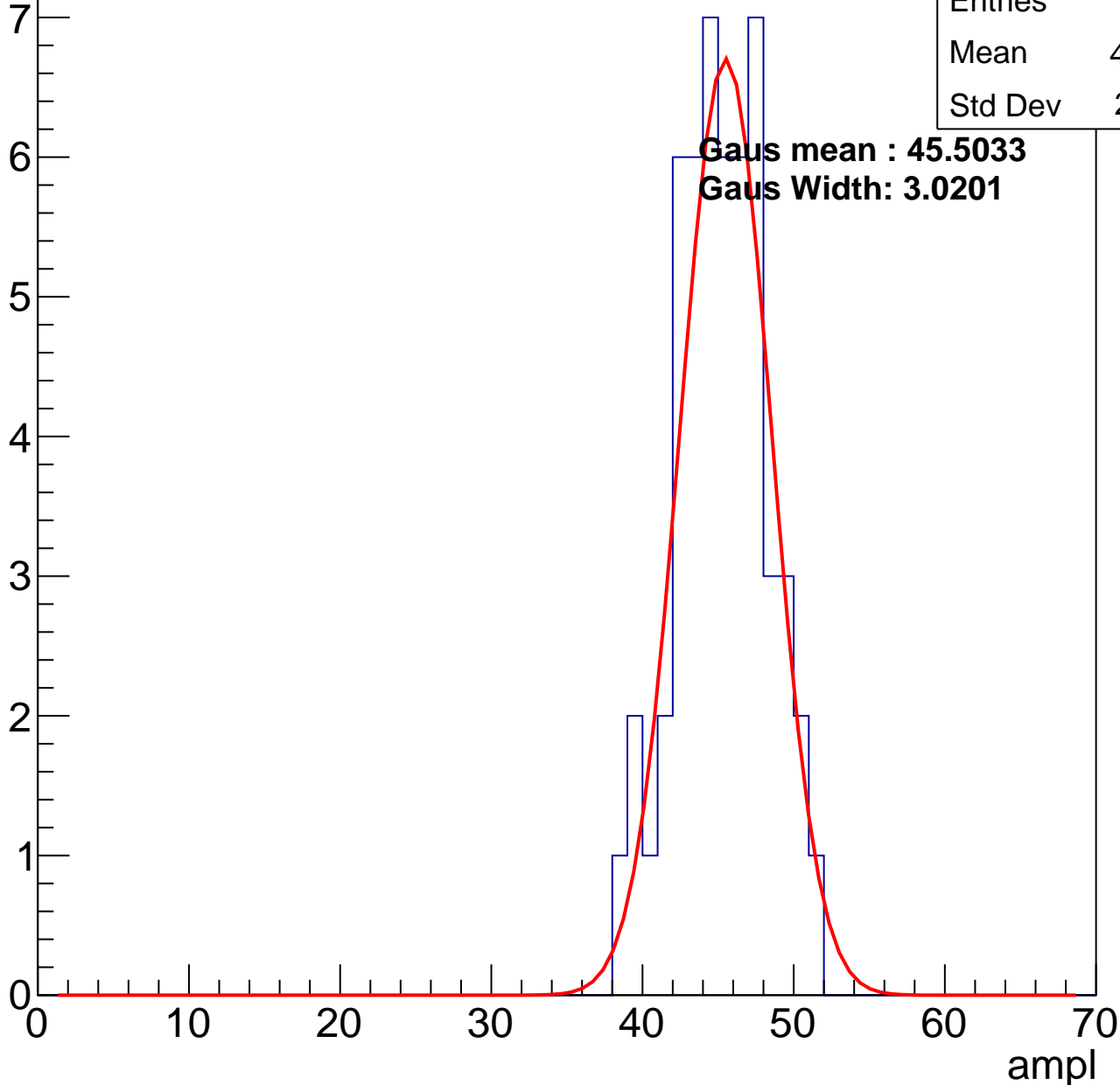
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.77
Std Dev	2.931

Gaus mean : 45.5033

Gaus Width: 3.0201

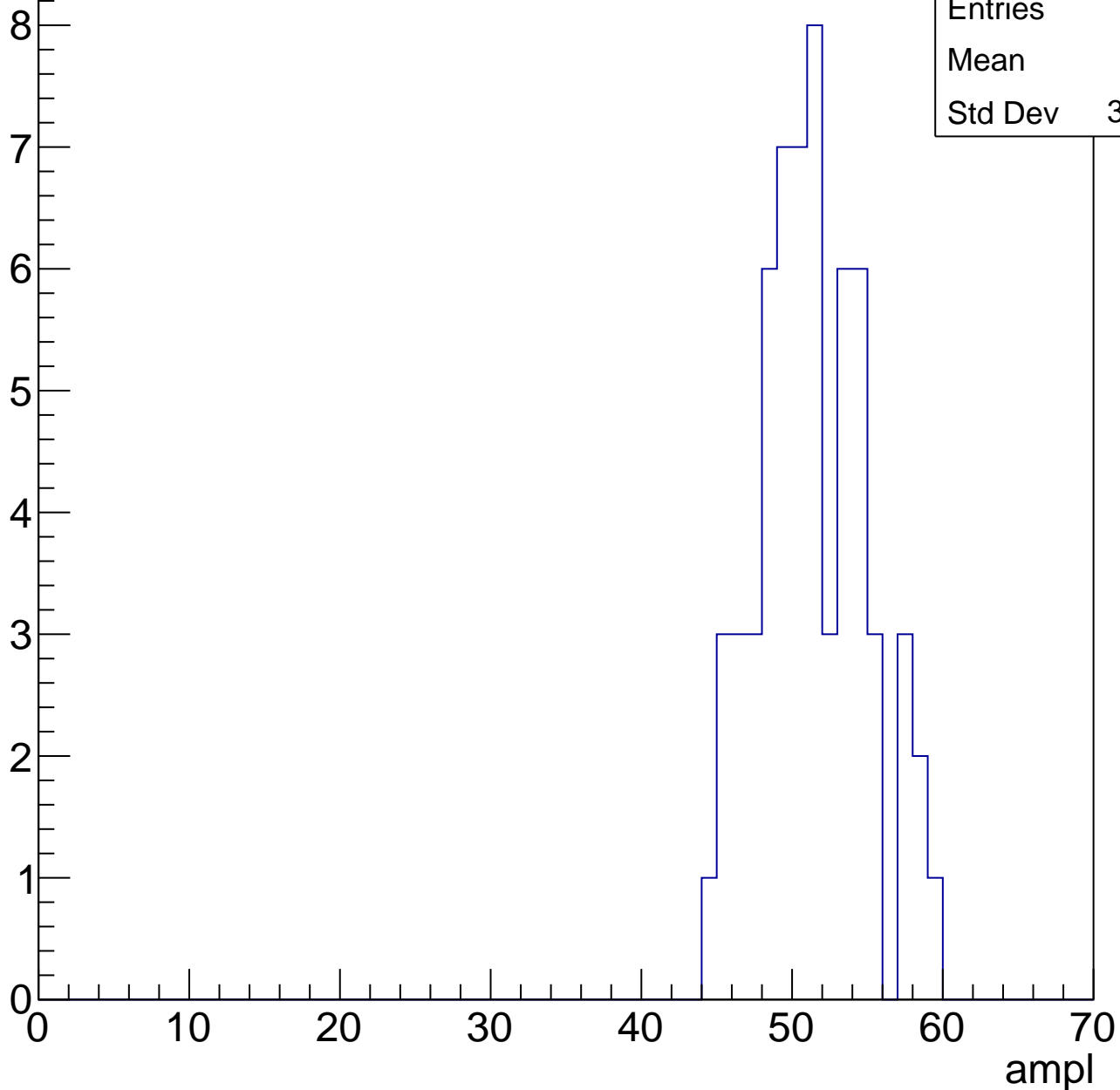


B0L001S, U25-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.9
Std Dev	3.518

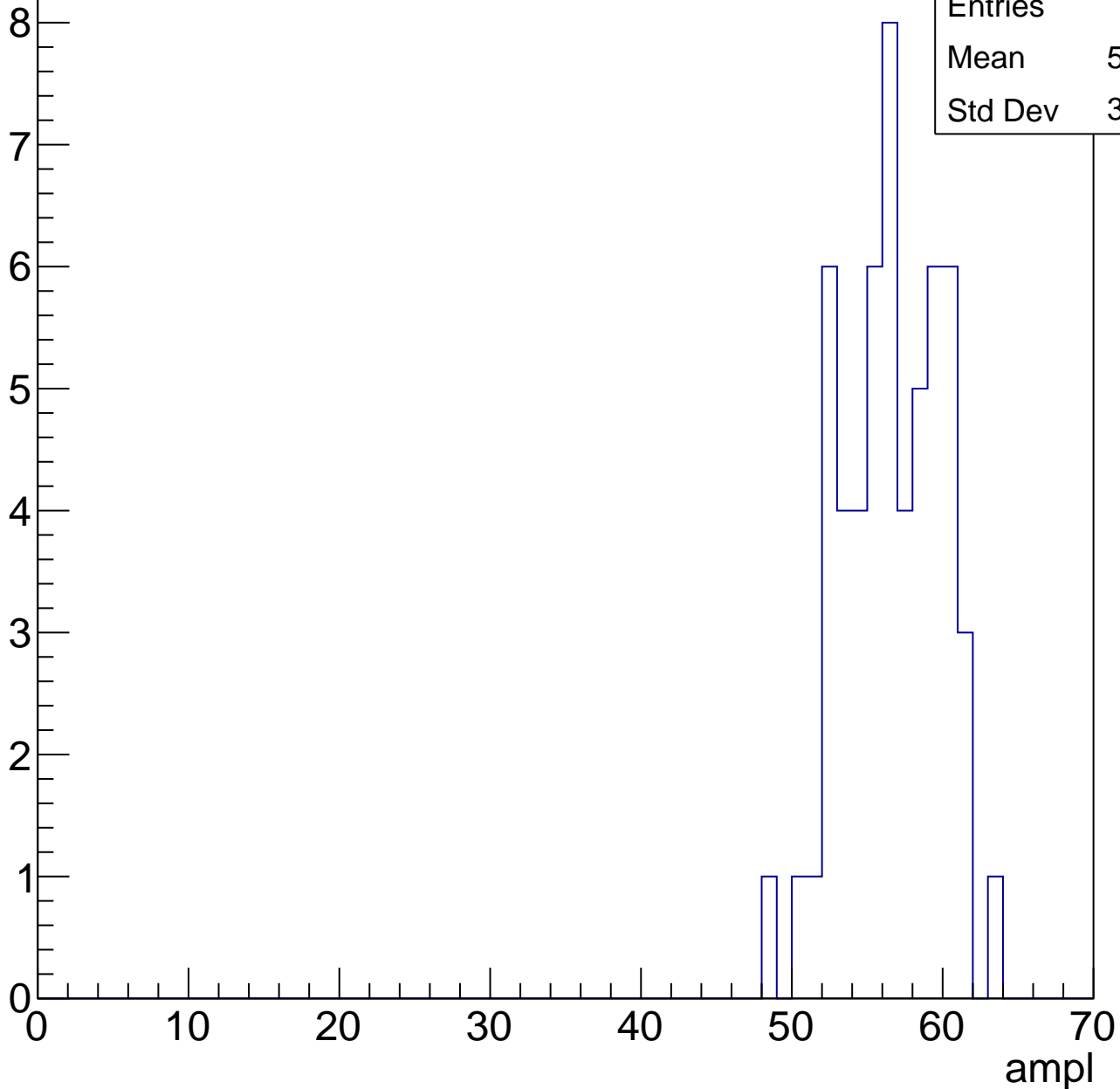


B0L001S, U25-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.16
Std Dev	3.206

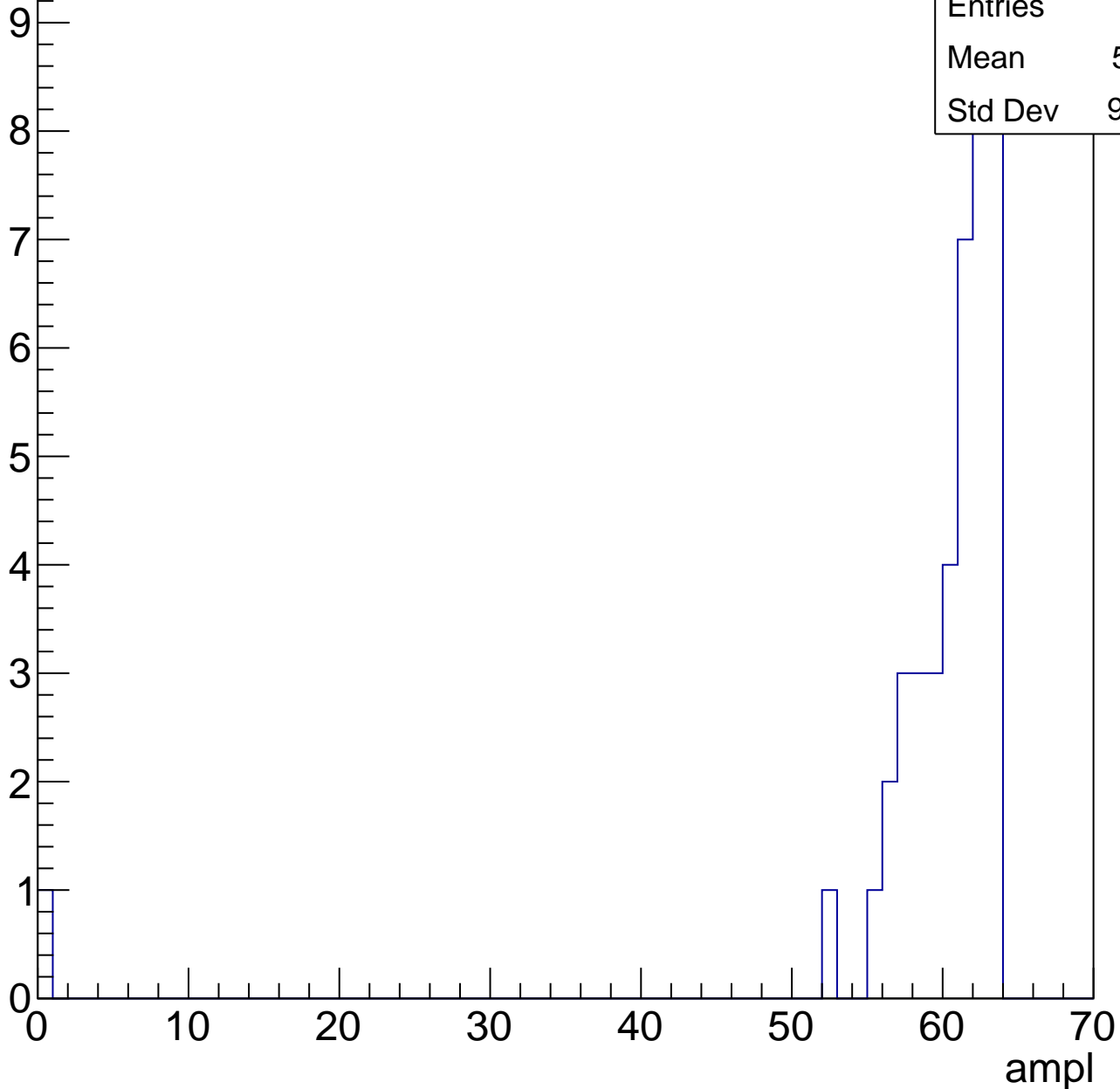


B0L001S, U25-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.91
Std Dev	9.445



B0L001S, U25-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch48, adc0

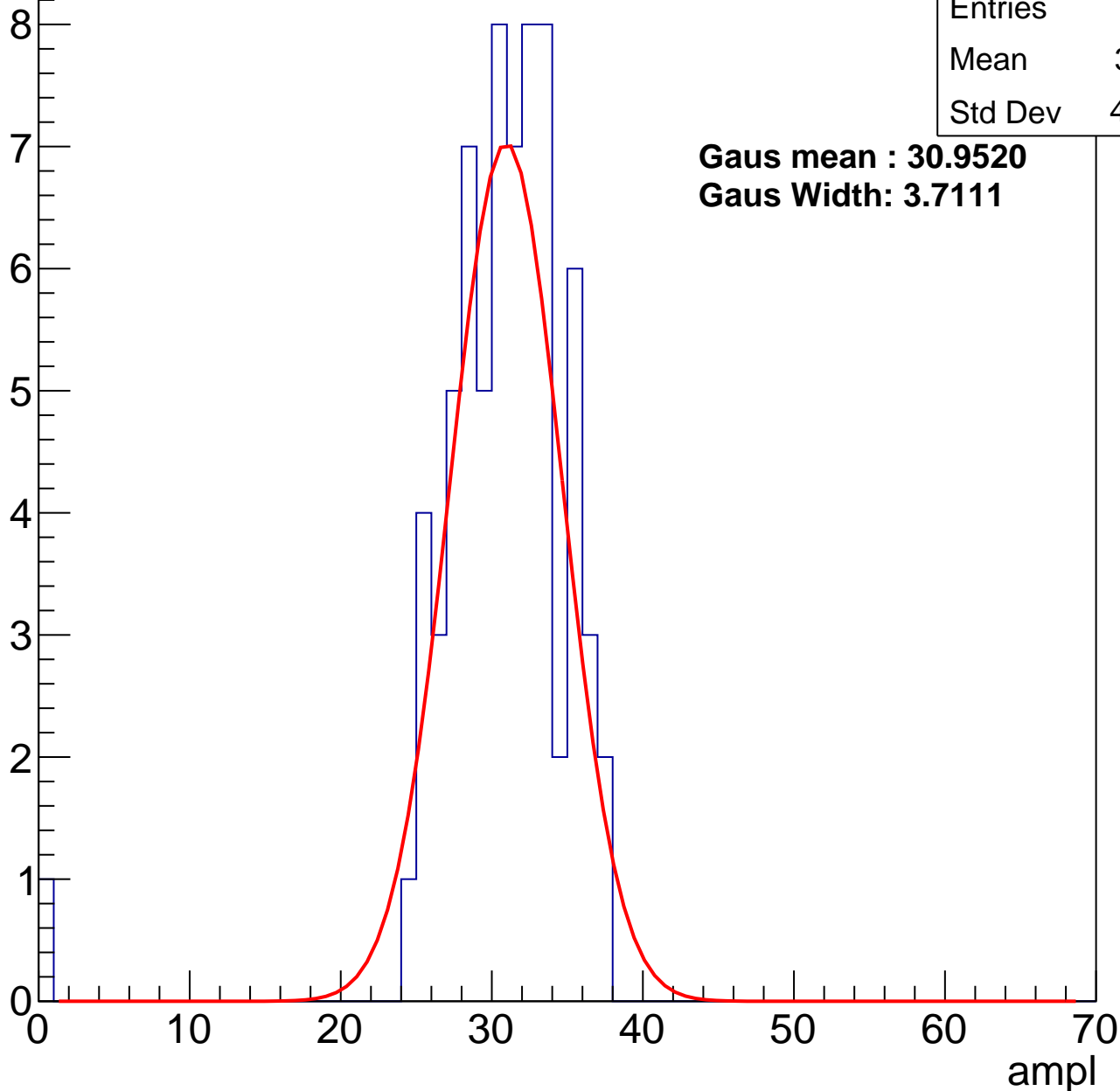
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.21
Std Dev	4.866

Gaus mean : 30.9520

Gaus Width: 3.7111



B0L001S, U25-ch48, adc1

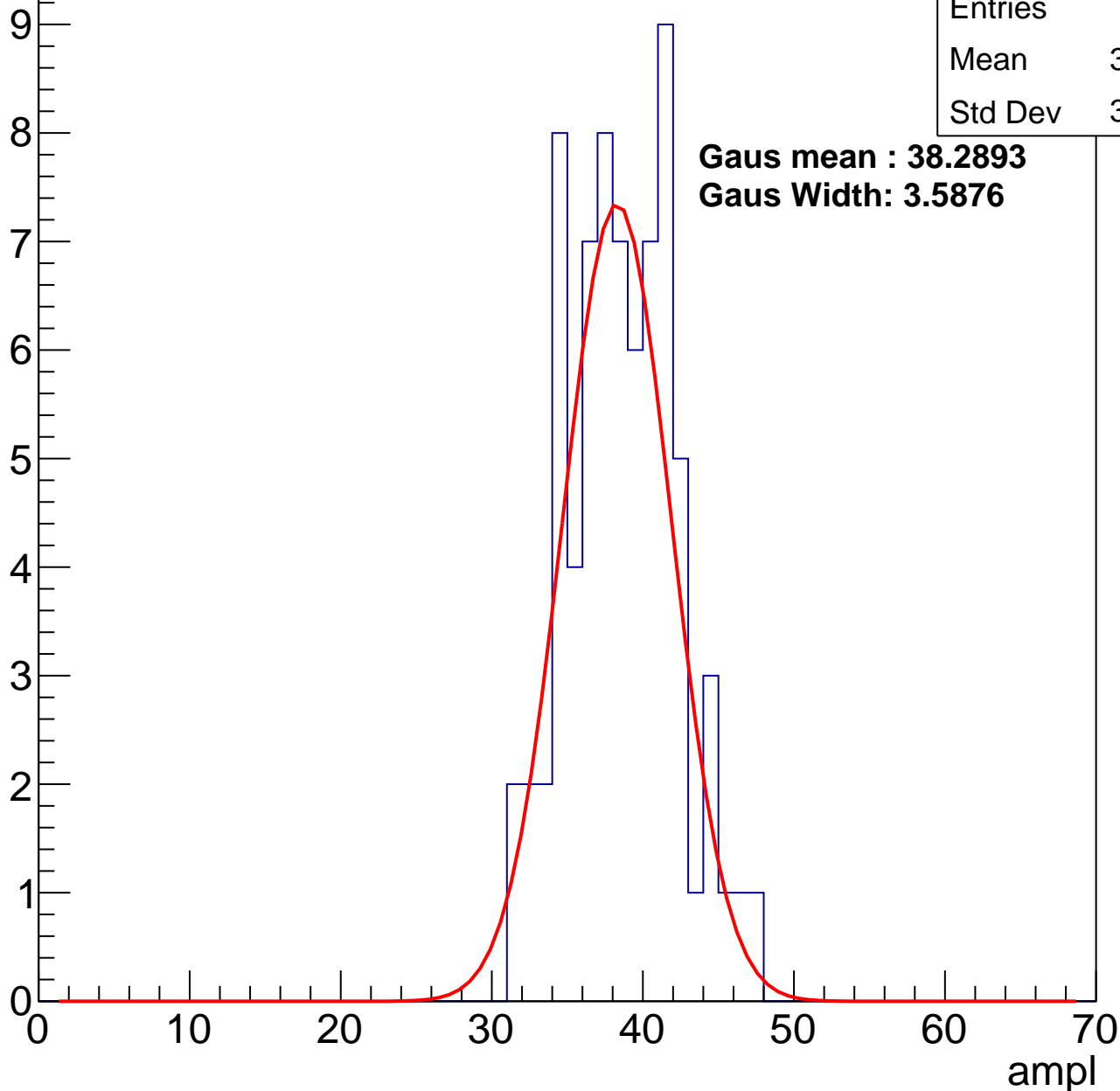
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	38.16
Std Dev	3.564

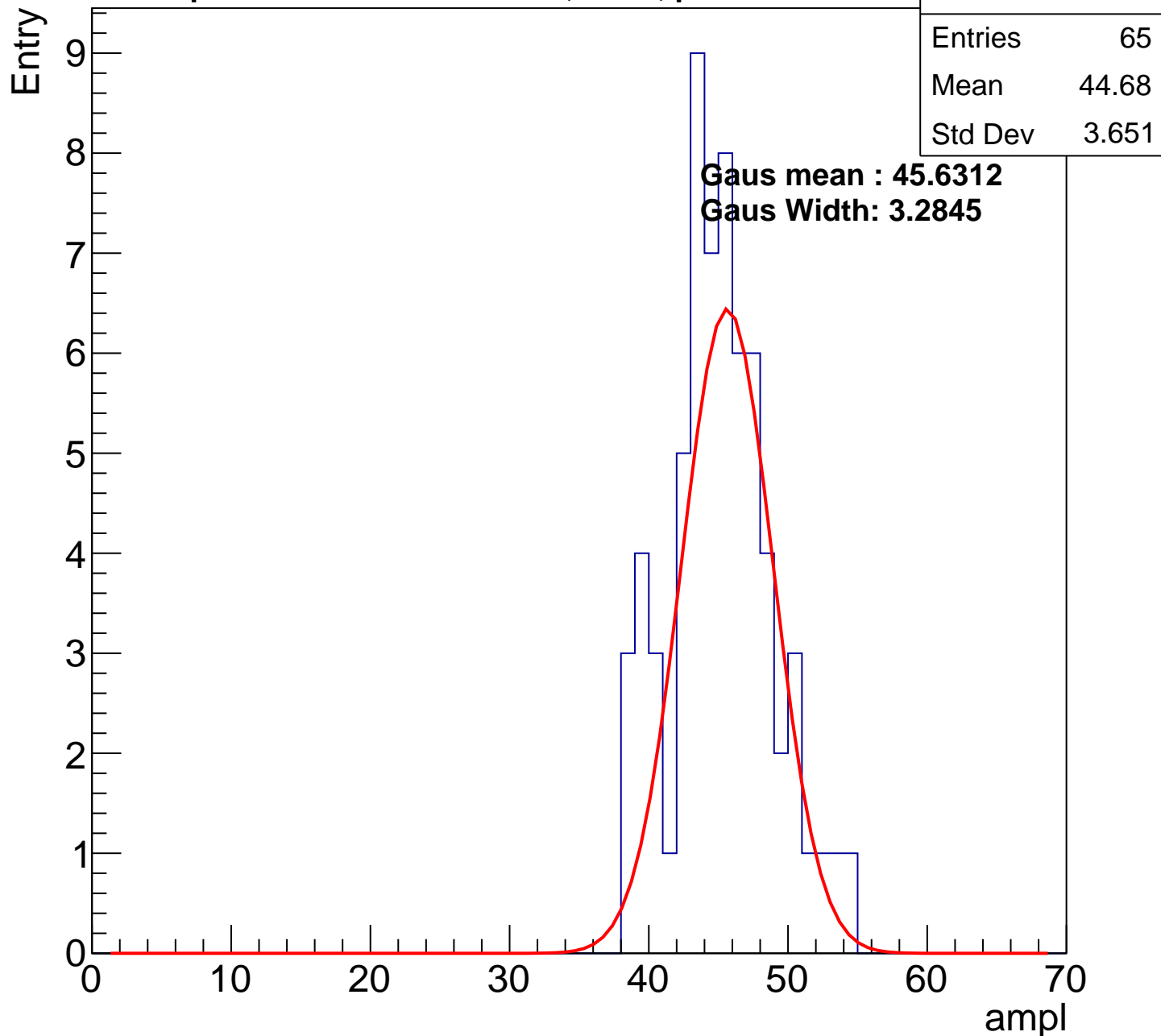
Gaus mean : 38.2893

Gaus Width: 3.5876



B0L001S, U25-ch48, adc2

calib_packv5_042523_0143.root, FC#9, port A1

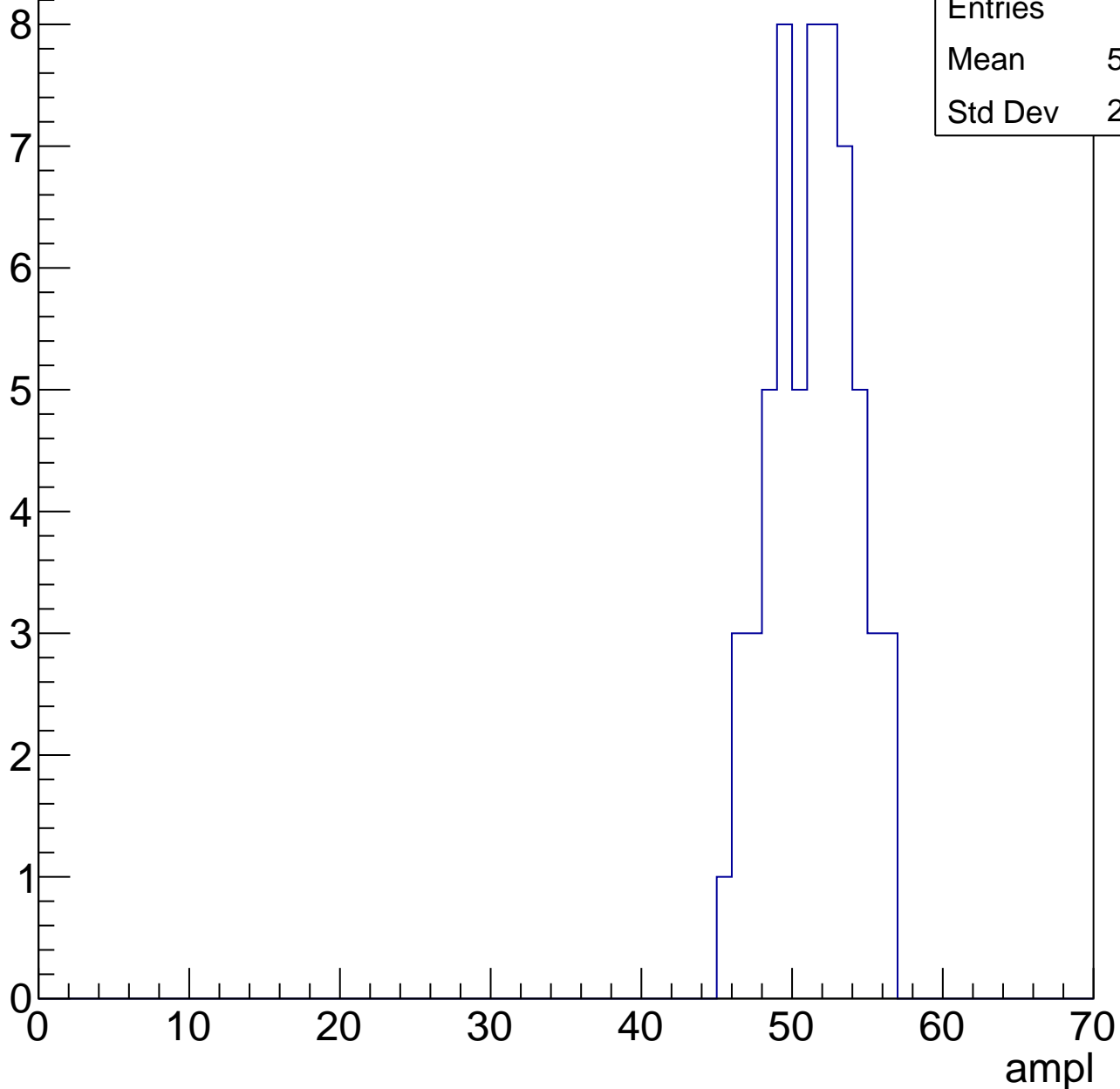


B0L001S, U25-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.92
Std Dev	2.745



B0L001S, U25-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

Entries 45

Mean 56.38

Std Dev 3.199

0

0

10

20

30

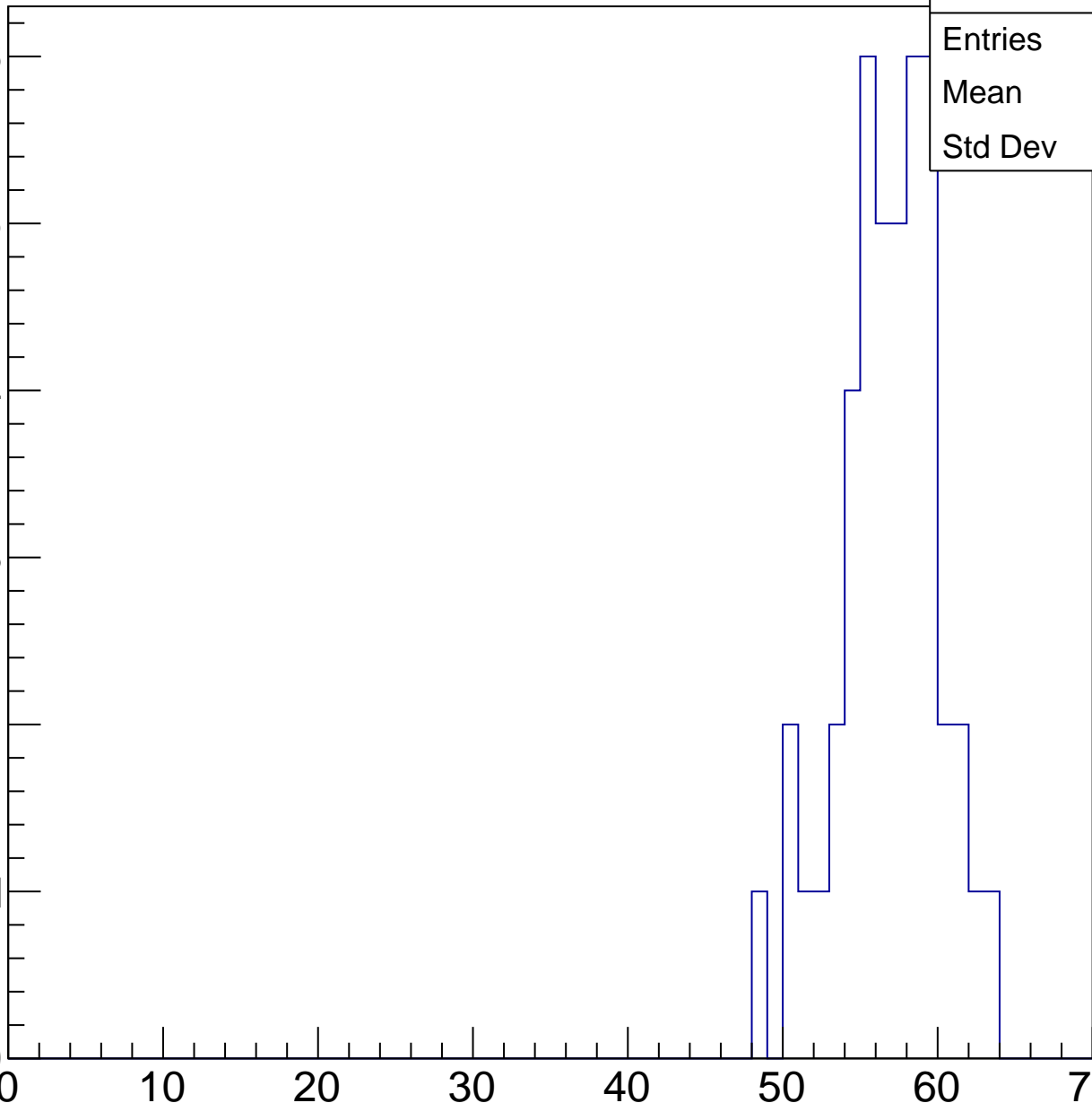
40

50

60

70

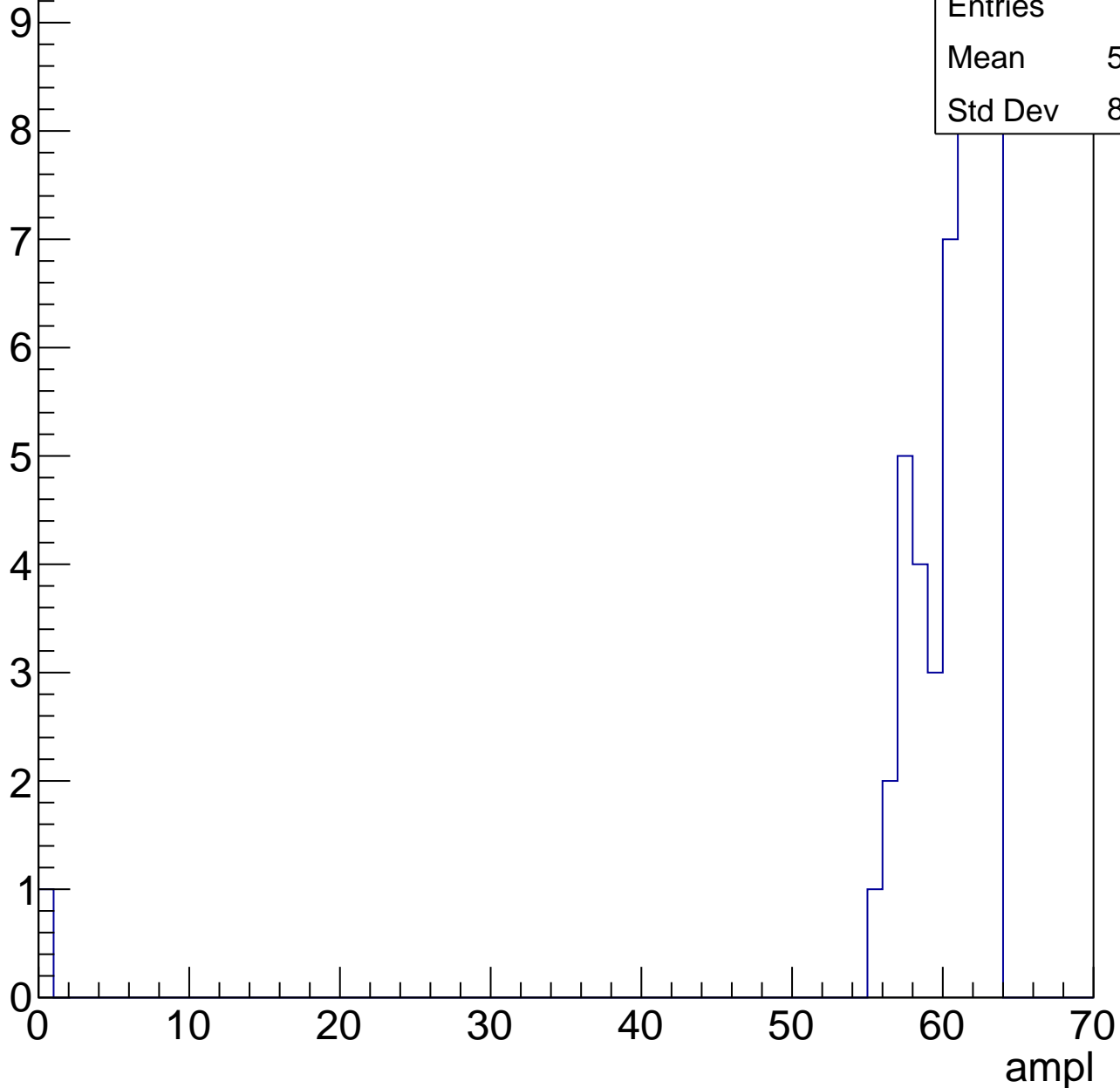
ampl



B0L001S, U25-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

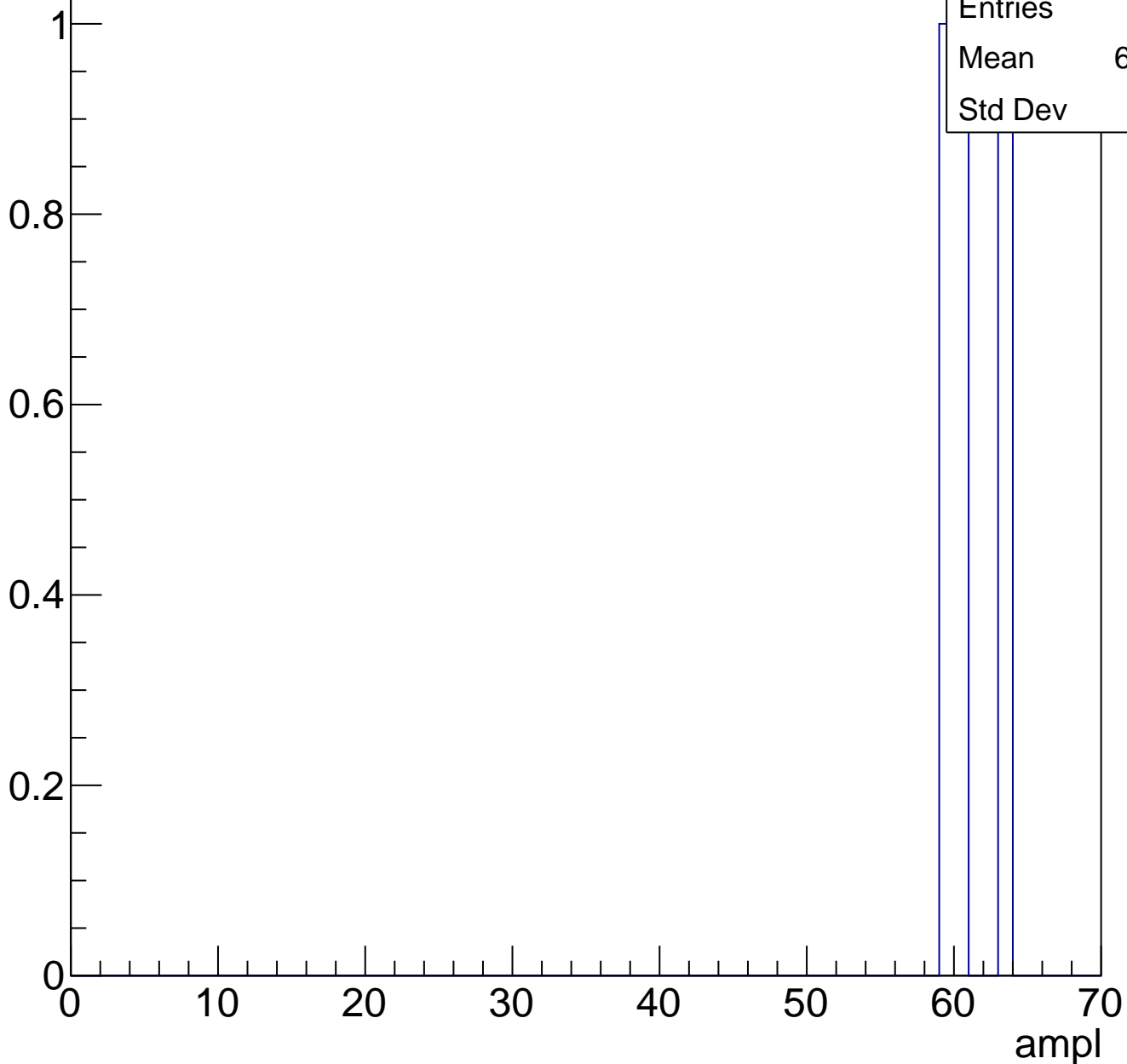
Entry



B0L001S, U25-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch49, adc0

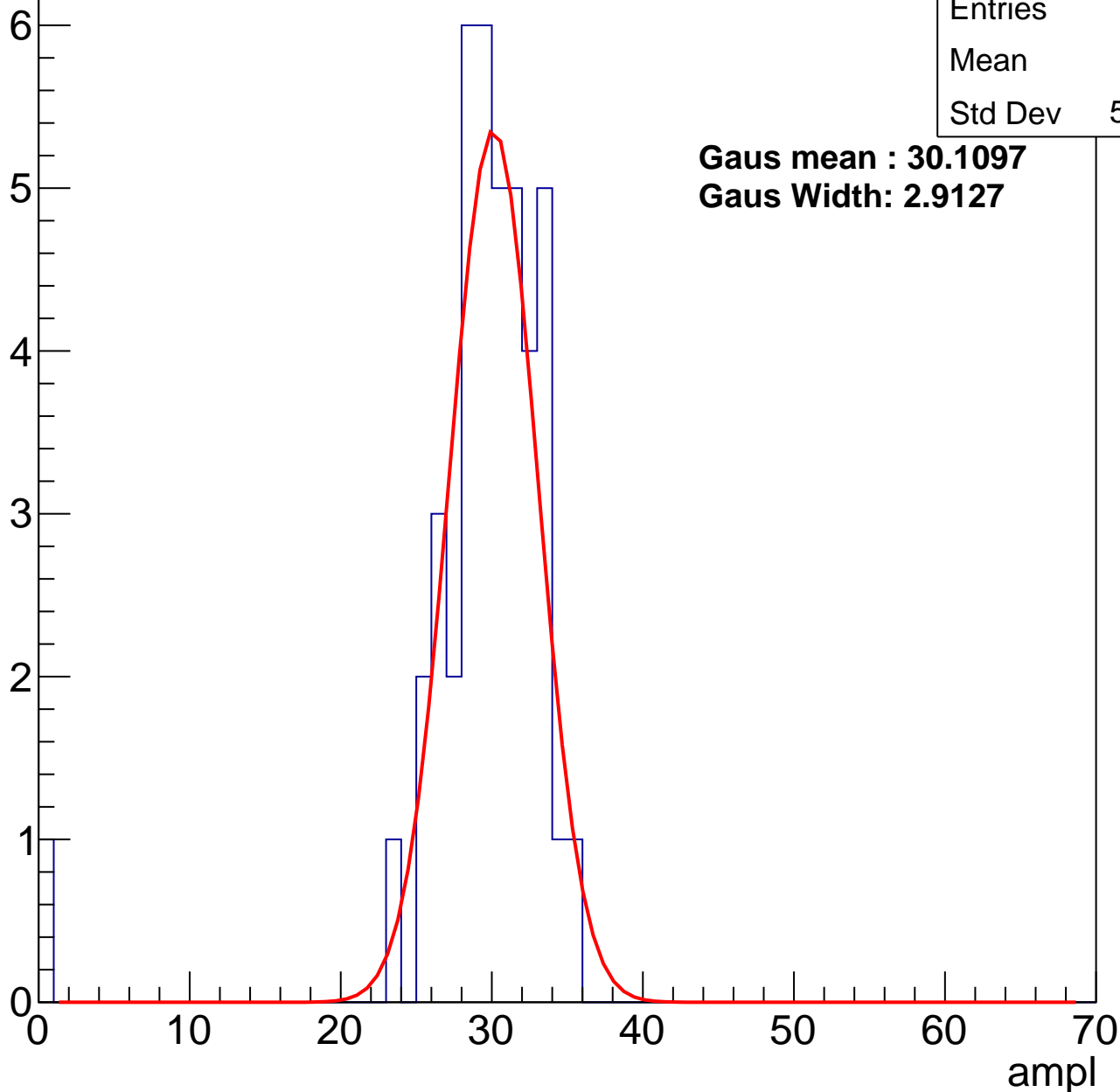
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	28.9
Std Dev	5.236

Gaus mean : 30.1097

Gaus Width: 2.9127



B0L001S, U25-ch49, adc1

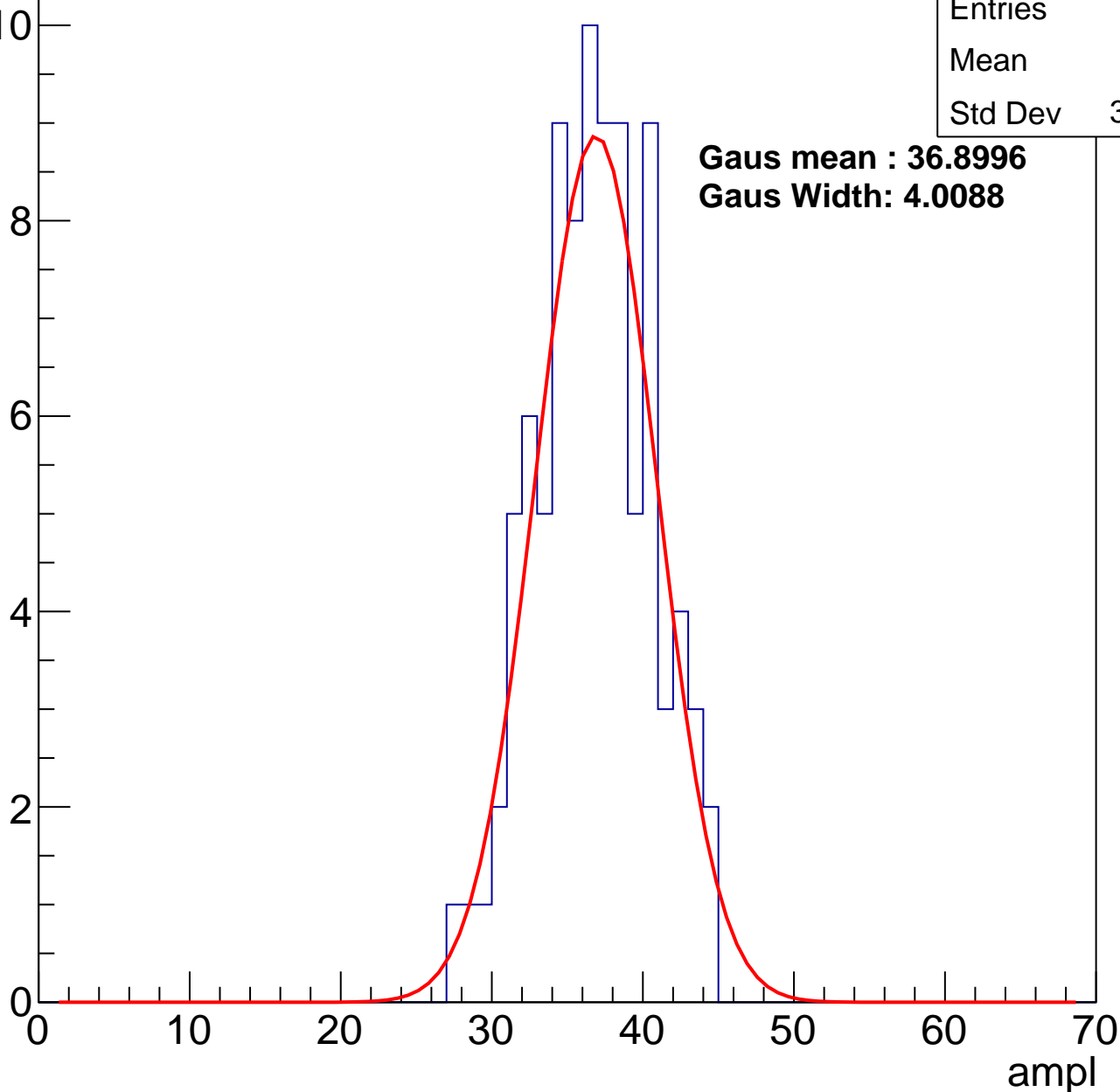
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	92
Mean	36.3
Std Dev	3.753

Gaus mean : 36.8996

Gaus Width: 4.0088

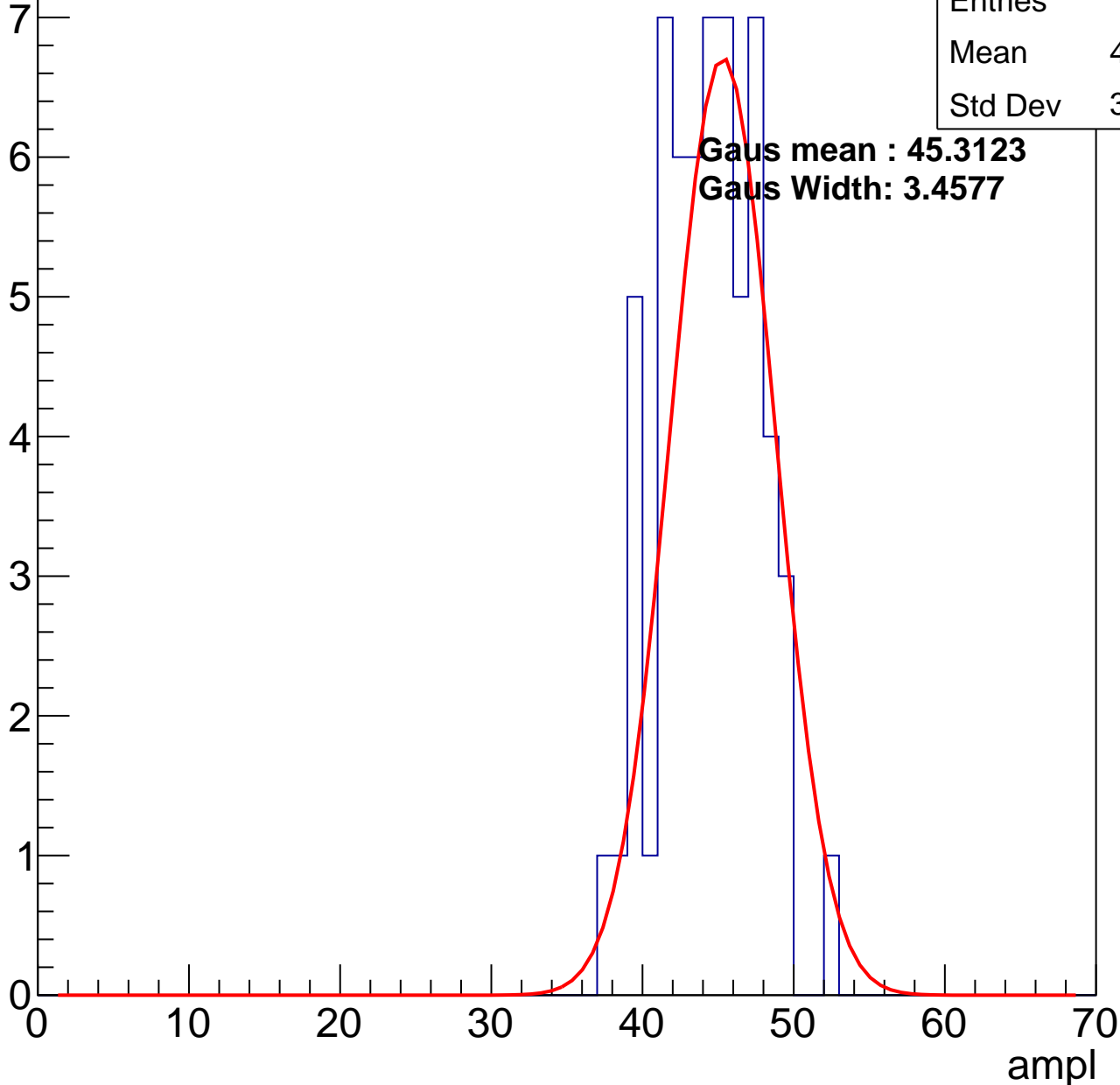


B0L001S, U25-ch49, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.93
Std Dev	3.167

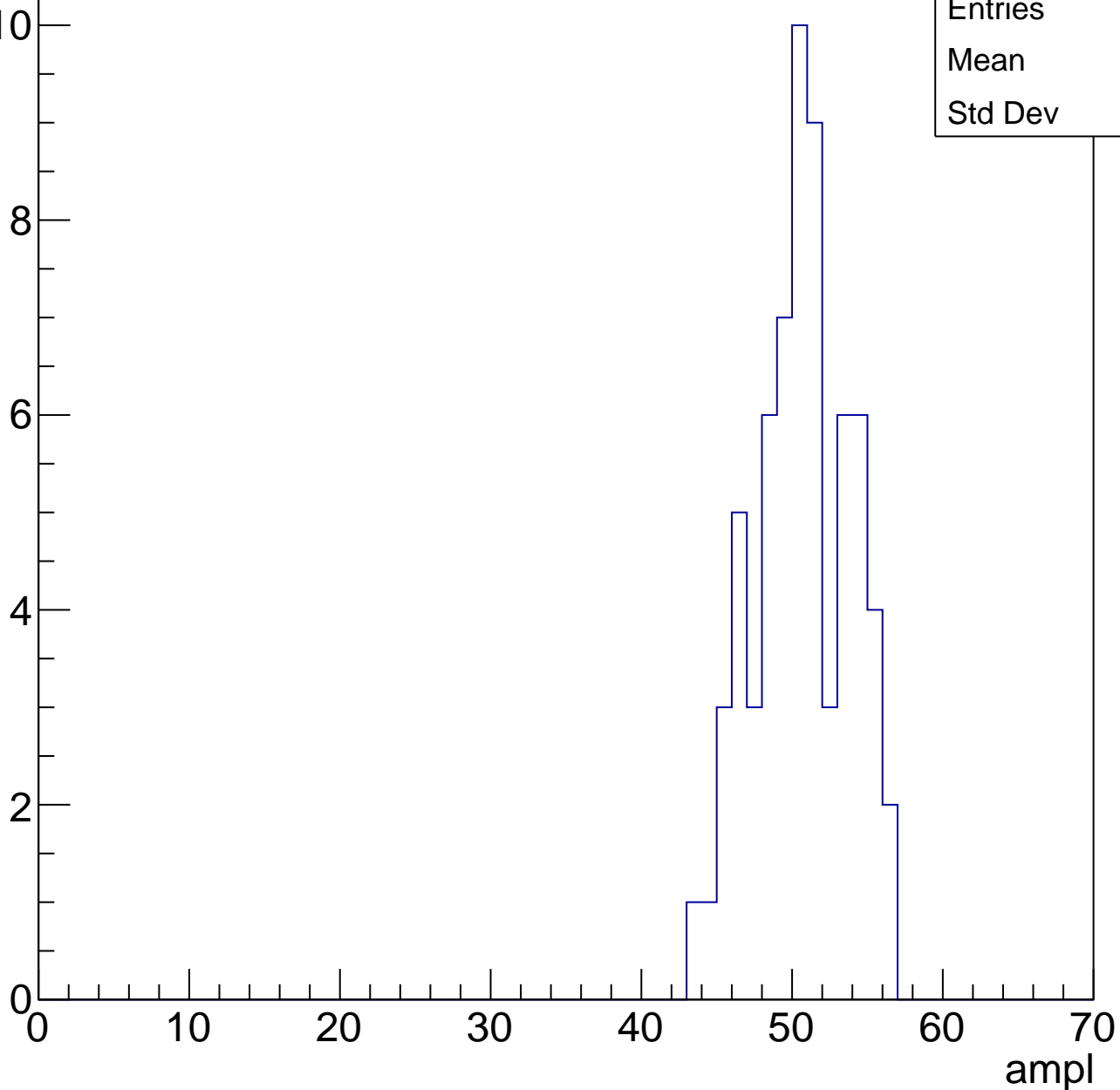


B0L001S, U25-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.2
Std Dev	3.11

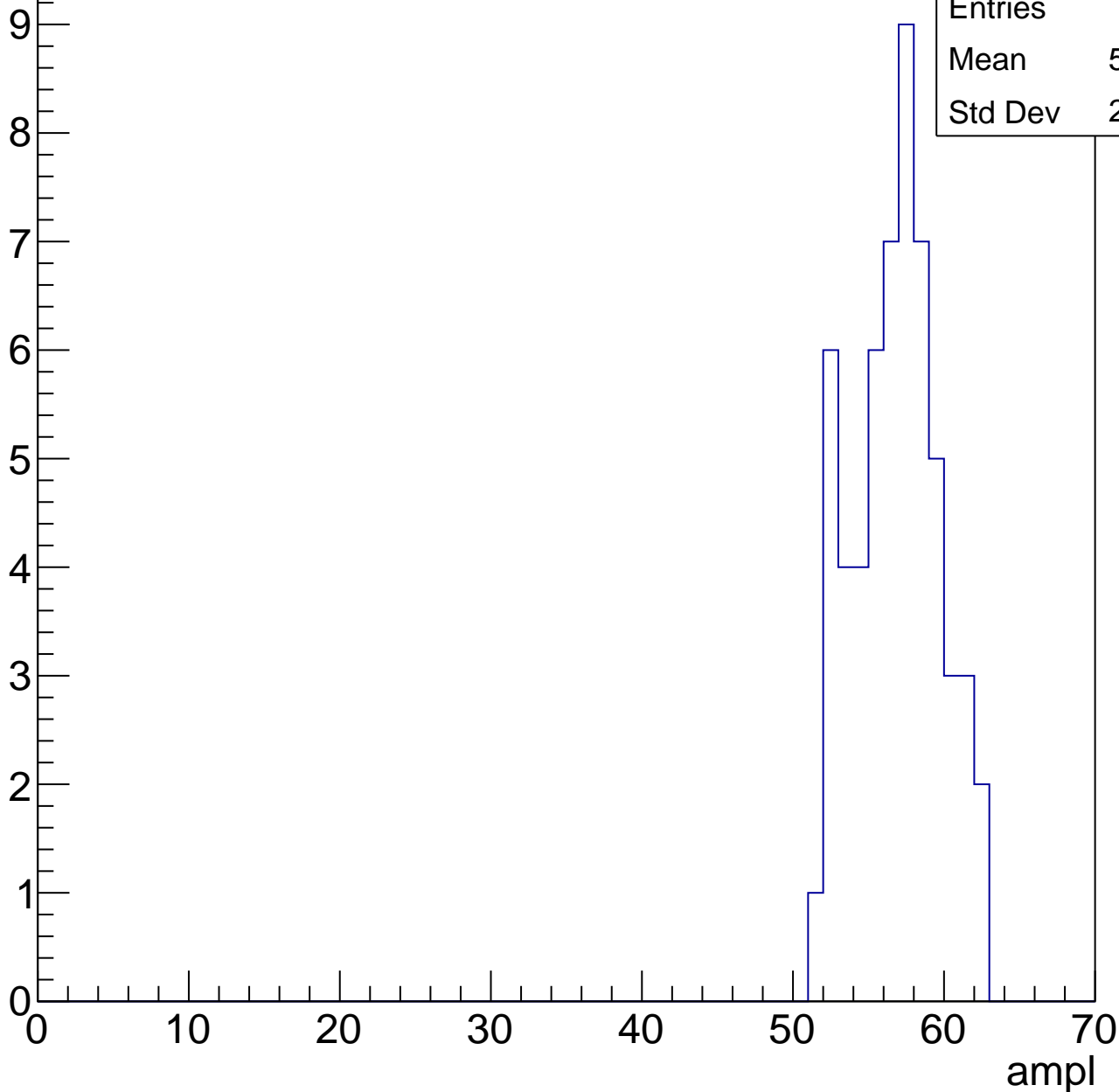


B0L001S, U25-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.39
Std Dev	2.802

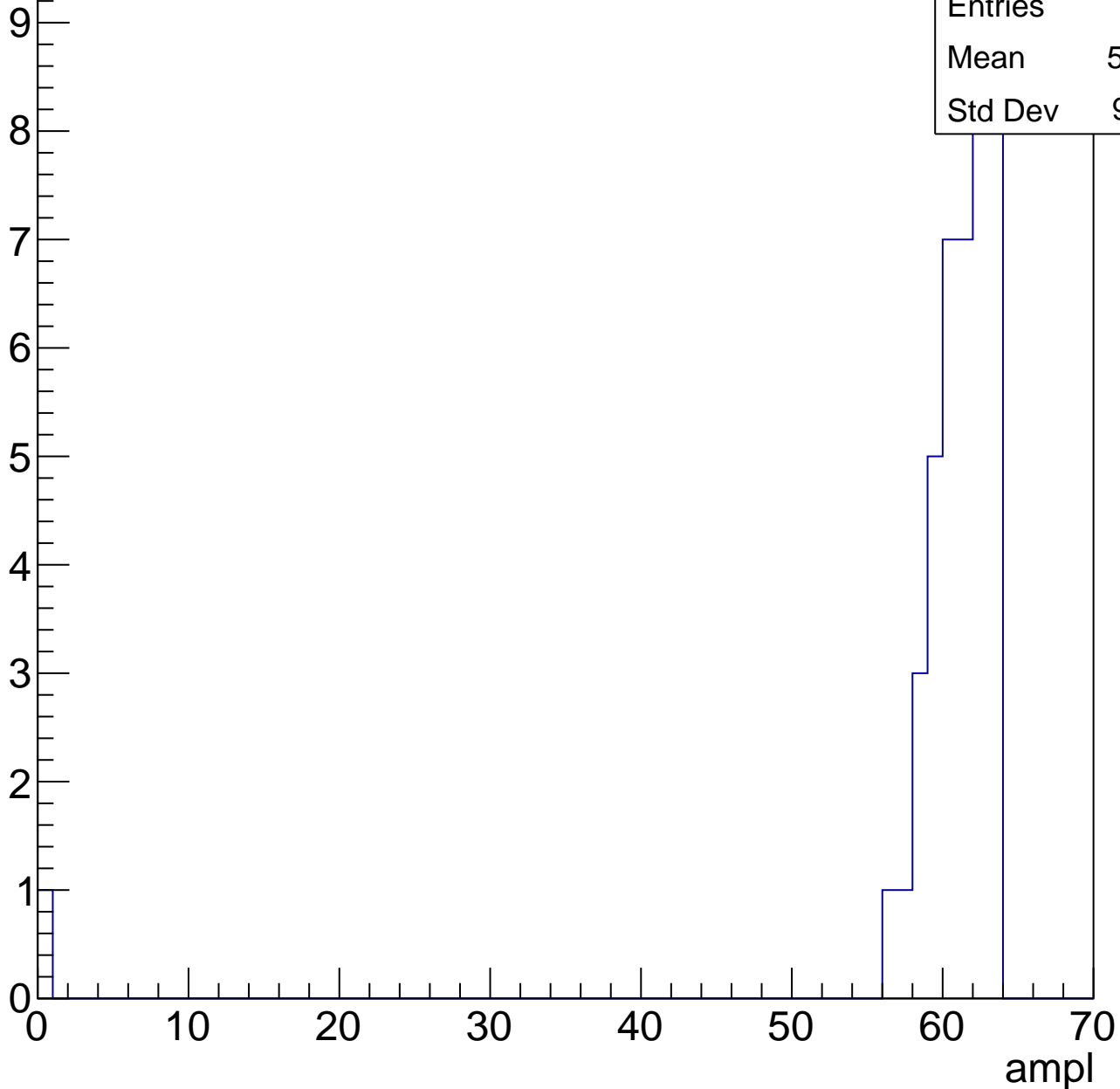


B0L001S, U25-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.33
Std Dev	9.441



B0L001S, U25-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

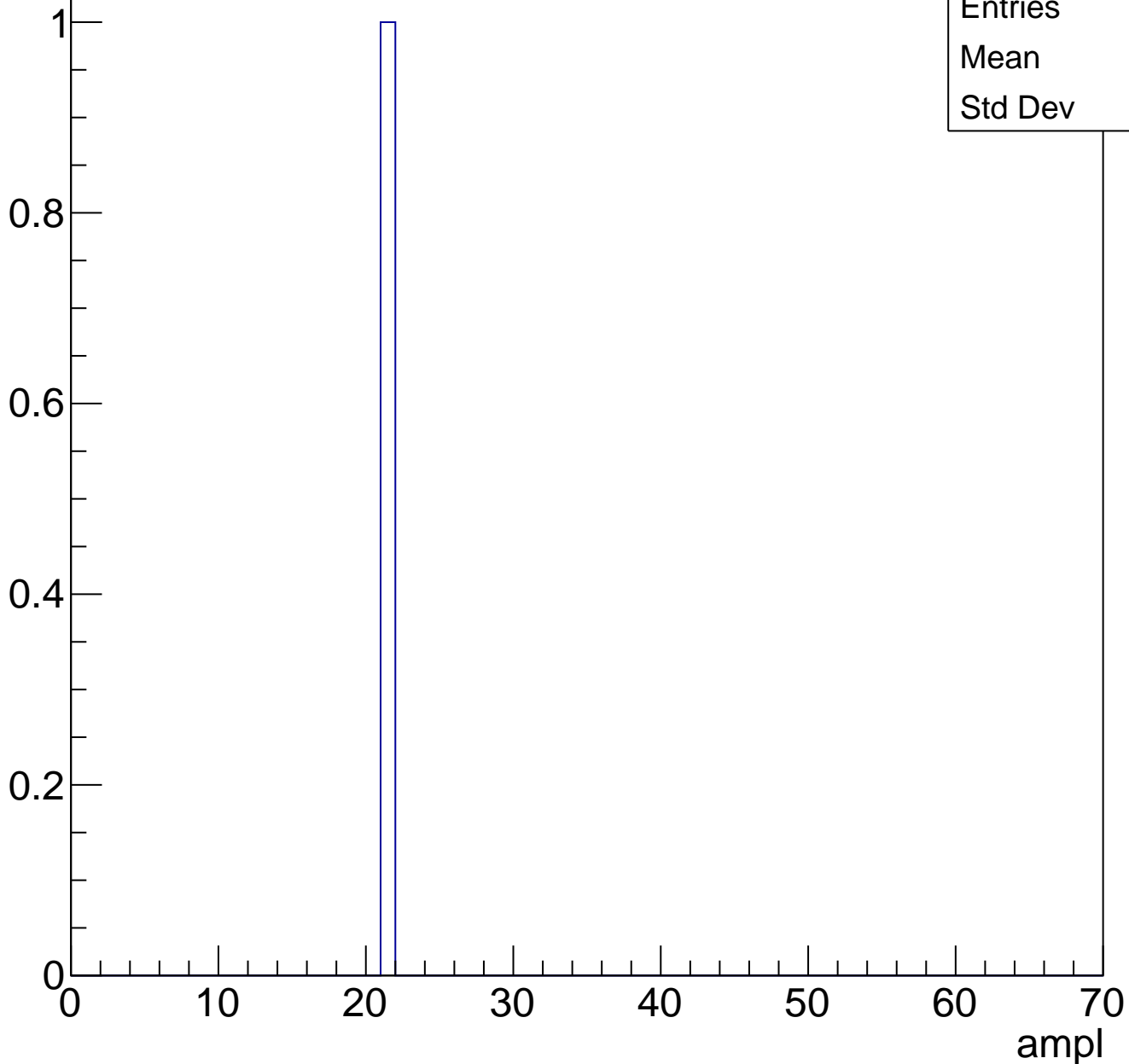
Entry



B0L001S, U25-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch50, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	28.46
Std Dev	3.39

Gaus mean : 29.1822

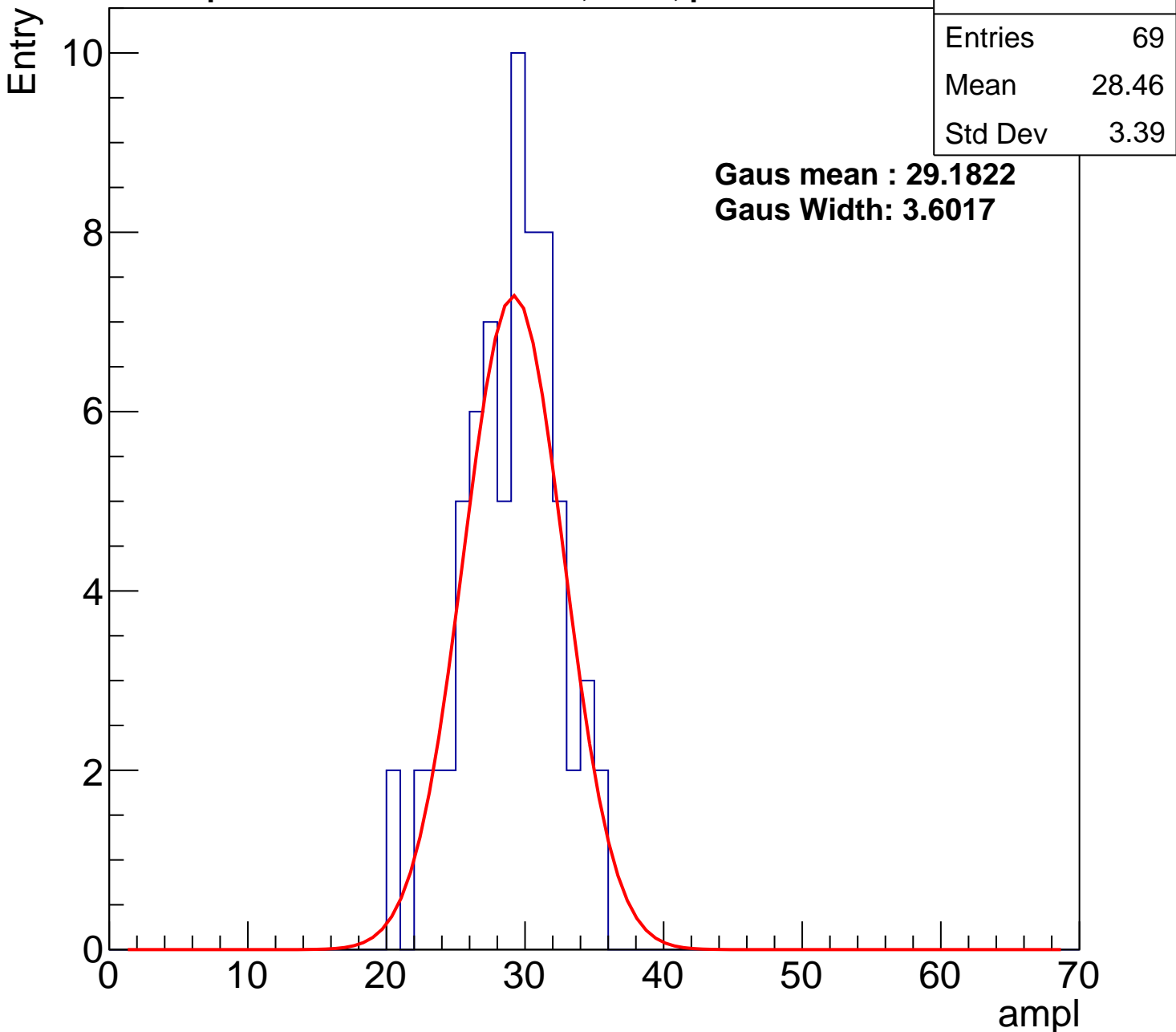
Gaus Width: 3.6017

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



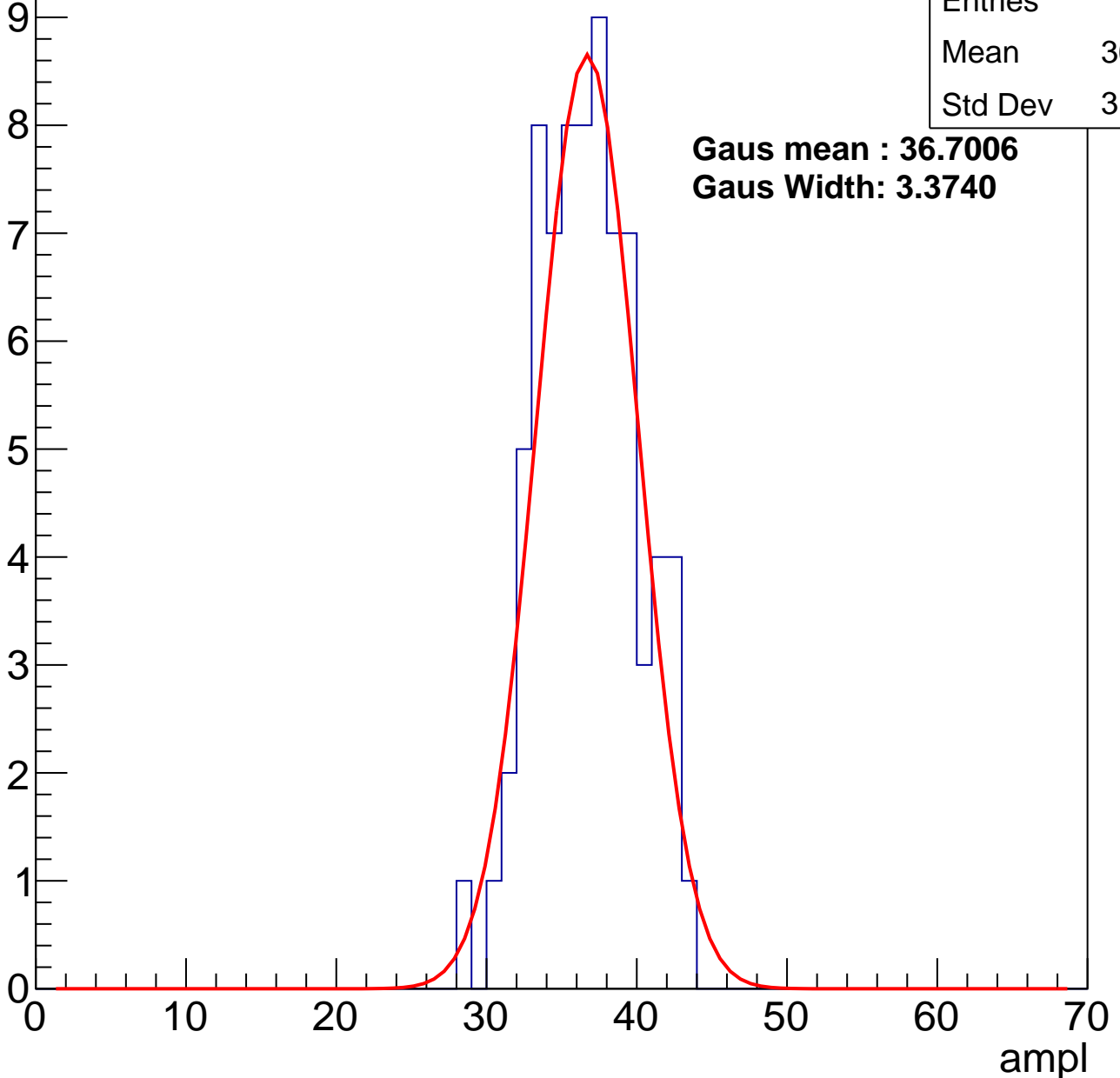
B0L001S, U25-ch50, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.23
Std Dev	3.215

Gaus mean : 36.7006
Gaus Width: 3.3740

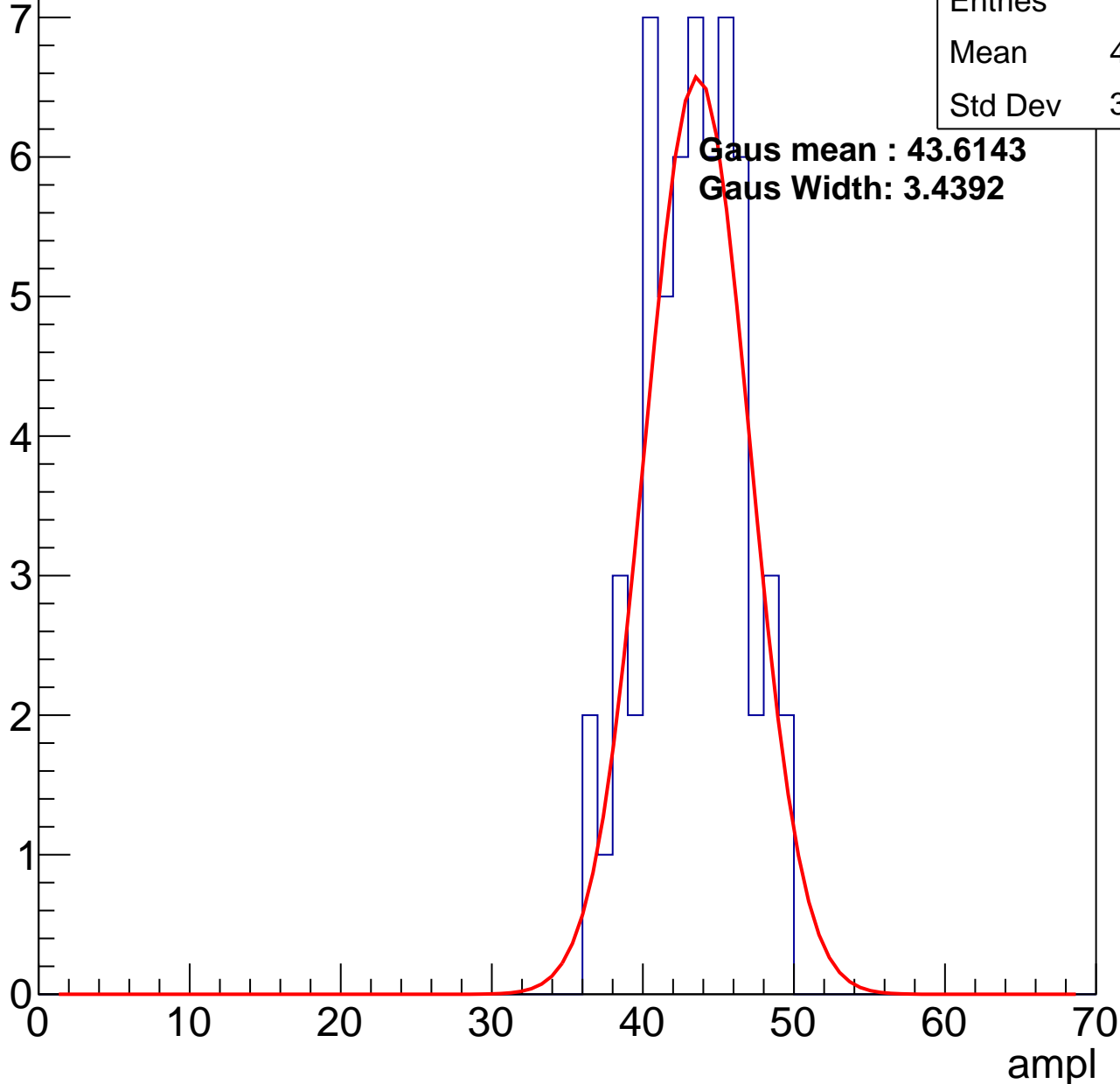


B0L001S, U25-ch50, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	42.88
Std Dev	3.179



B0L001S, U25-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

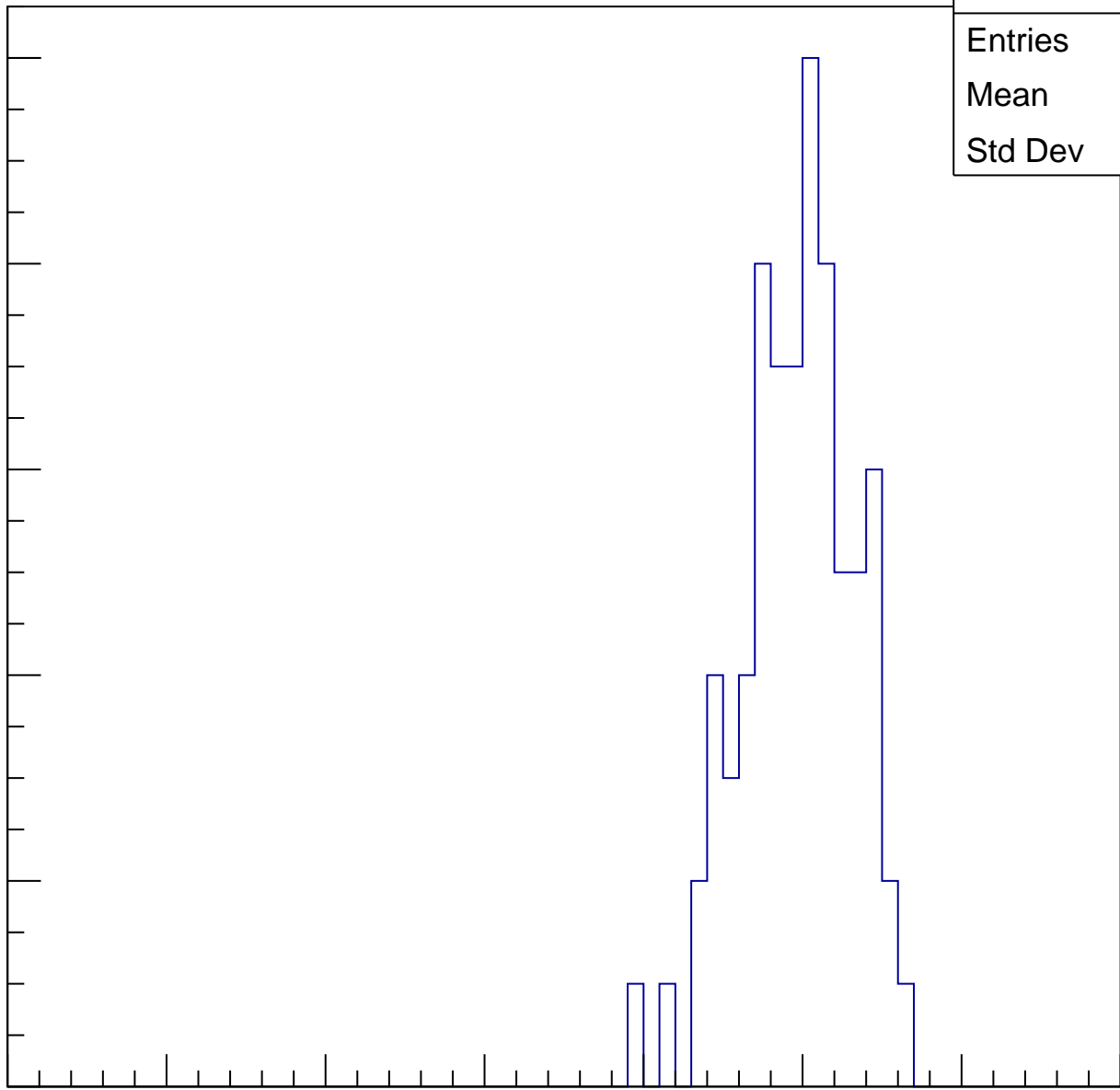
Entries	74
Mean	49.18
Std Dev	3.477

Entry

10
8
6
4
2
0

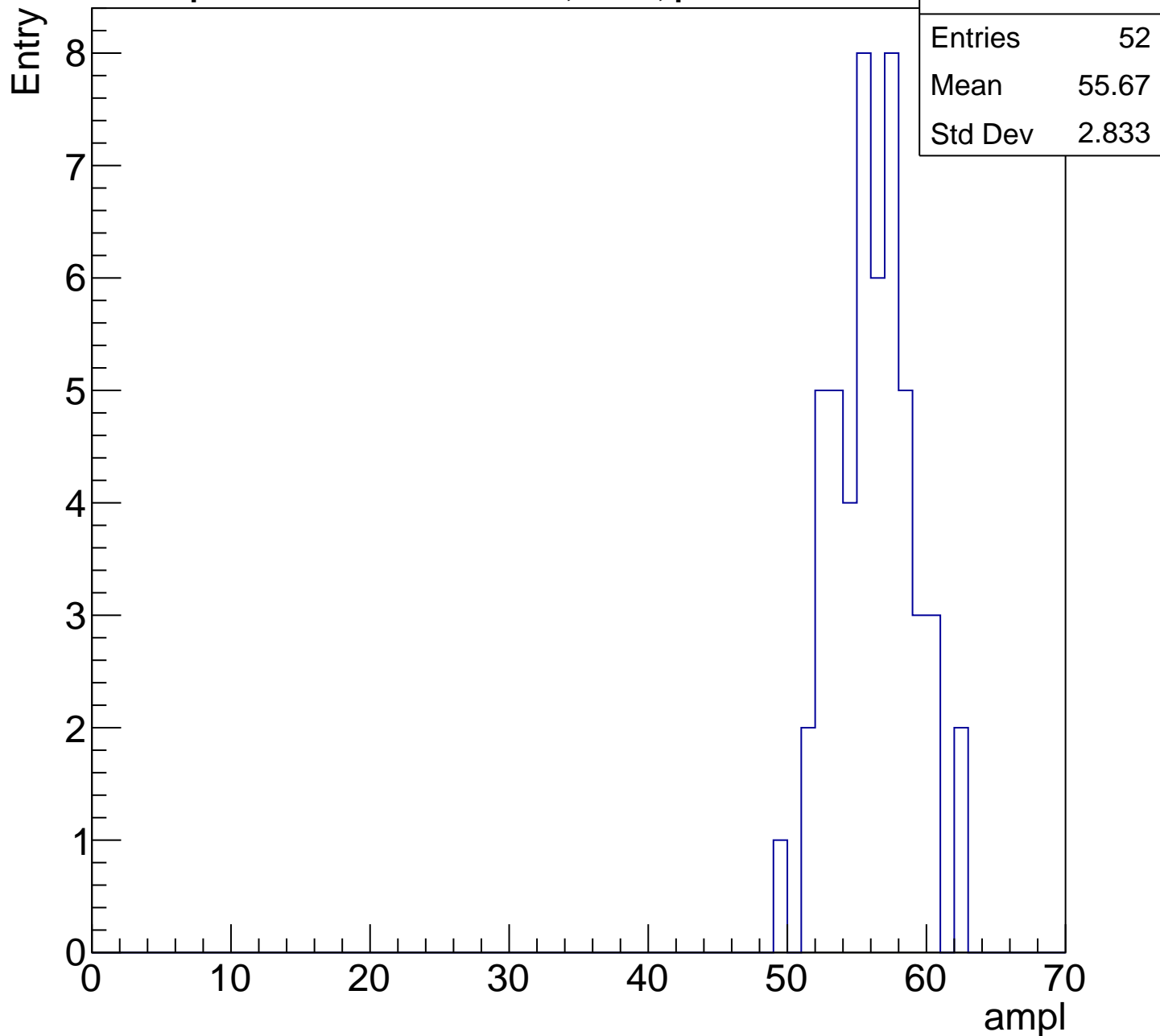
ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

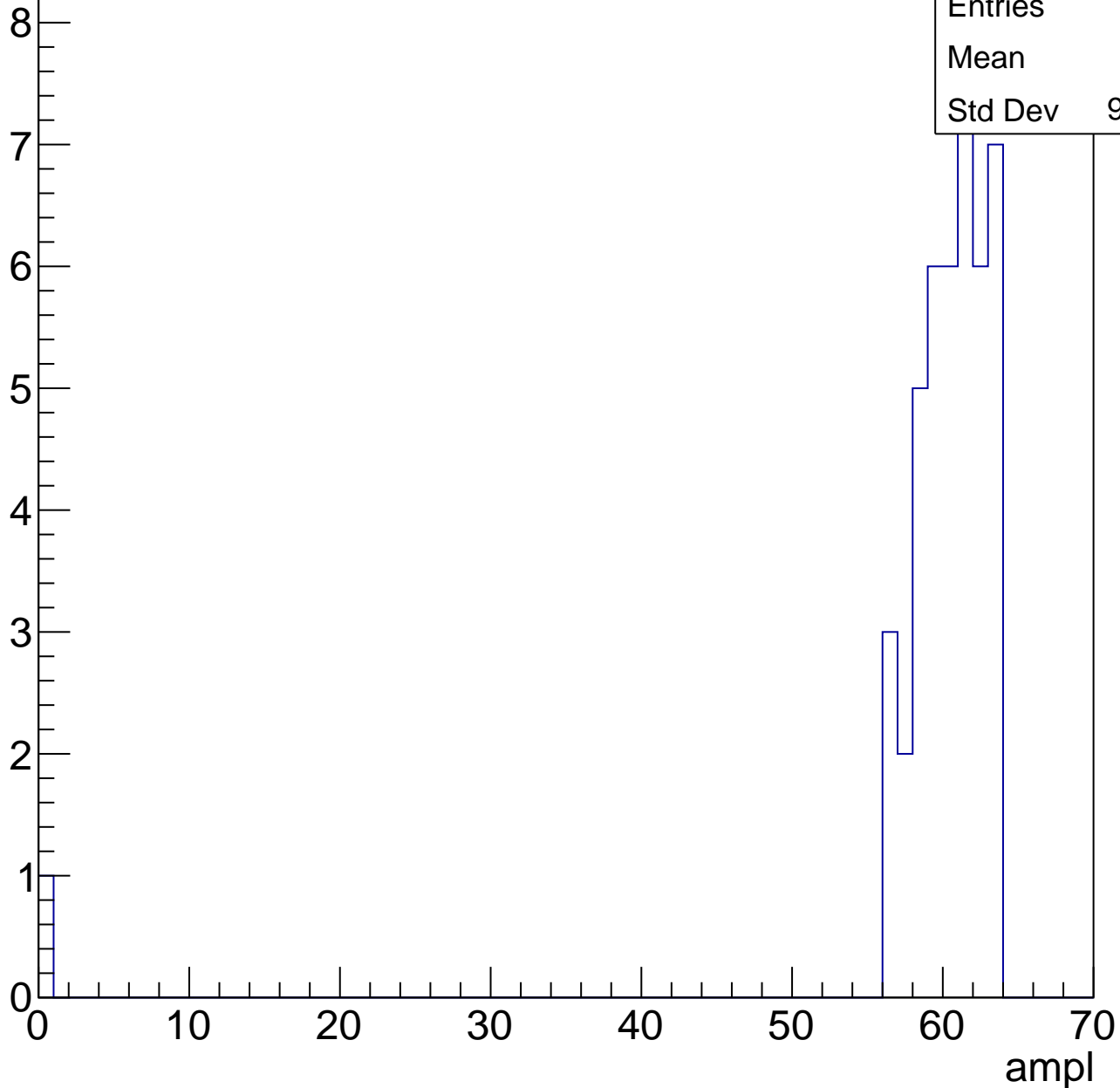


B0L001S, U25-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

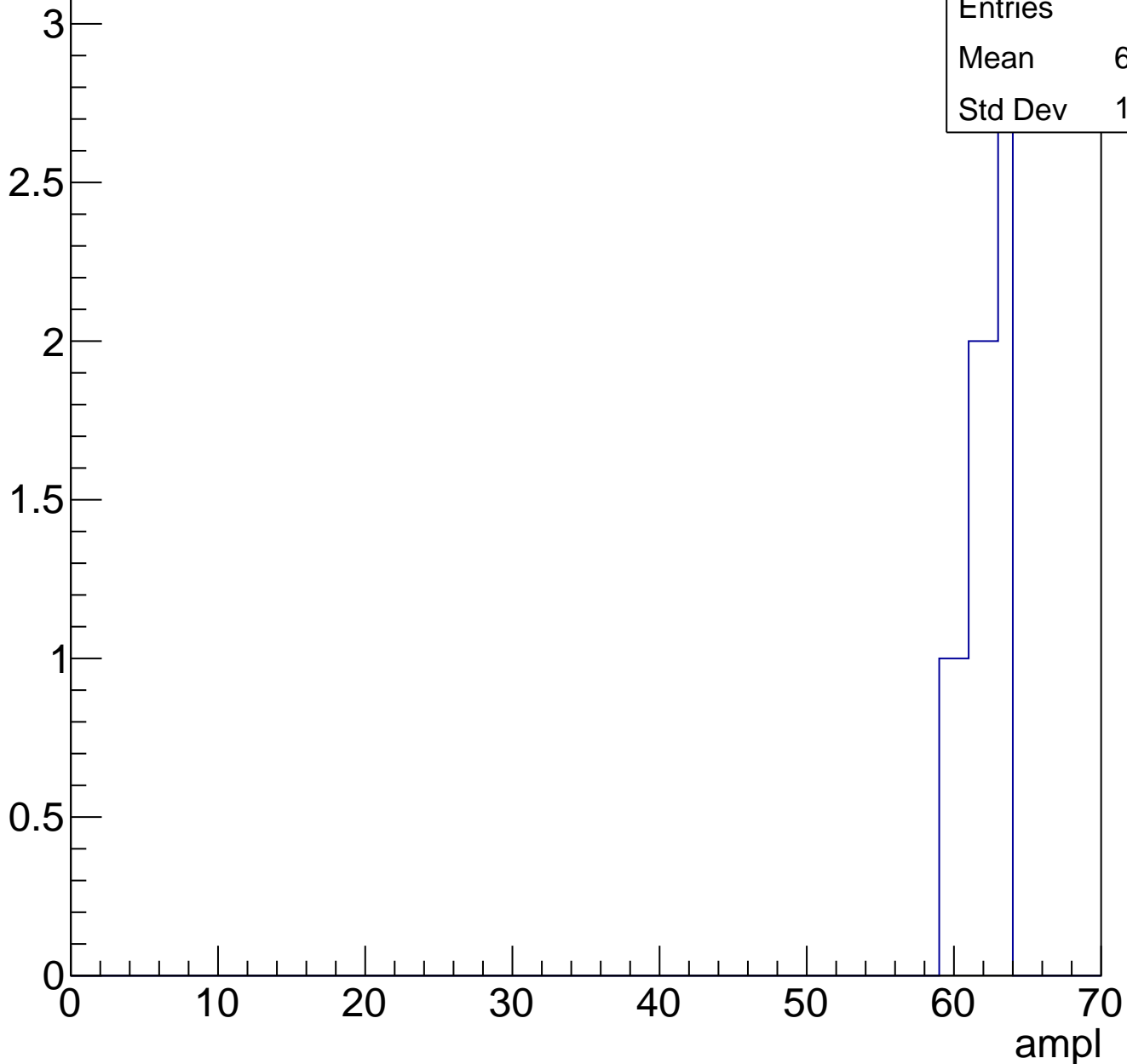
Entries	44
Mean	58.8
Std Dev	9.199



B0L001S, U25-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch51, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	29.33
Std Dev	5.992

Gaus mean : 30.4041

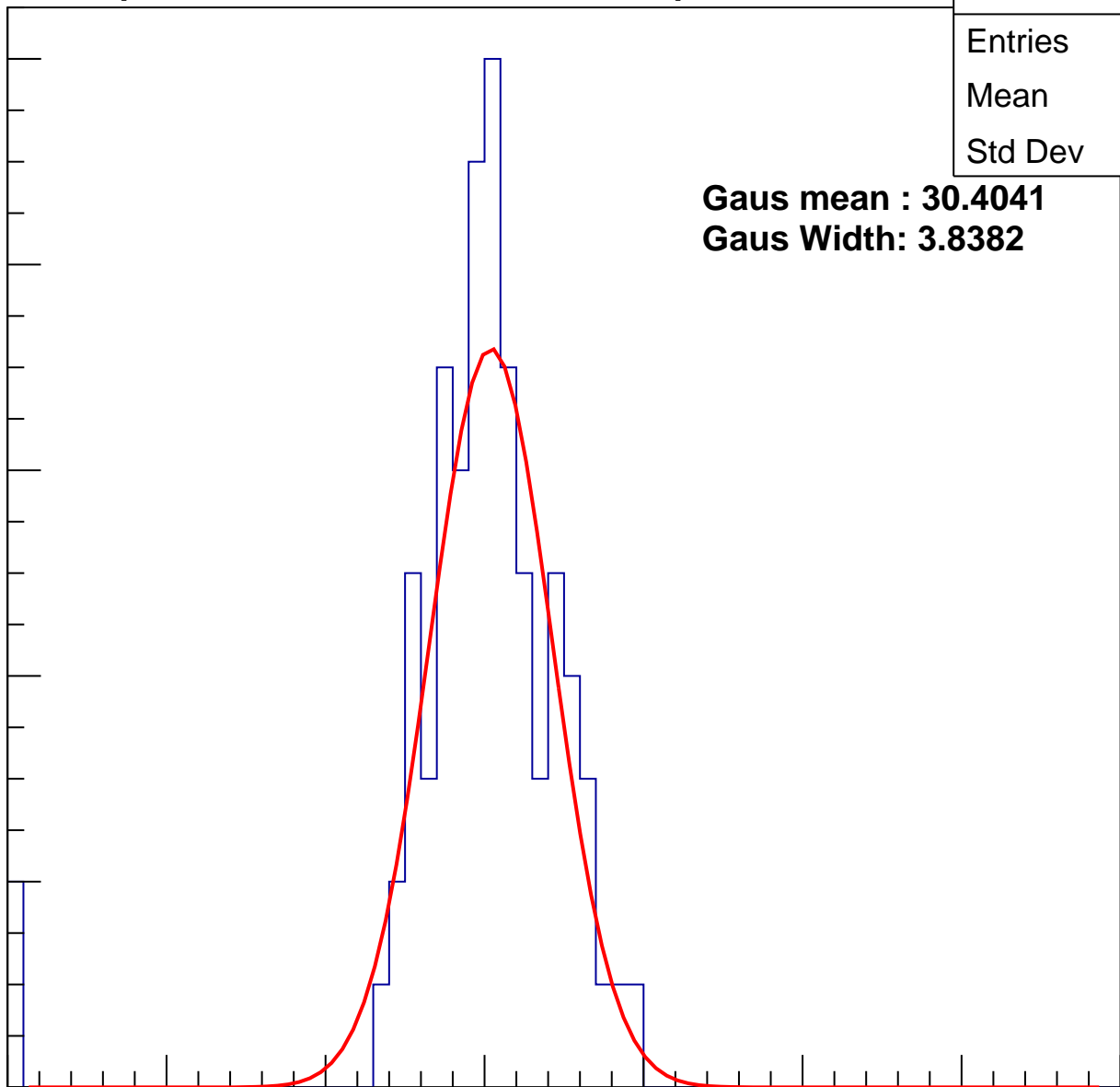
Gaus Width: 3.8382

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch51, adc1

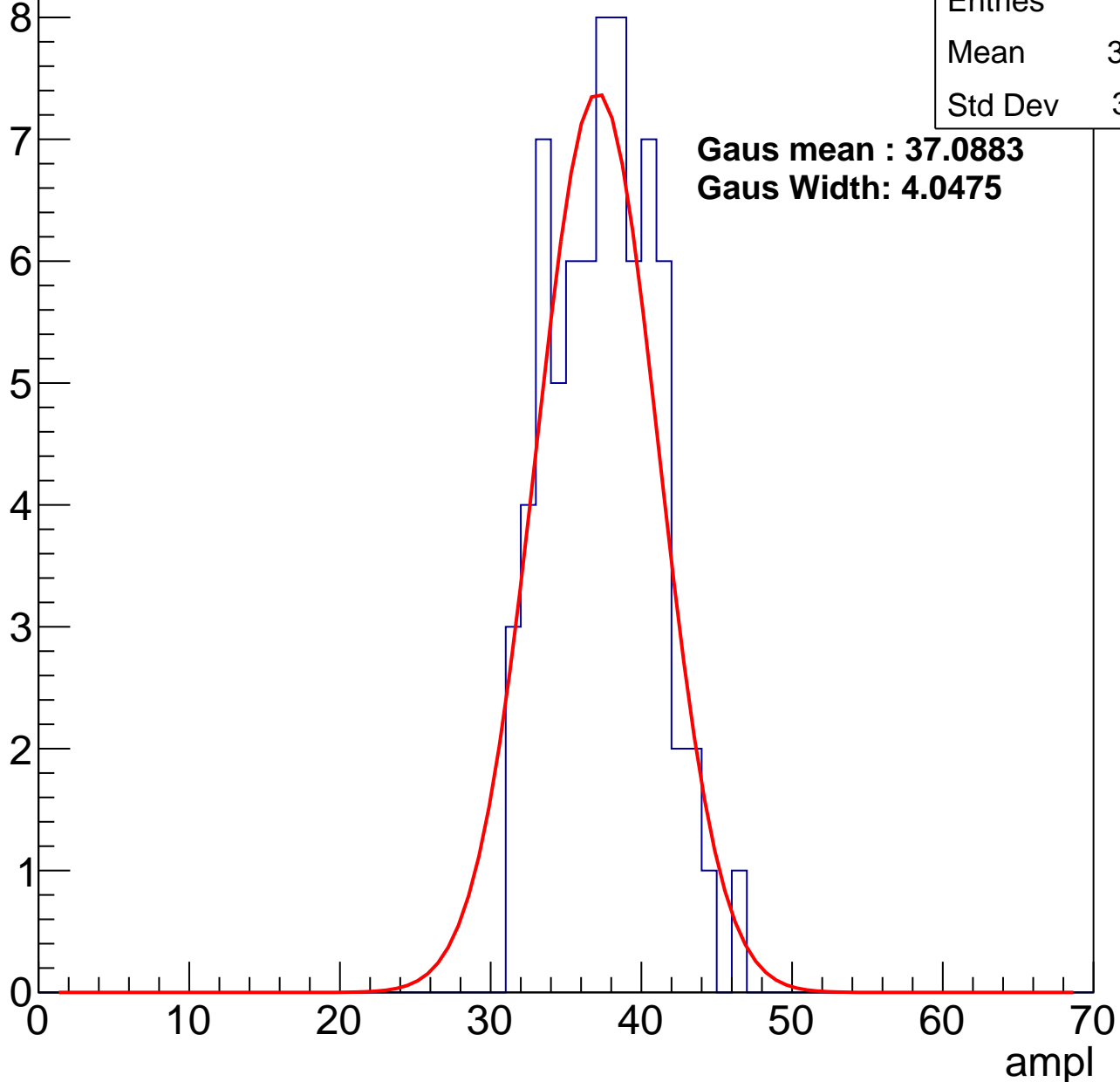
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.06
Std Dev	3.411

Gaus mean : 37.0883

Gaus Width: 4.0475



B0L001S, U25-ch51, adc2

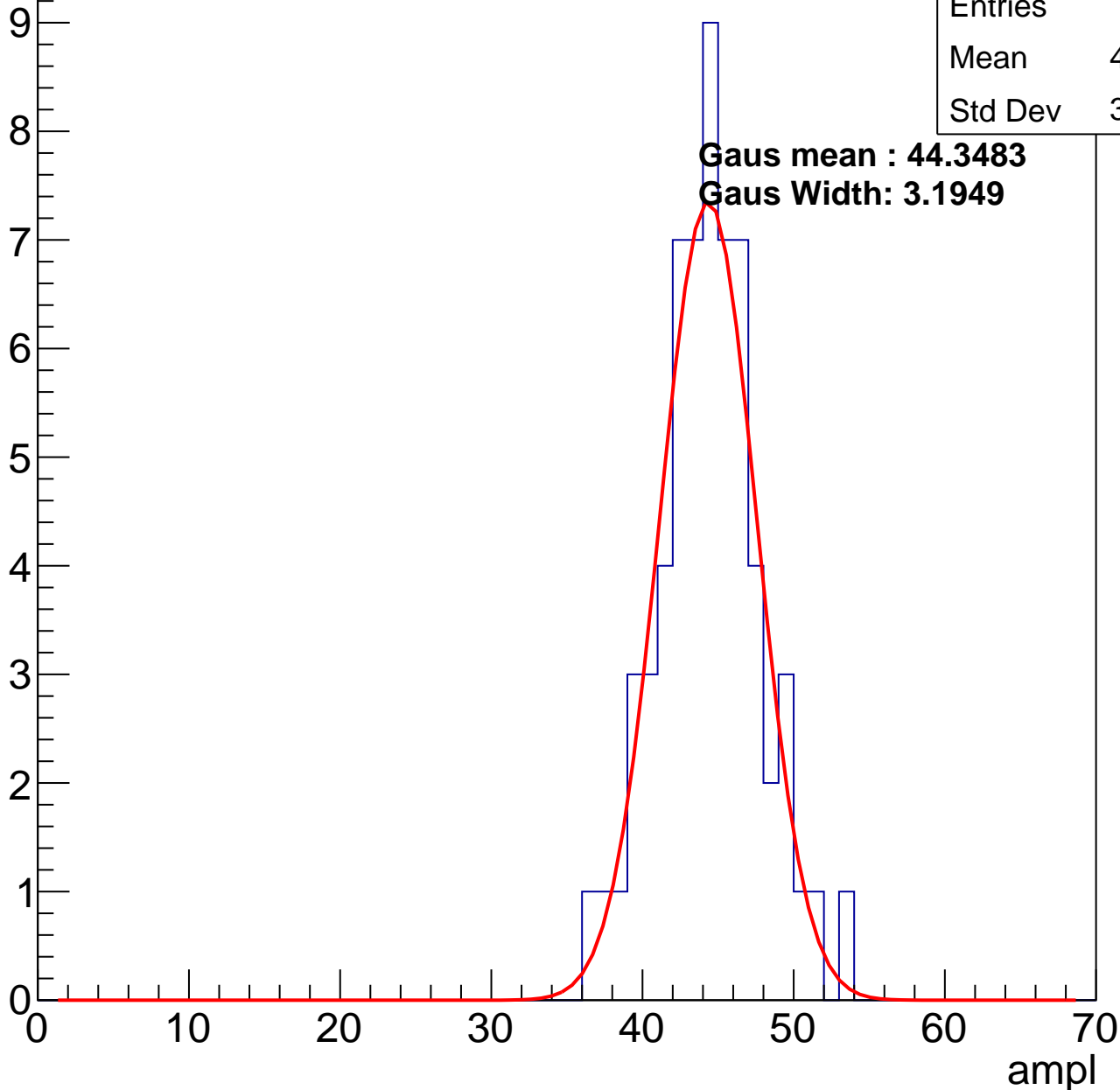
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.95
Std Dev	3.328

Gaus mean : 44.3483

Gaus Width: 3.1949

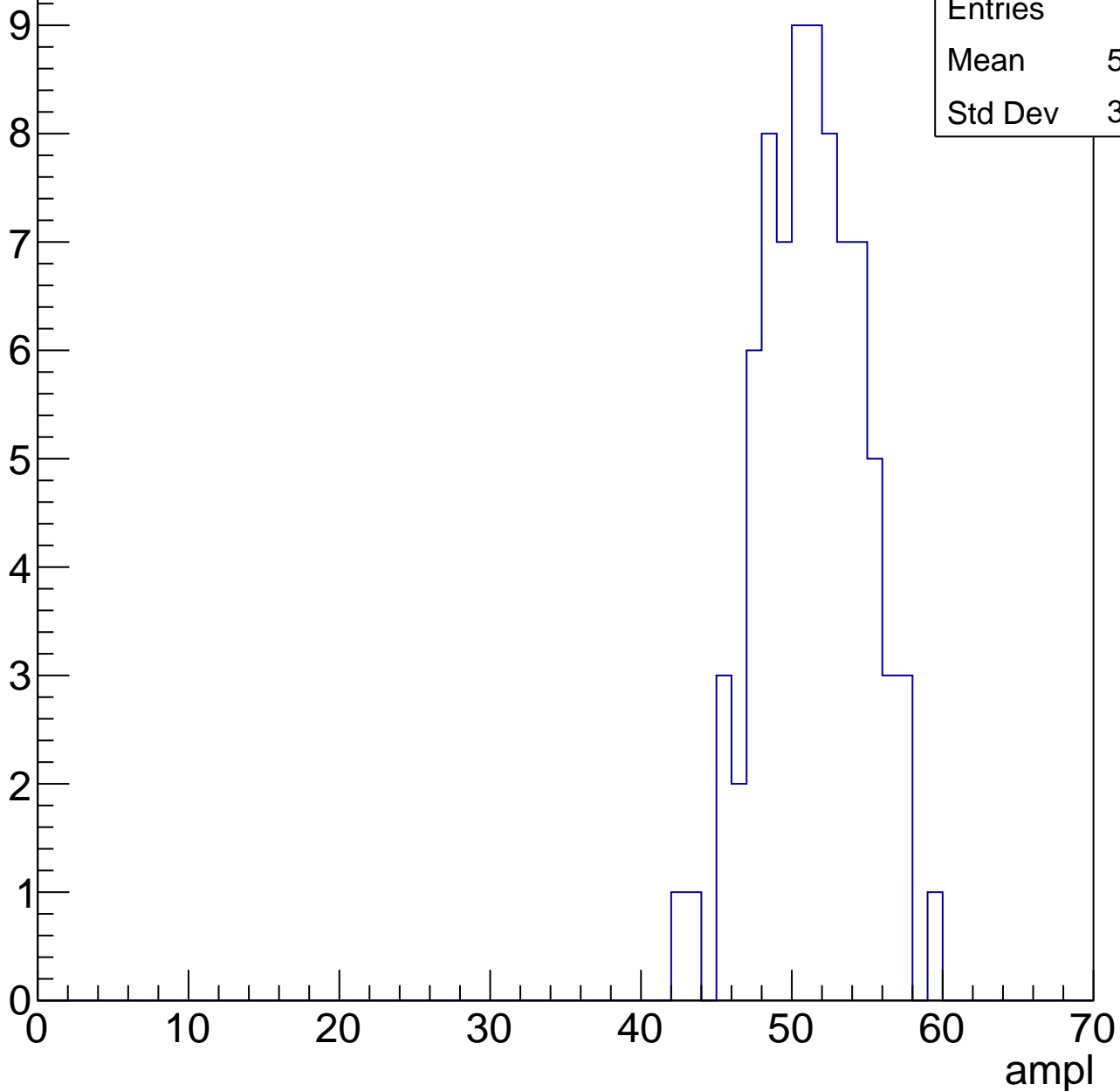


B0L001S, U25-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

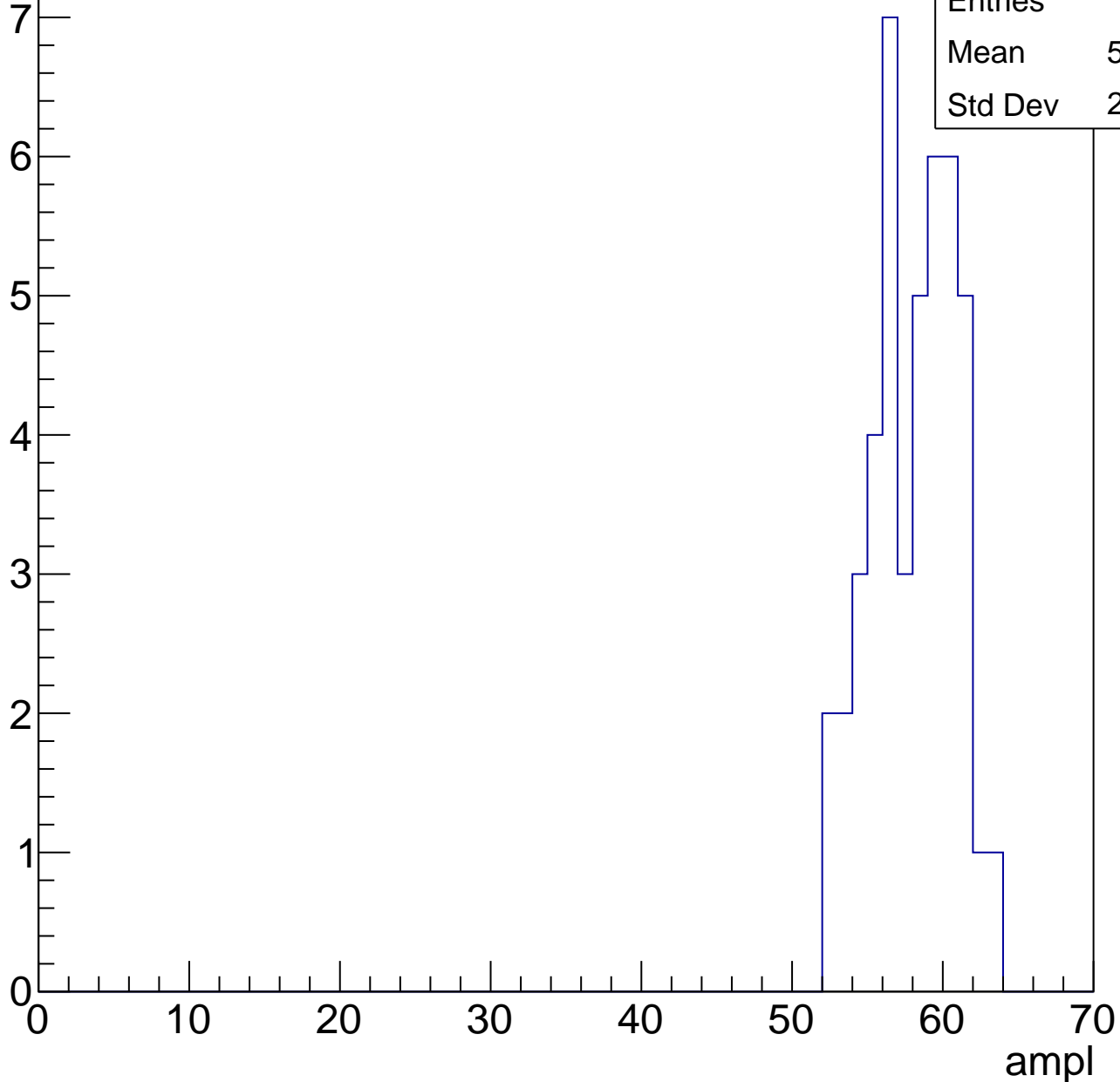
Entries	80
Mean	50.85
Std Dev	3.414



B0L001S, U25-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



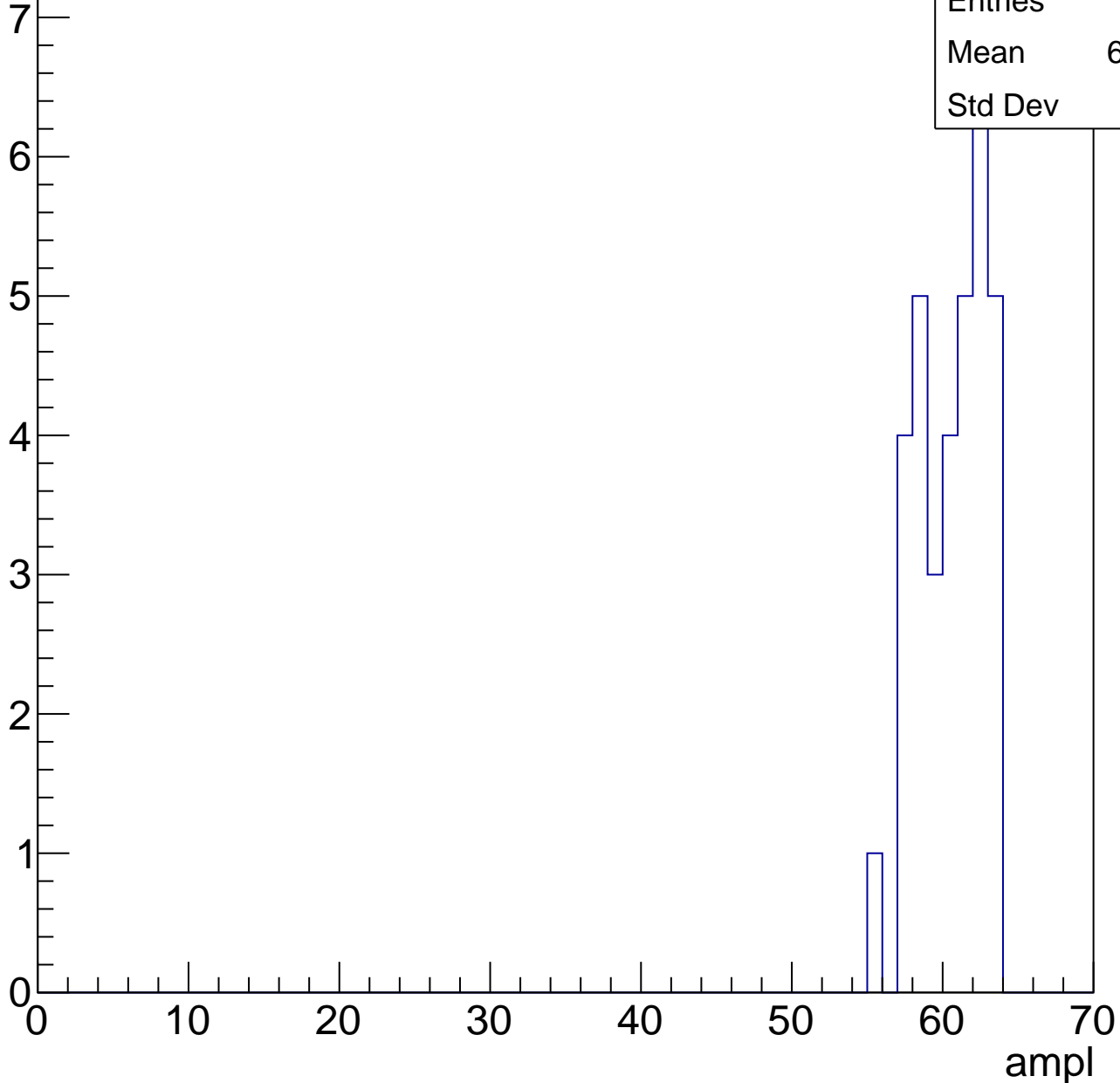
Entries	45
Mean	57.53
Std Dev	2.762

B0L001S, U25-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	60.12
Std Dev	2.18



B0L001S, U25-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L001S, U25-ch52, adc0

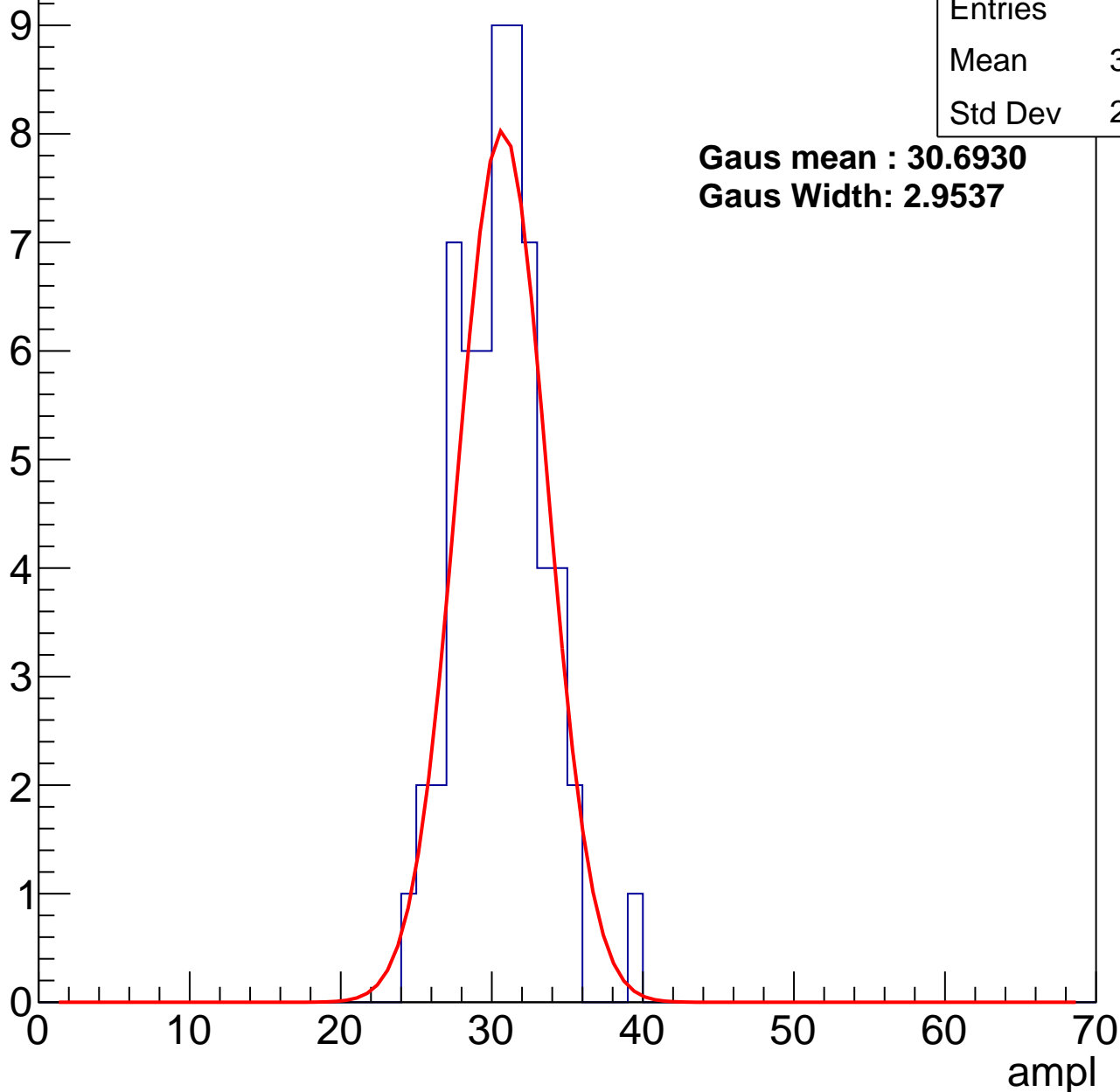
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.12
Std Dev	2.823

Gaus mean : 30.6930

Gaus Width: 2.9537



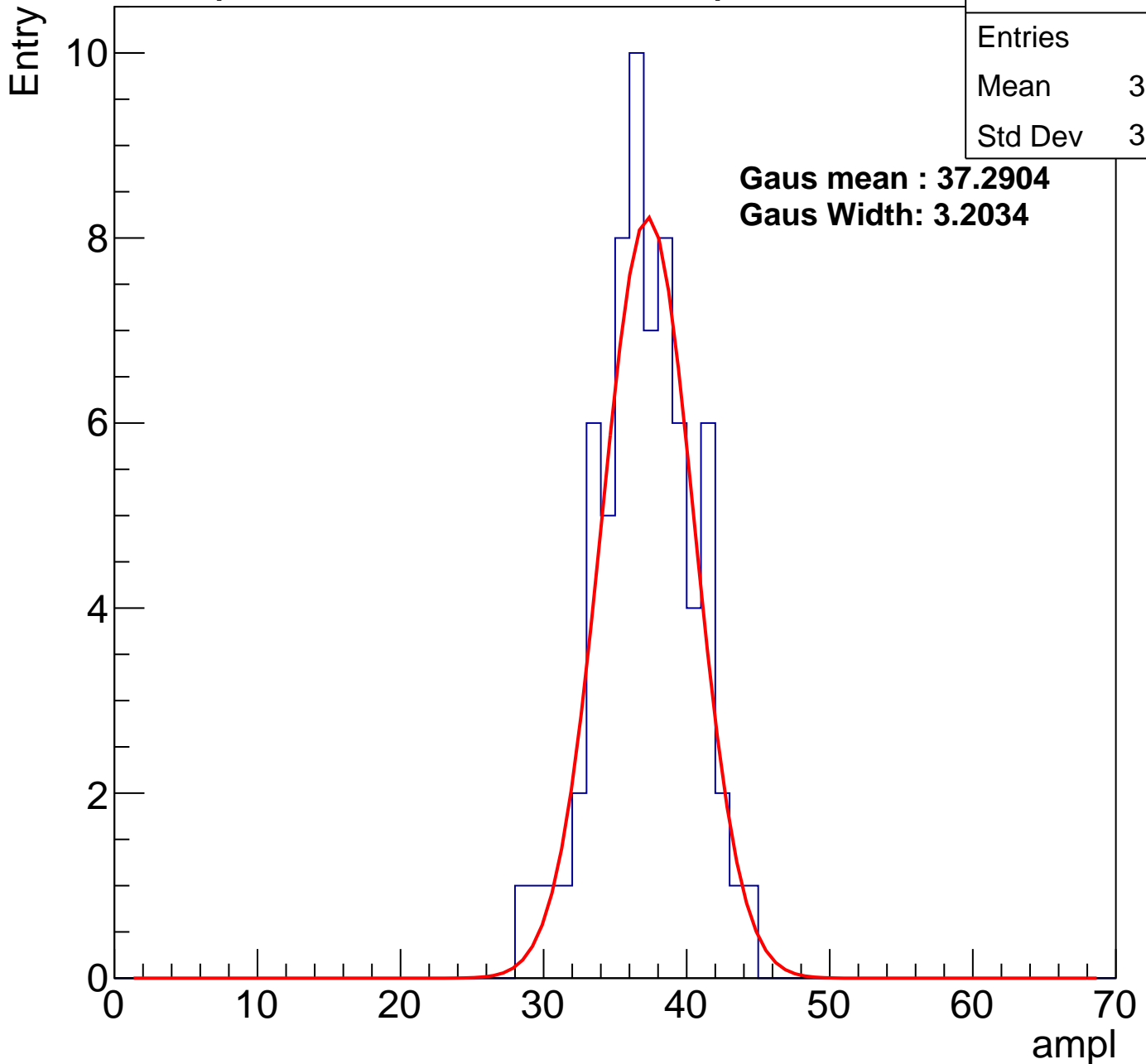
B0L001S, U25-ch52, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	36.63
Std Dev	3.278

Gaus mean : 37.2904

Gaus Width: 3.2034



B0L001S, U25-ch52, adc2

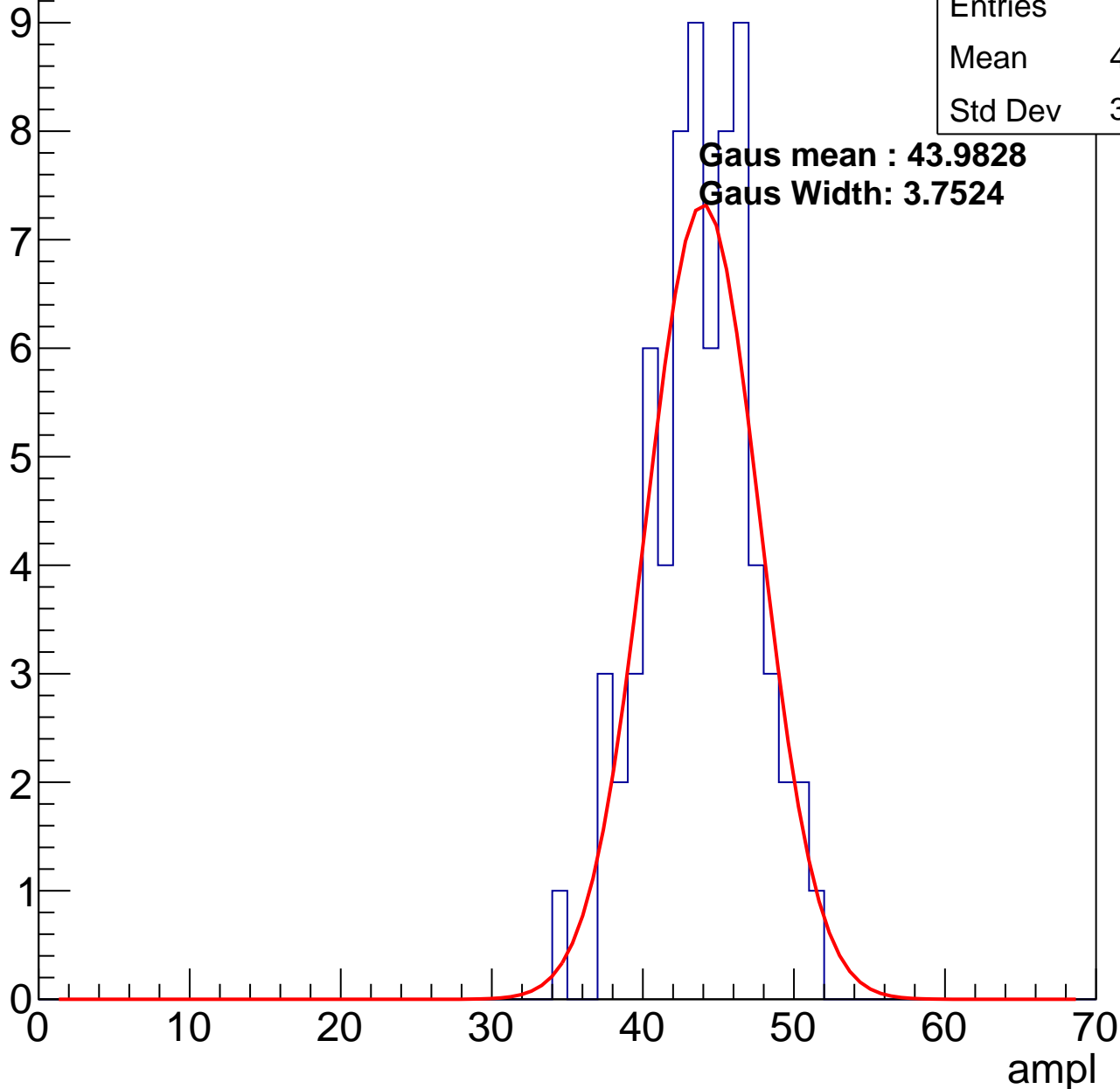
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.44
Std Dev	3.447

Gaus mean : 43.9828

Gaus Width: 3.7524

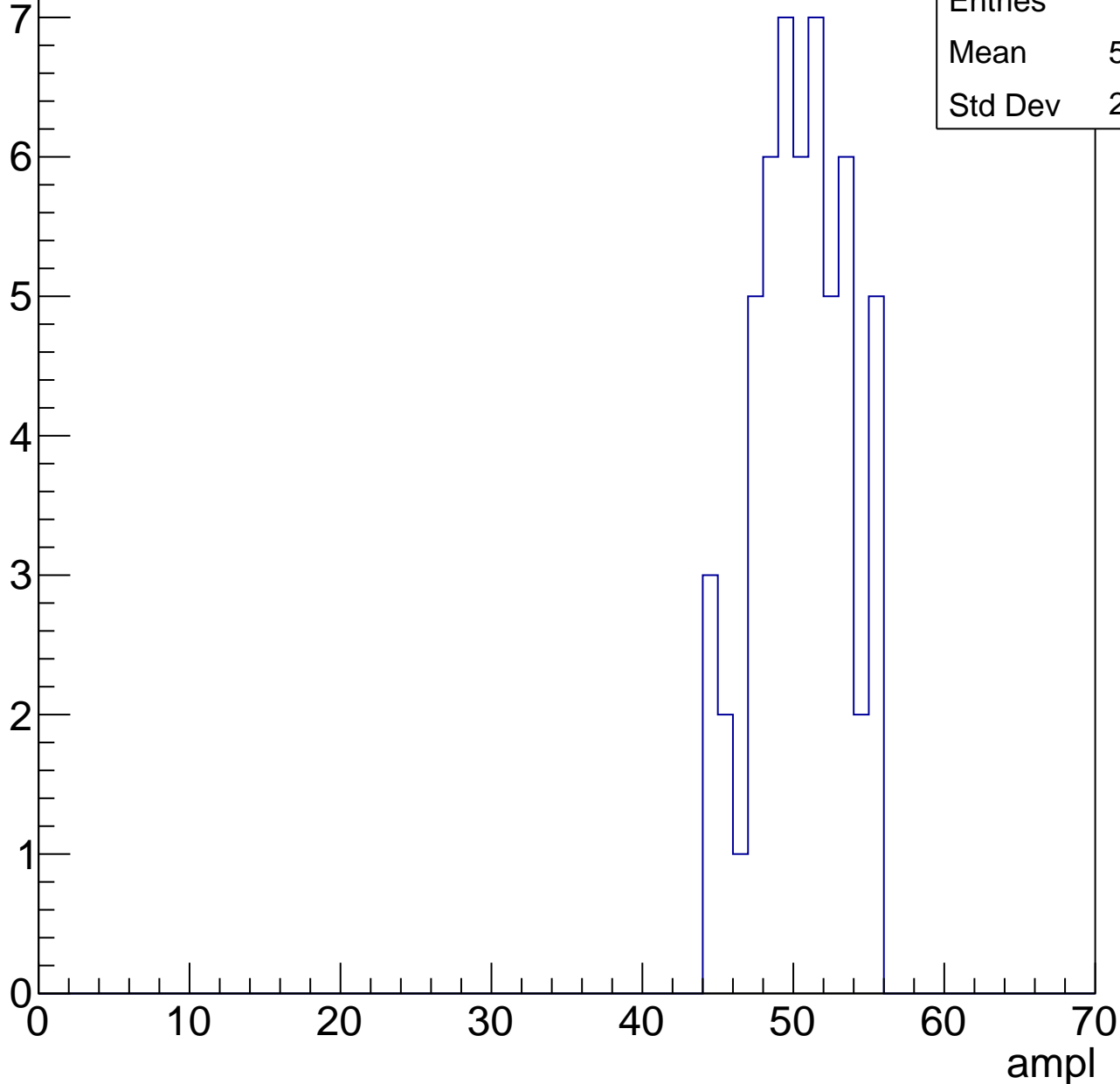


B0L001S, U25-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

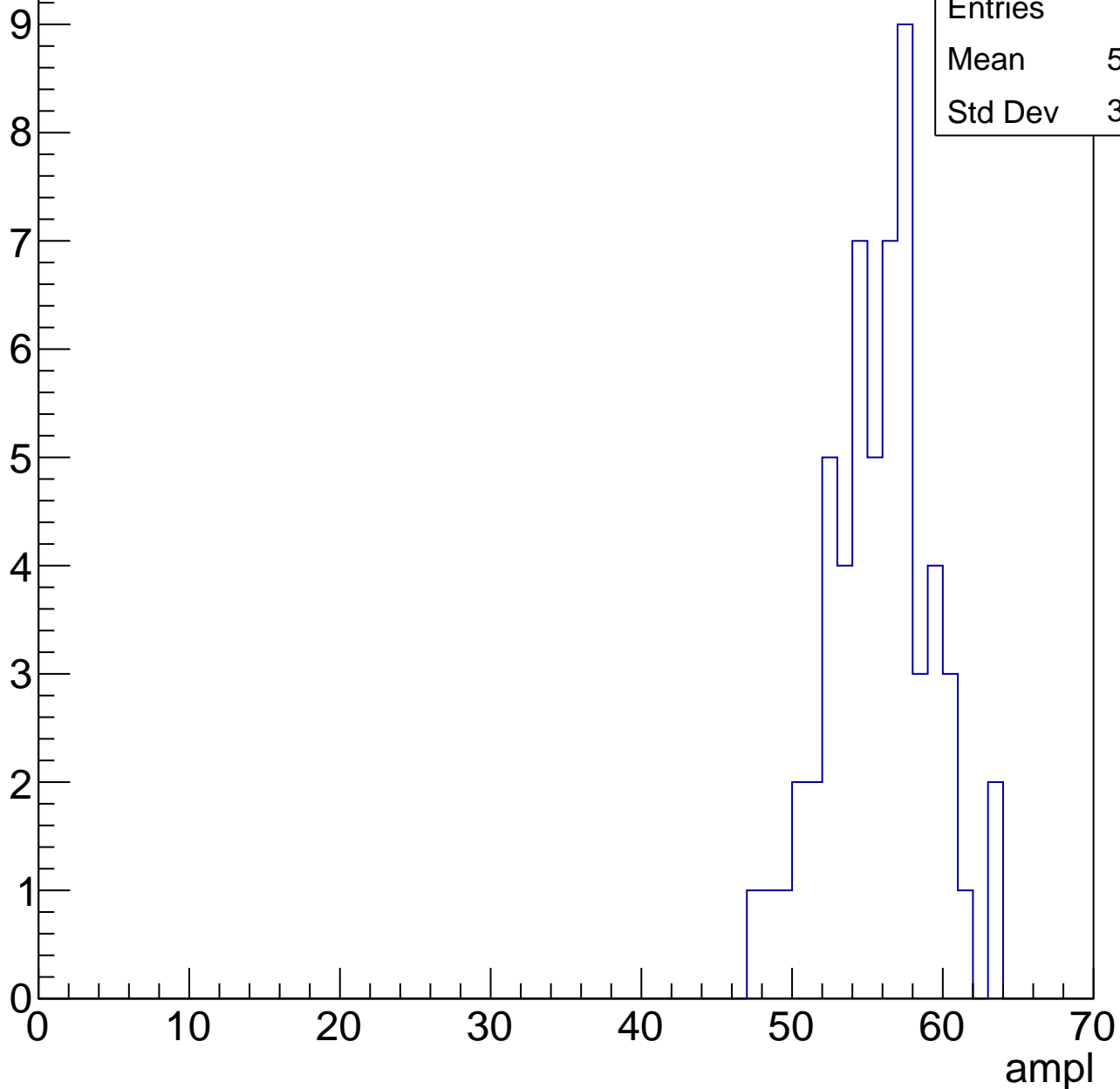
Entries	55
Mean	50.04
Std Dev	2.978



B0L001S, U25-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

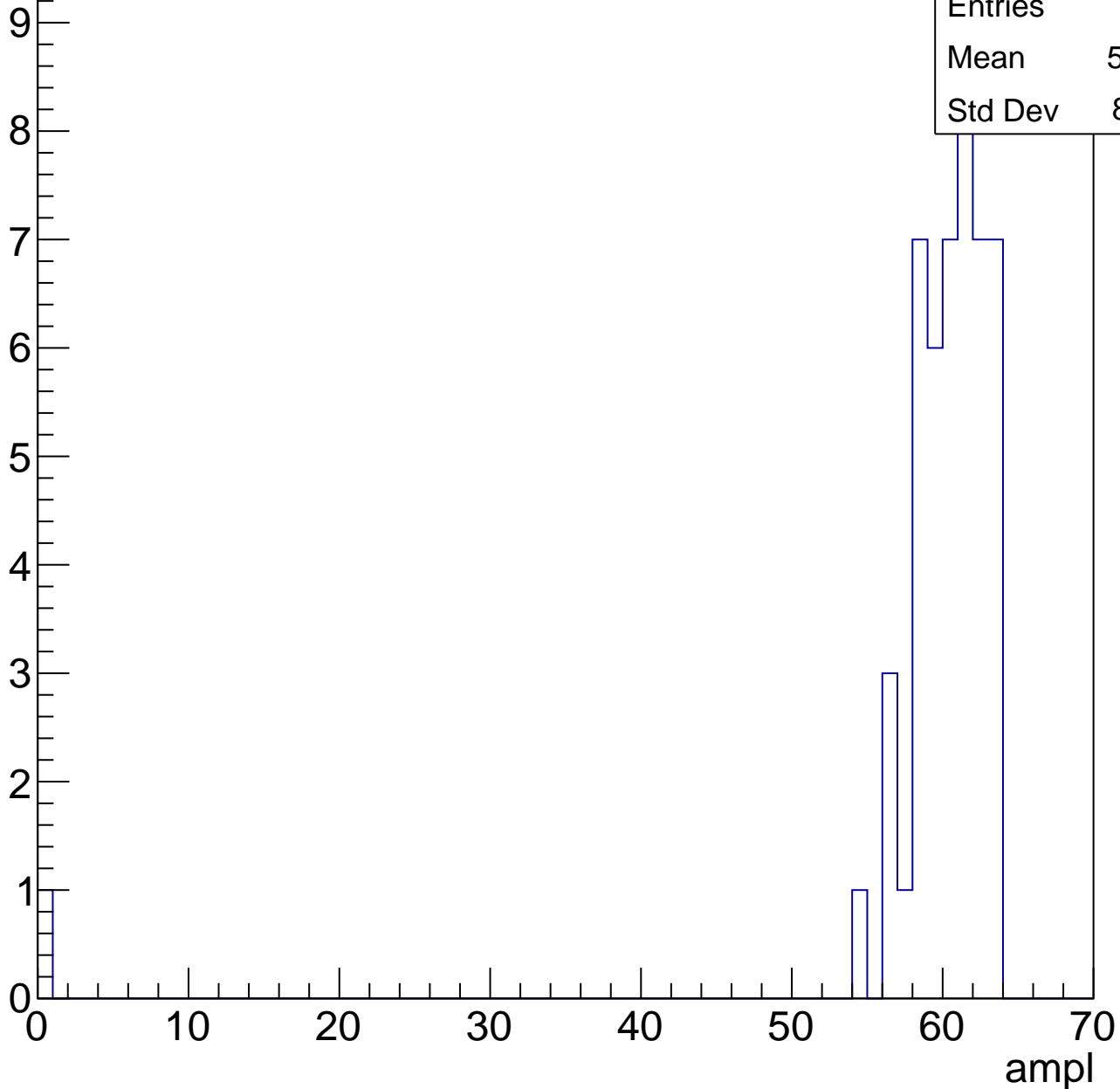


B0L001S, U25-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.84
Std Dev	8.761



B0L001S, U25-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U25-ch53, adc0

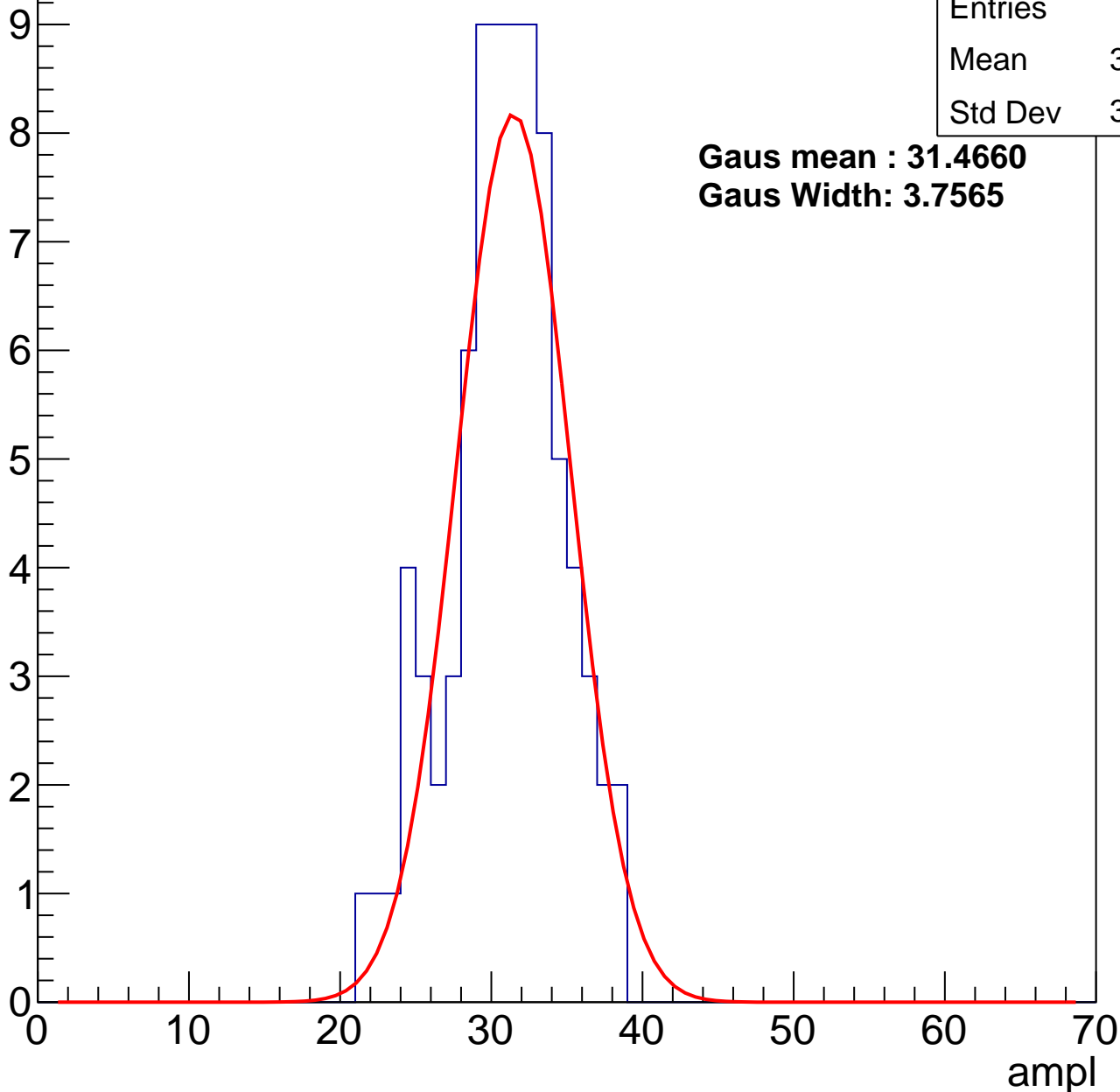
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.47
Std Dev	3.715

Gaus mean : 31.4660

Gaus Width: 3.7565



B0L001S, U25-ch53, adc1

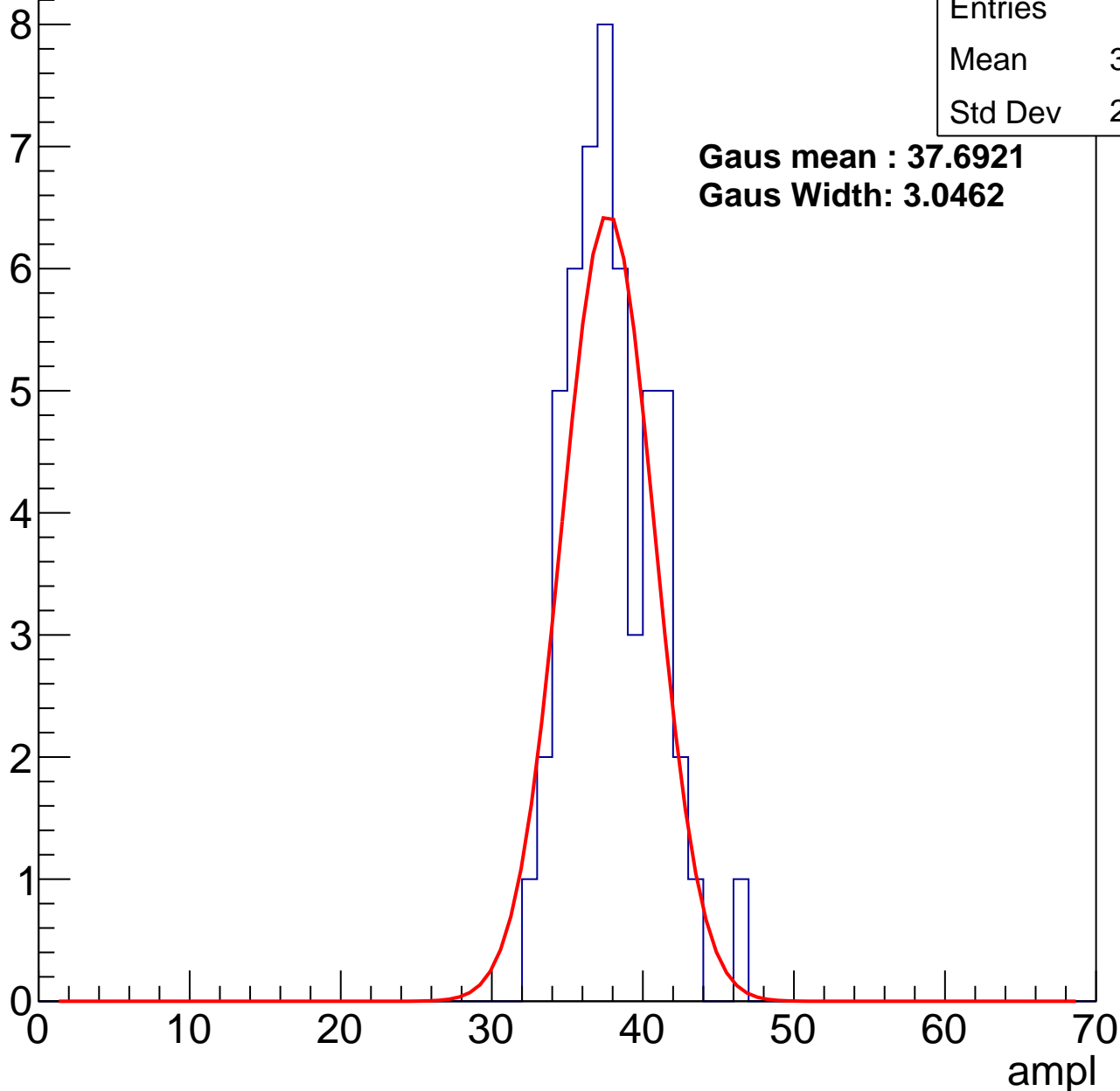
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	37.48
Std Dev	2.879

Gaus mean : 37.6921

Gaus Width: 3.0462



B0L001S, U25-ch53, adc2

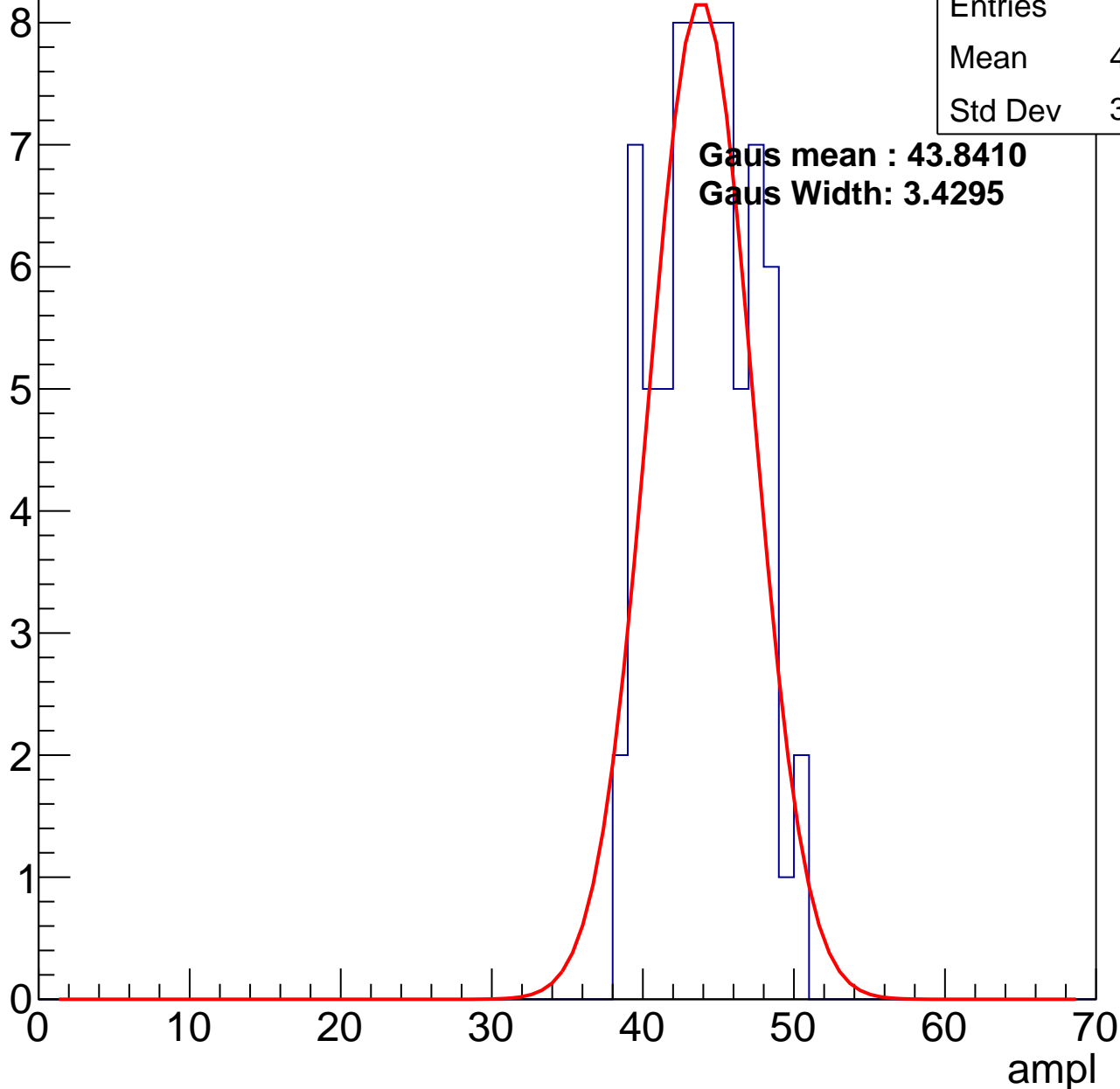
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.64
Std Dev	3.088

Gaus mean : 43.8410

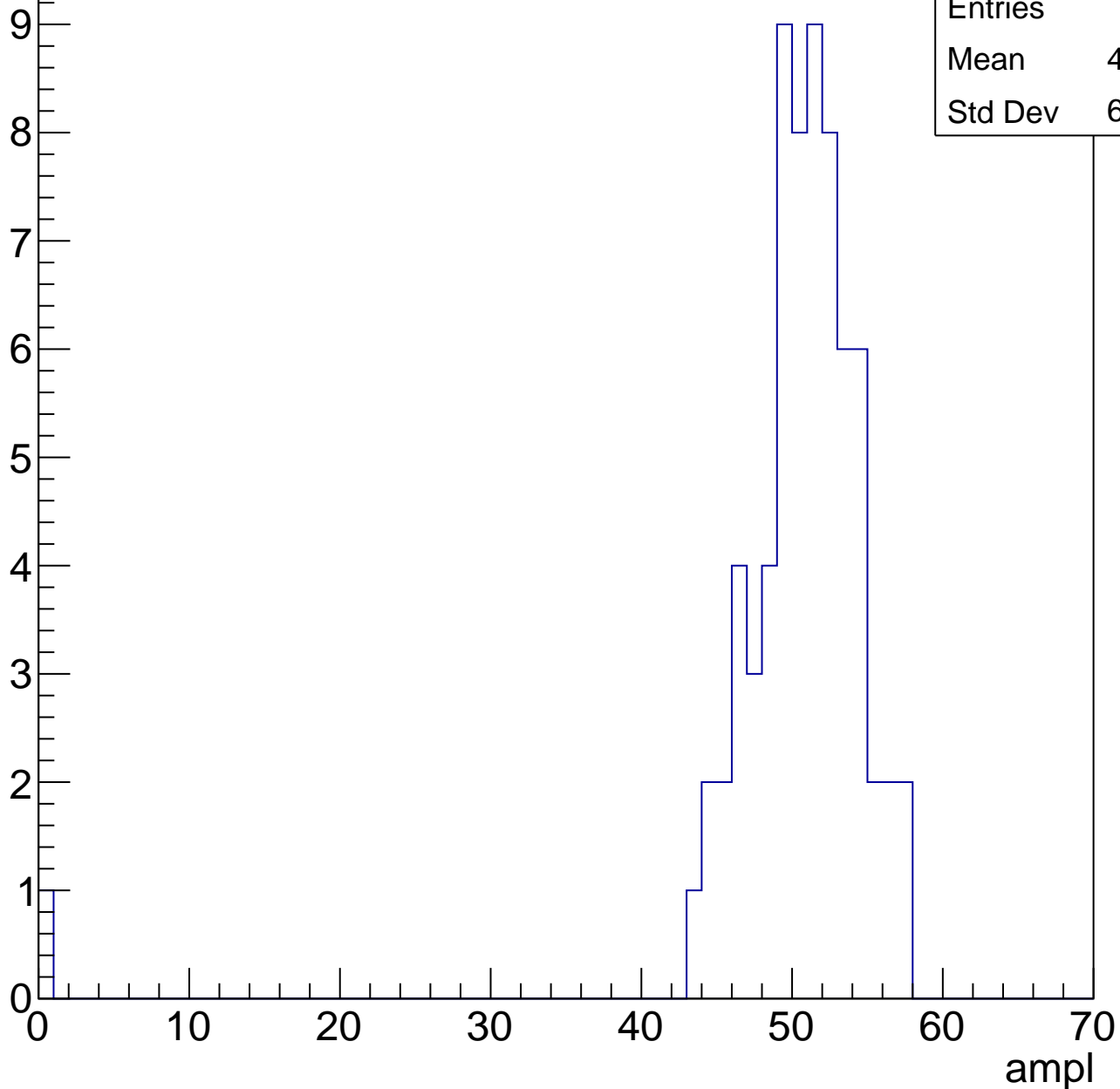
Gaus Width: 3.4295



B0L001S, U25-ch53, adc3

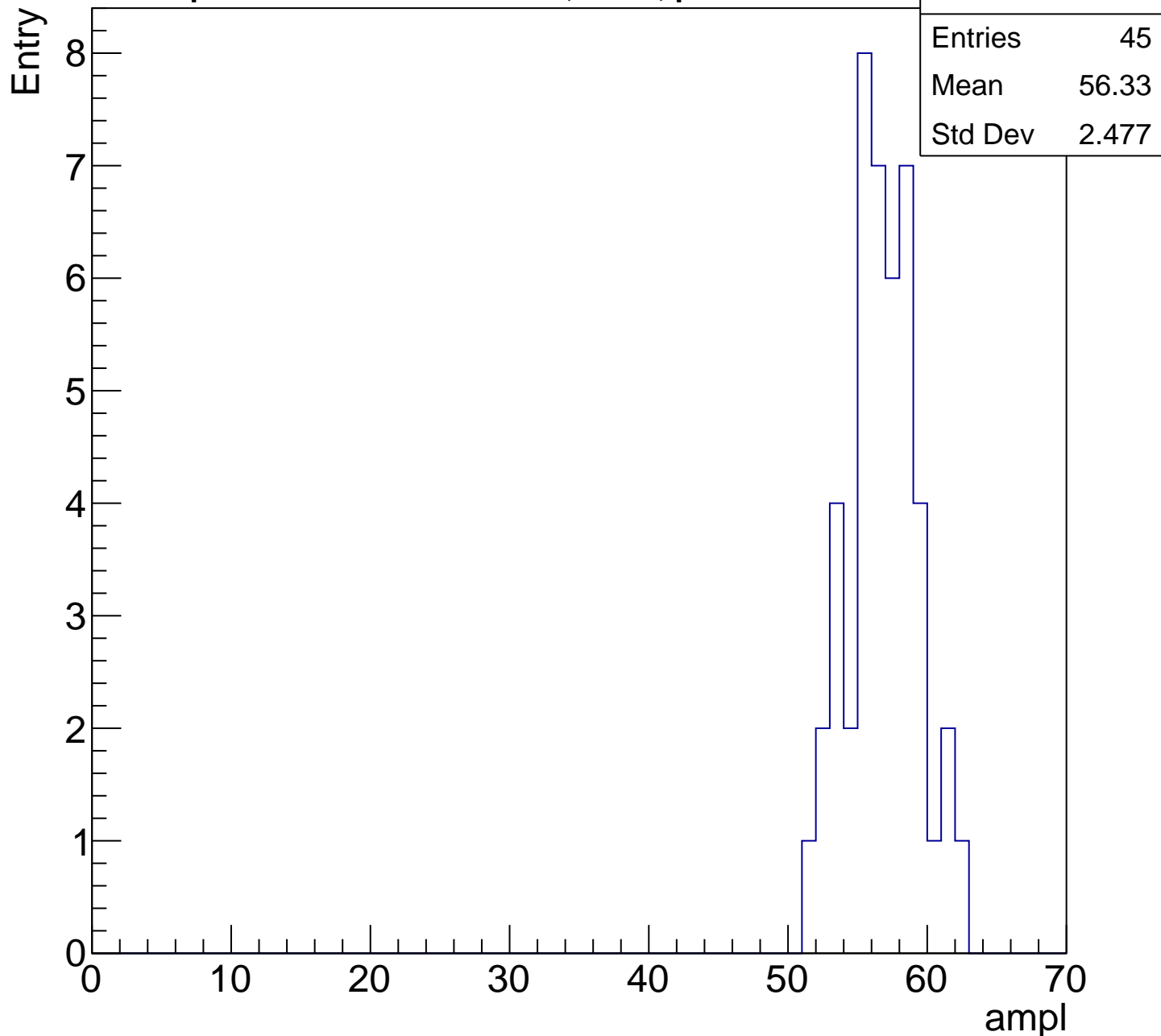
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

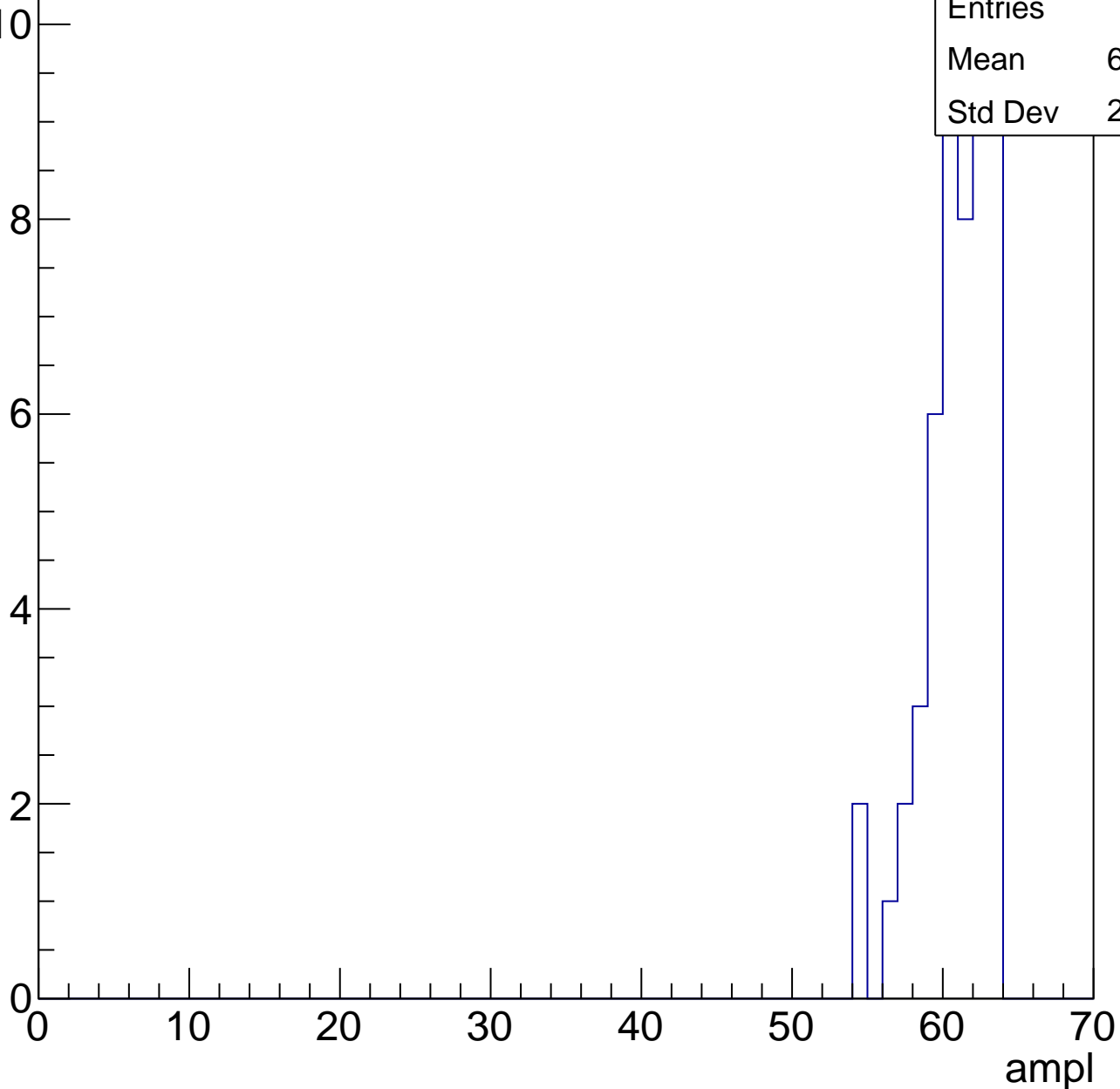


B0L001S, U25-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	60.44
Std Dev	2.229



B0L001S, U25-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U25-ch54, adc0

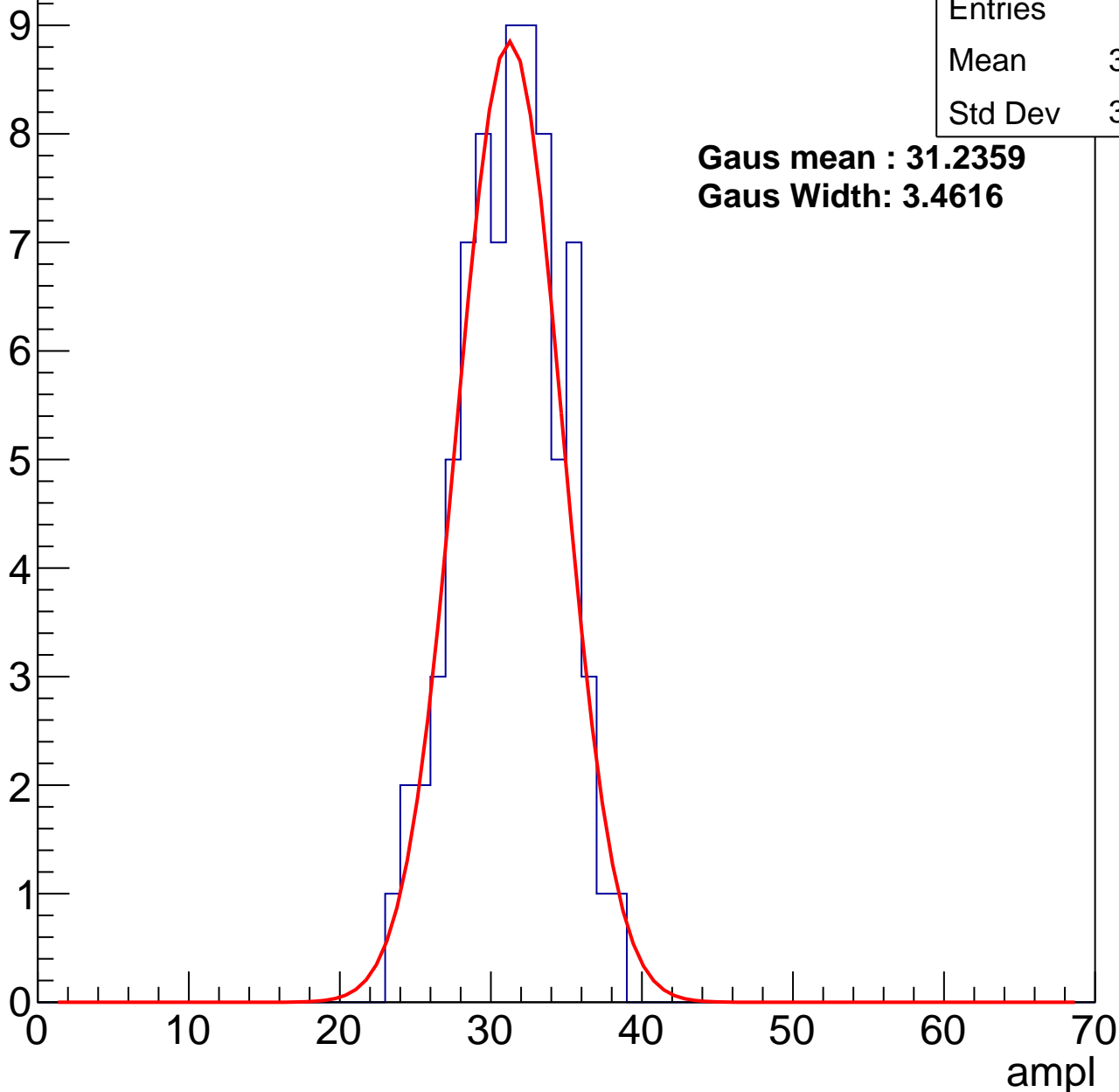
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.78
Std Dev	3.292

Gaus mean : 31.2359

Gaus Width: 3.4616



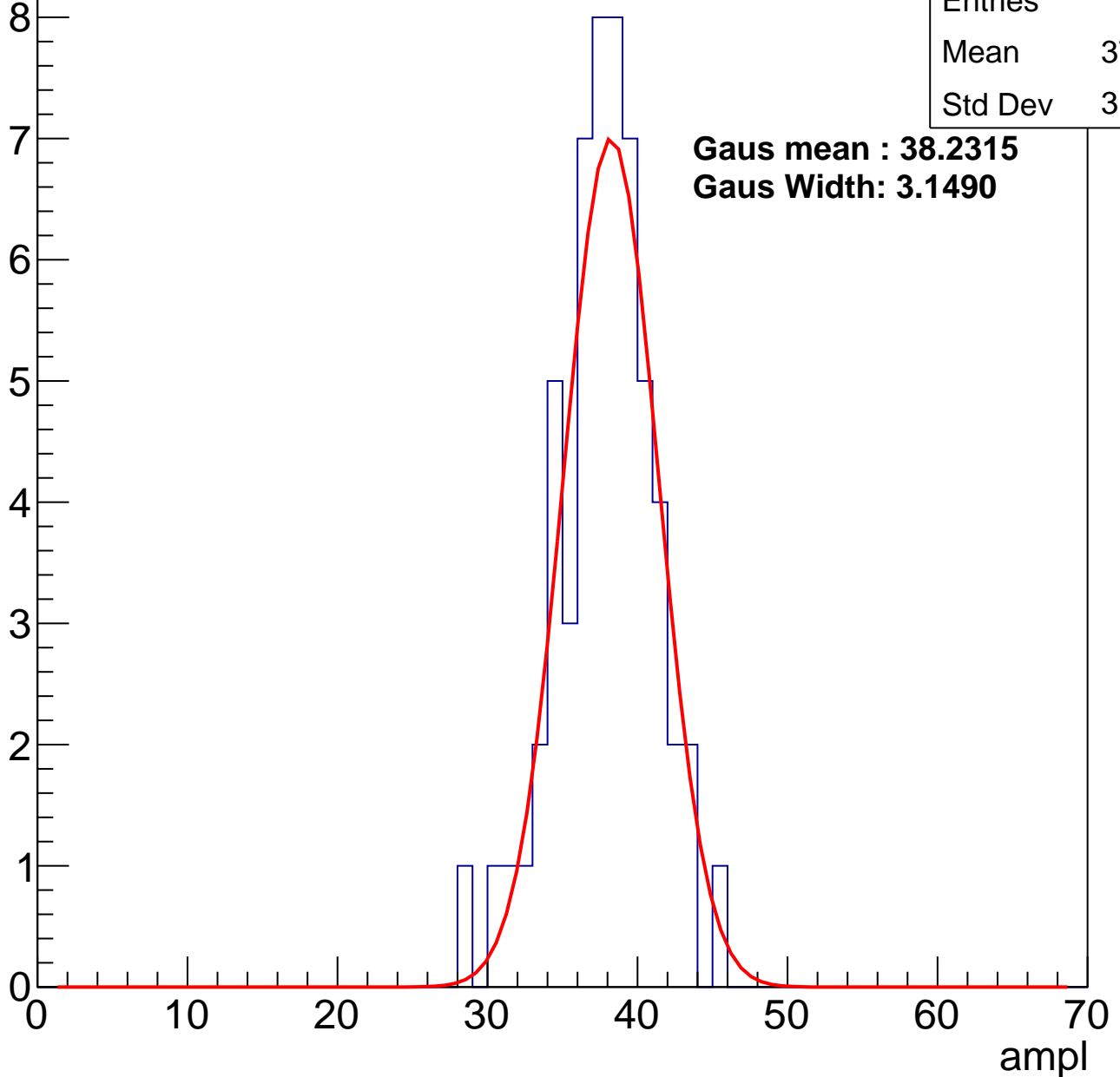
B0L001S, U25-ch54, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.34
Std Dev	3.246

Gaus mean : 38.2315
Gaus Width: 3.1490



B0L001S, U25-ch54, adc2

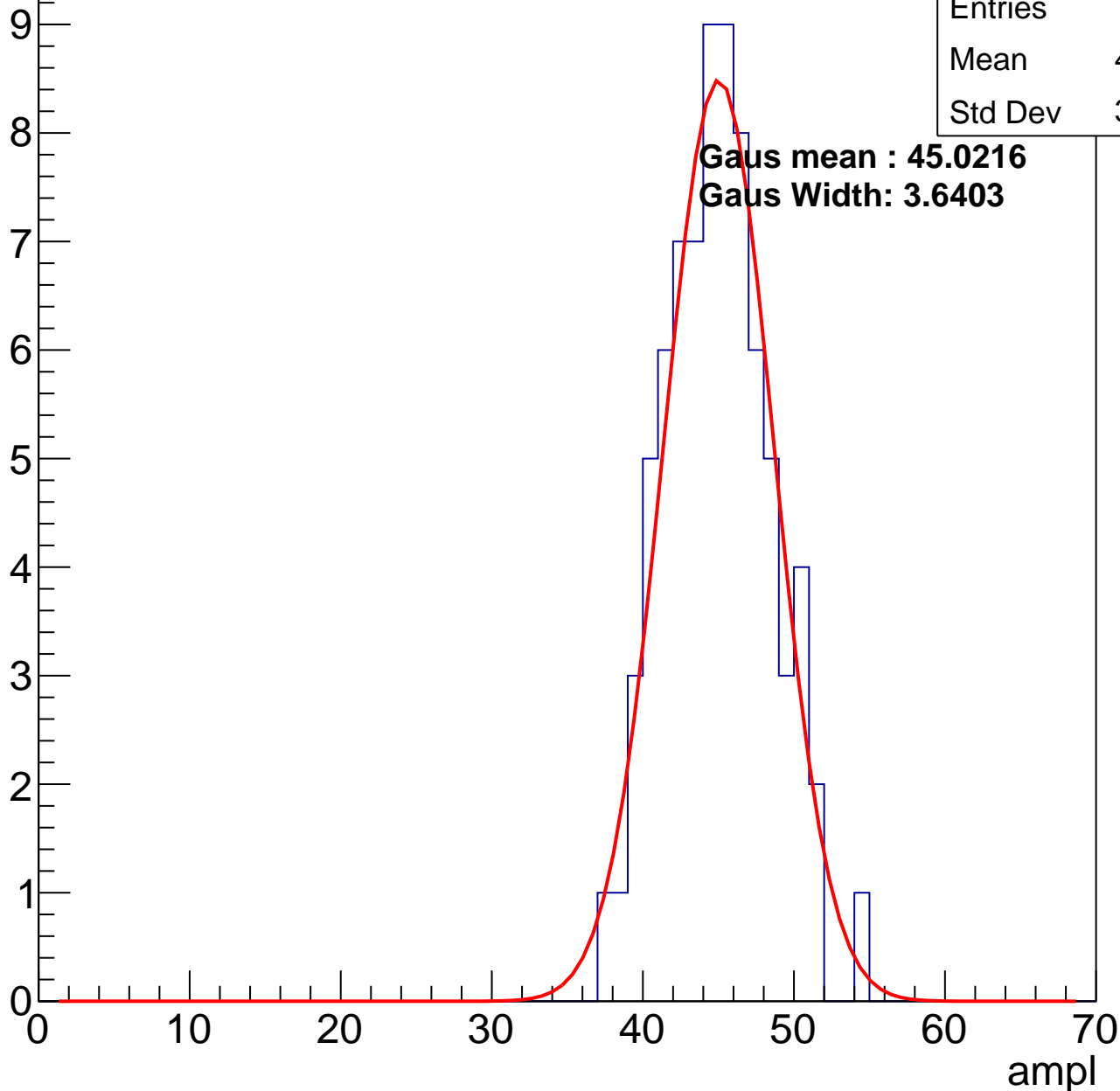
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.51
Std Dev	3.421

Gaus mean : 45.0216

Gaus Width: 3.6403

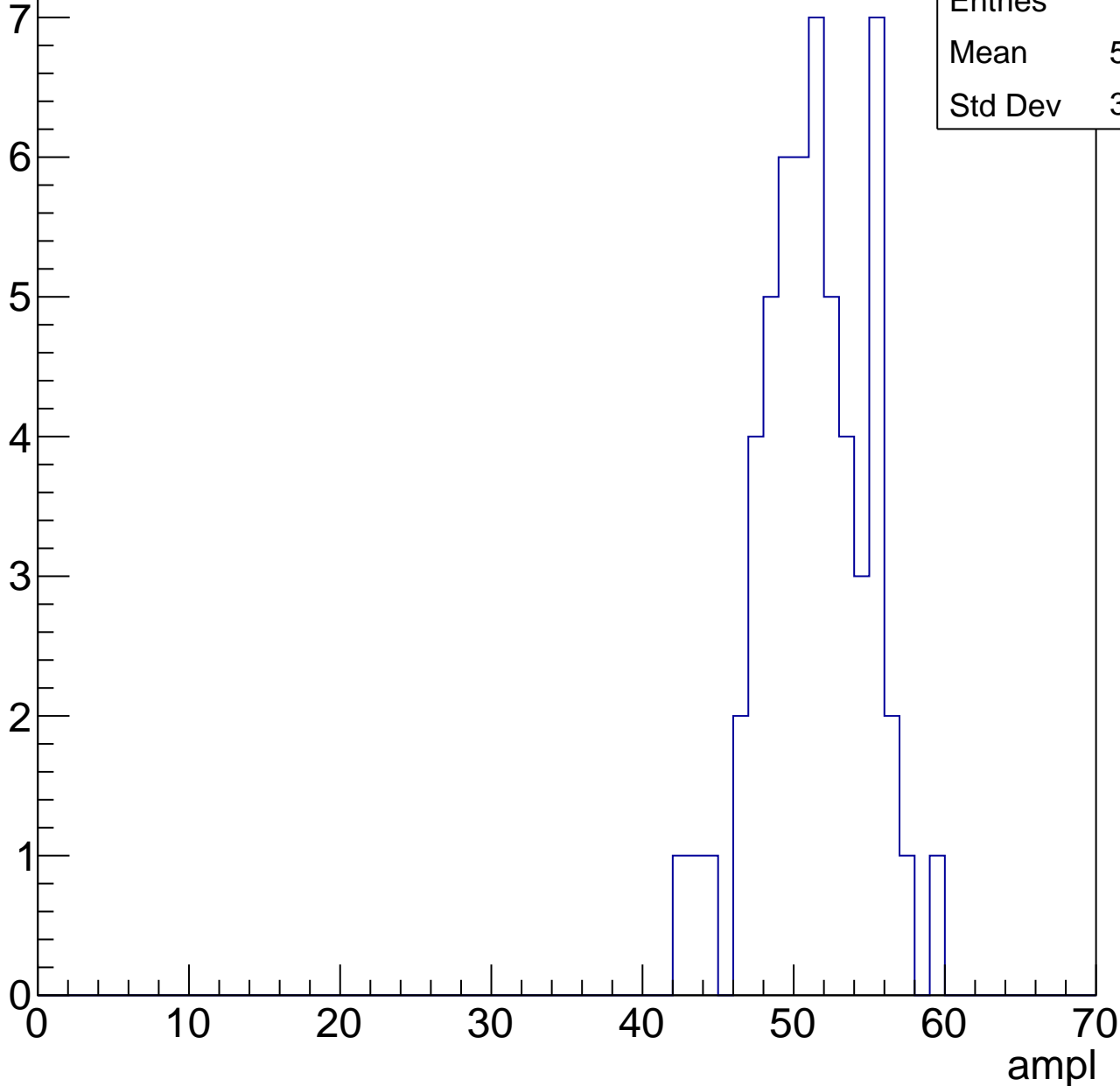


B0L001S, U25-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

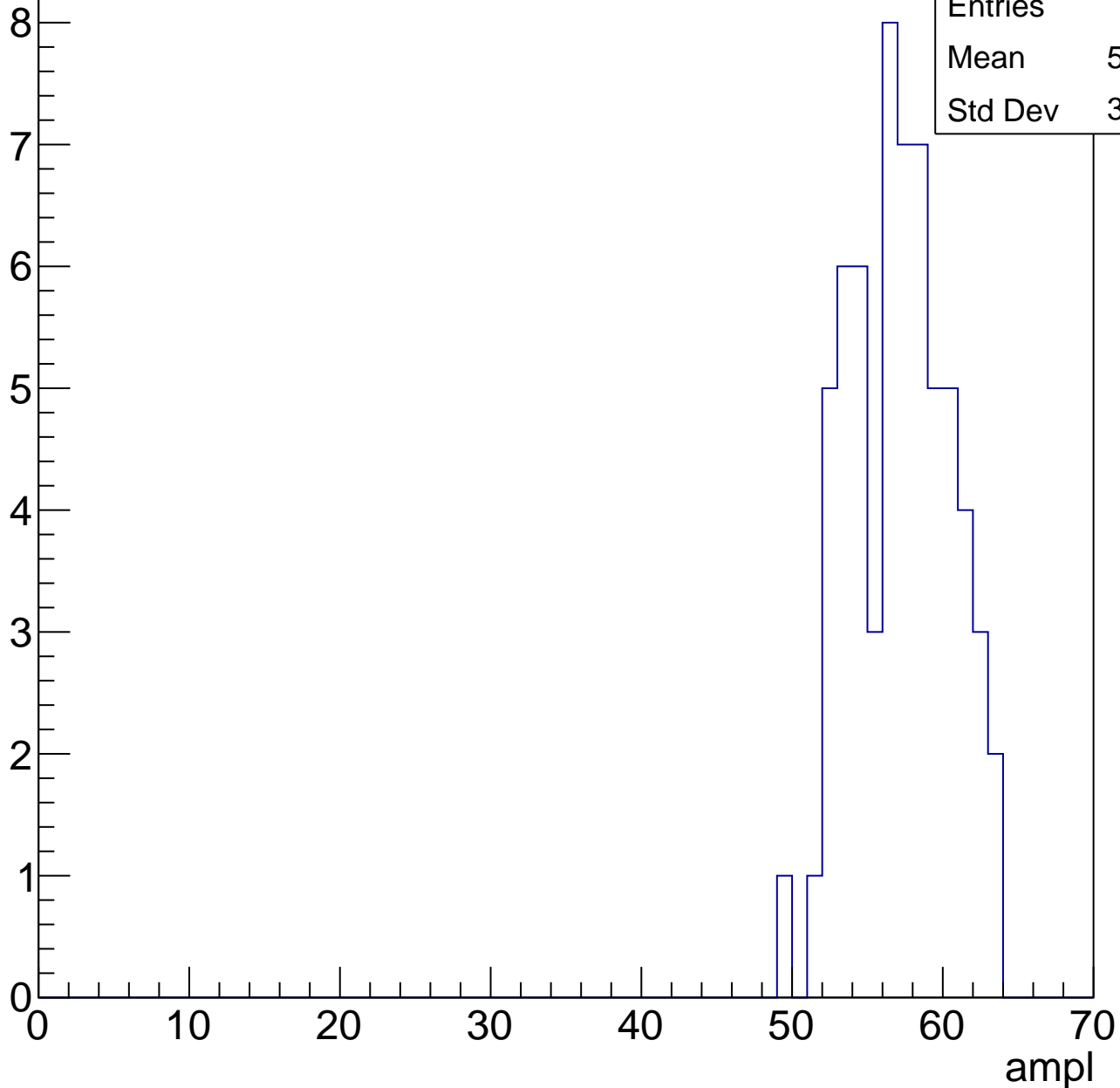
Entries	56
Mean	50.84
Std Dev	3.514



B0L001S, U25-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

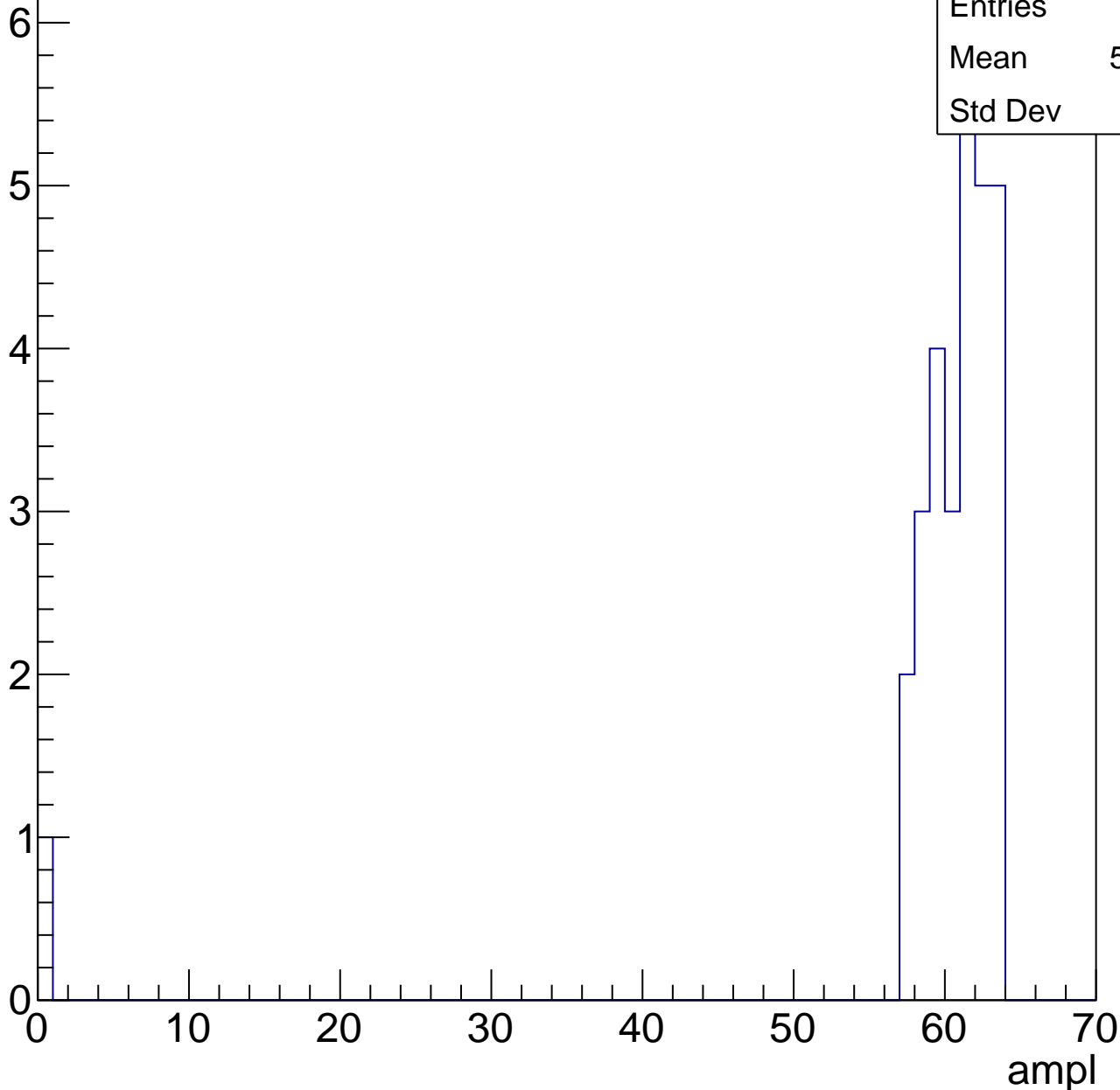


B0L001S, U25-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

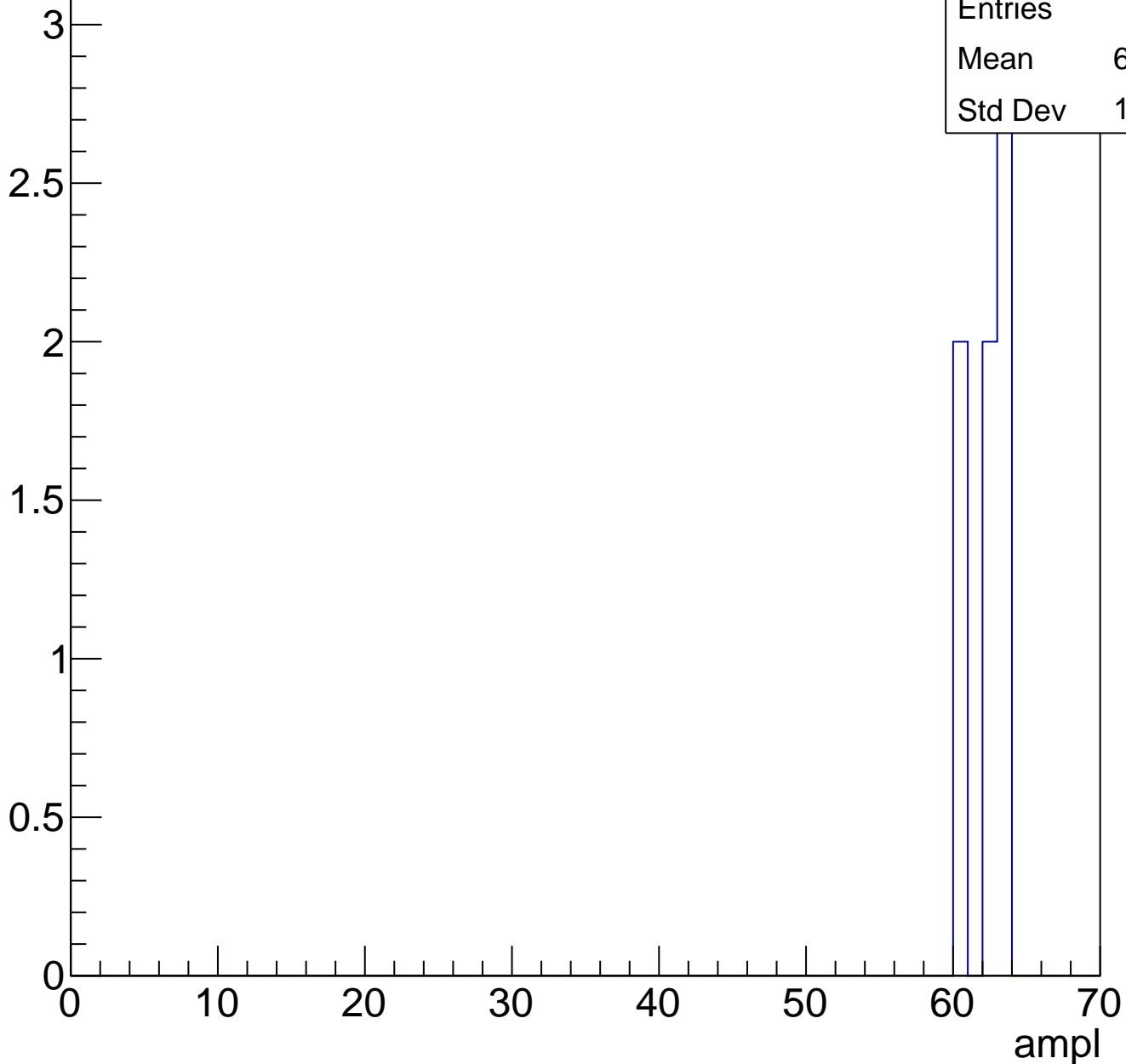
Entries	29
Mean	58.45
Std Dev	11.2



B0L001S, U25-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U25-ch55, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	31.39
Std Dev	3.843

Gaus mean : 31.5370

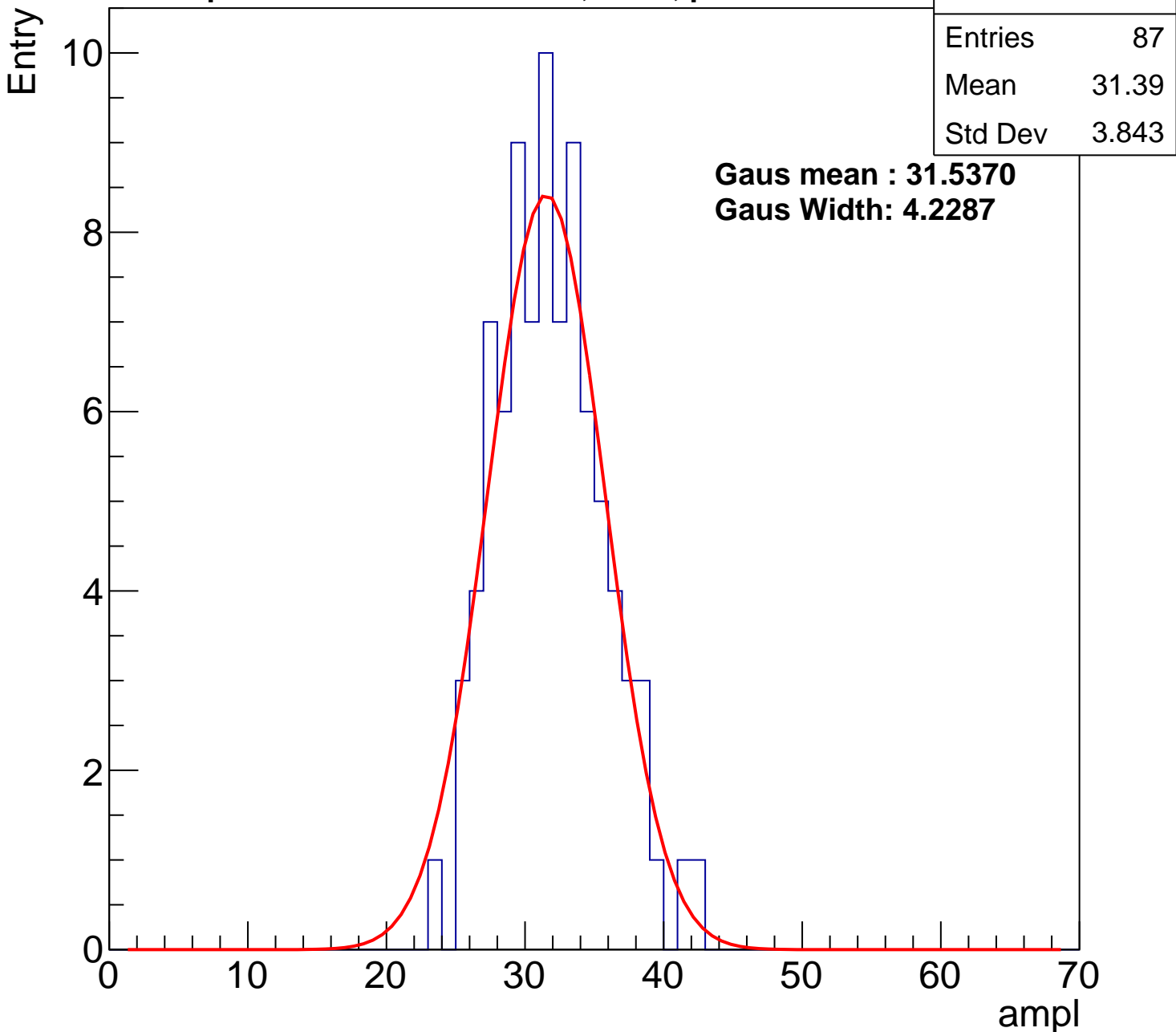
Gaus Width: 4.2287

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch55, adc1

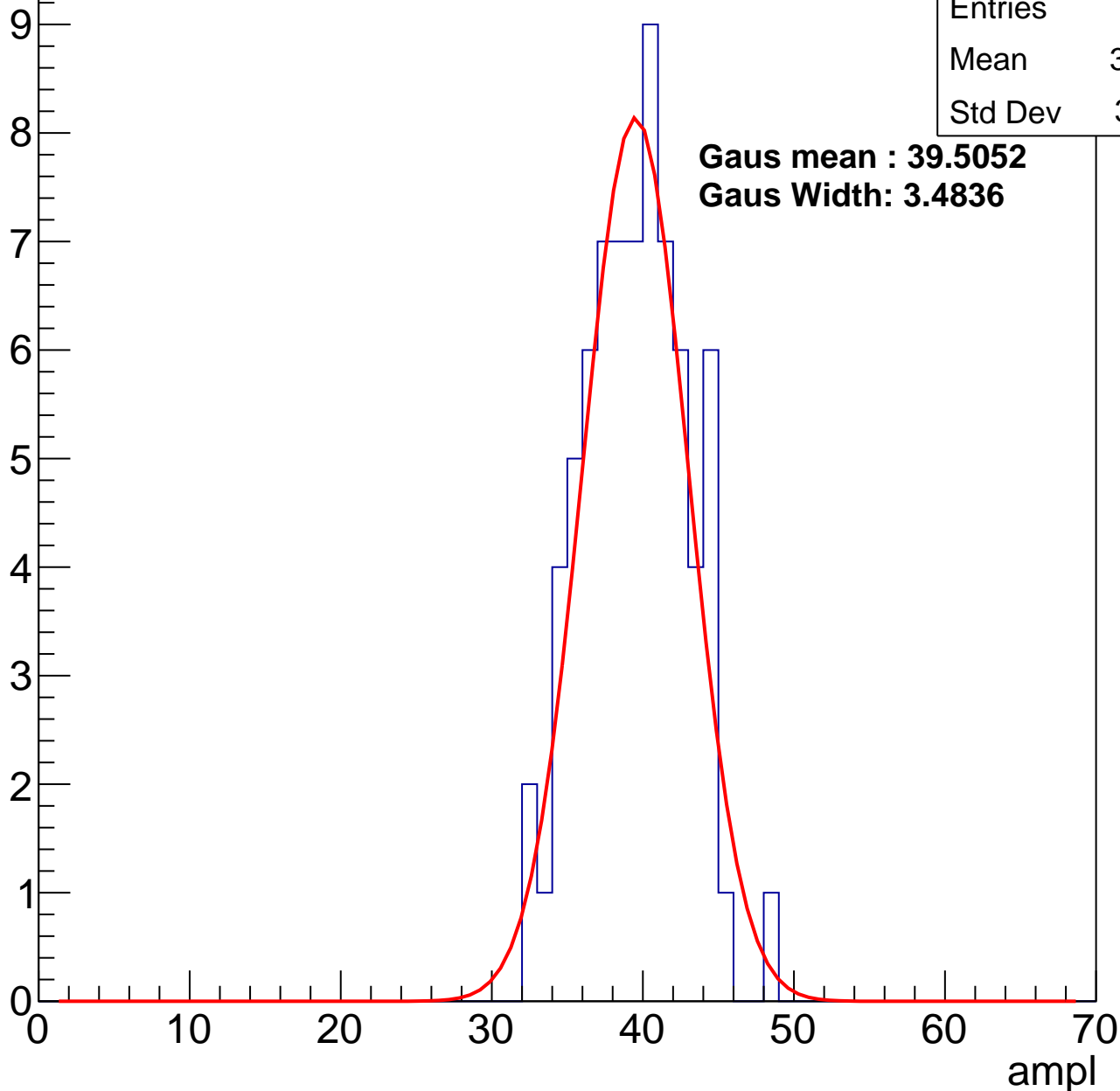
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	39.04
Std Dev	3.361

Gaus mean : 39.5052

Gaus Width: 3.4836



B0L001S, U25-ch55, adc2

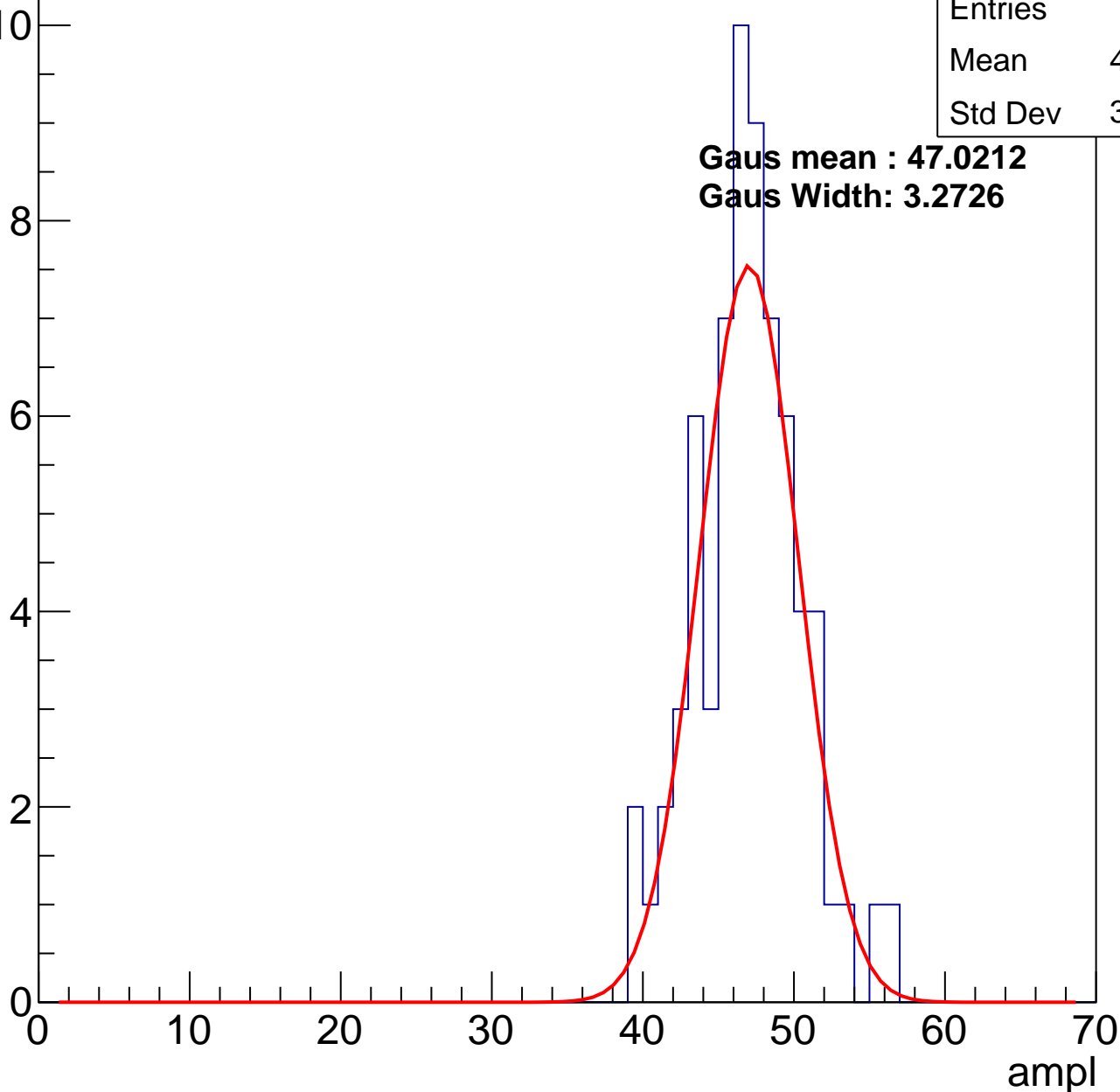
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	46.53
Std Dev	3.445

Gaus mean : 47.0212

Gaus Width: 3.2726

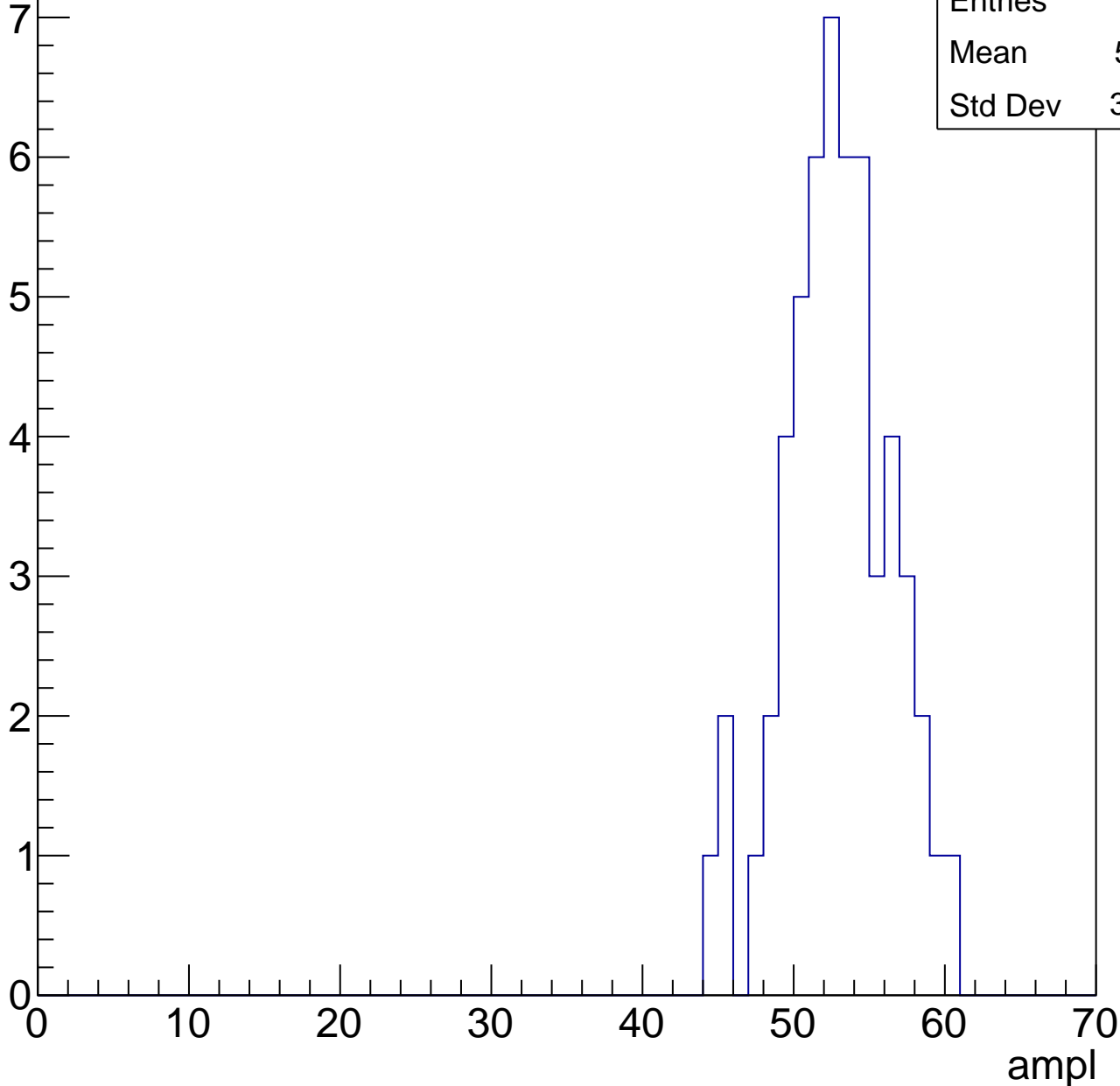


B0L001S, U25-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	52.41
Std Dev	3.478

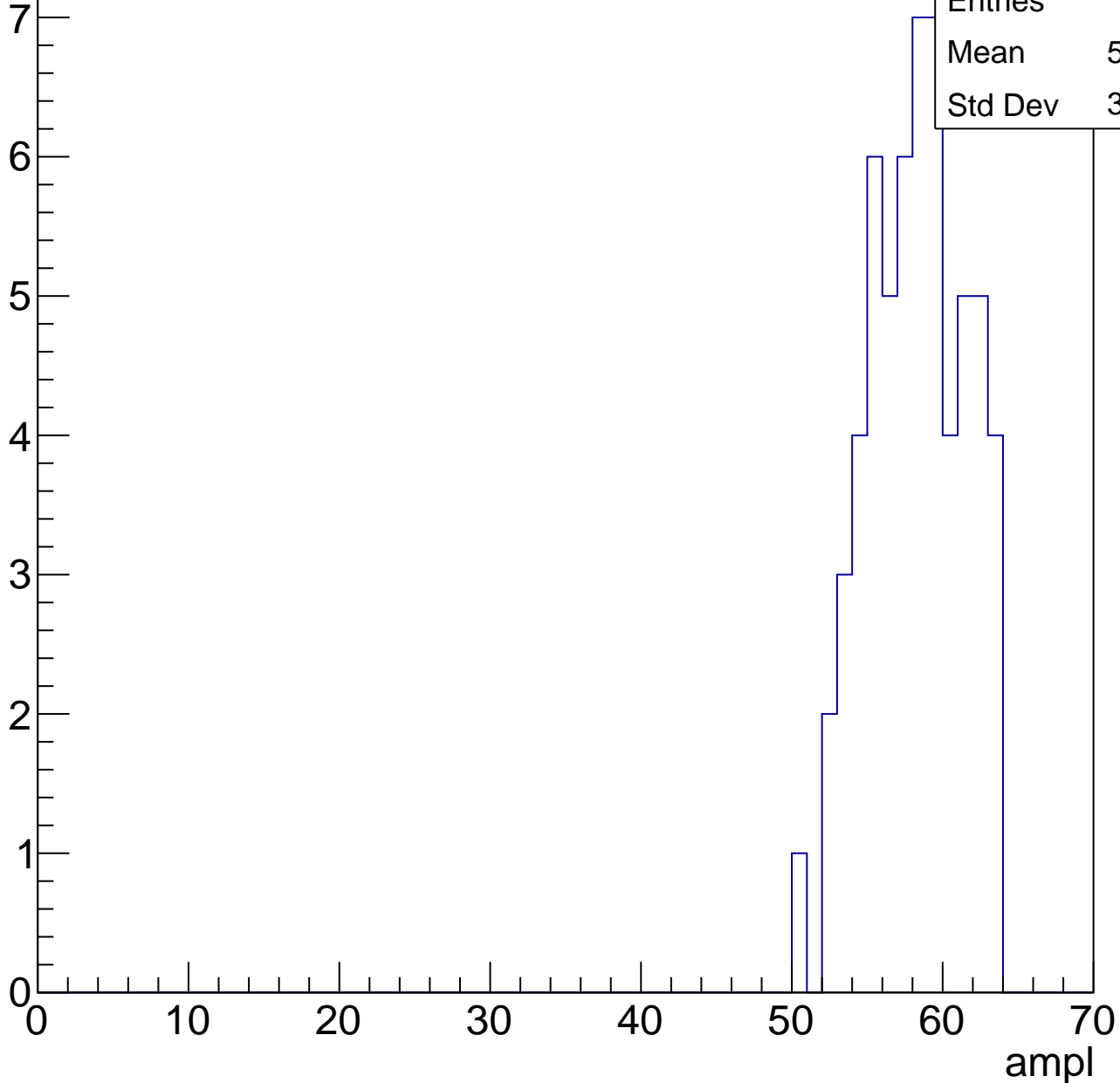


B0L001S, U25-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.75
Std Dev	3.187

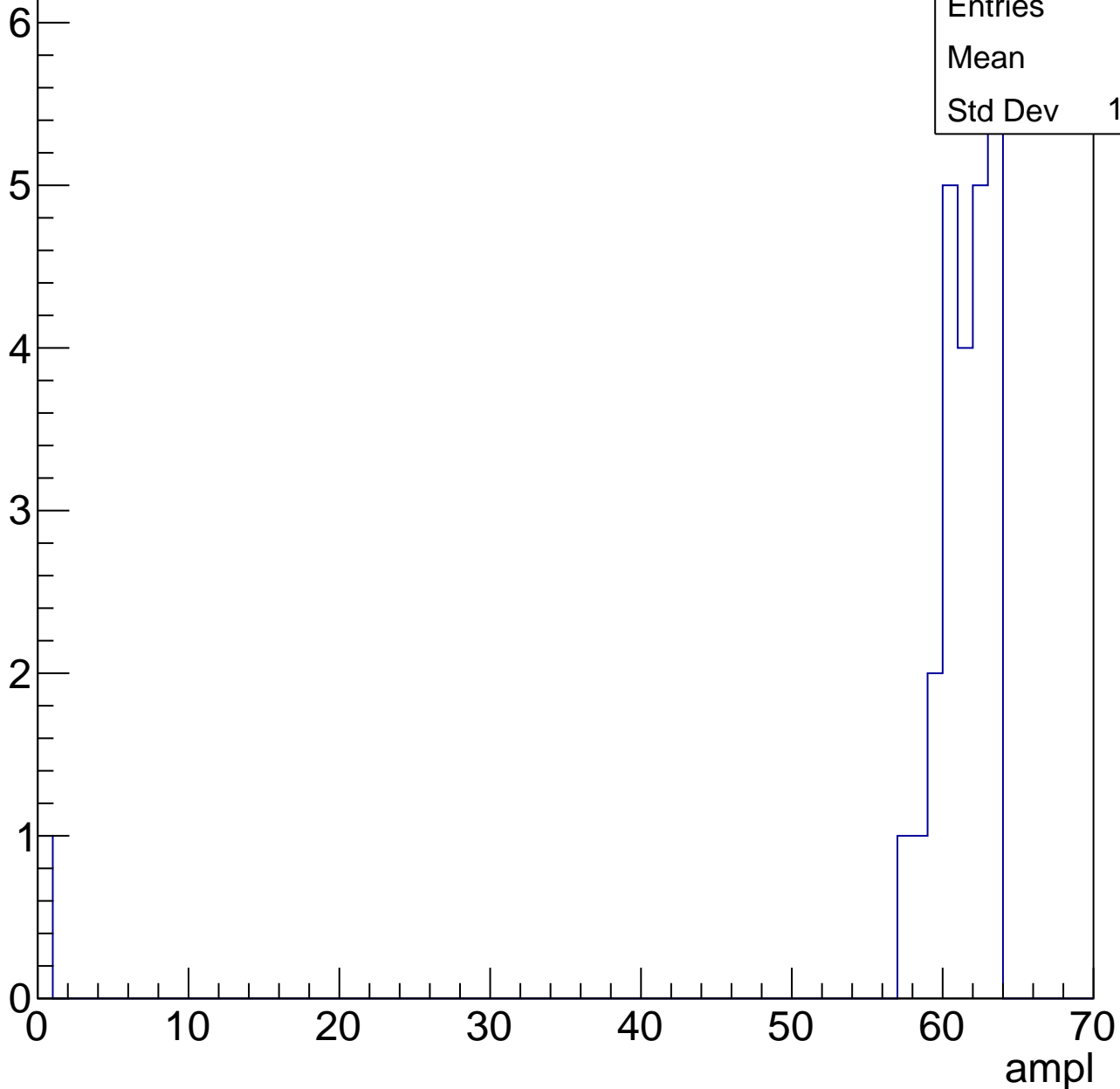


B0L001S, U25-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.6
Std Dev	12.07



B0L001S, U25-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch56, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	28.53
Std Dev	7.589

Gaus mean : 31.0209

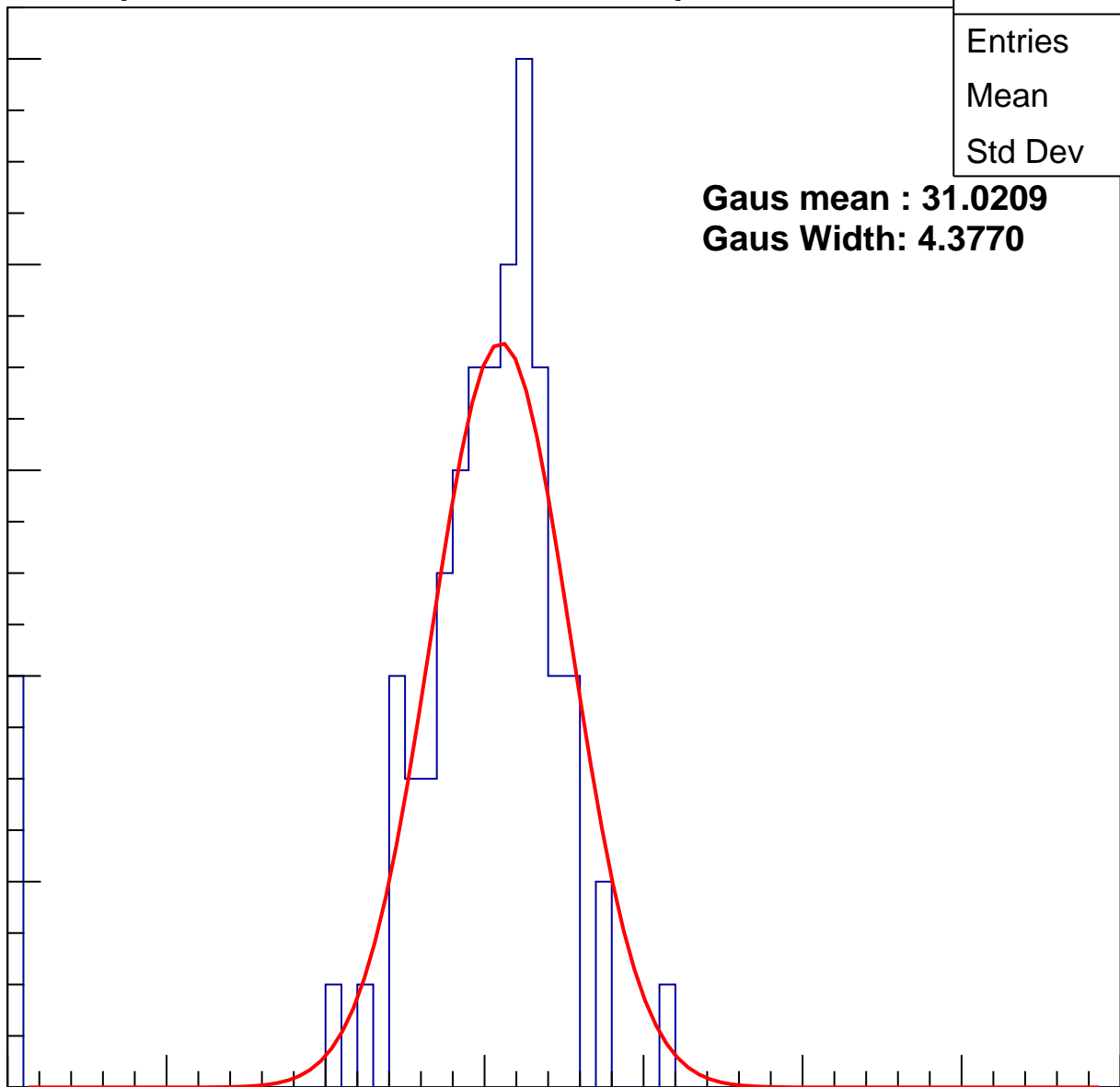
Gaus Width: 4.3770

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



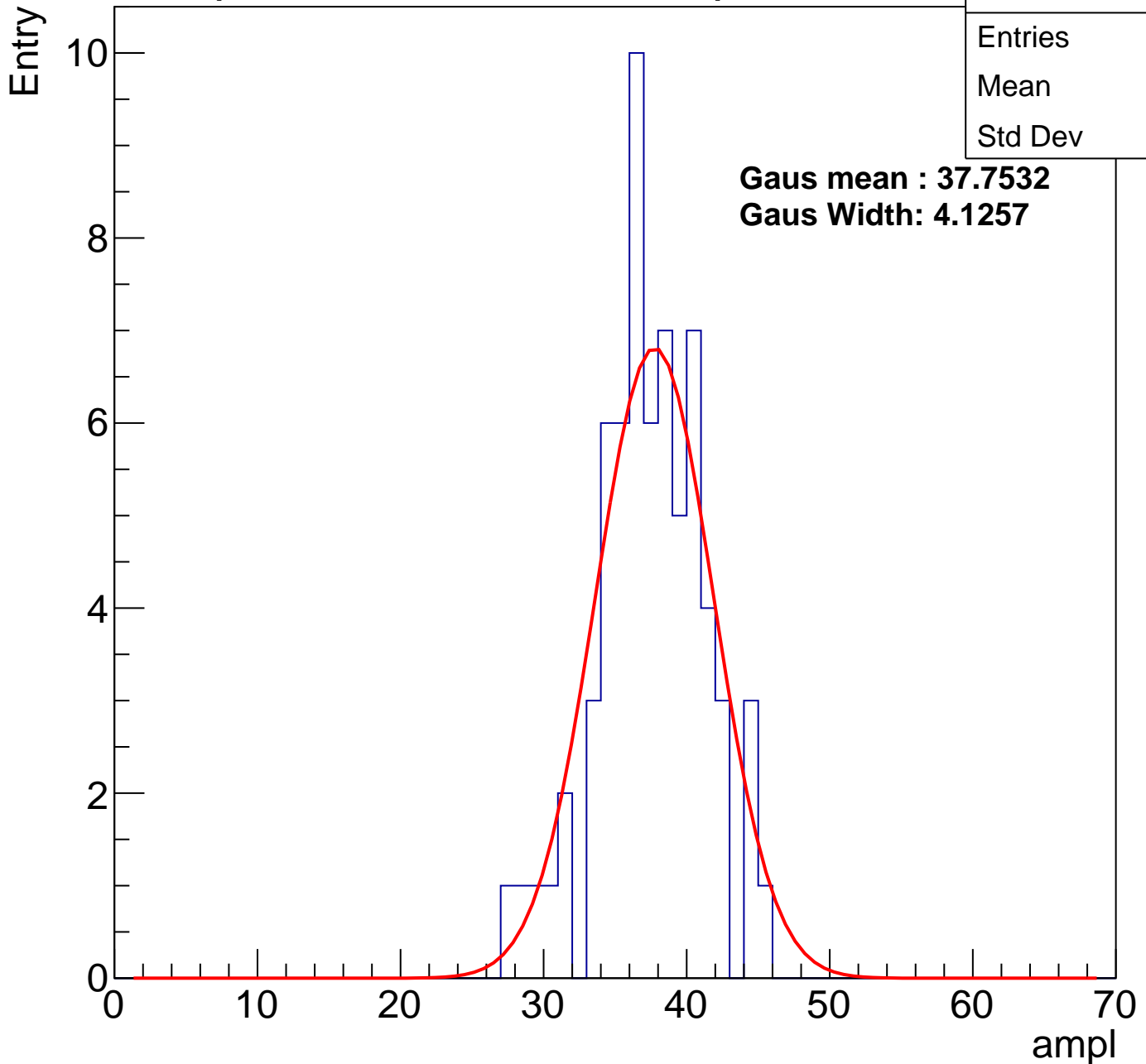
B0L001S, U25-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37
Std Dev	3.75

Gaus mean : 37.7532

Gaus Width: 4.1257



B0L001S, U25-ch56, adc2

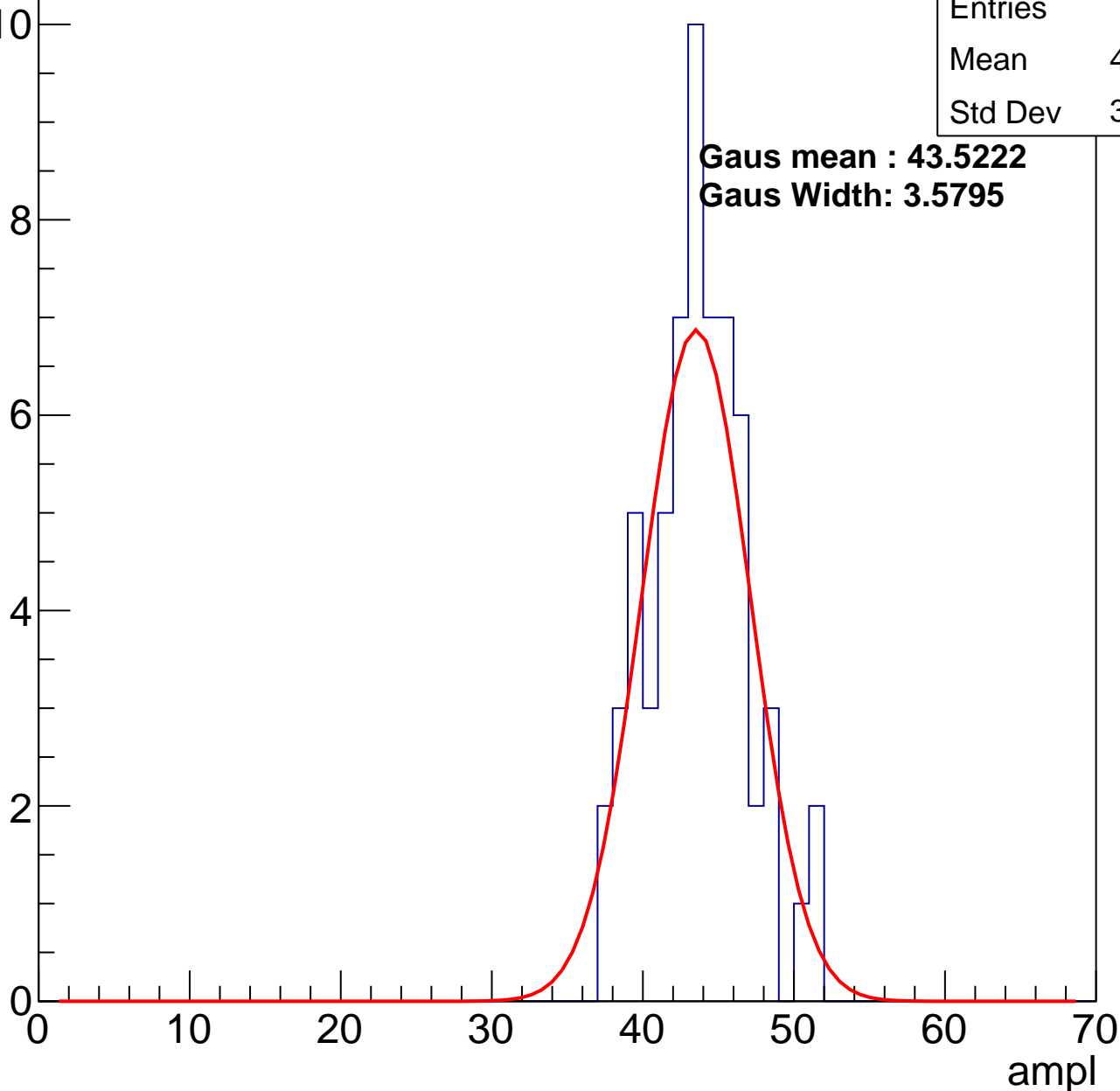
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.19
Std Dev	3.216

Gaus mean : 43.5222

Gaus Width: 3.5795

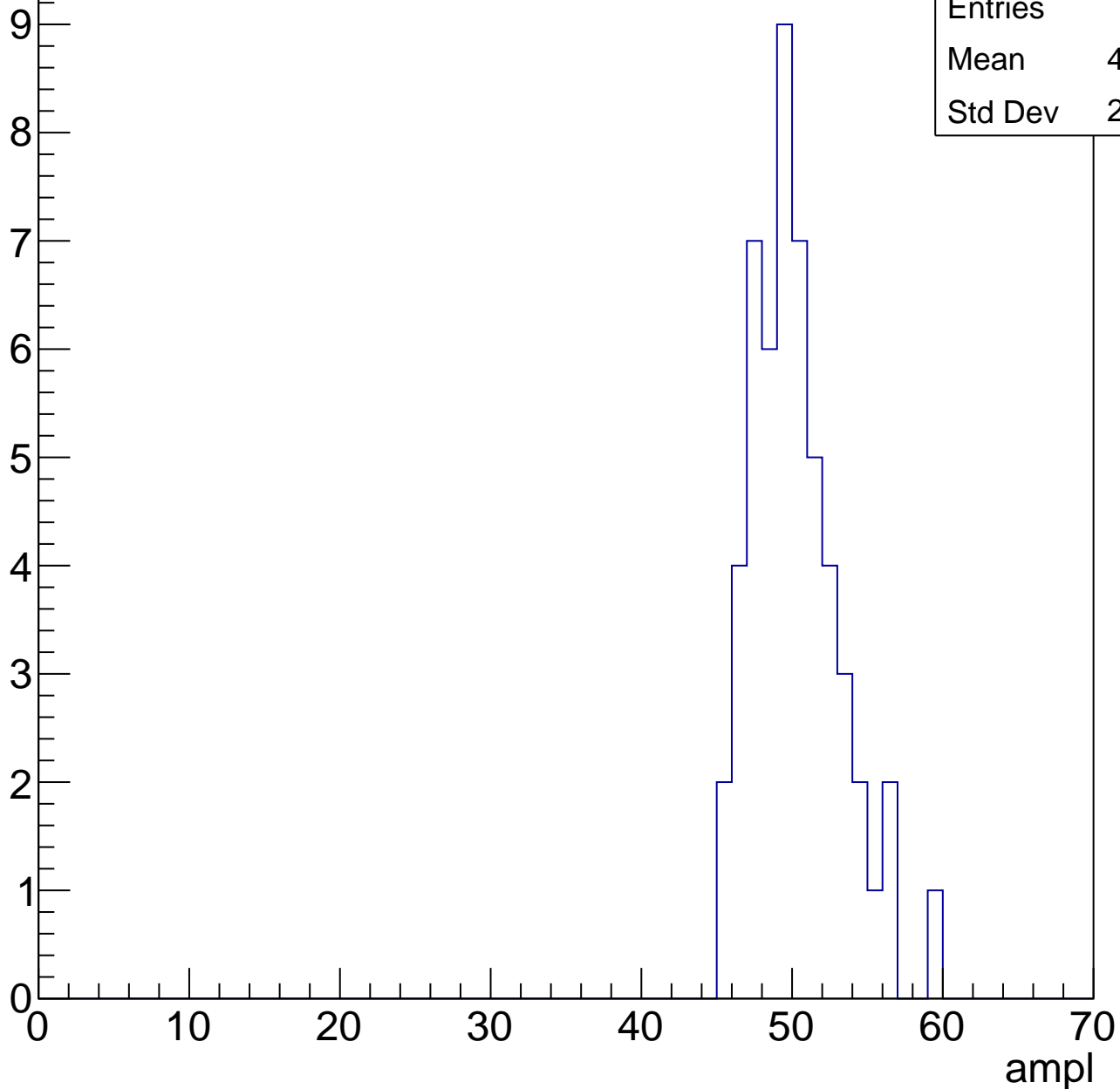


B0L001S, U25-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	49.77
Std Dev	2.963

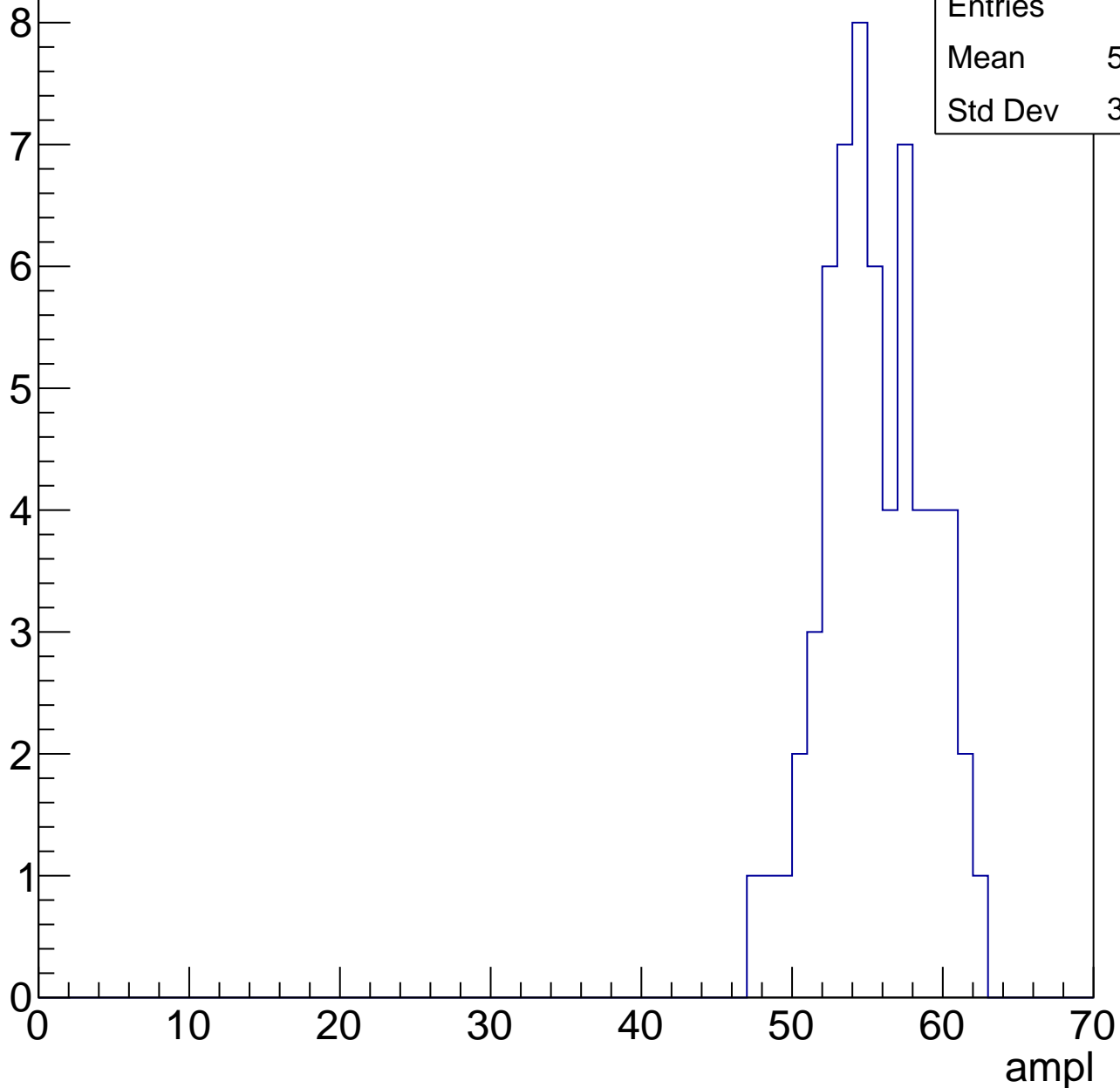


B0L001S, U25-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

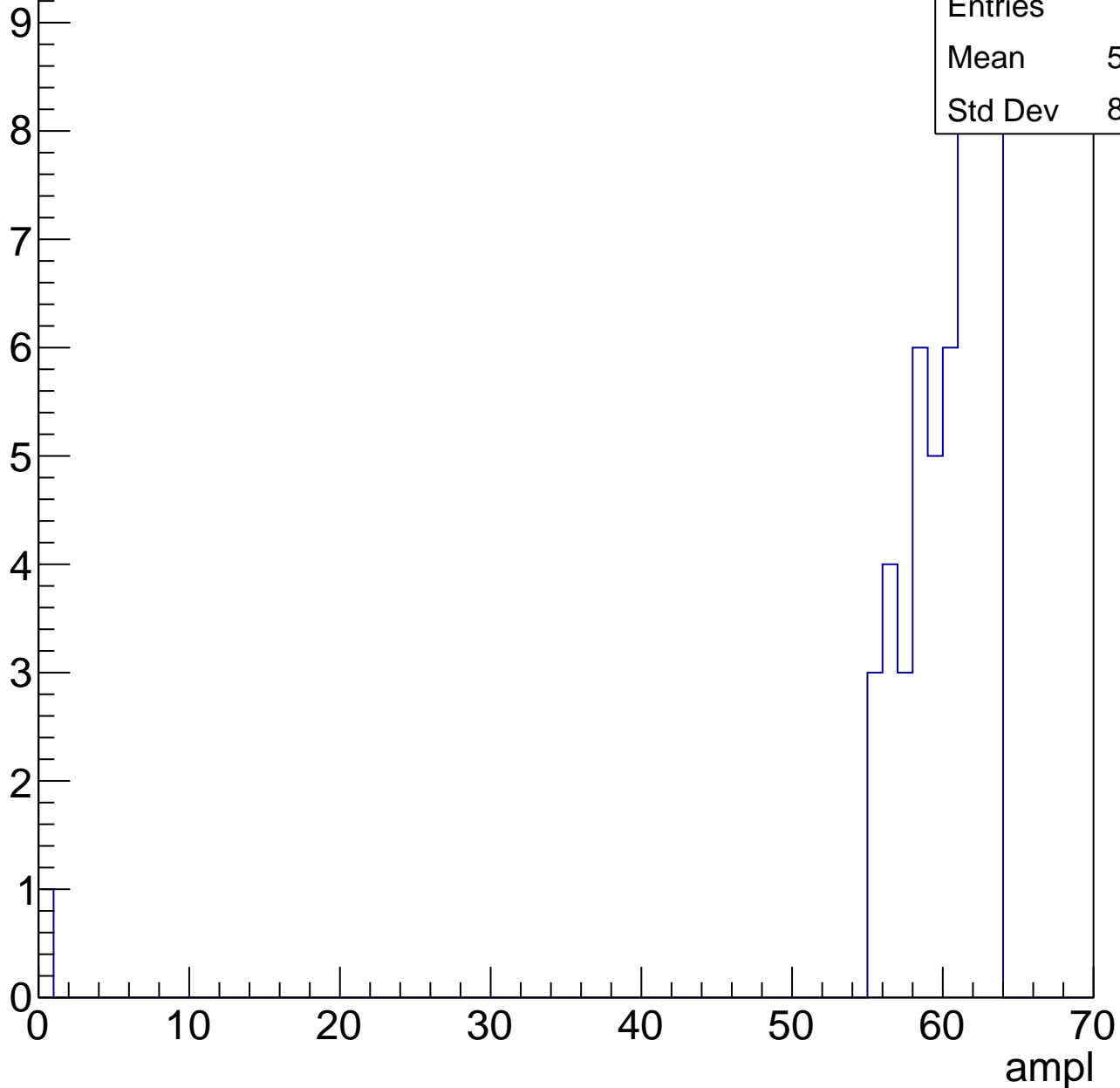
Entries	61
Mean	55.03
Std Dev	3.363



B0L001S, U25-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch57, adc0

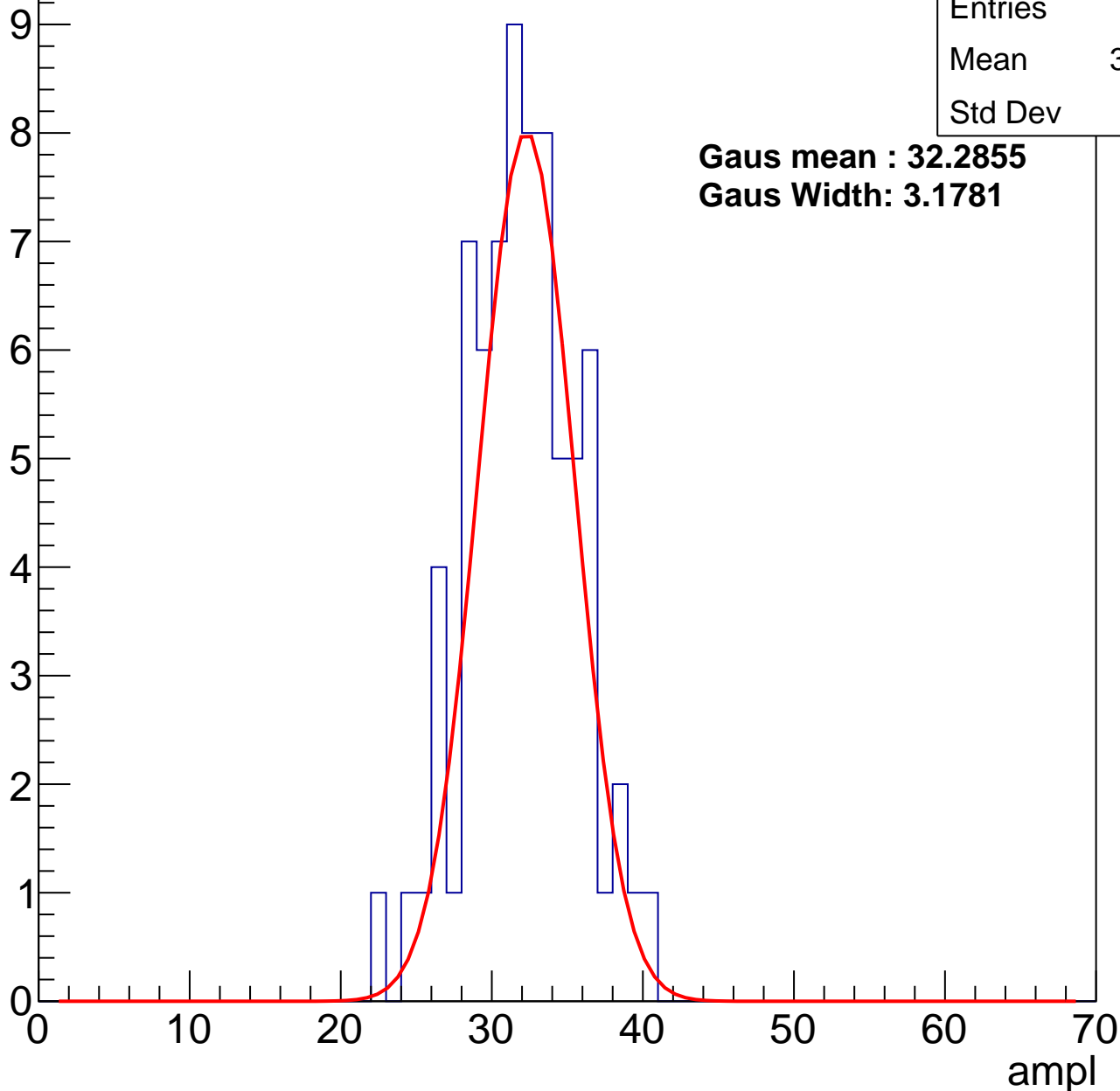
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	31.54
Std Dev	3.58

Gaus mean : 32.2855

Gaus Width: 3.1781



B0L001S, U25-ch57, adc1

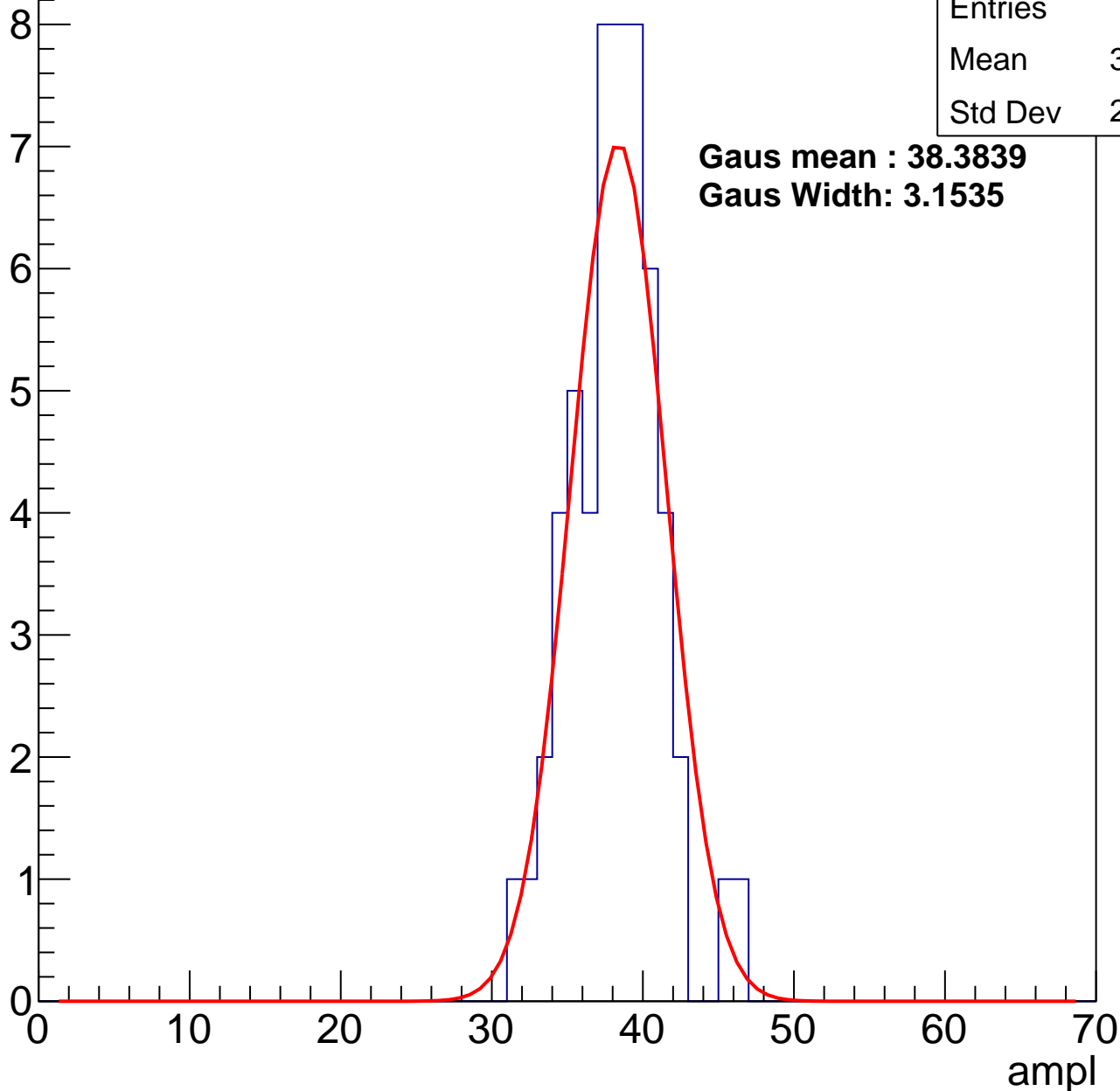
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.73
Std Dev	2.945

Gaus mean : 38.3839

Gaus Width: 3.1535



B0L001S, U25-ch57, adc2

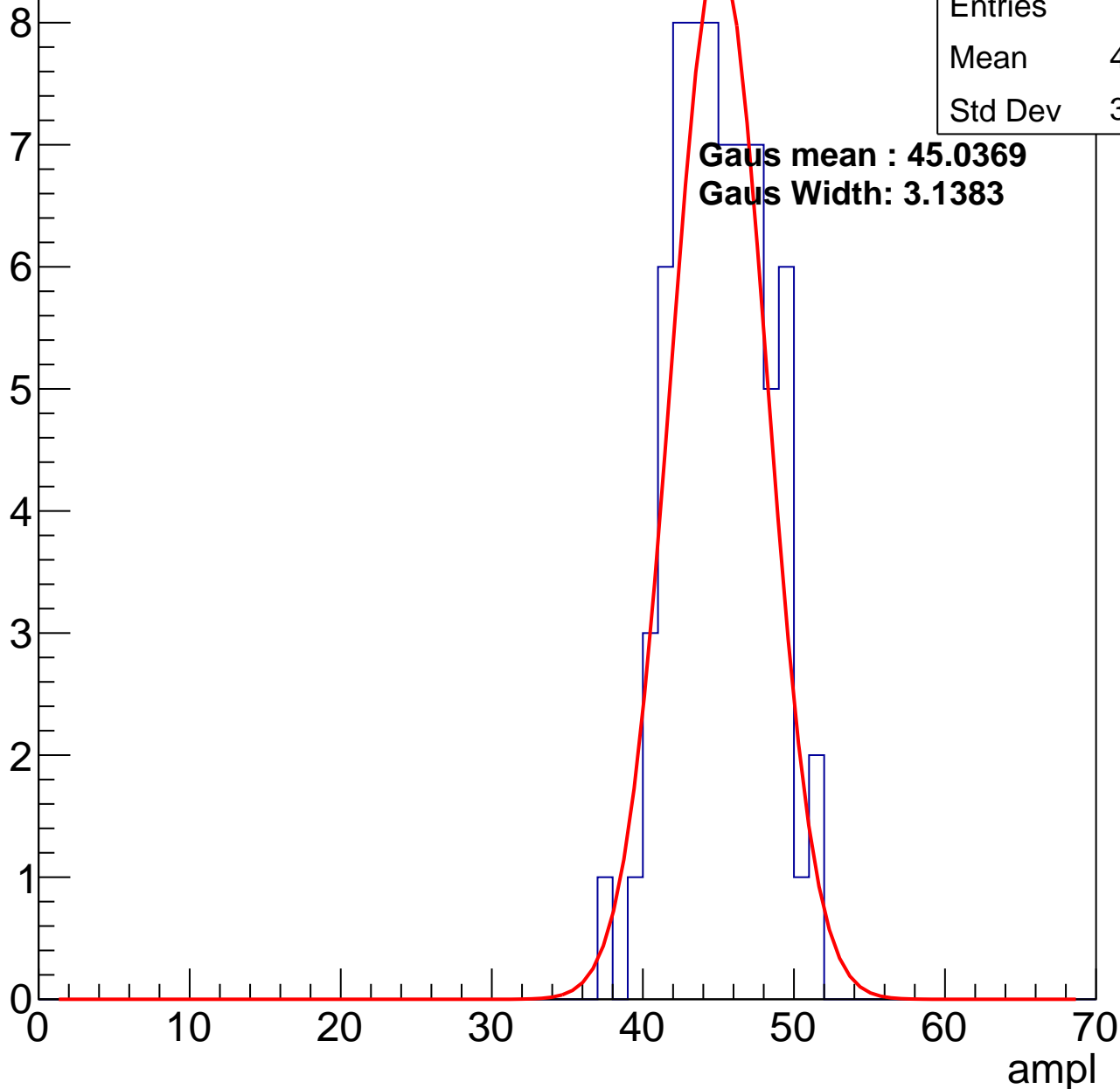
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.66
Std Dev	3.042

Gaus mean : 45.0369

Gaus Width: 3.1383



B0L001S, U25-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	50.97
Std Dev	3.622

Entry

10

8

6

4

2

0

0

10

20

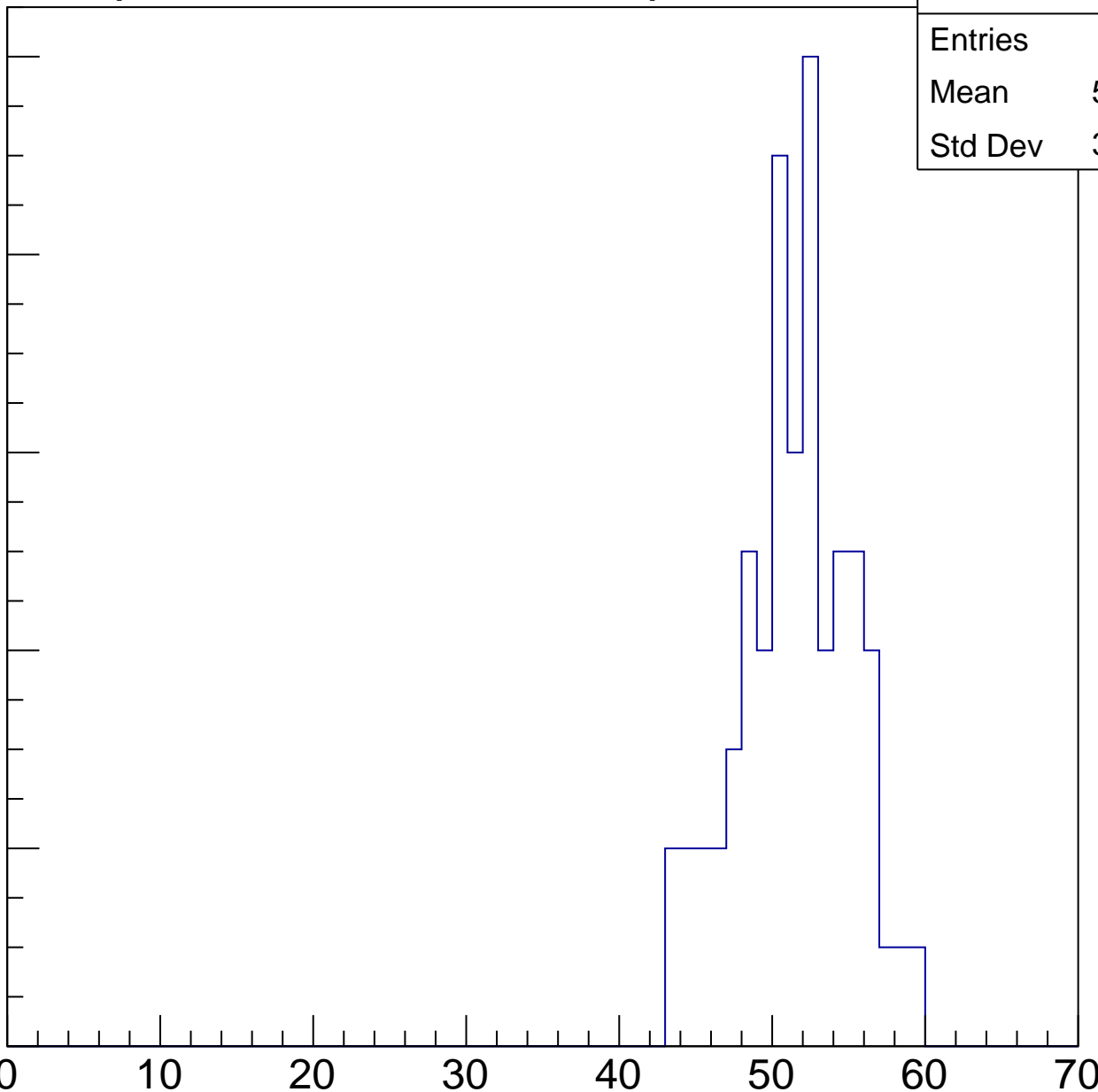
30

40

50

60

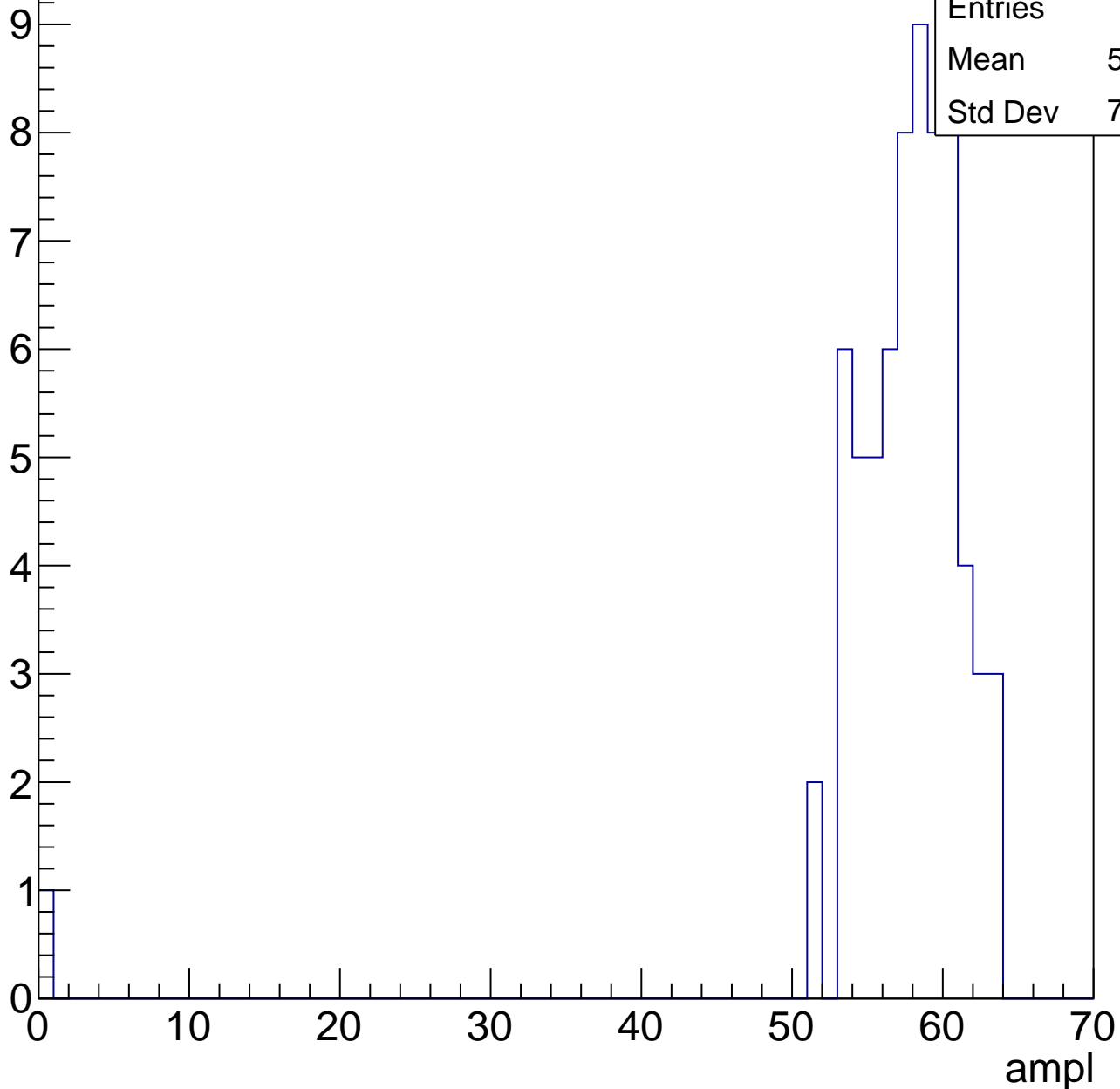
ampl



B0L001S, U25-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

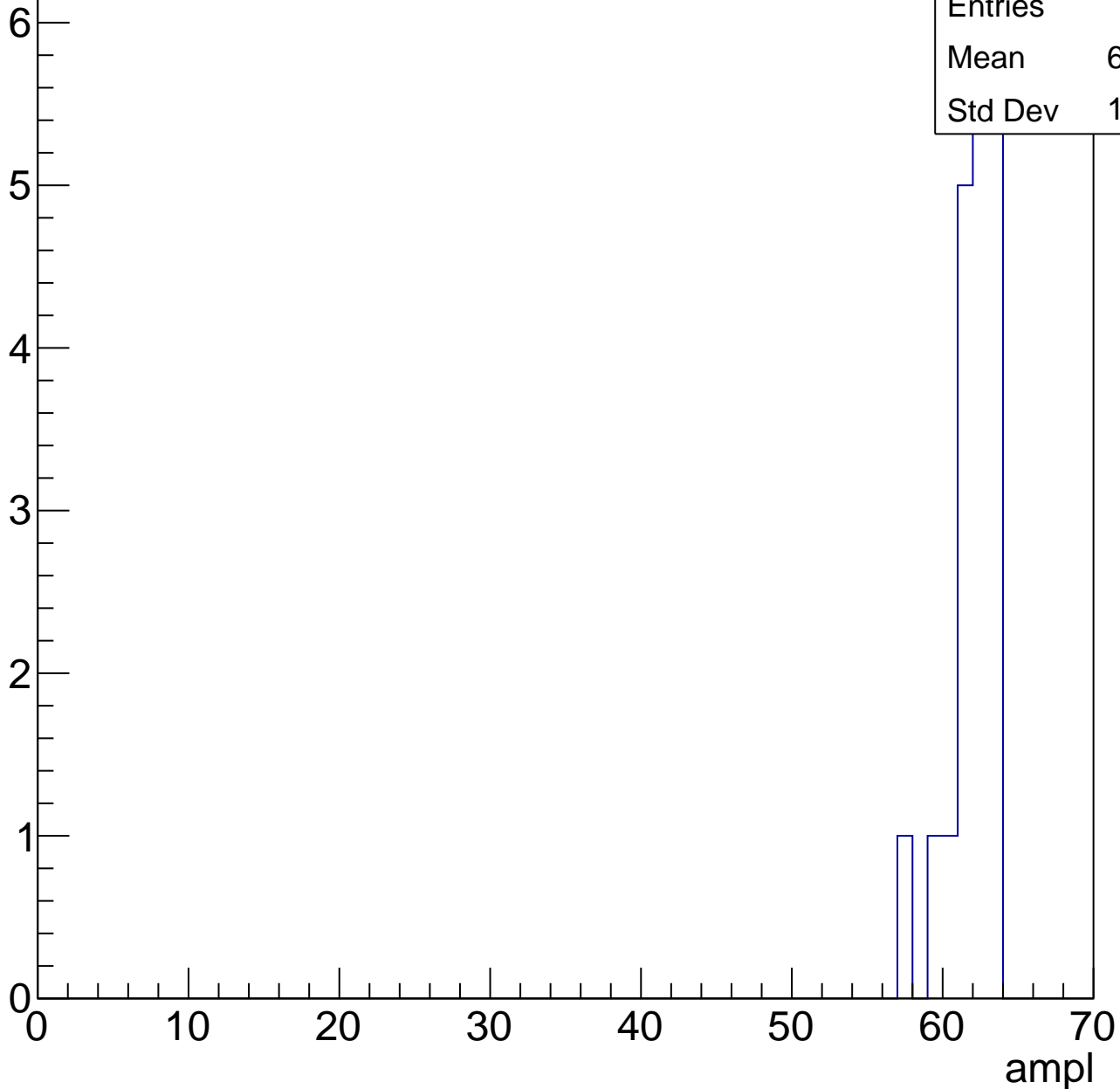


B0L001S, U25-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	20
Mean	61.55
Std Dev	1.499



B0L001S, U25-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch58, adc0

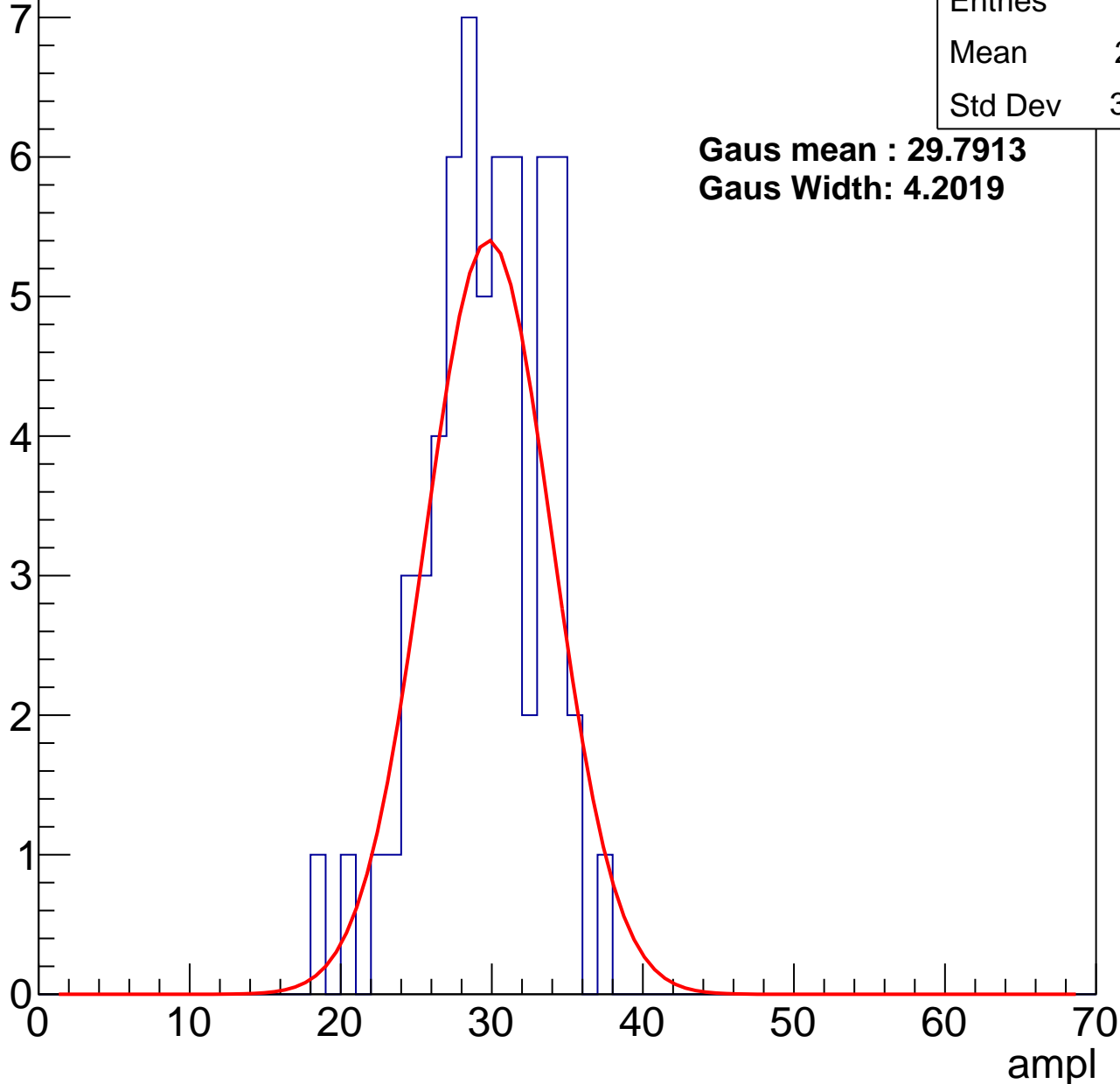
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	29.11
Std Dev	3.854

Gaus mean : 29.7913

Gaus Width: 4.2019



B0L001S, U25-ch58, adc1

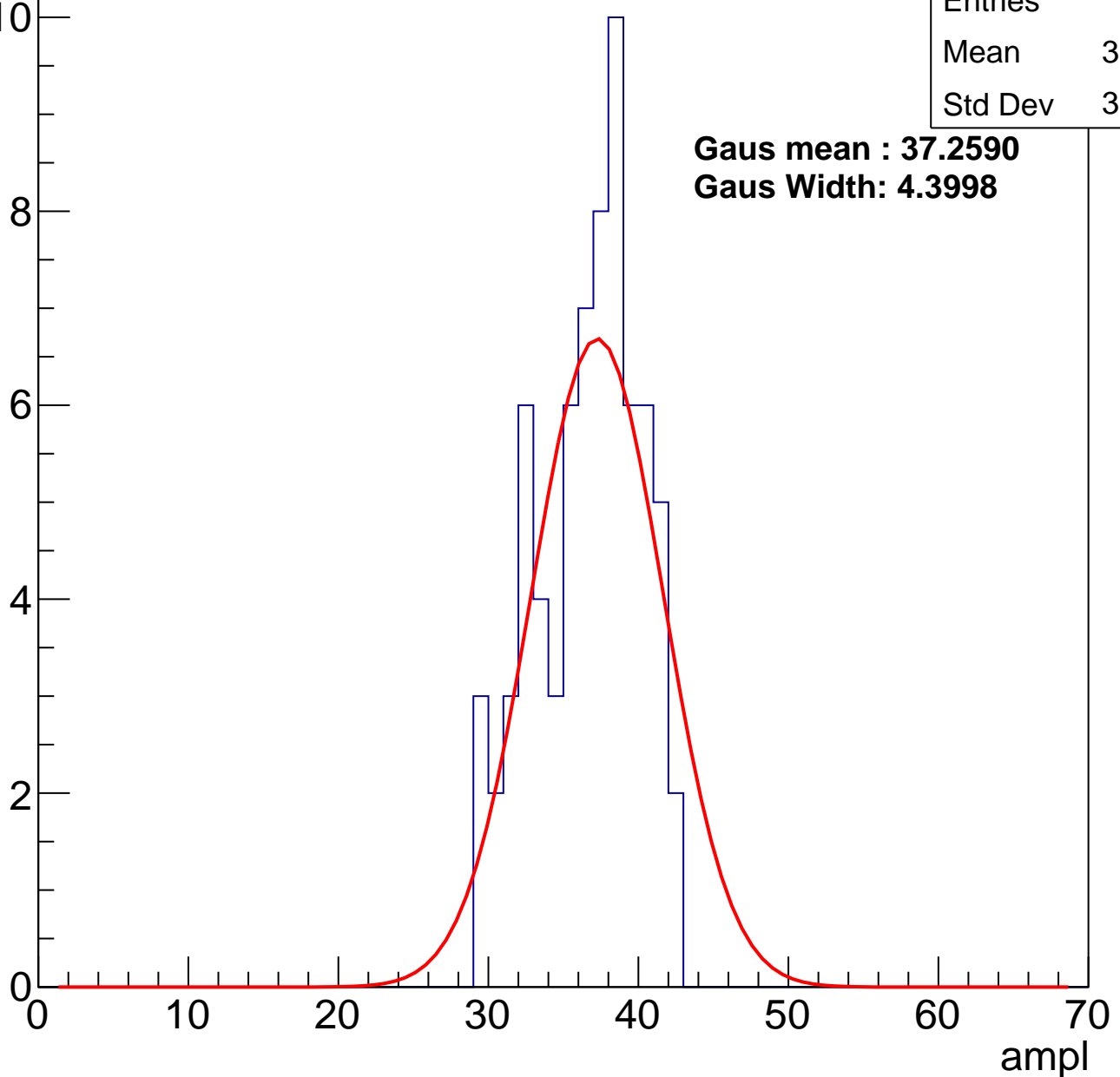
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.15
Std Dev	3.434

Gaus mean : 37.2590

Gaus Width: 4.3998



B0L001S, U25-ch58, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	43.45
Std Dev	3.877

Gaus mean : 44.1687

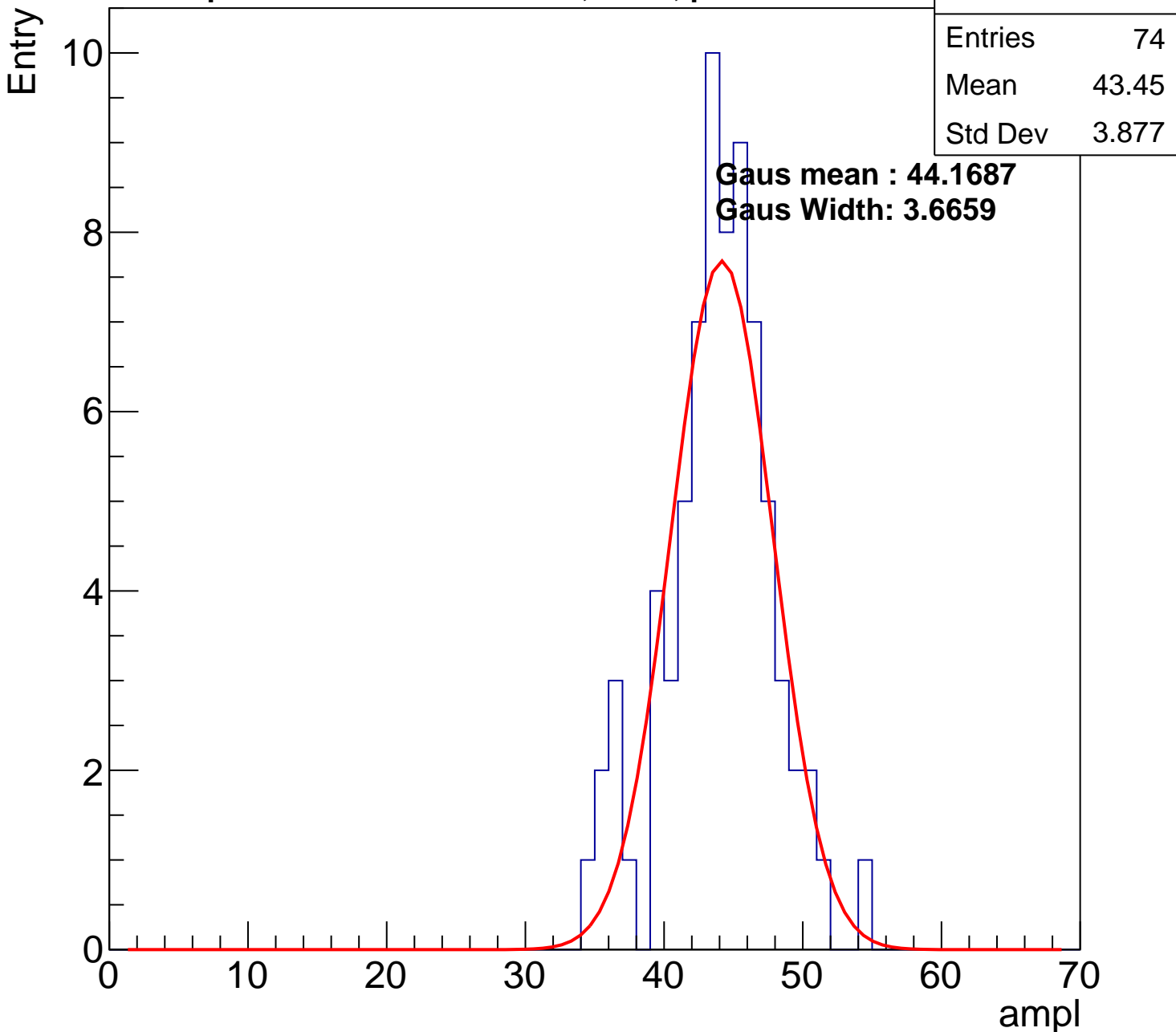
Gaus Width: 3.6659

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

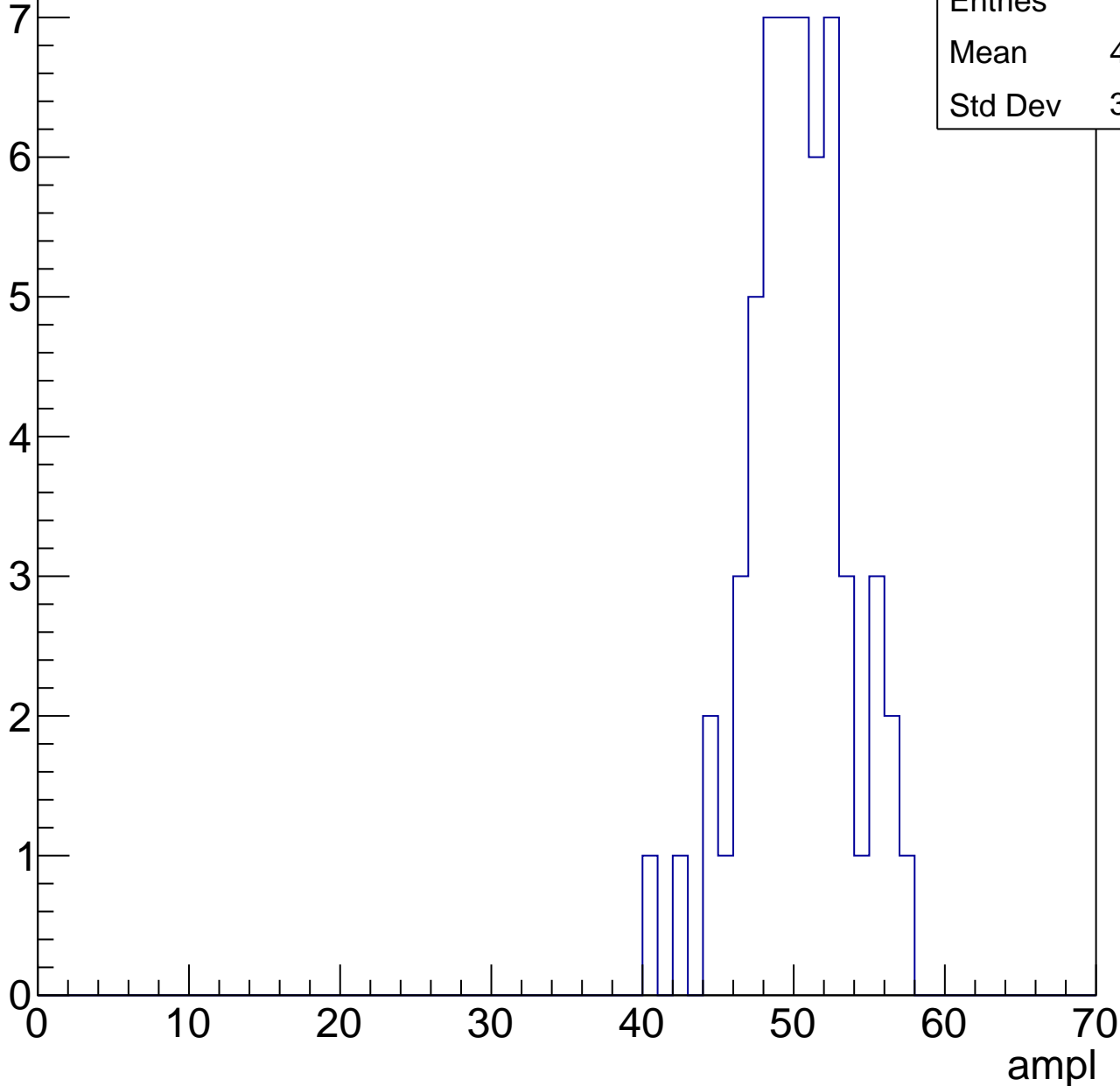


B0L001S, U25-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

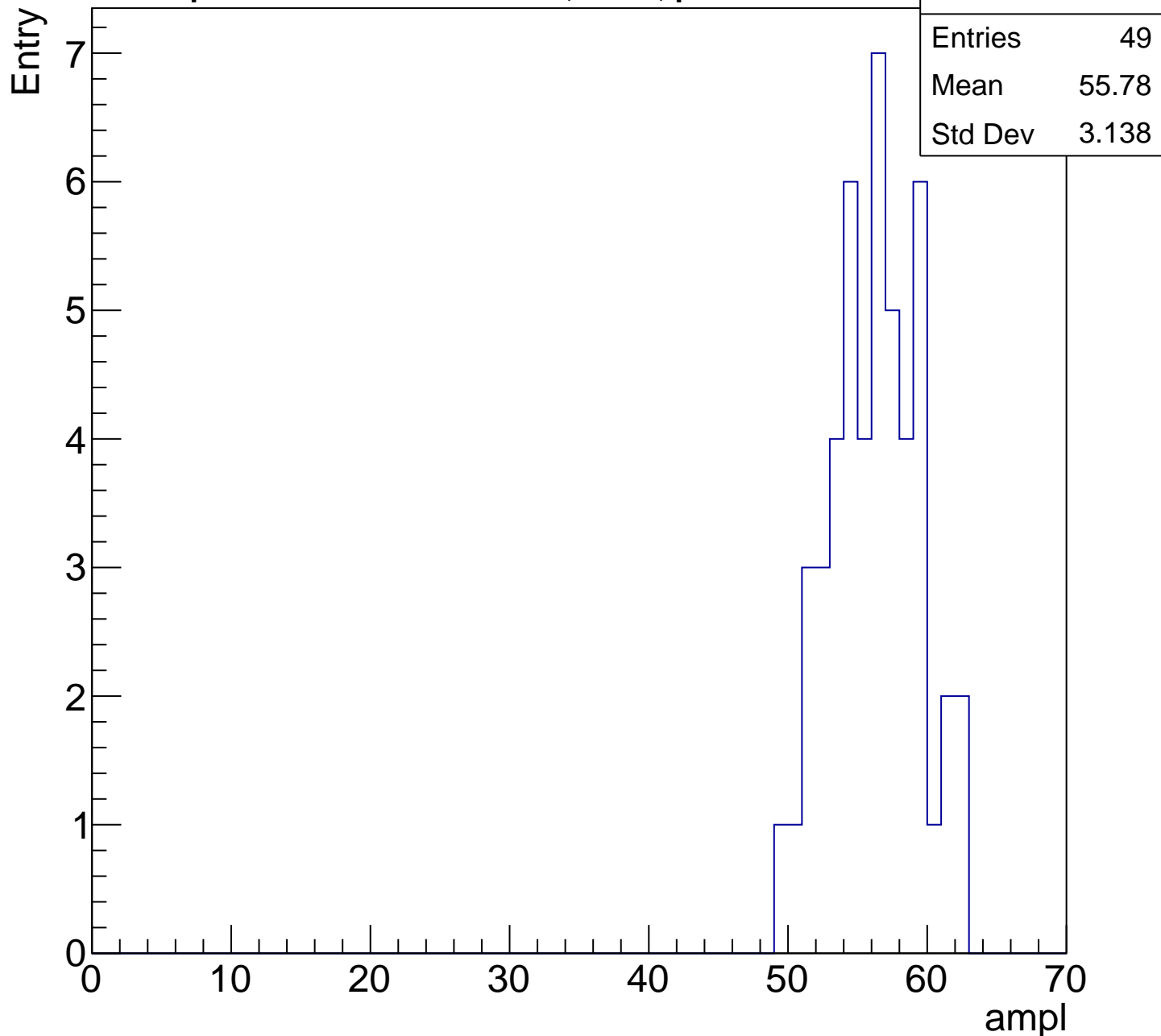
Entry

Entries	57
Mean	49.72
Std Dev	3.396



B0L001S, U25-ch58, adc4

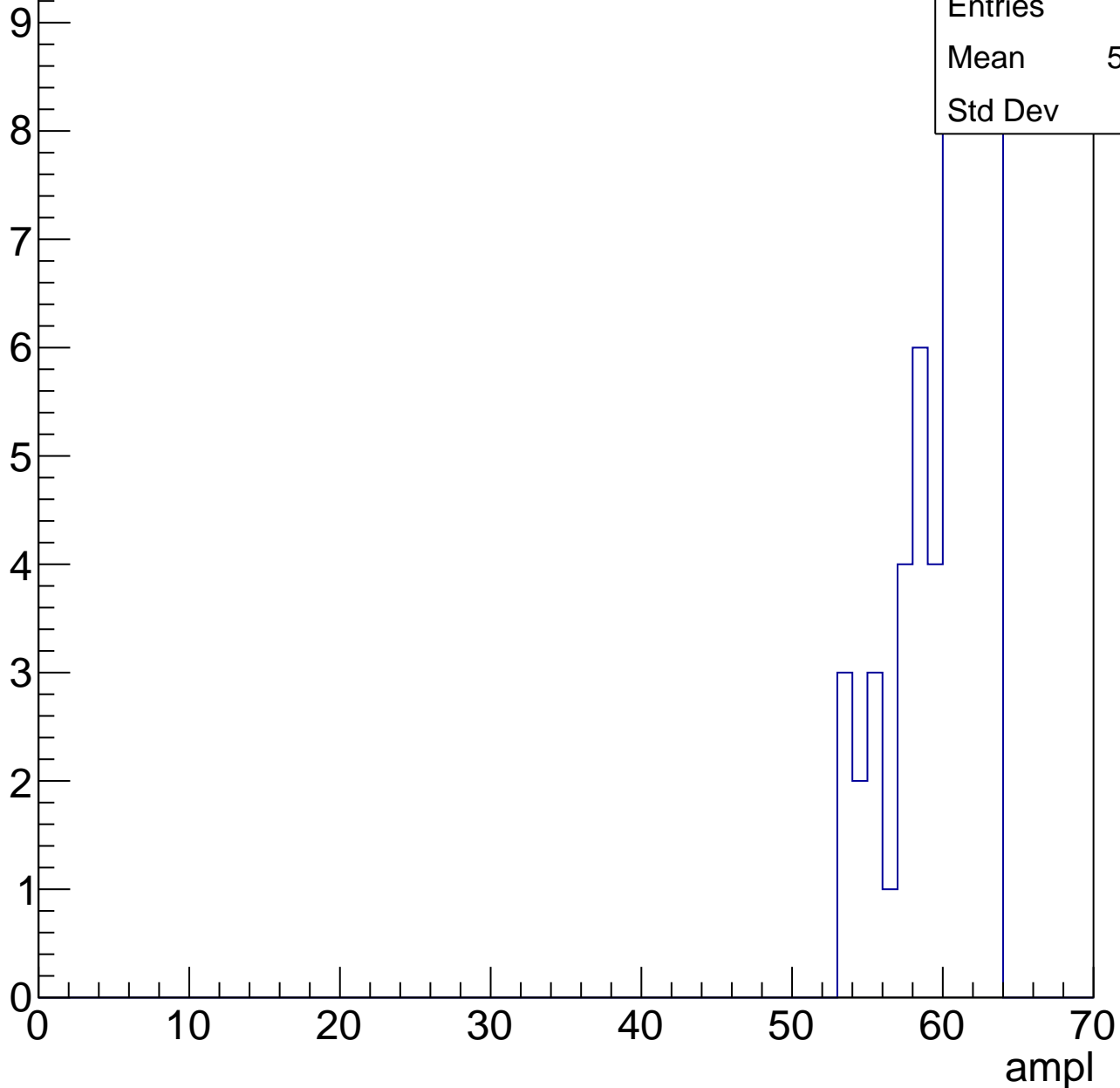
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch59, adc0

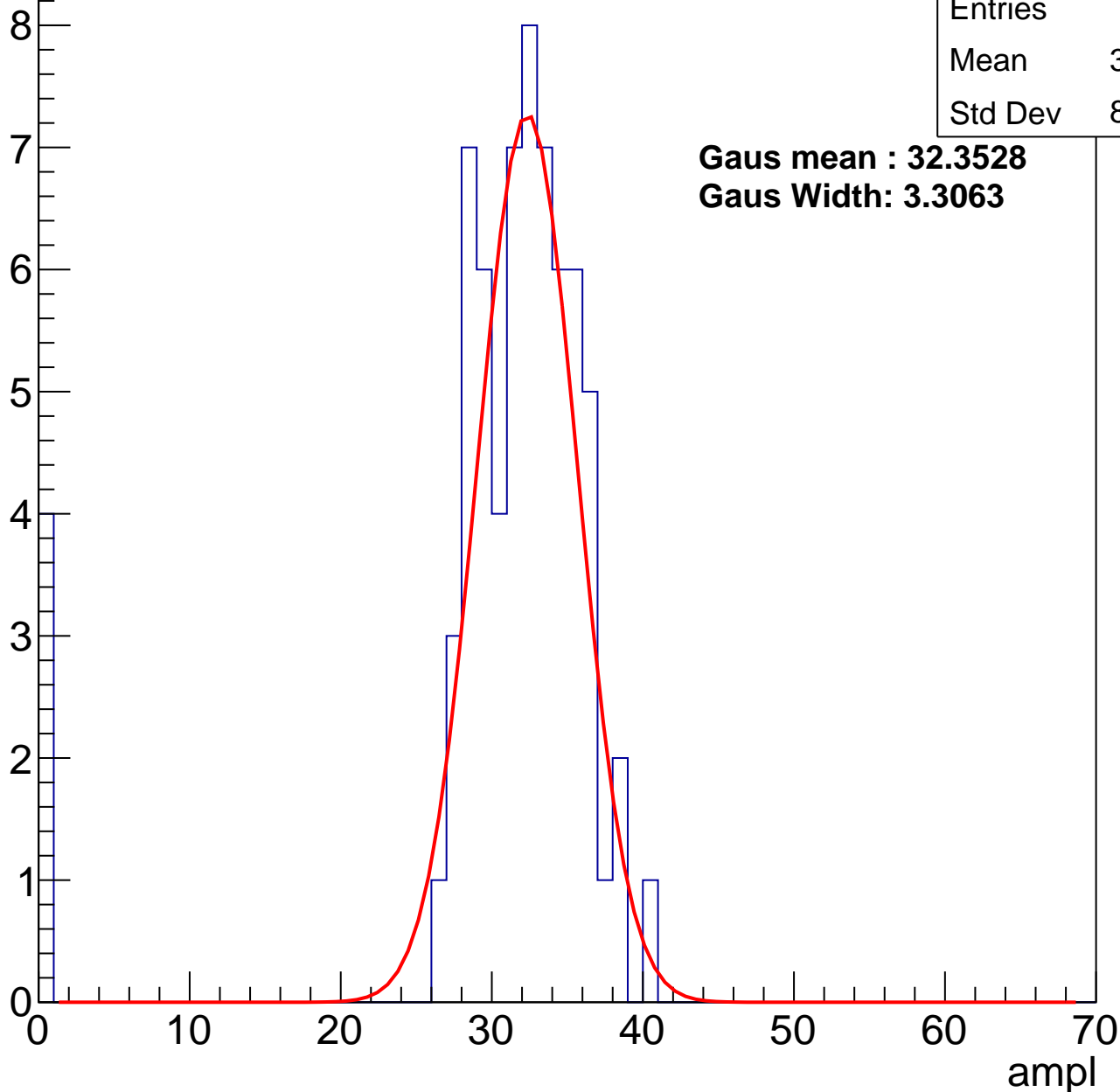
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.12
Std Dev	8.118

Gaus mean : 32.3528

Gaus Width: 3.3063



B0L001S, U25-ch59, adc1

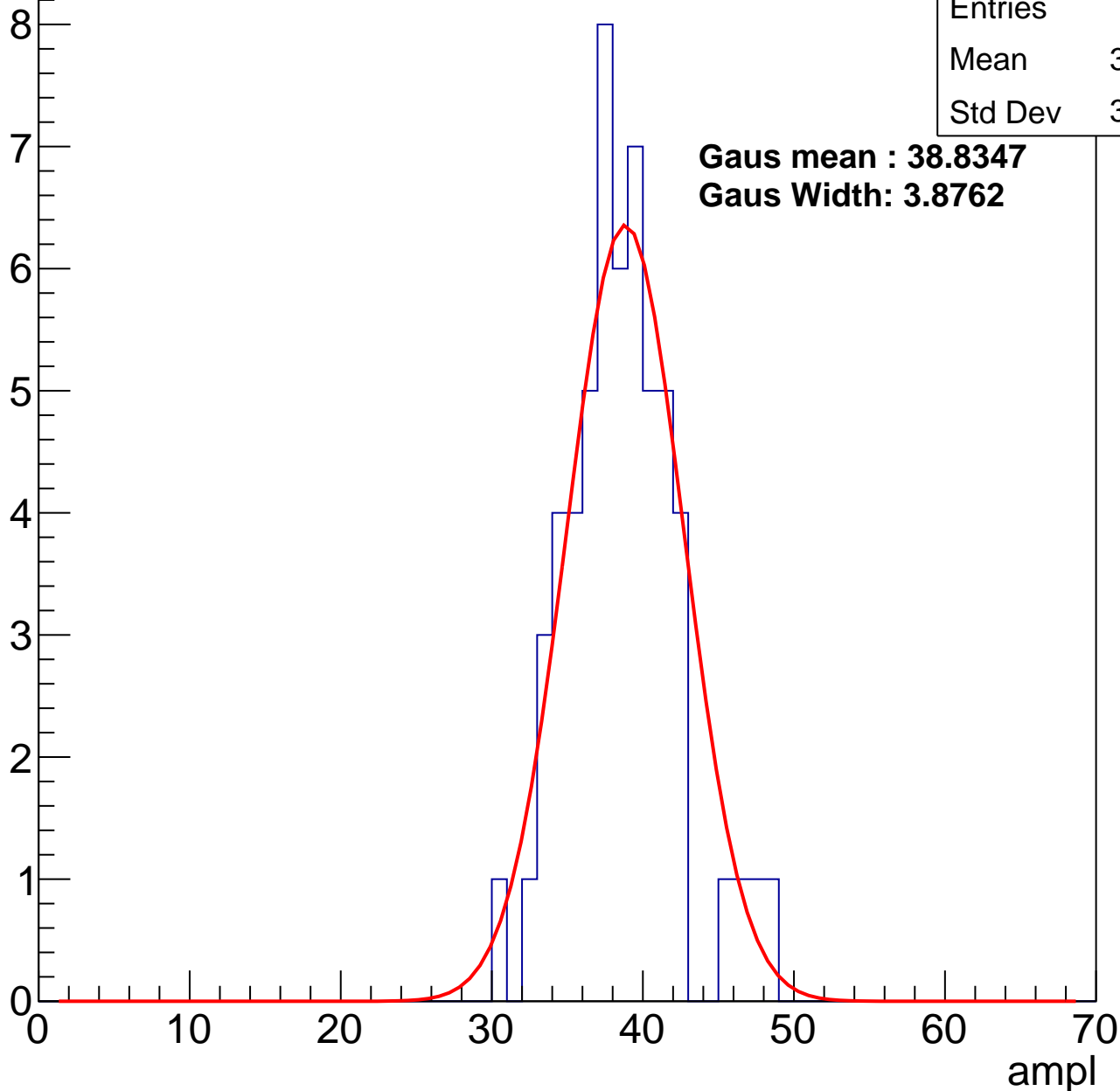
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	38.12
Std Dev	3.584

Gaus mean : 38.8347

Gaus Width: 3.8762



B0L001S, U25-ch59, adc2

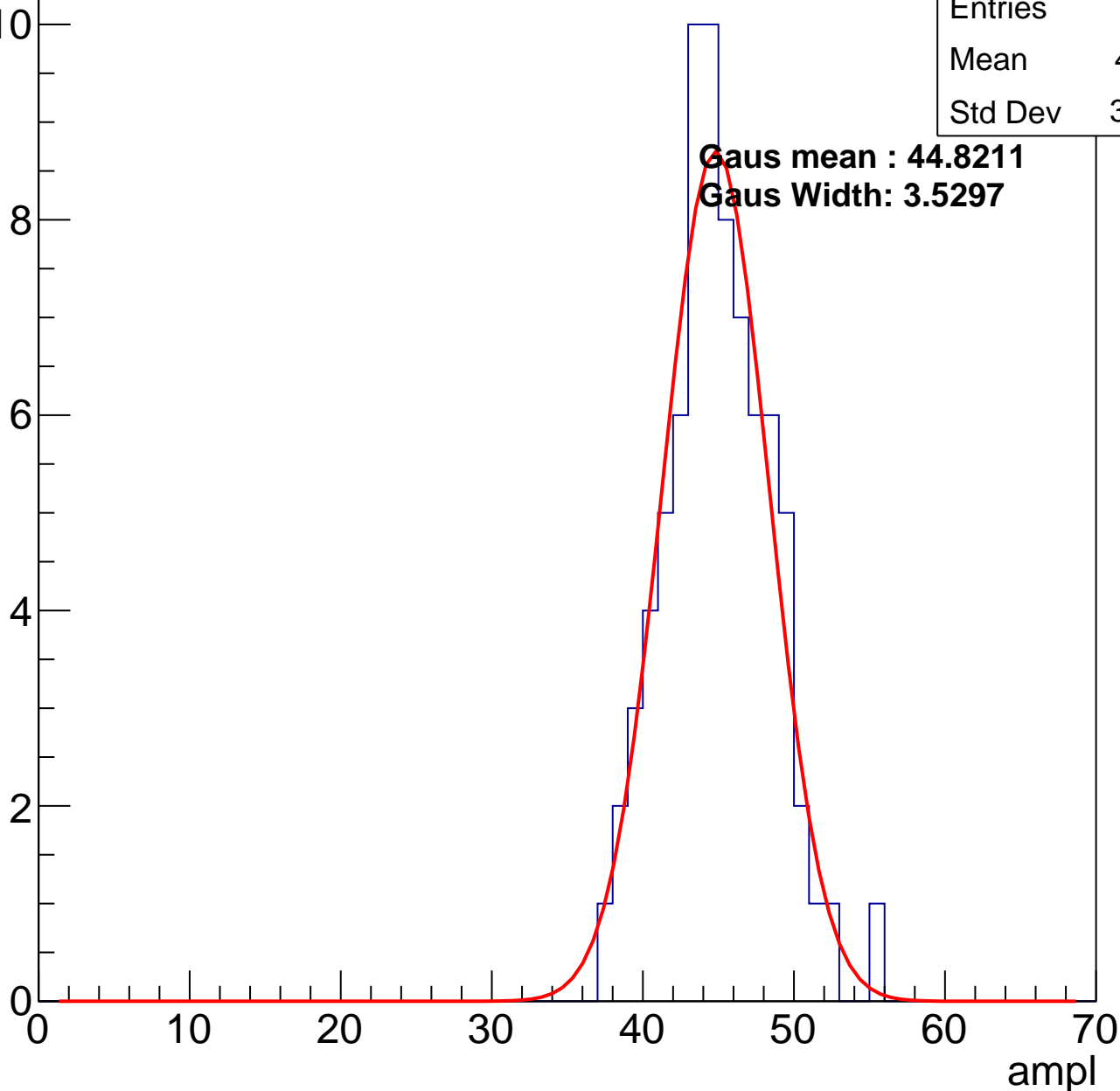
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	44.51
Std Dev	3.463

Gaus mean : 44.8211

Gaus Width: 3.5297

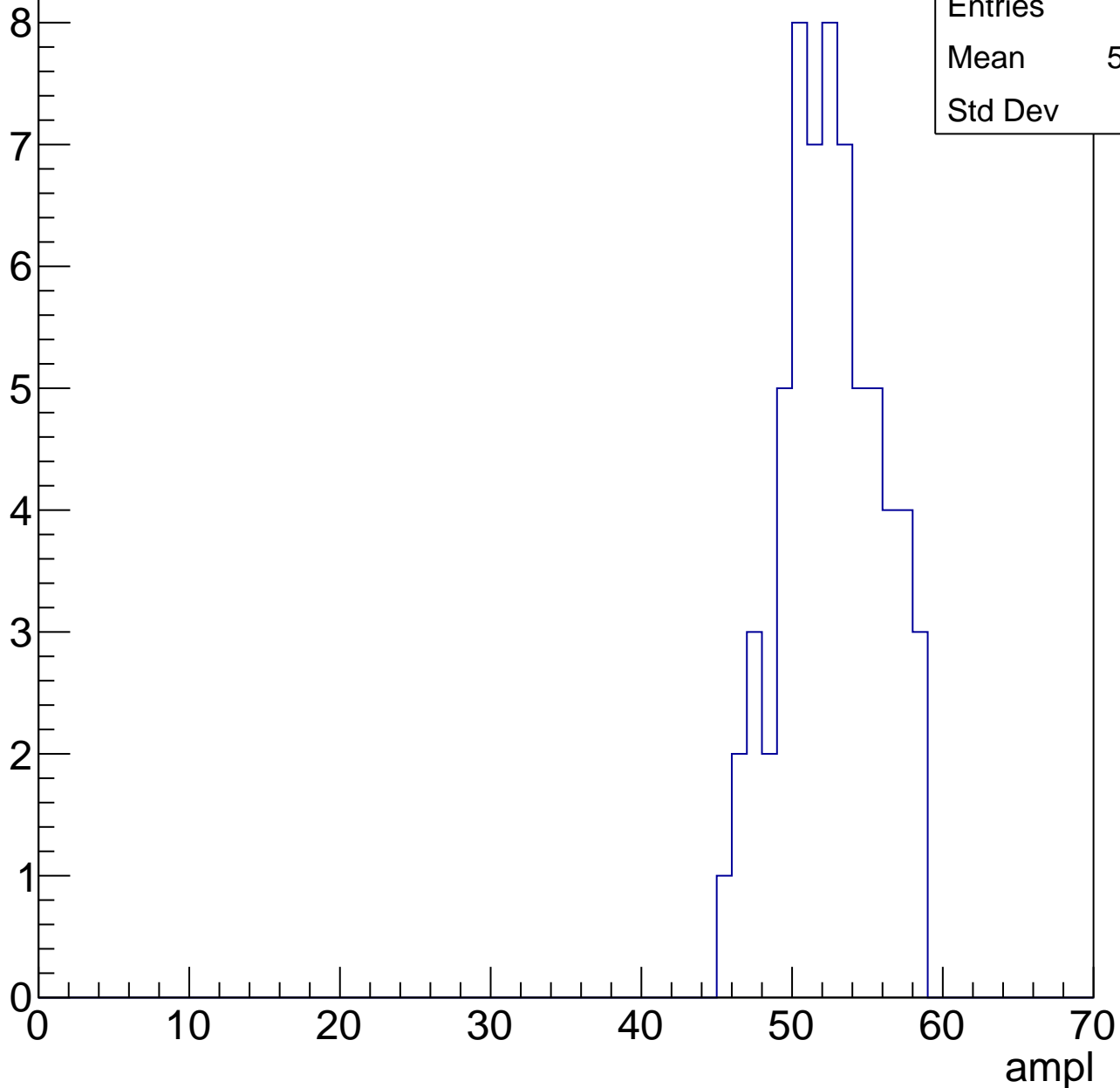


B0L001S, U25-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	52.09
Std Dev	3.2

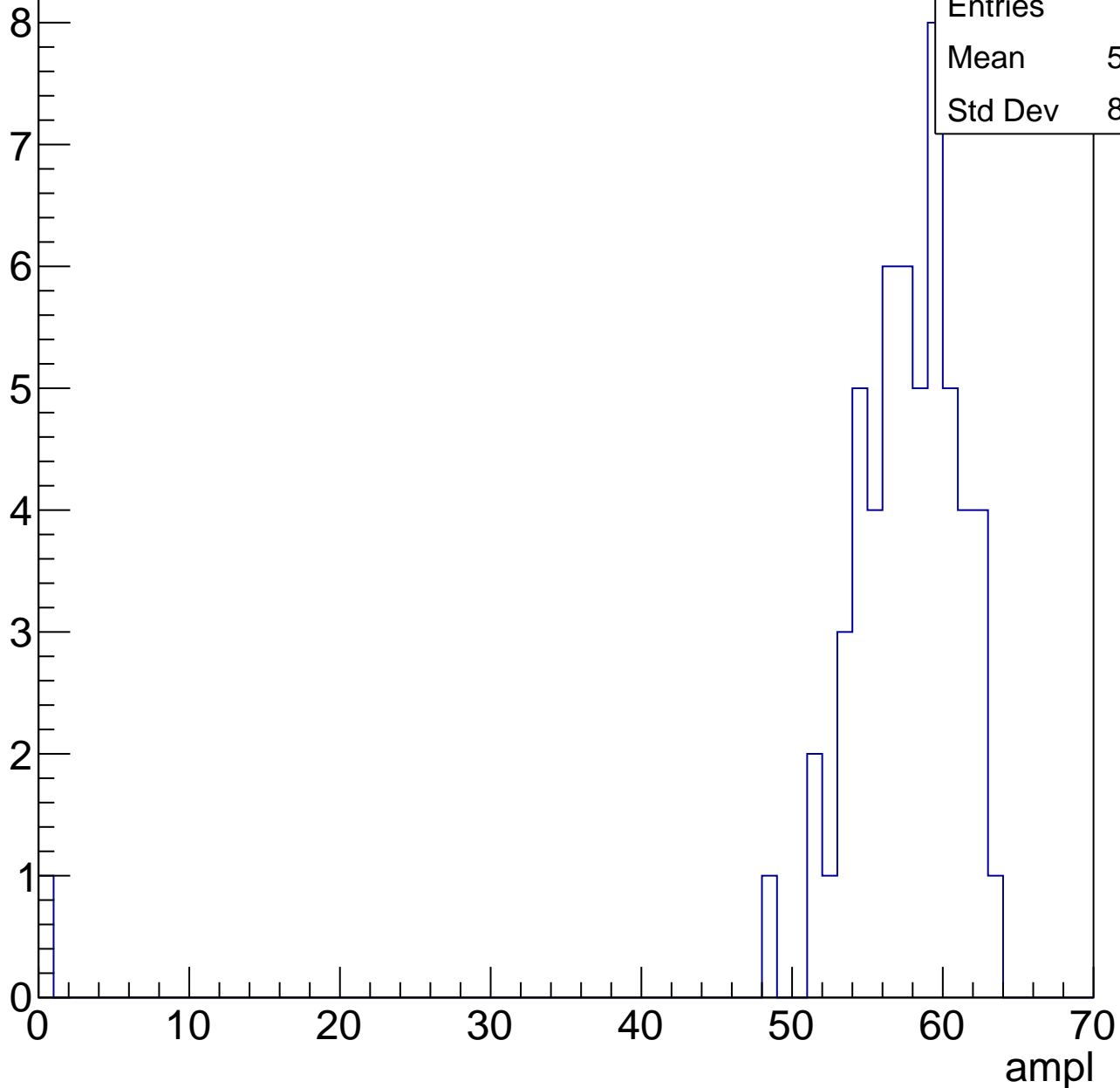


B0L001S, U25-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.18
Std Dev	8.223



B0L001S, U25-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	30
Mean	61.3
Std Dev	1.531

ampl

0 10 20 30 40 50 60 70

0

B0L001S, U25-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch60, adc0

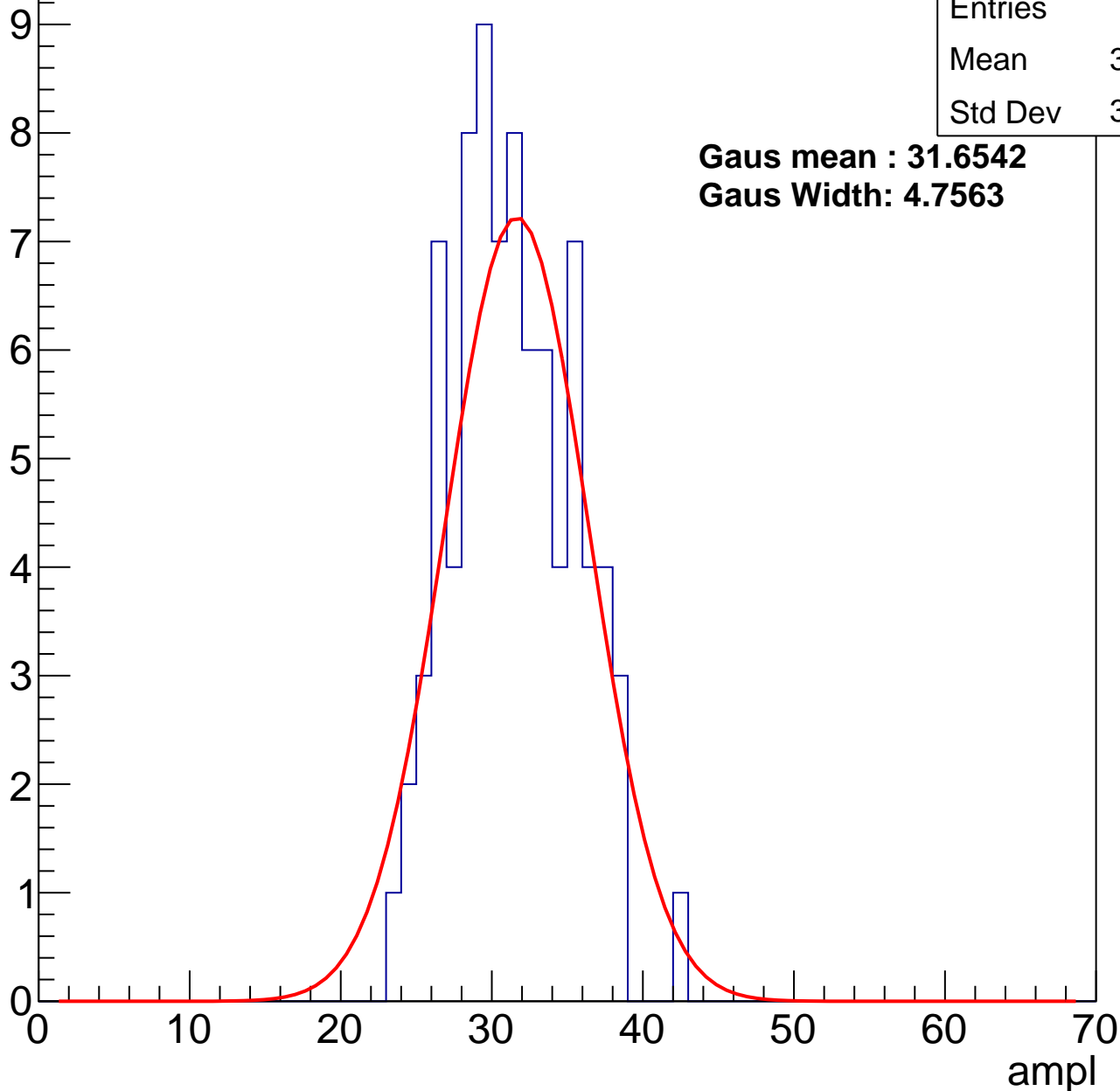
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	30.93
Std Dev	3.954

Gaus mean : 31.6542

Gaus Width: 4.7563



B0L001S, U25-ch60, adc1

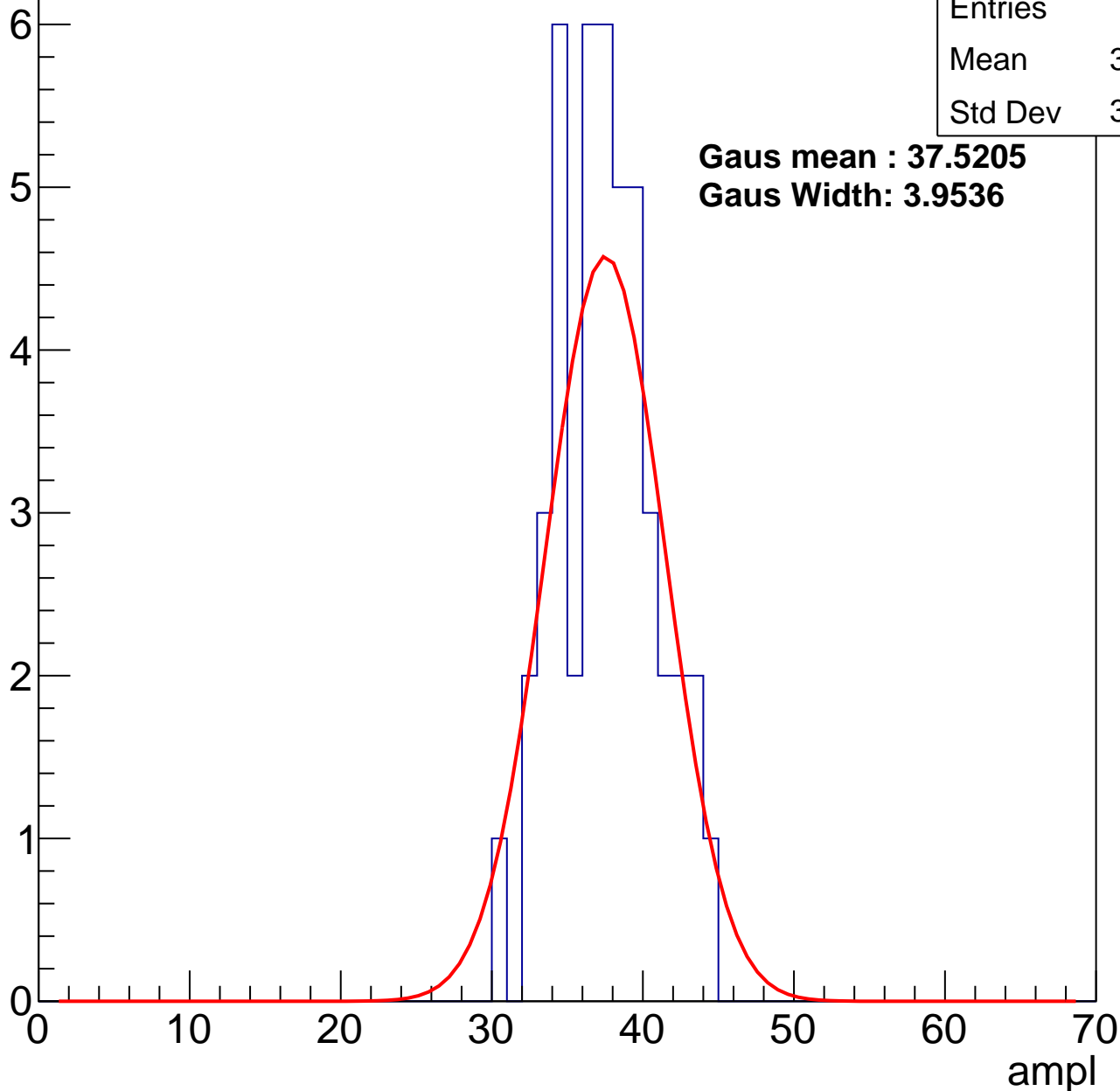
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	37.09
Std Dev	3.195

Gaus mean : 37.5205

Gaus Width: 3.9536



B0L001S, U25-ch60, adc2

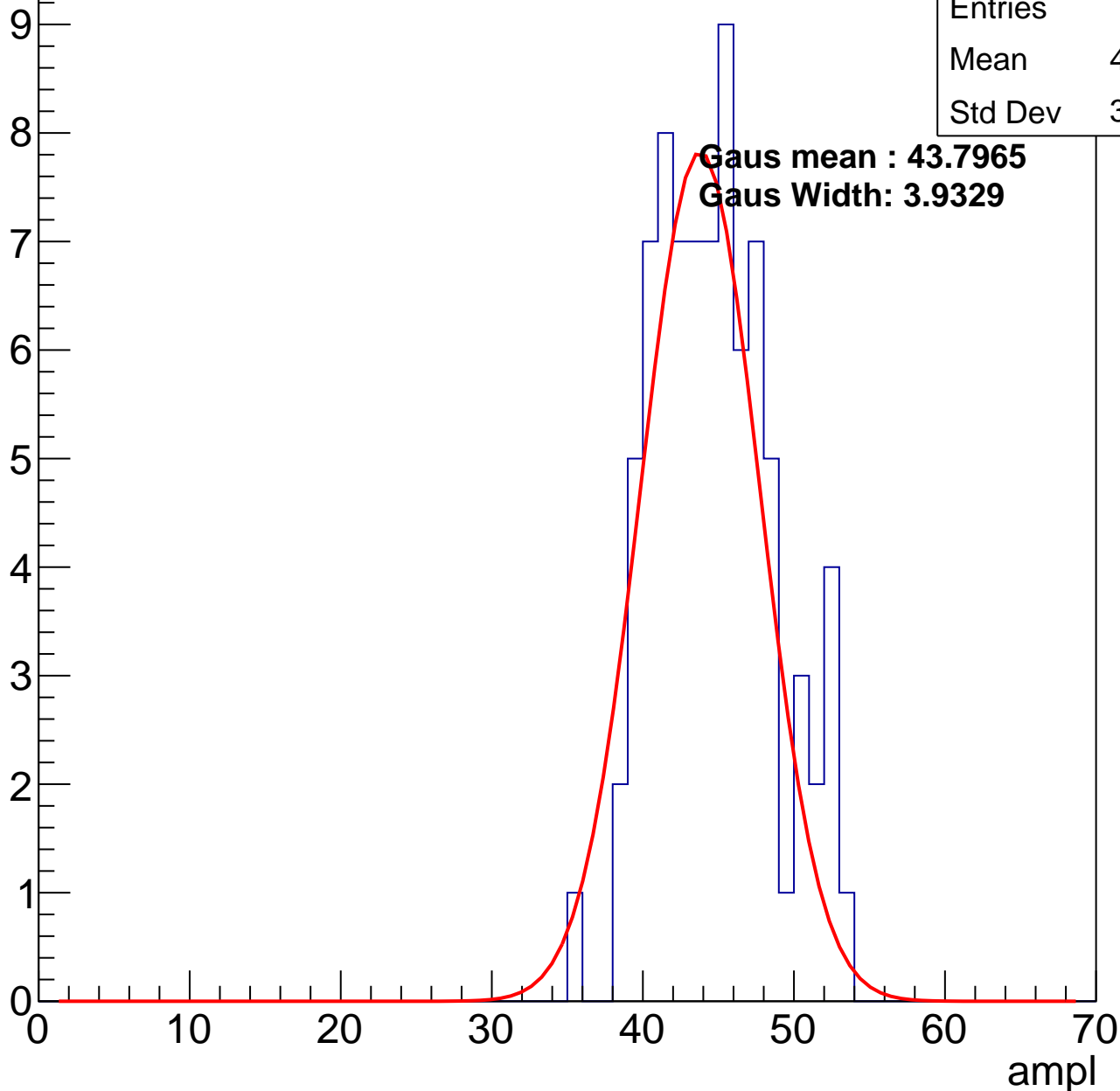
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	44.26
Std Dev	3.882

Gaus mean : 43.7965

Gaus Width: 3.9329

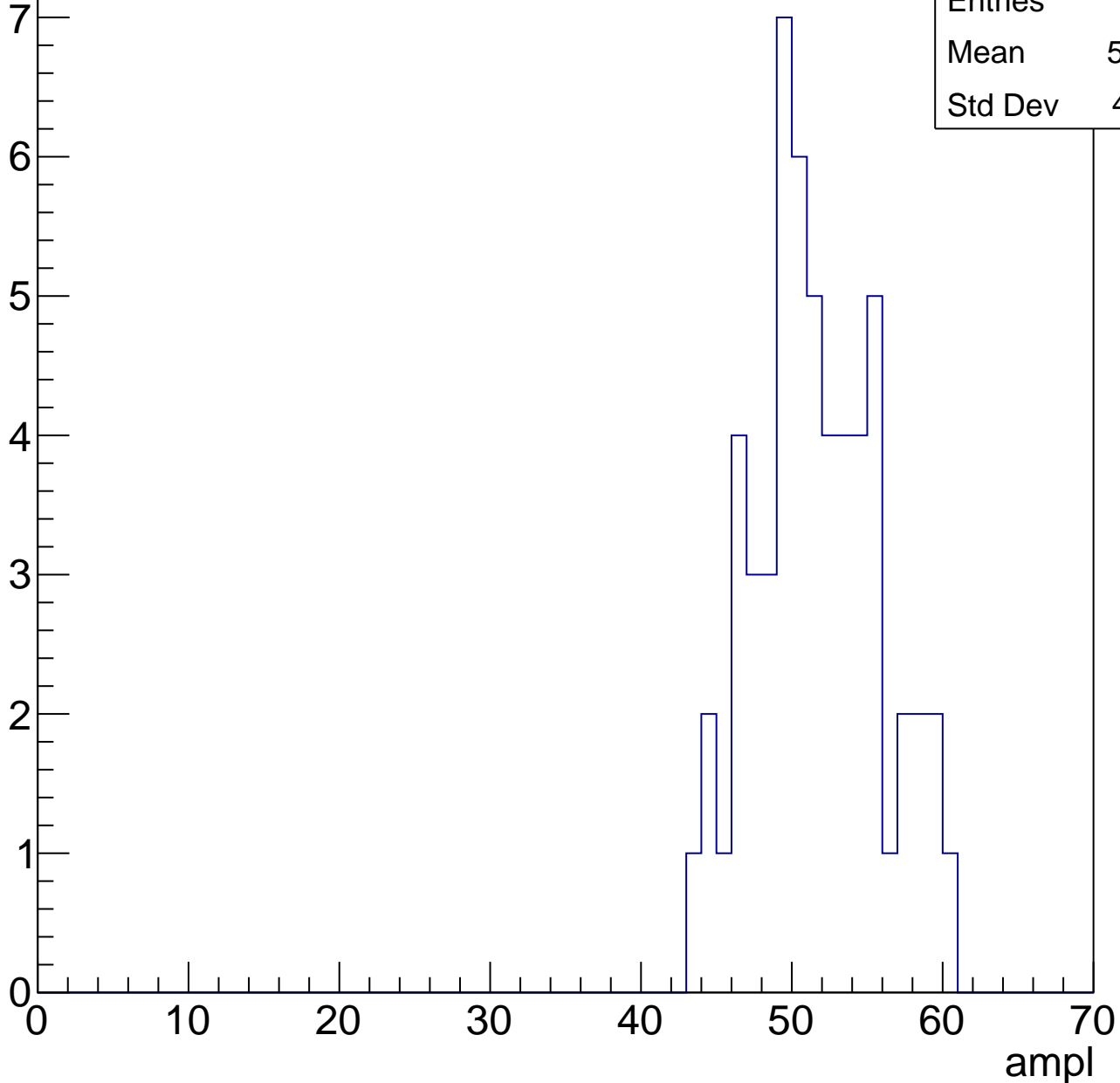


B0L001S, U25-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

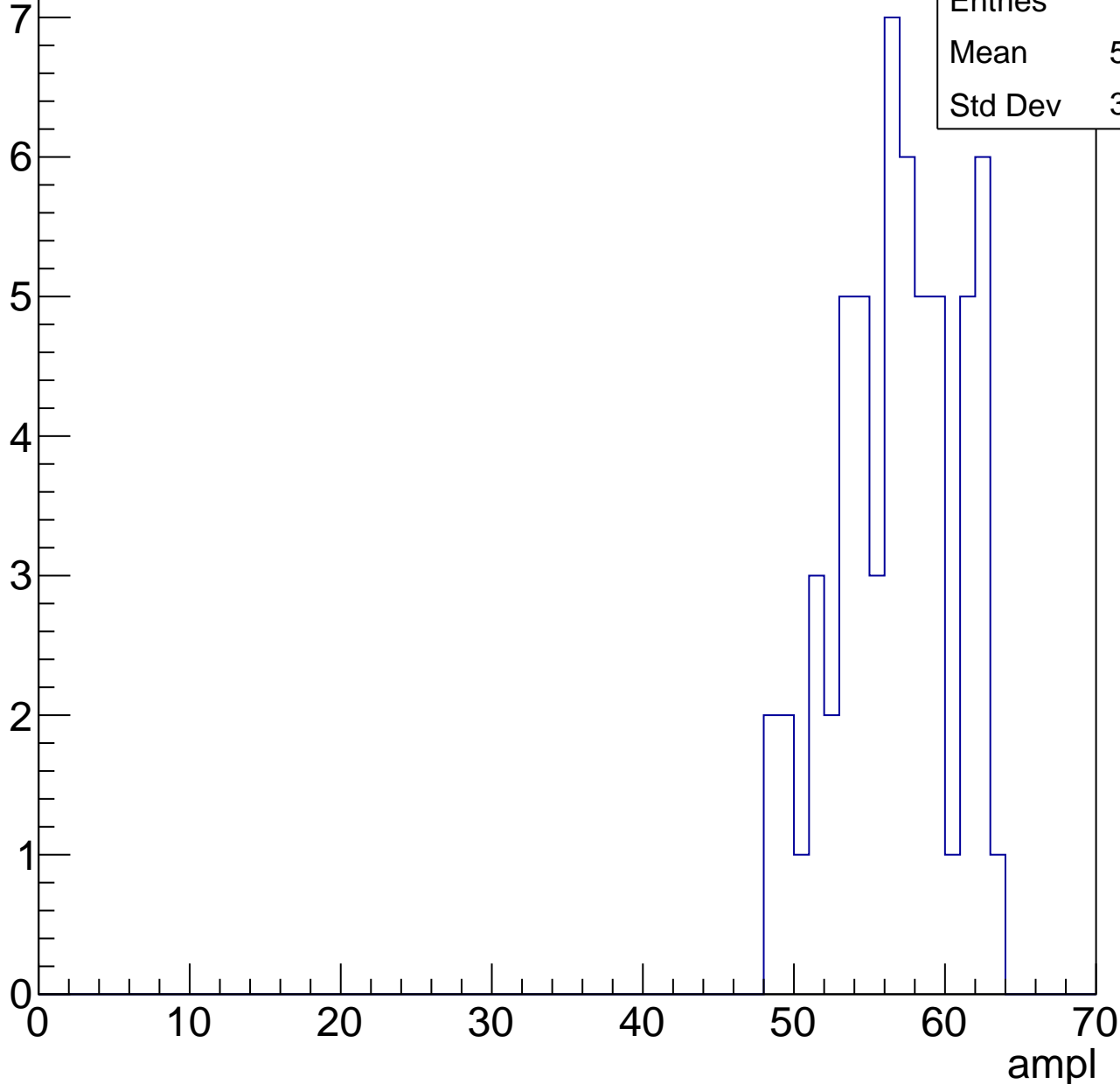
Entries	57
Mean	51.19
Std Dev	4.071



B0L001S, U25-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



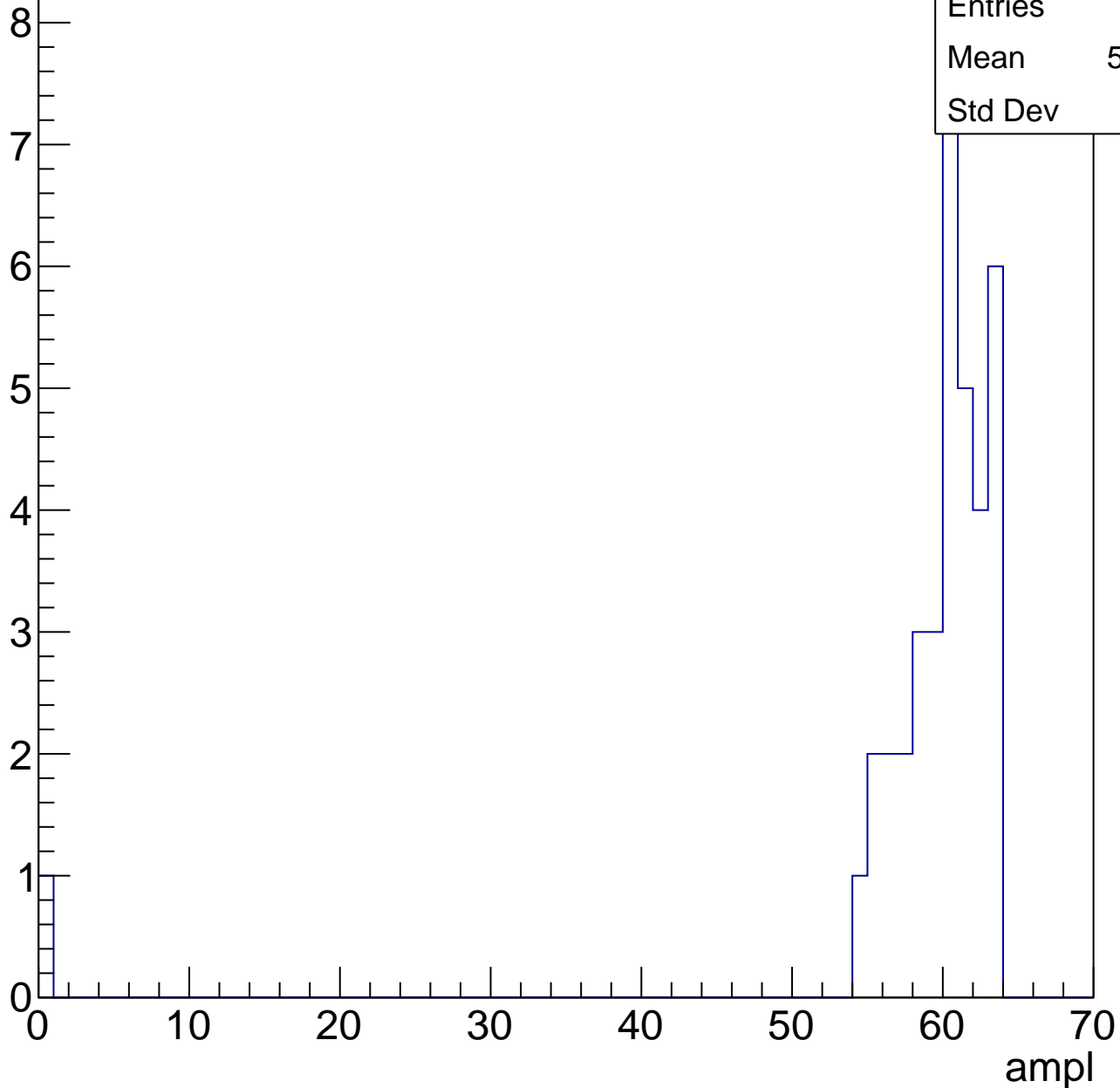
Entries	59
Mean	56.27
Std Dev	3.927

B0L001S, U25-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.16
Std Dev	10



B0L001S, U25-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch61, adc0

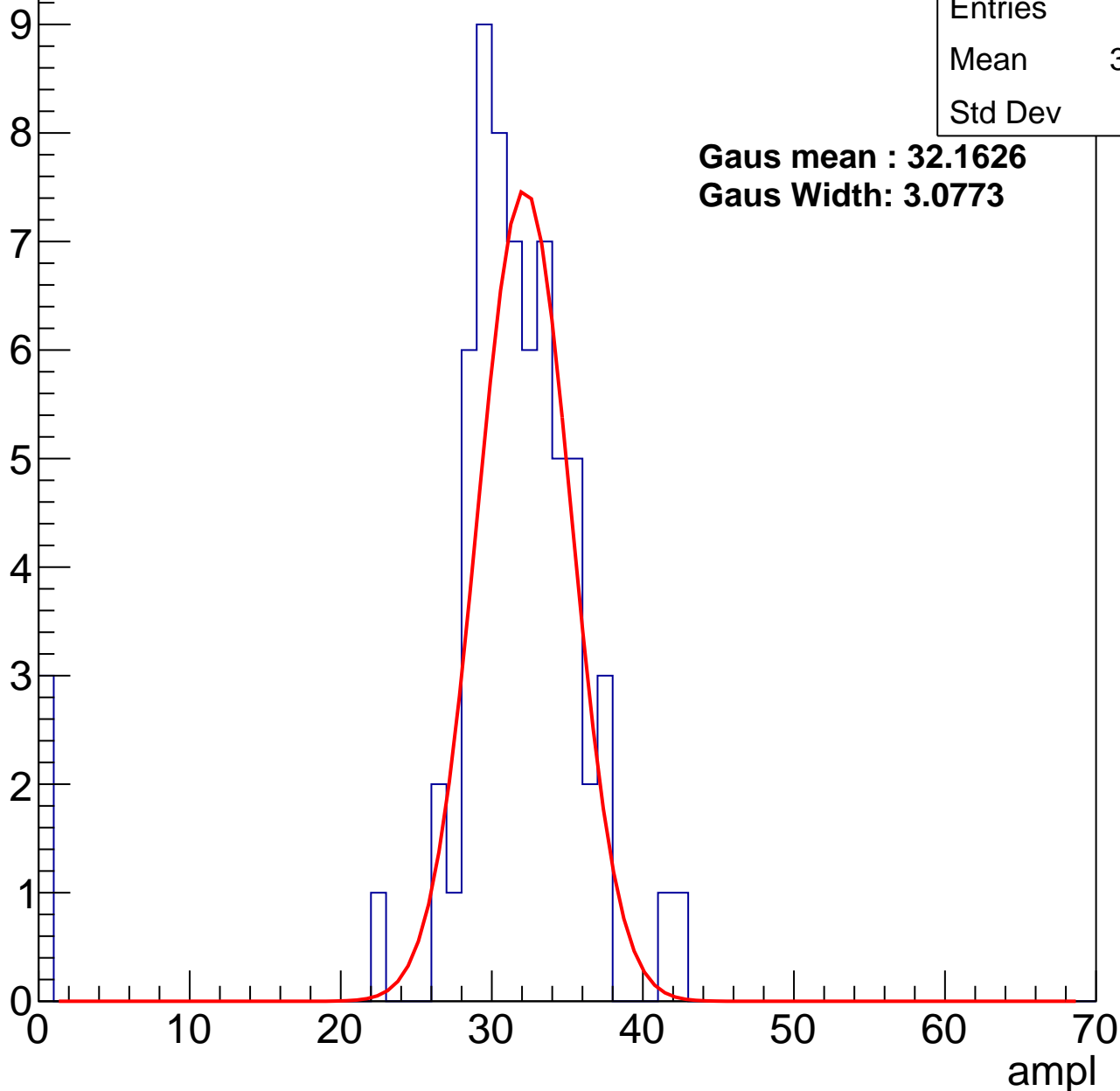
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.16
Std Dev	7.35

Gaus mean : 32.1626

Gaus Width: 3.0773



B0L001S, U25-ch61, adc1

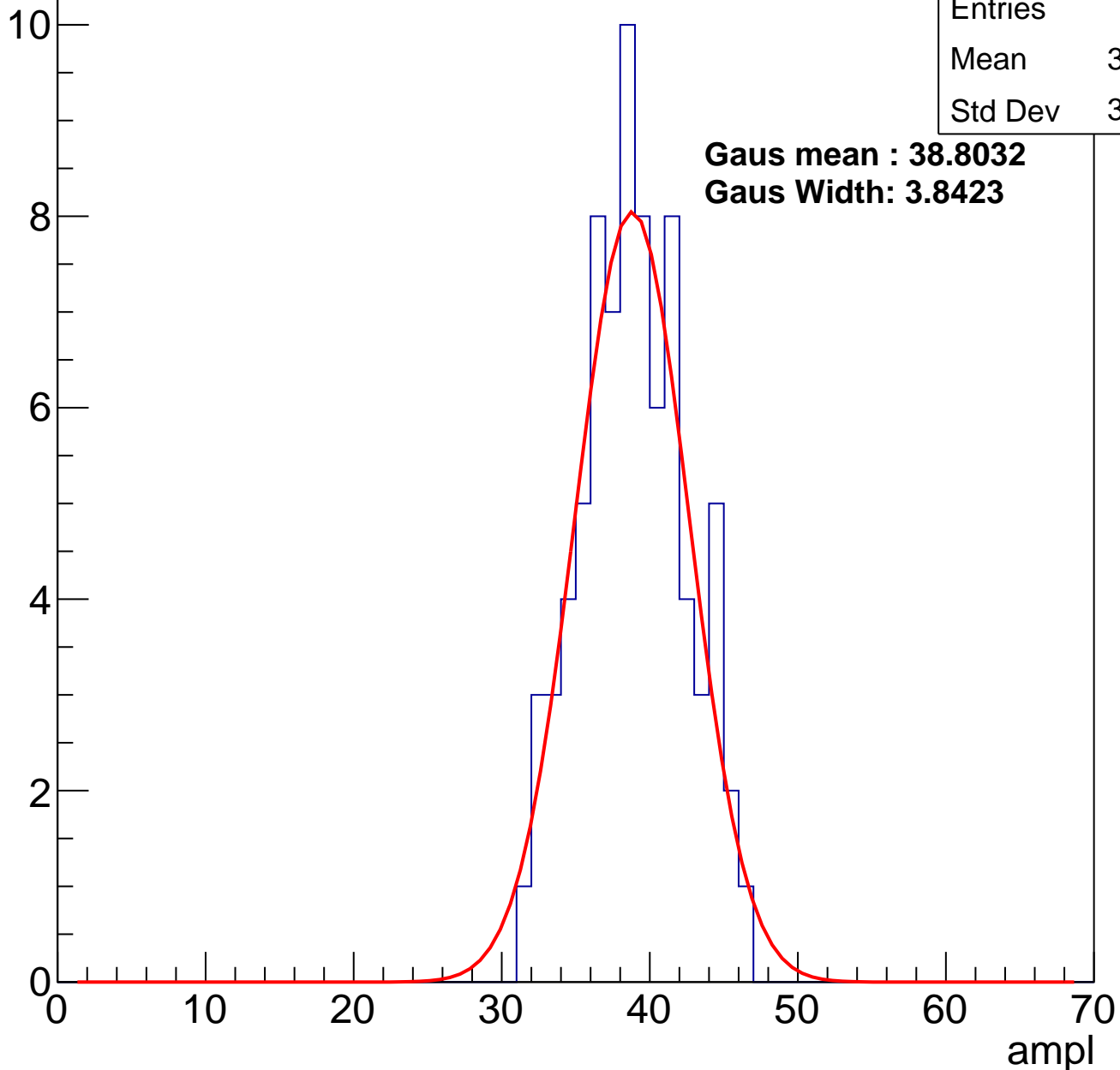
calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	38.42
Std Dev	3.492

Gaus mean : 38.8032

Gaus Width: 3.8423

Entry



B0L001S, U25-ch61, adc2

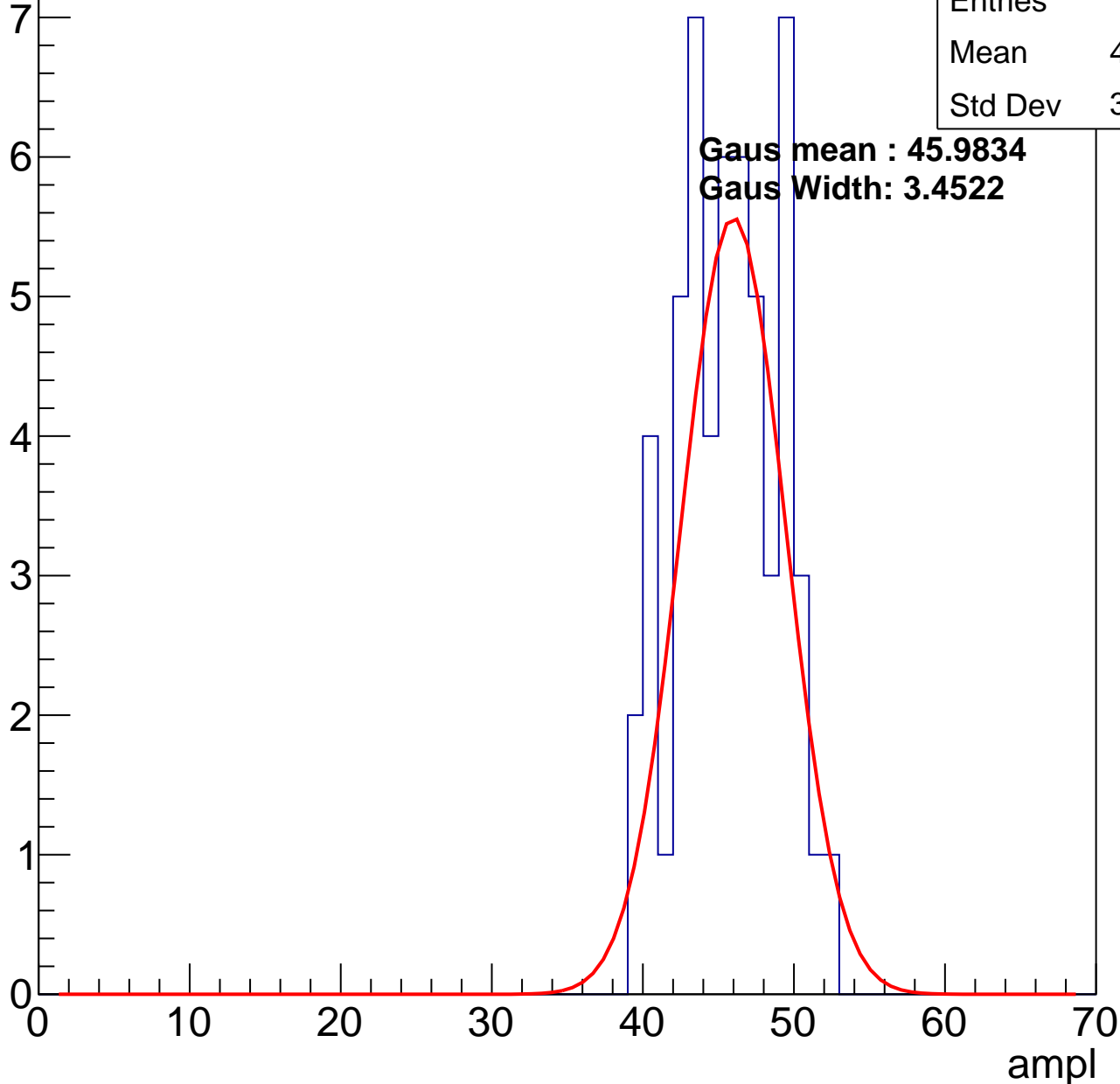
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	45.22
Std Dev	3.268

Gaus mean : 45.9834

Gaus Width: 3.4522

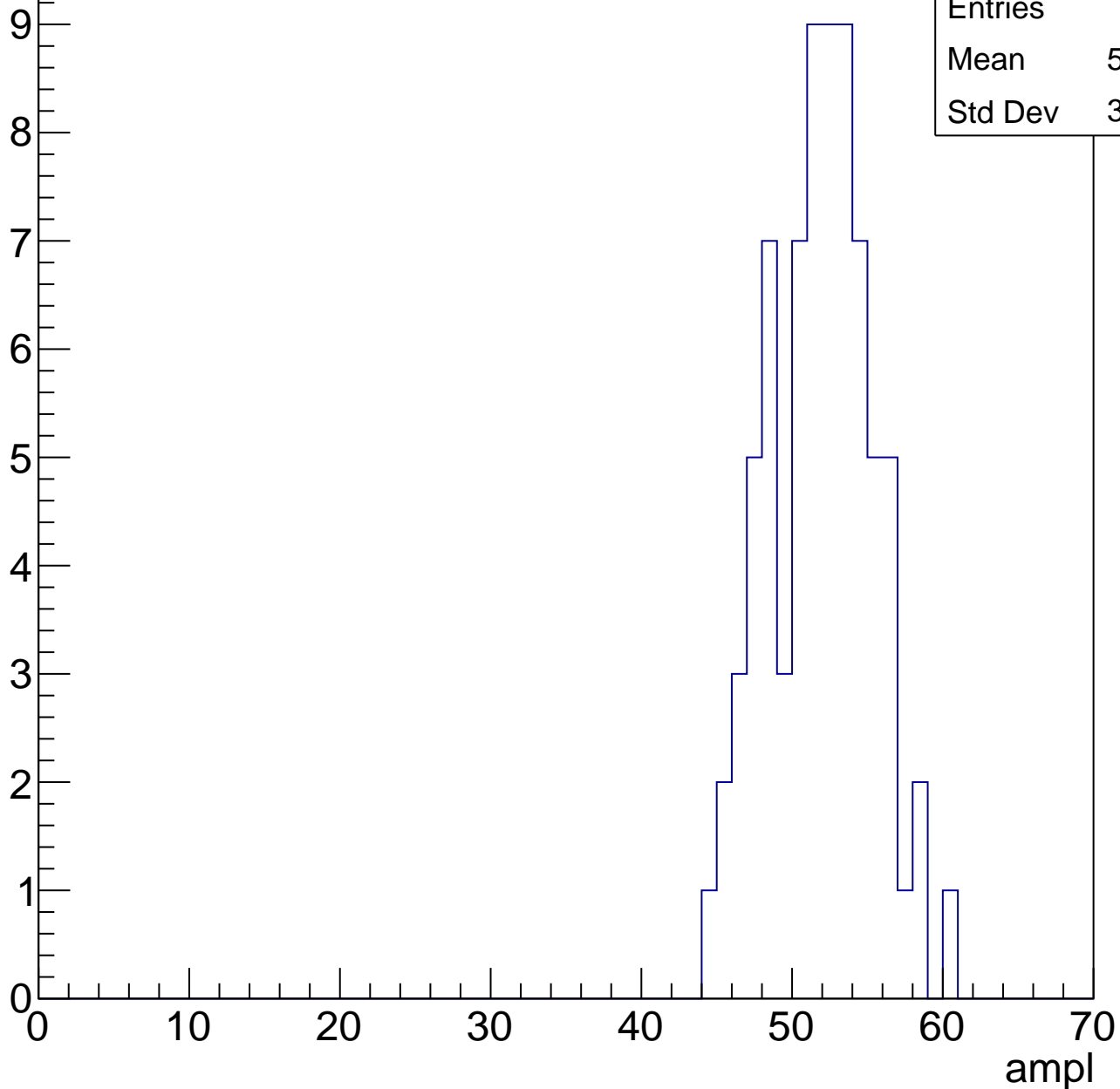


B0L001S, U25-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.45
Std Dev	3.385

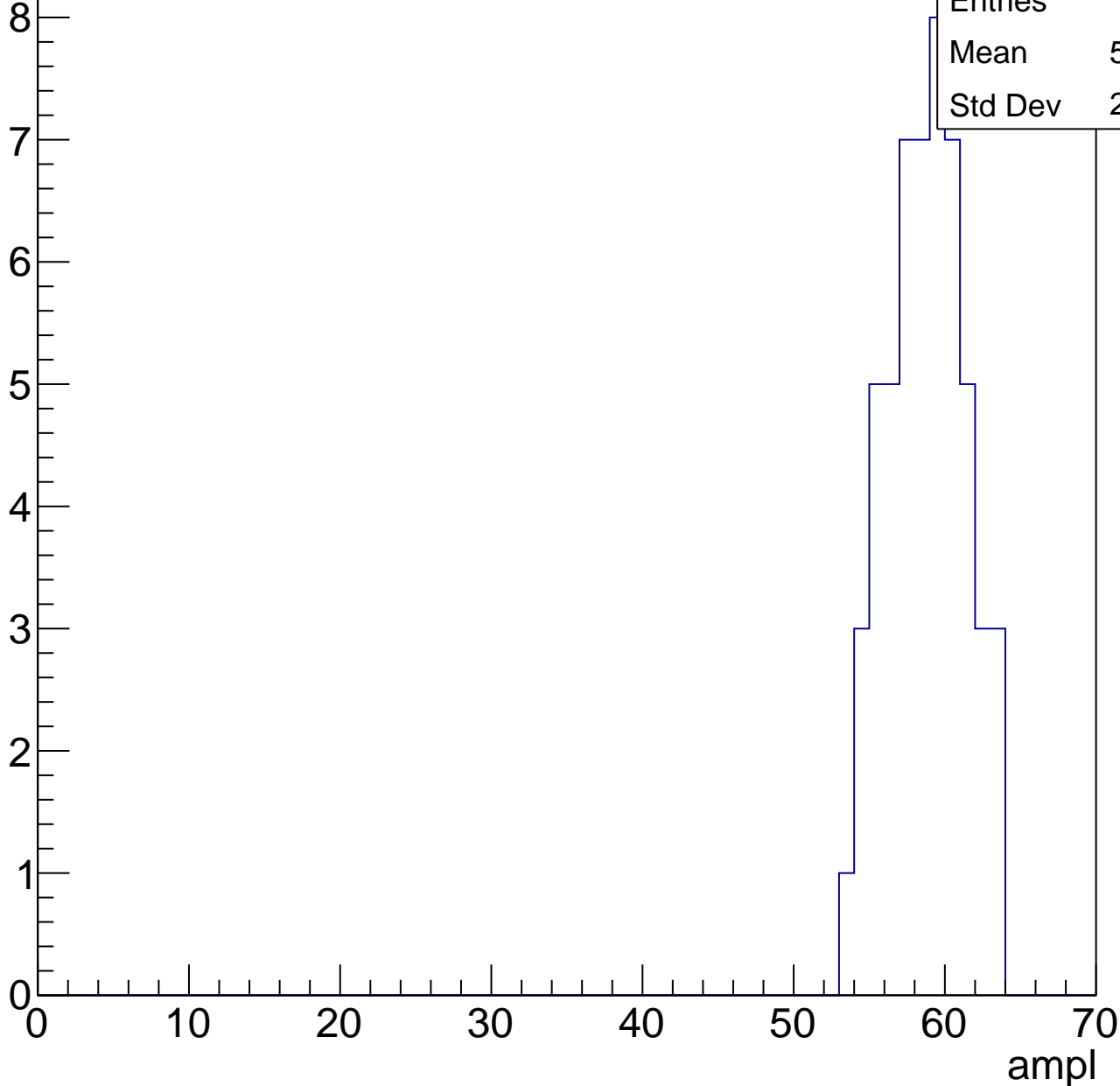


B0L001S, U25-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	58.28
Std Dev	2.527

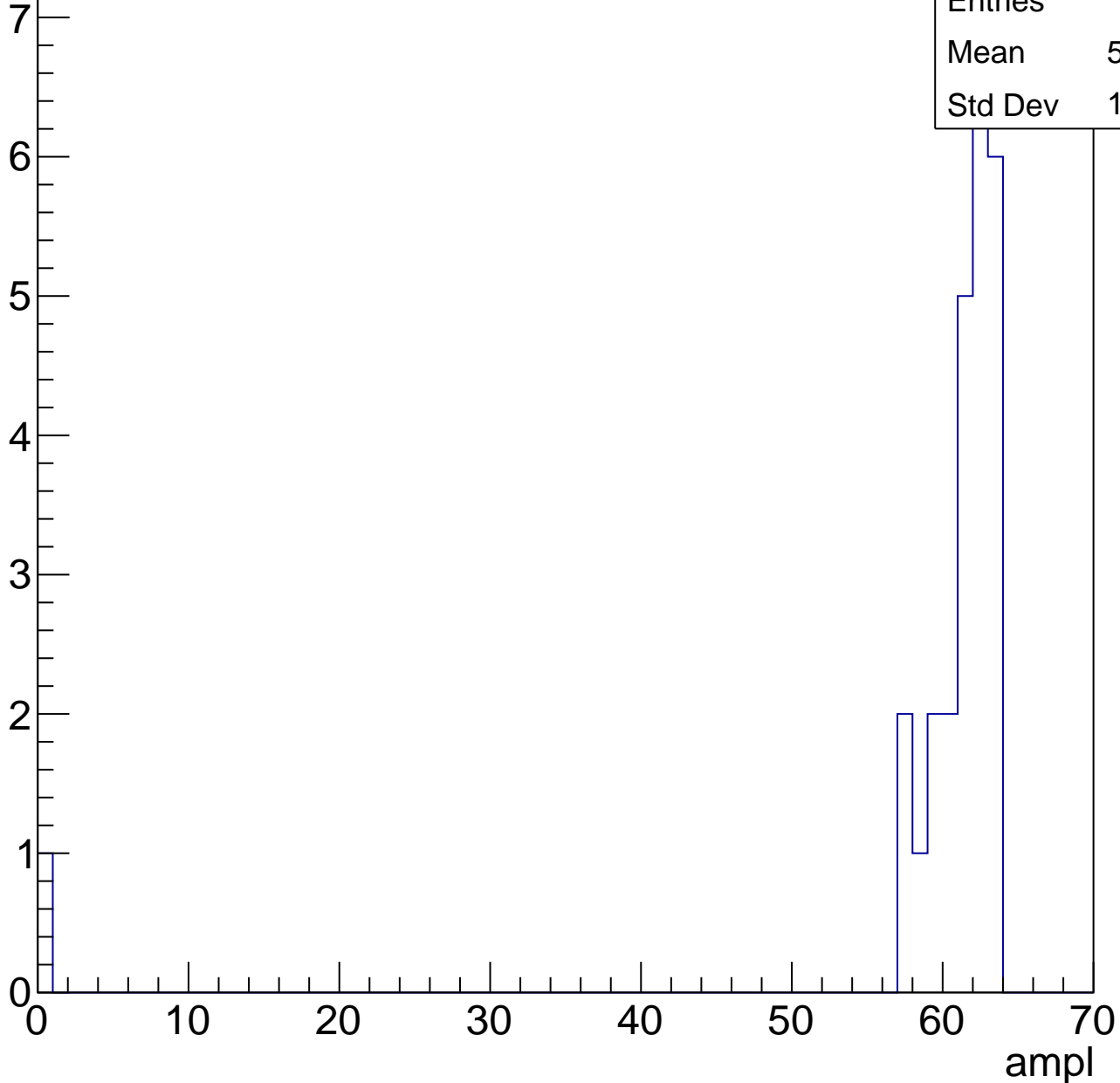


B0L001S, U25-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.73
Std Dev	11.88



B0L001S, U25-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch62, adc0

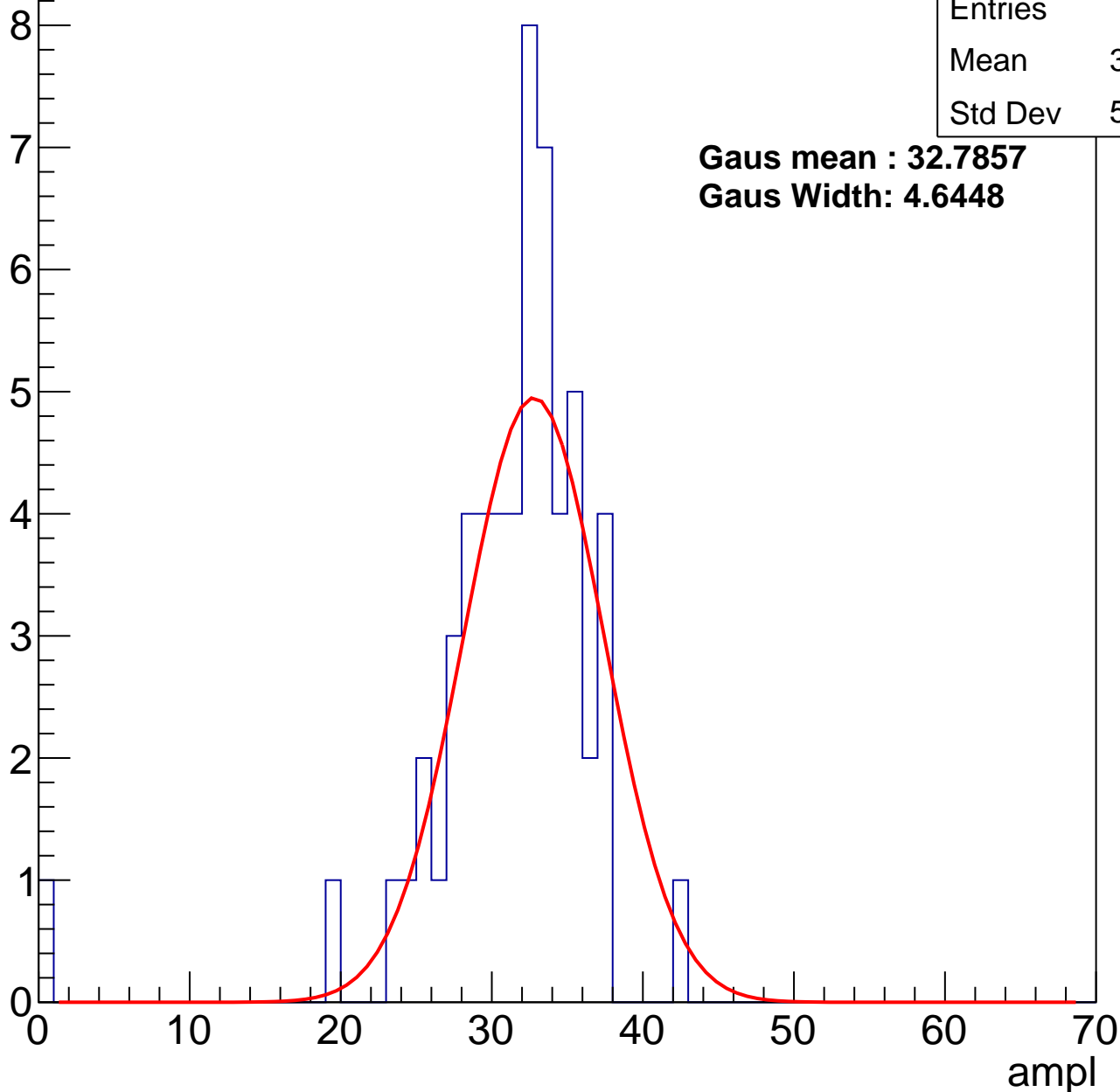
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.79
Std Dev	5.748

Gaus mean : 32.7857

Gaus Width: 4.6448



B0L001S, U25-ch62, adc1

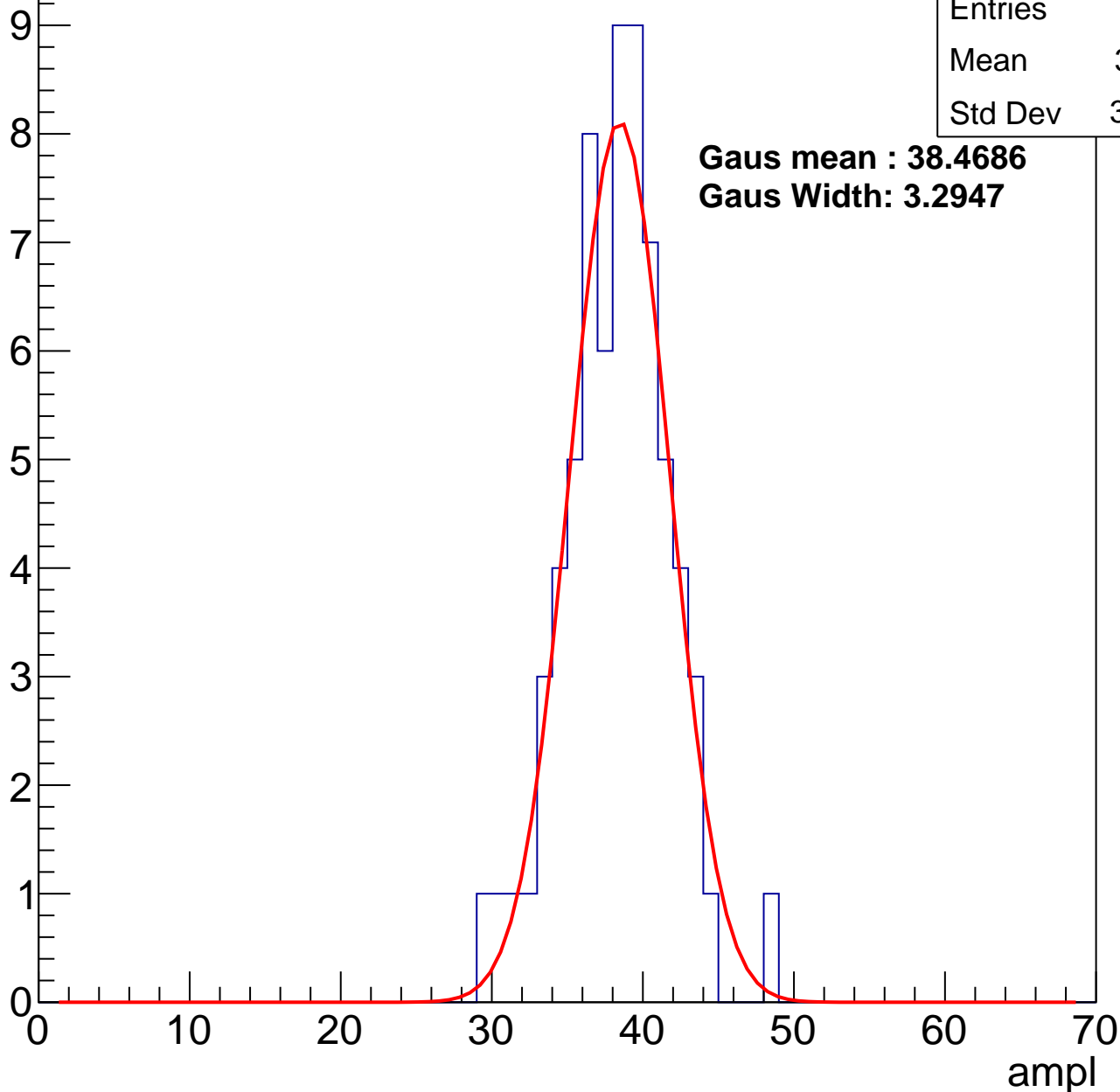
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.81
Std Dev	3.419

Gaus mean : 38.4686

Gaus Width: 3.2947



B0L001S, U25-ch62, adc2

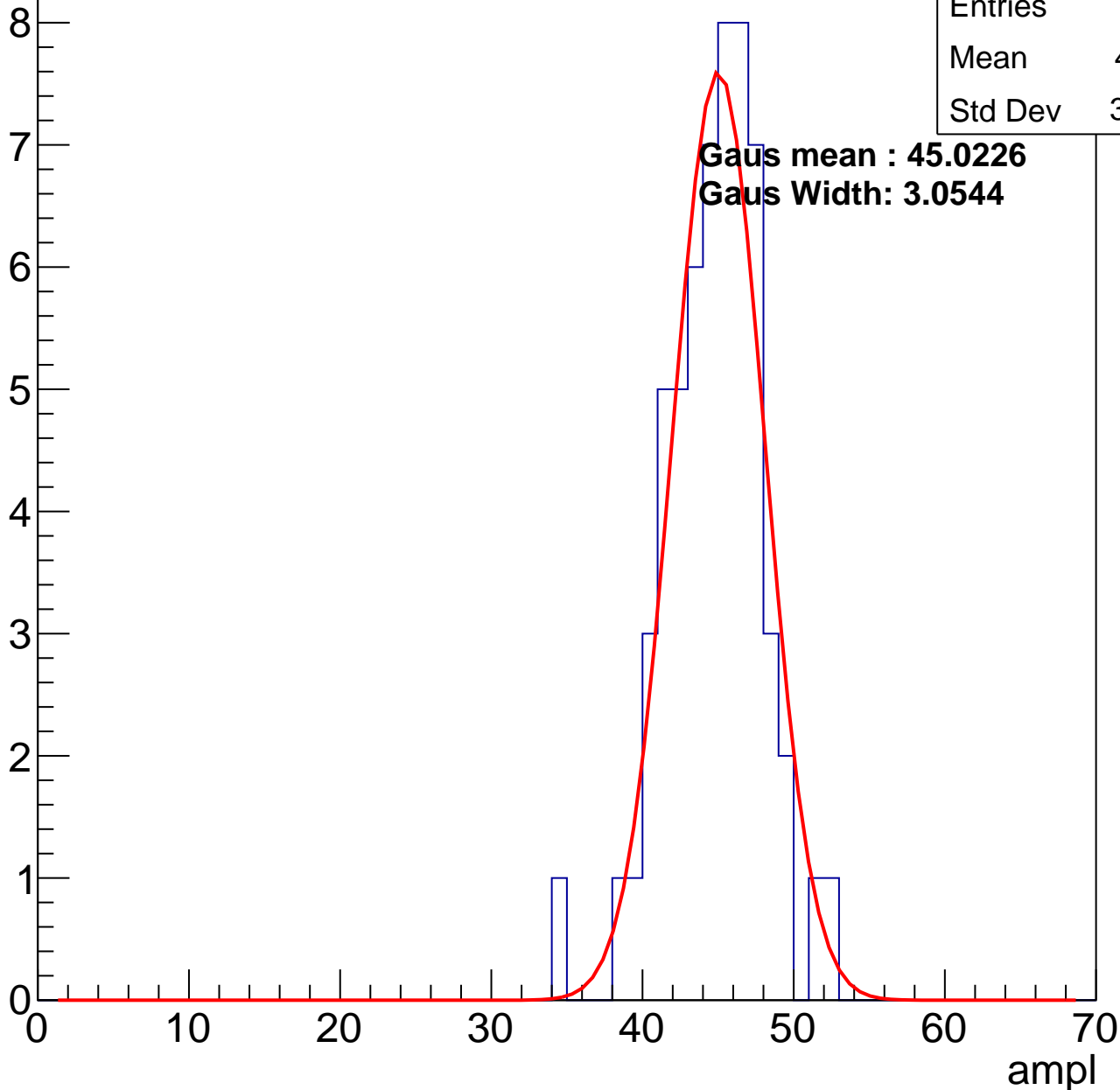
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.31
Std Dev	3.158

Gaus mean : 45.0226

Gaus Width: 3.0544

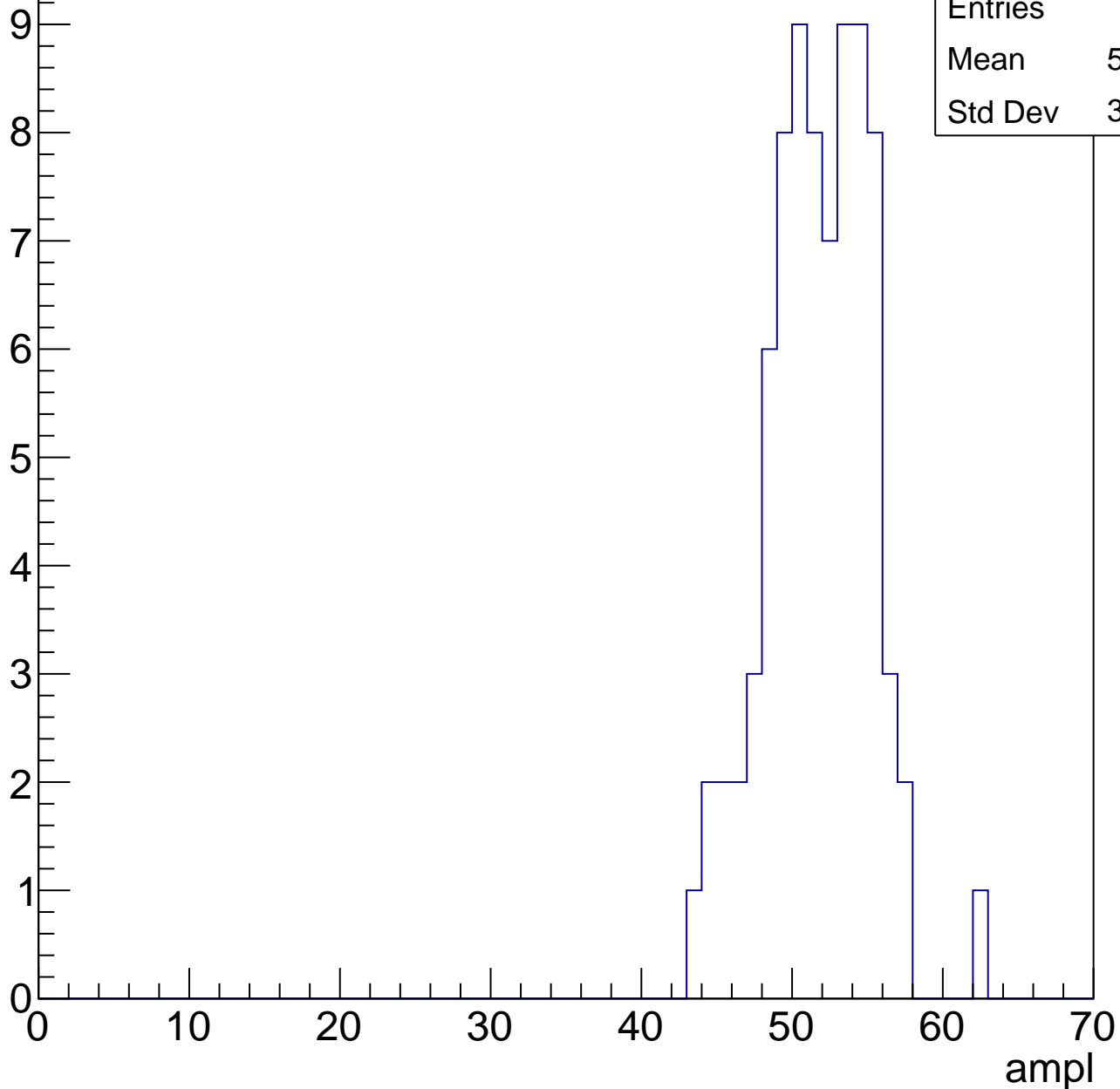


B0L001S, U25-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	51.29
Std Dev	3.429

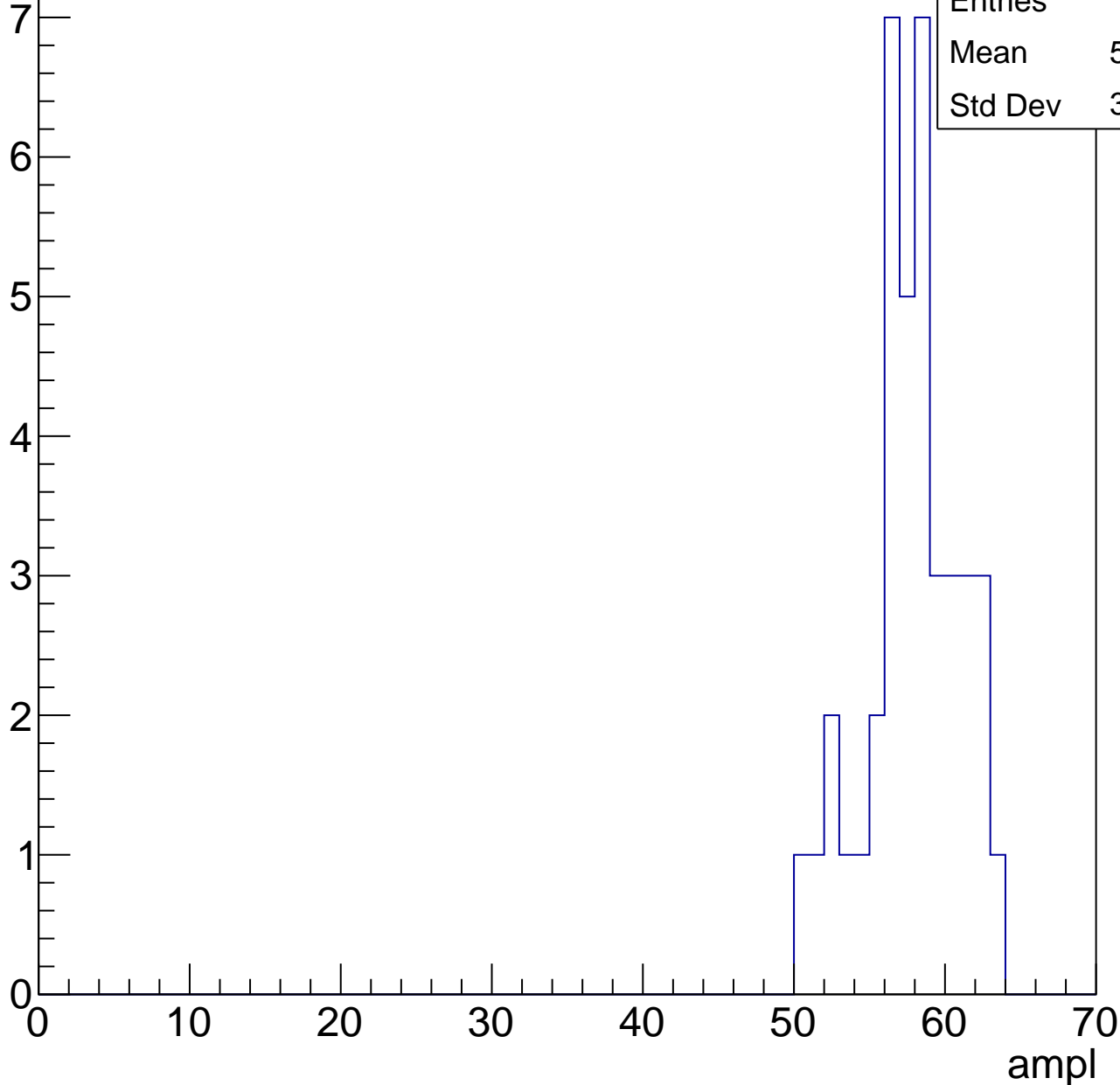


B0L001S, U25-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	57.35
Std Dev	3.062

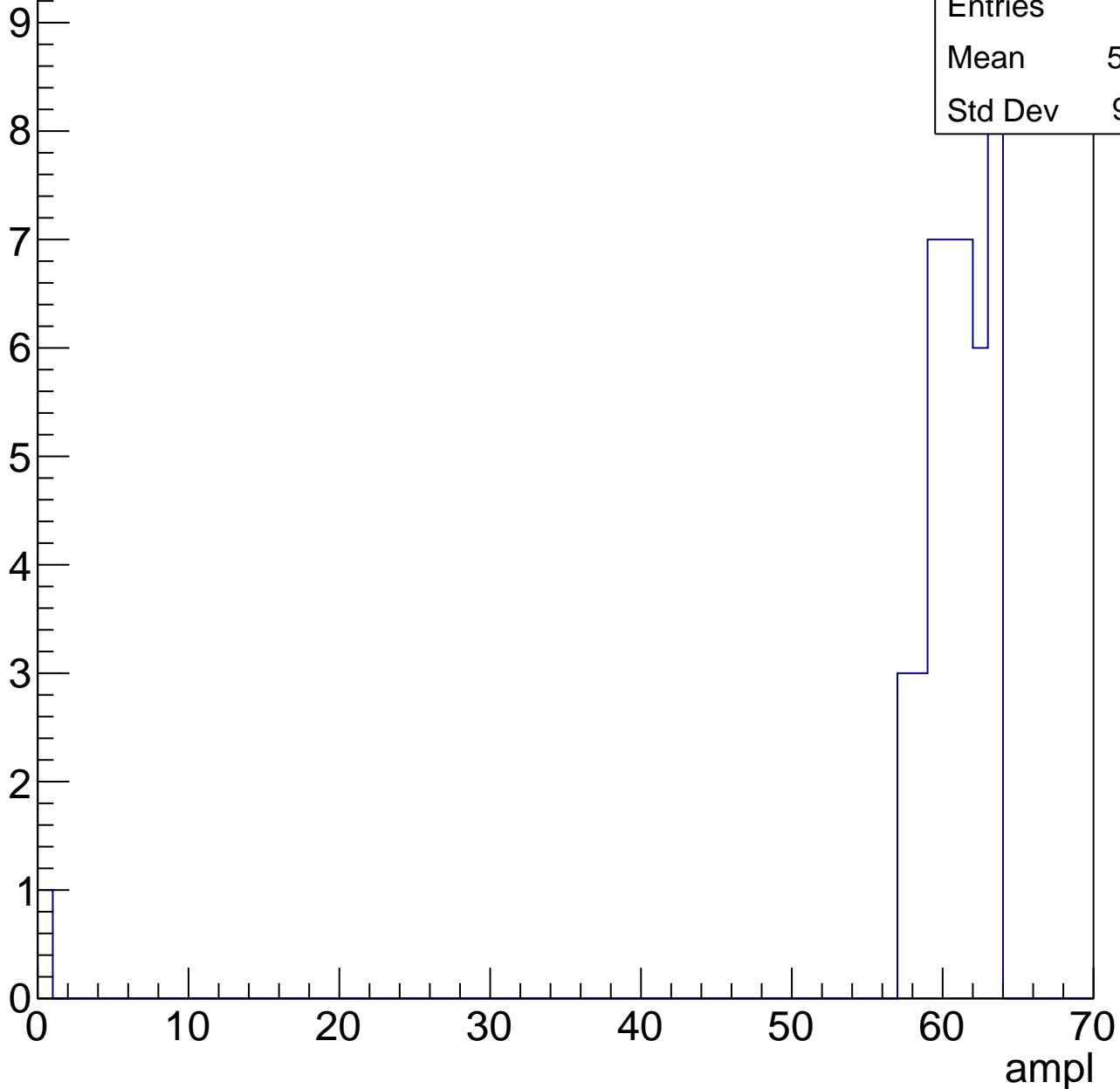


B0L001S, U25-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	59.16
Std Dev	9.311



B0L001S, U25-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch63, adc0

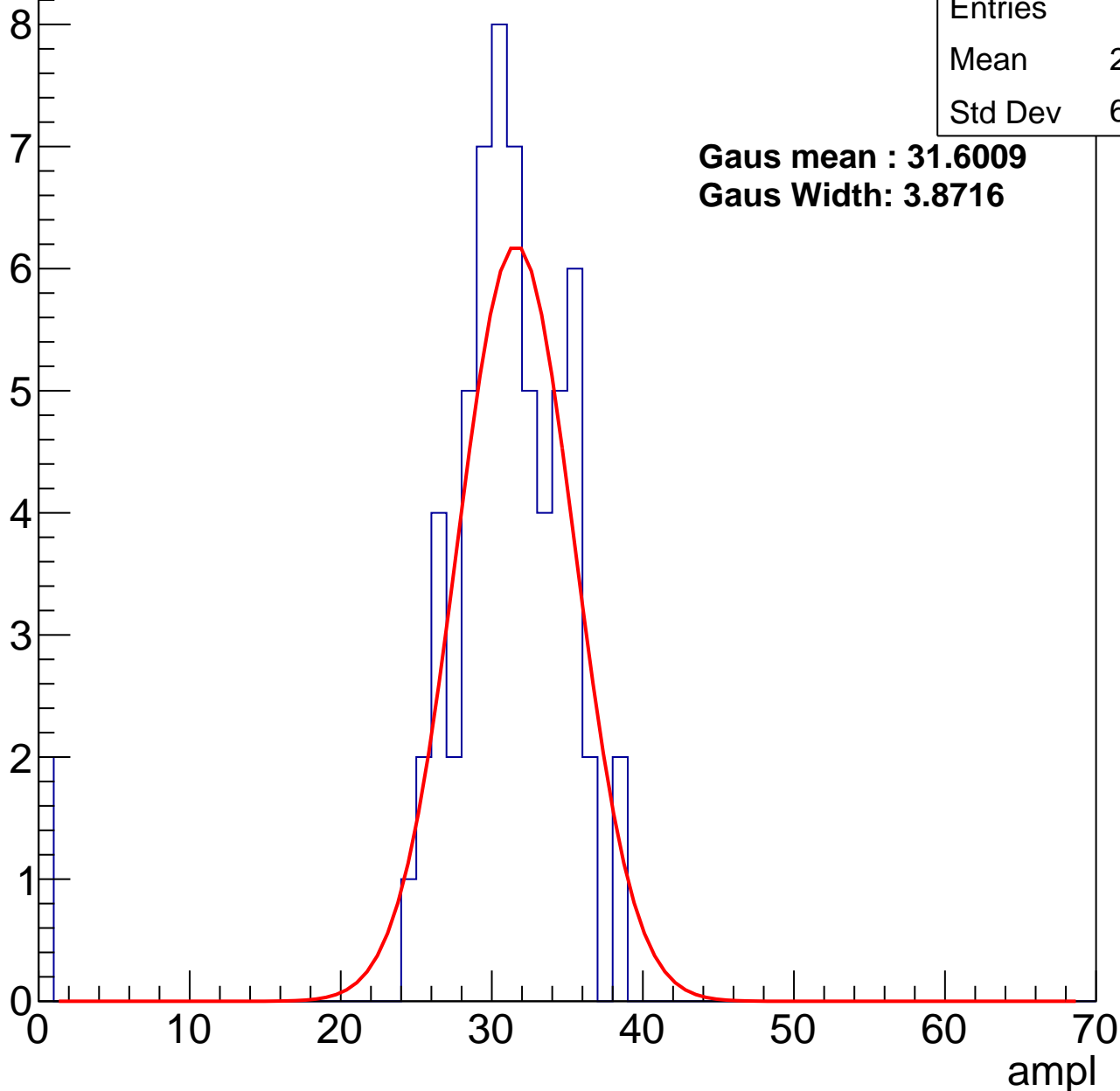
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.87
Std Dev	6.333

Gaus mean : 31.6009

Gaus Width: 3.8716



B0L001S, U25-ch63, adc1

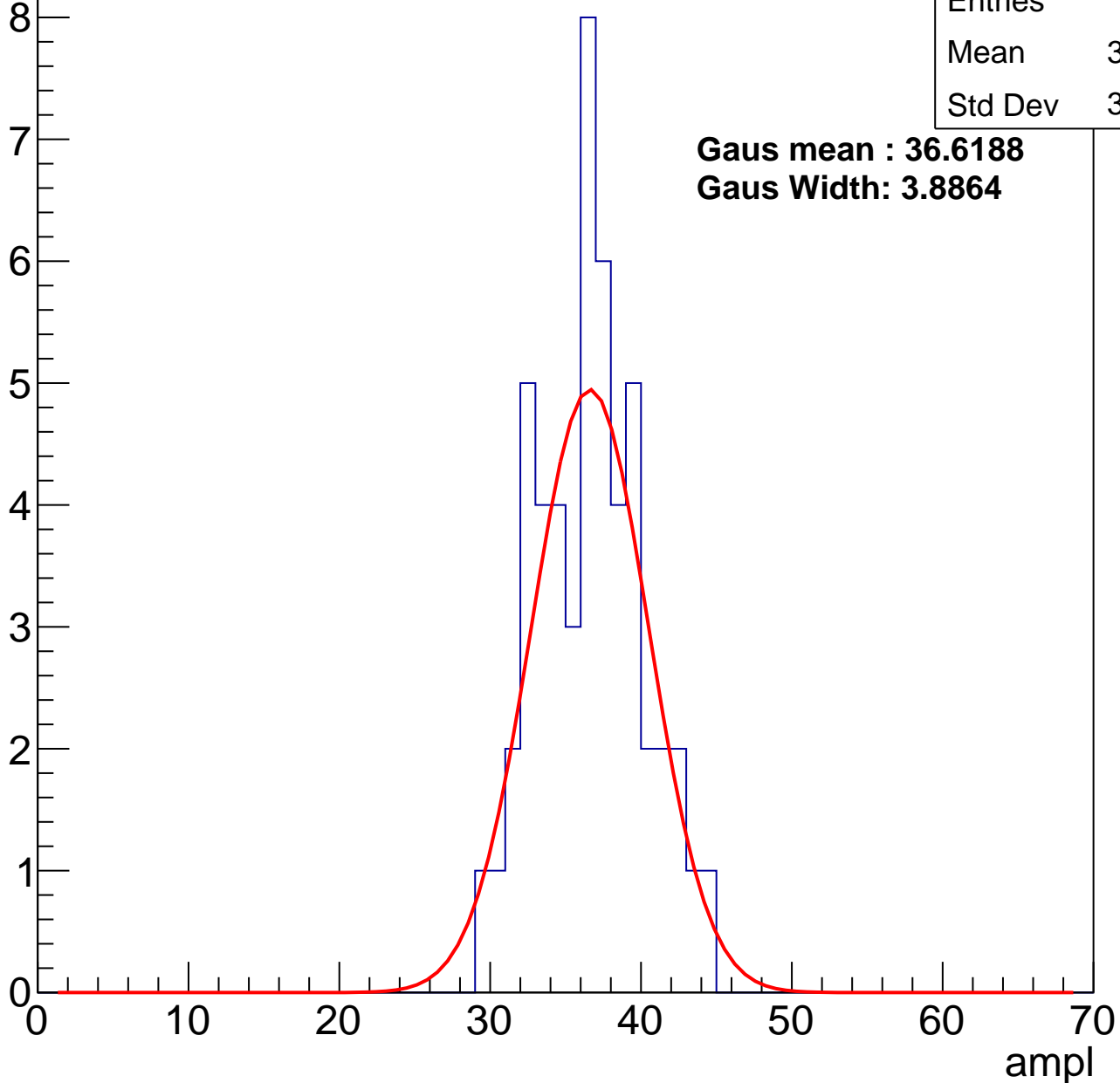
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	36.16
Std Dev	3.438

Gaus mean : 36.6188

Gaus Width: 3.8864



B0L001S, U25-ch63, adc2

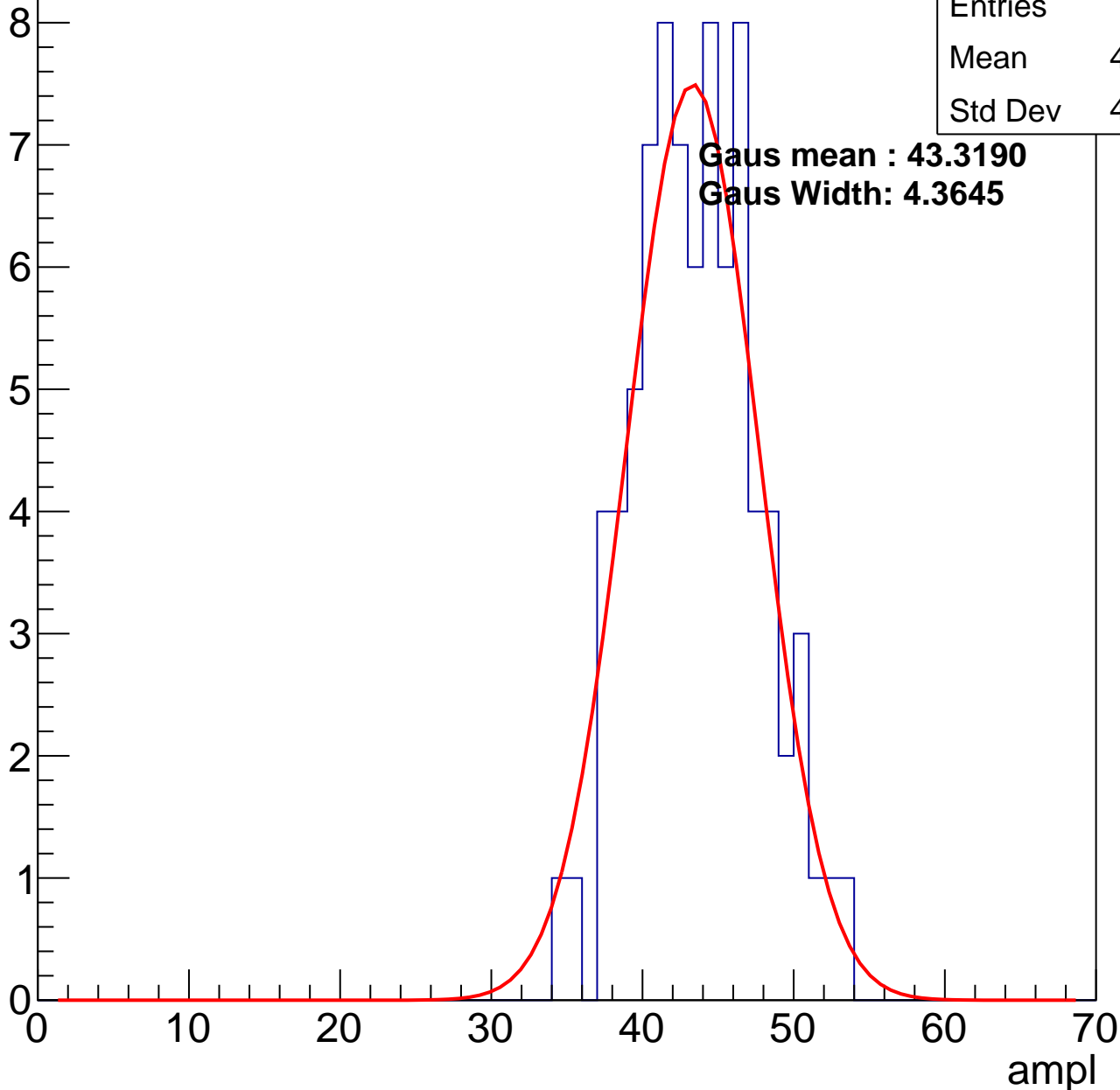
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	43.19
Std Dev	4.007

Gaus mean : 43.3190

Gaus Width: 4.3645

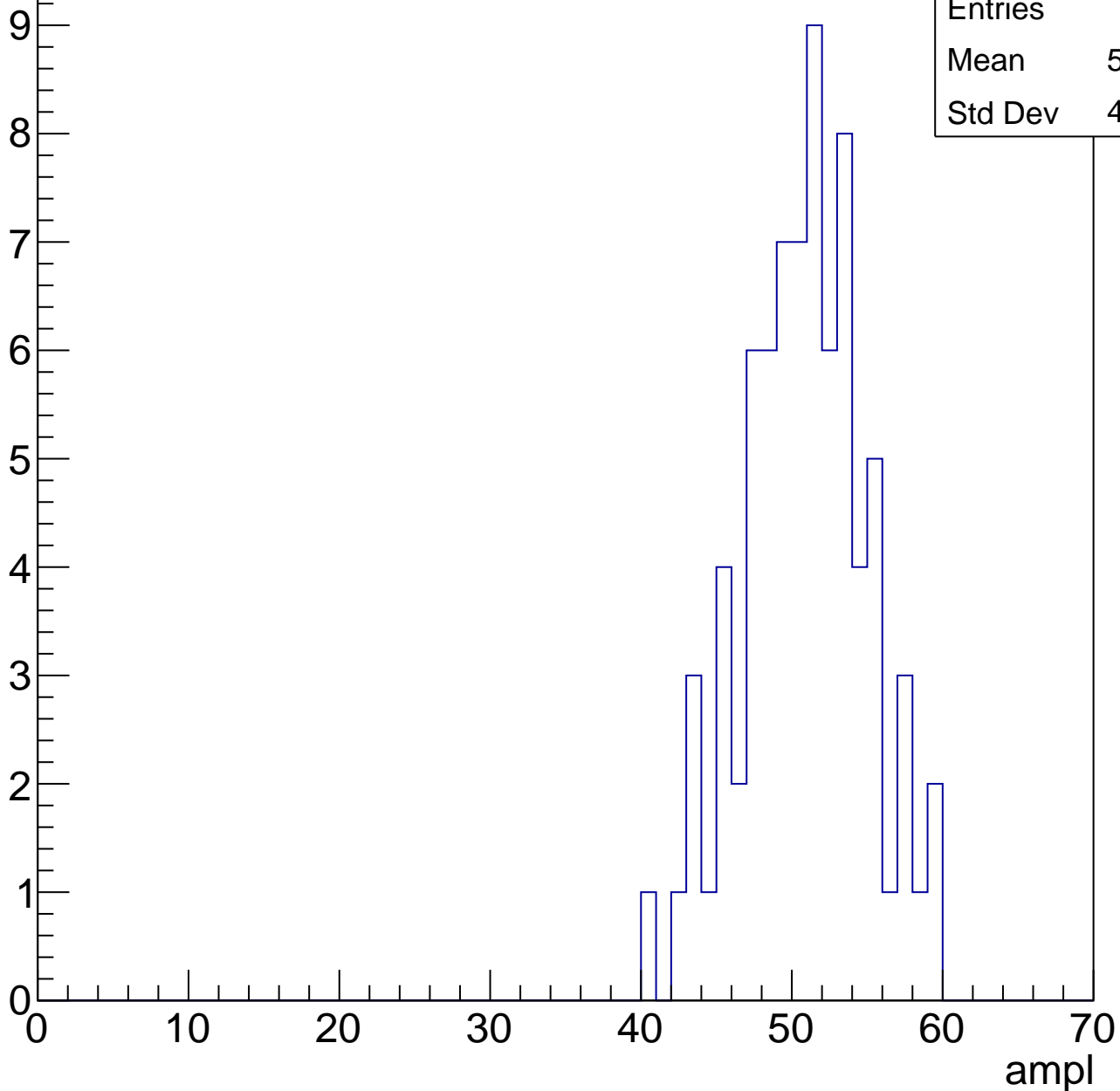


B0L001S, U25-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	50.38
Std Dev	4.039

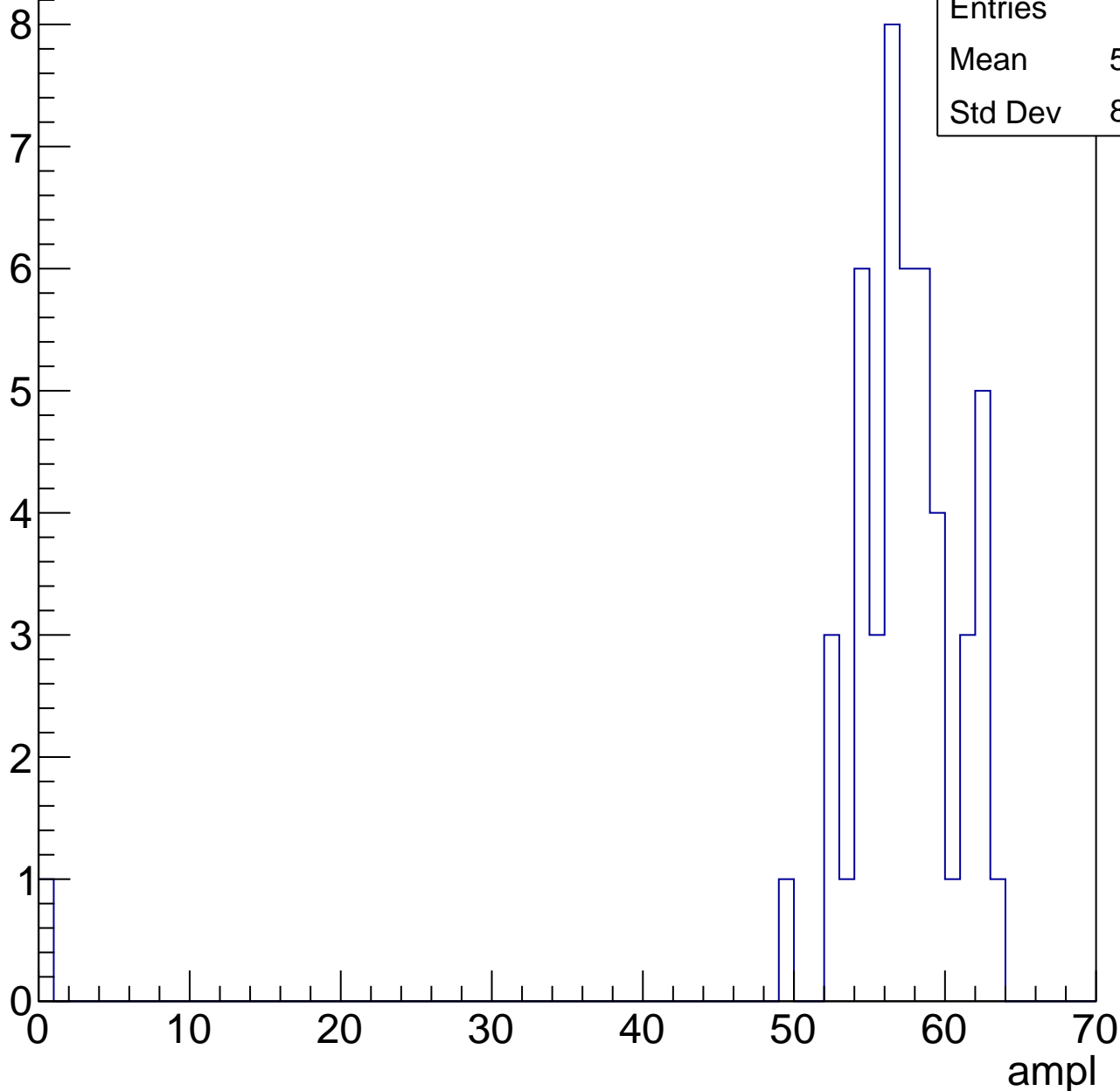


B0L001S, U25-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

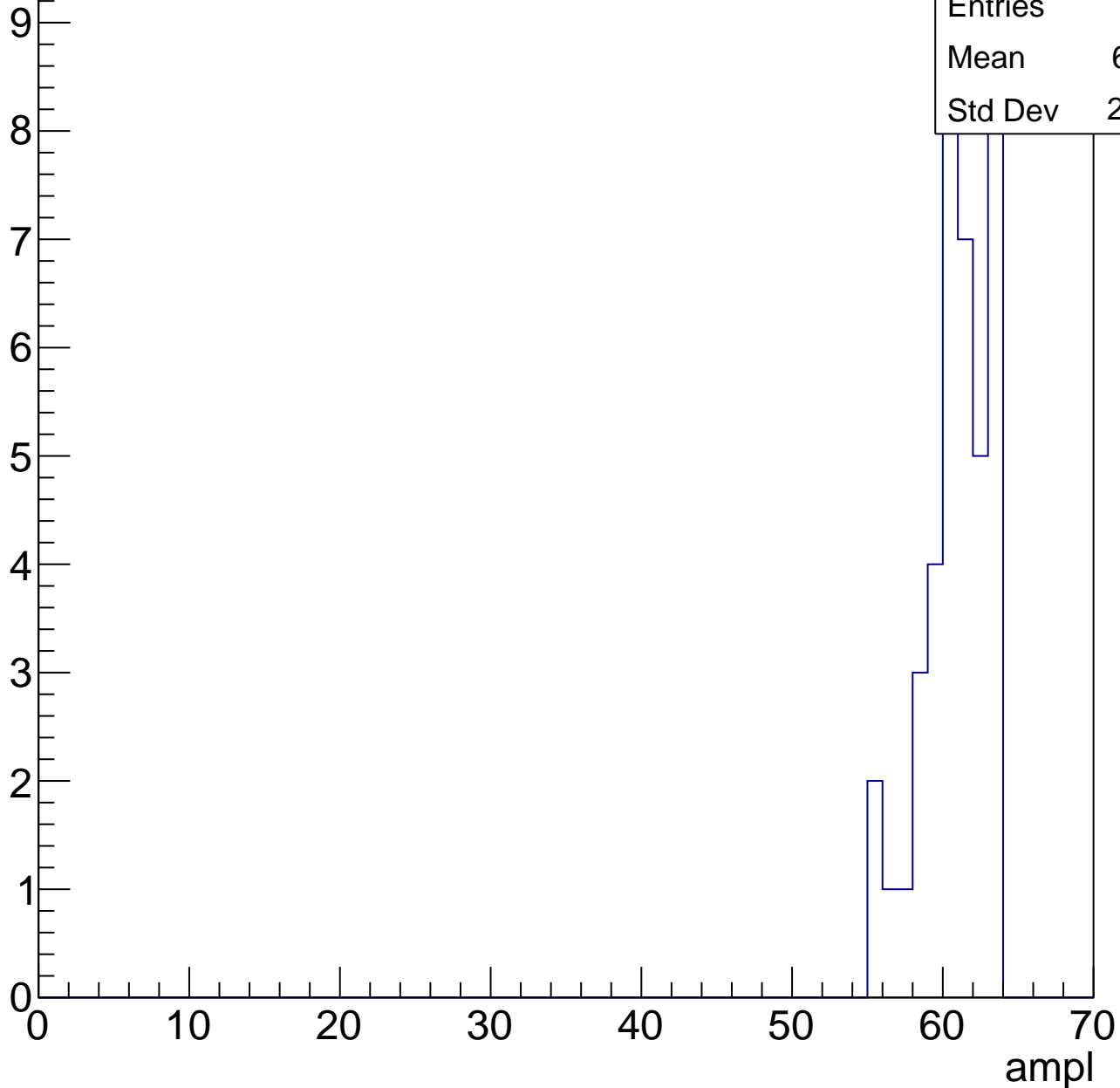
Entries	49
Mean	55.86
Std Dev	8.635



B0L001S, U25-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch63, adc7

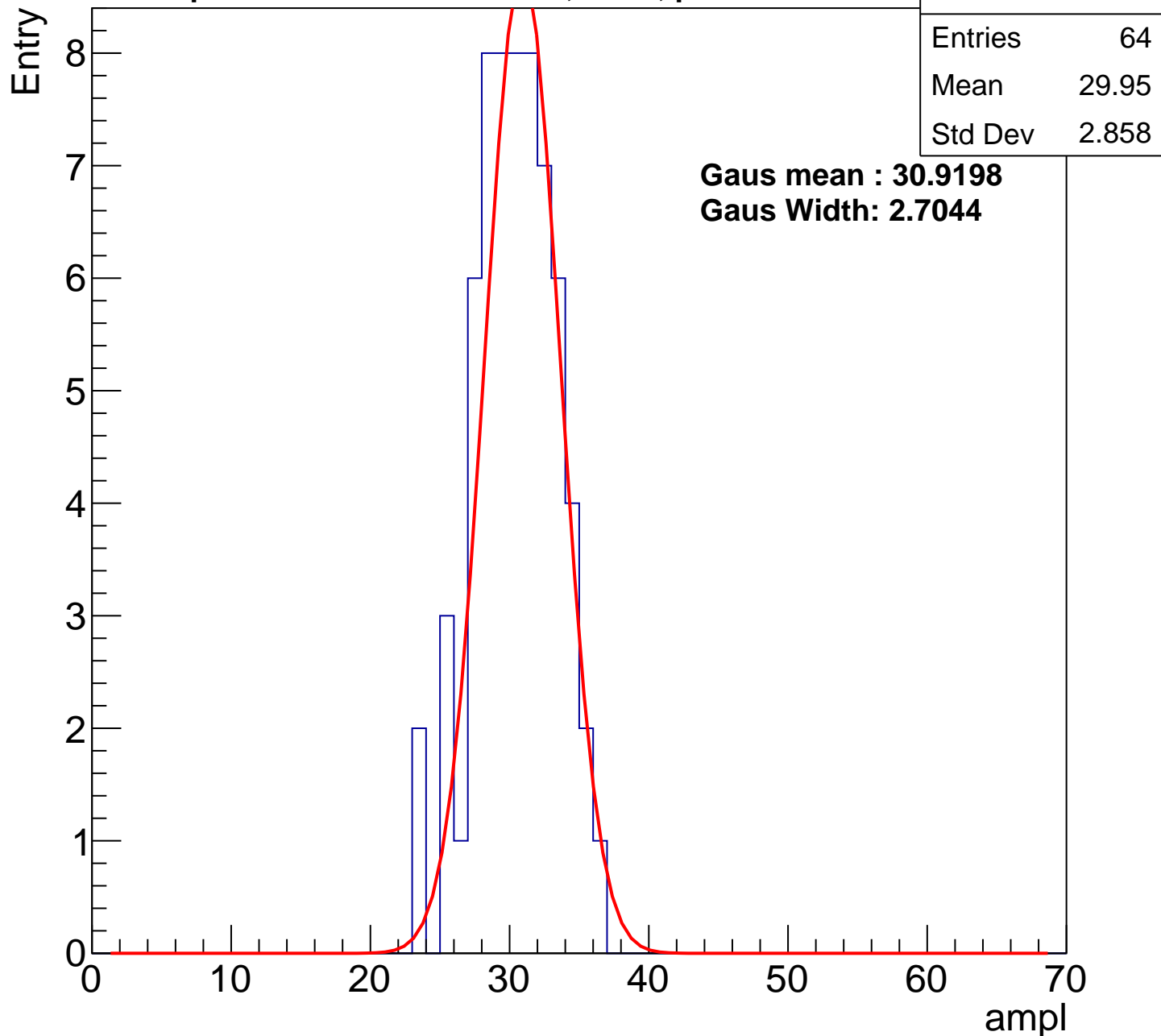
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch64, adc0

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch64, adc1

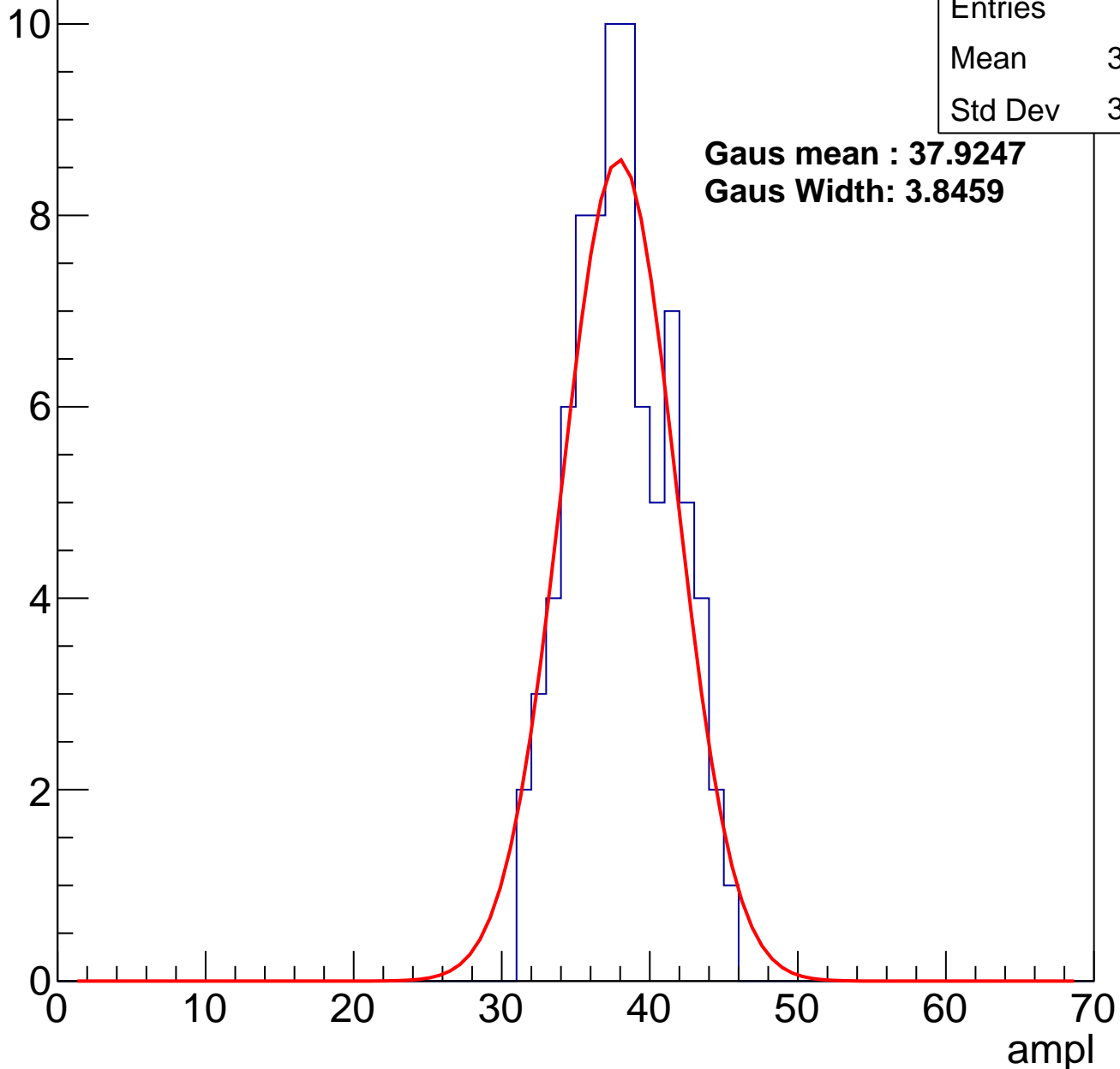
calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	37.63
Std Dev	3.324

Gaus mean : 37.9247

Gaus Width: 3.8459

Entry



B0L001S, U25-ch64, adc2

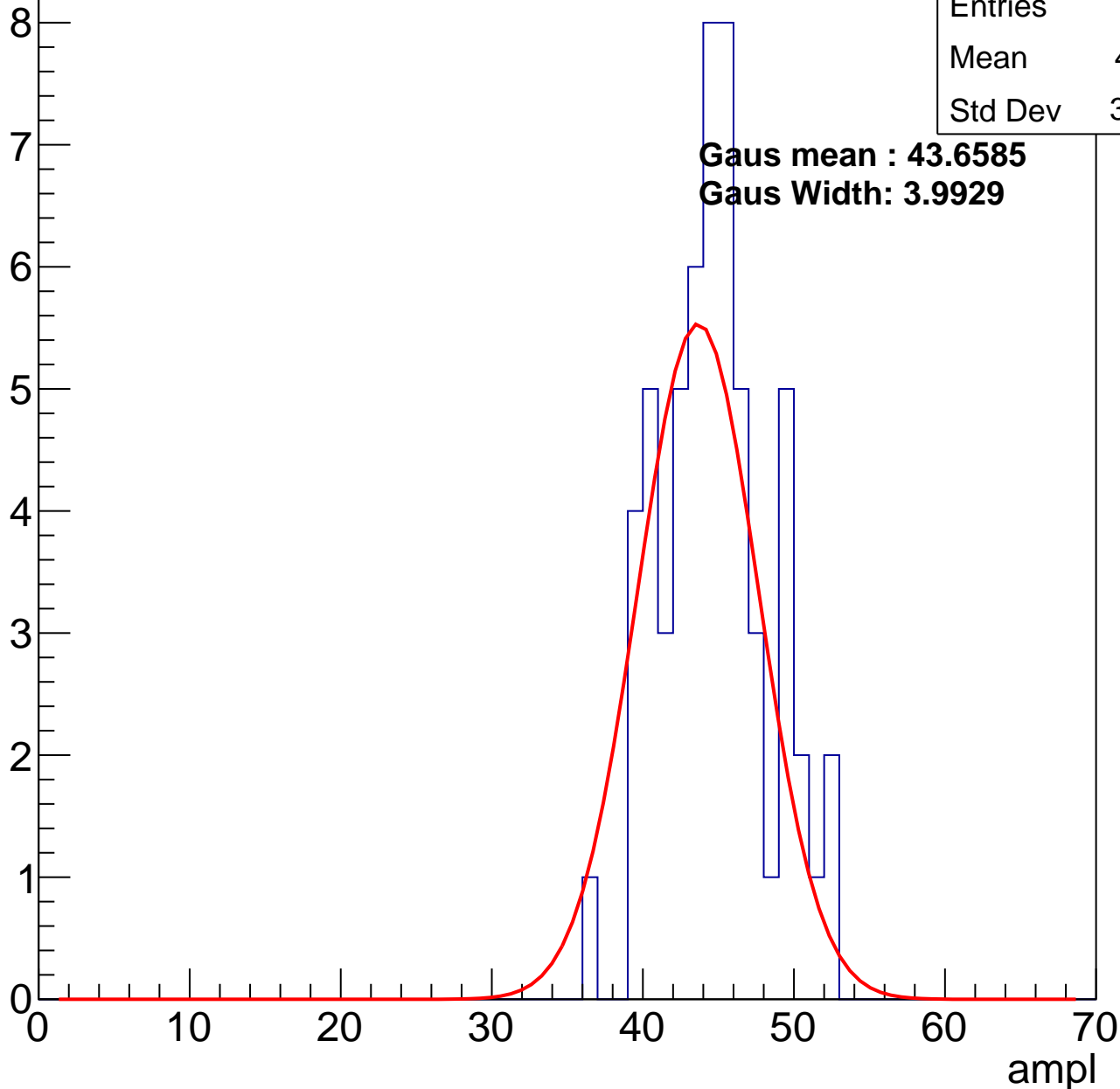
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.31
Std Dev	3.533

Gaus mean : 43.6585

Gaus Width: 3.9929

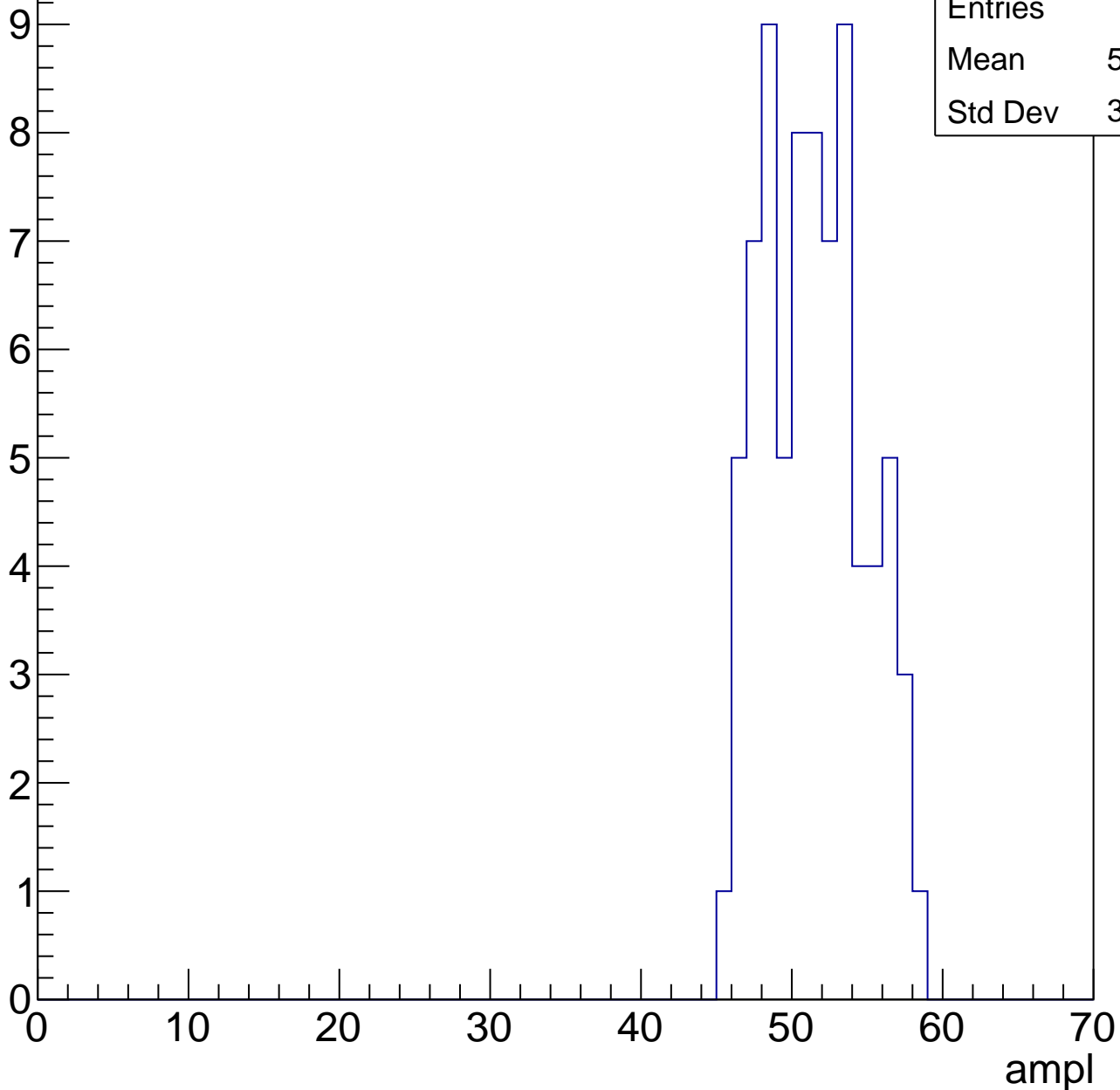


B0L001S, U25-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

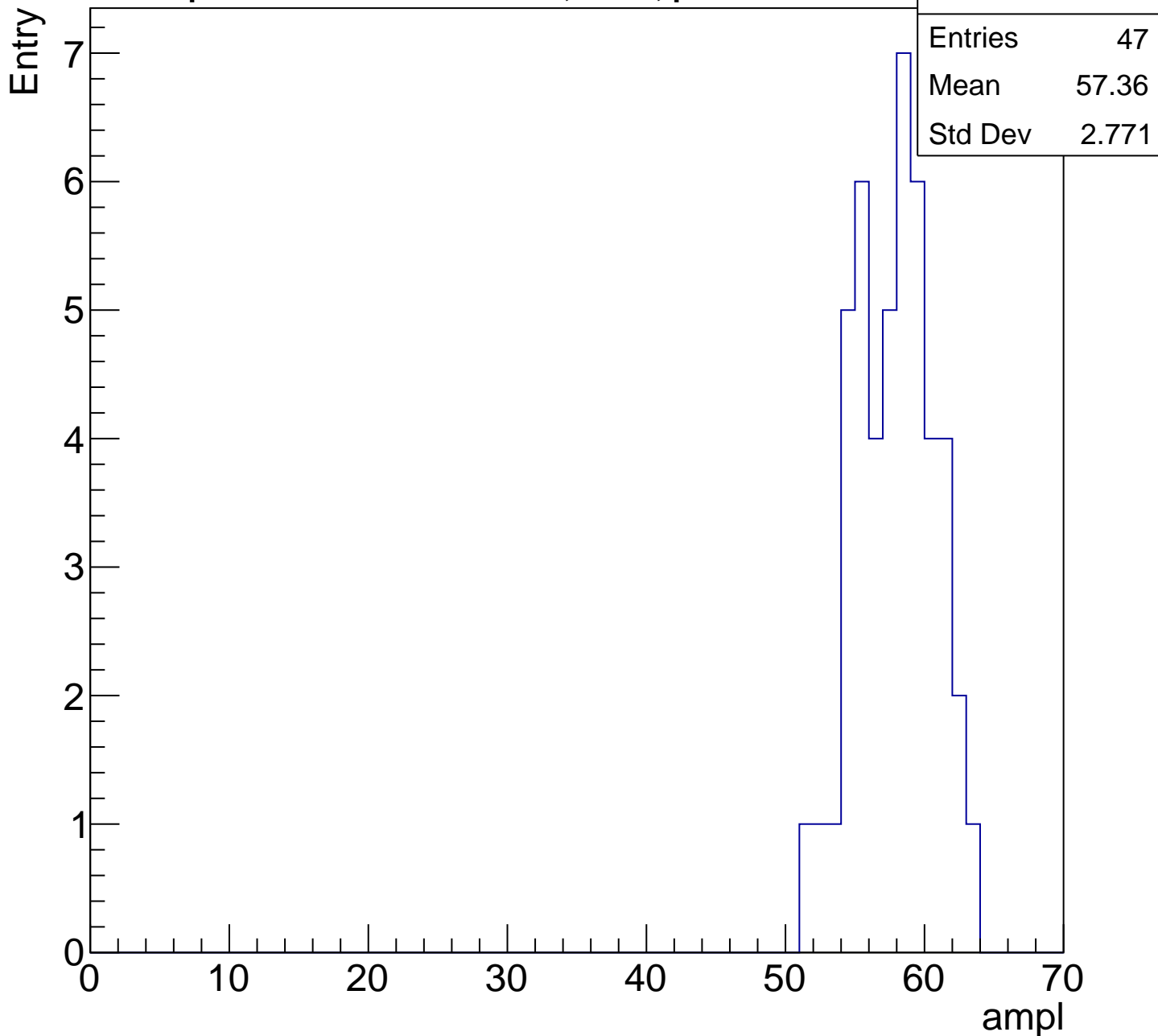
Entry

Entries	76
Mean	50.99
Std Dev	3.259



B0L001S, U25-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

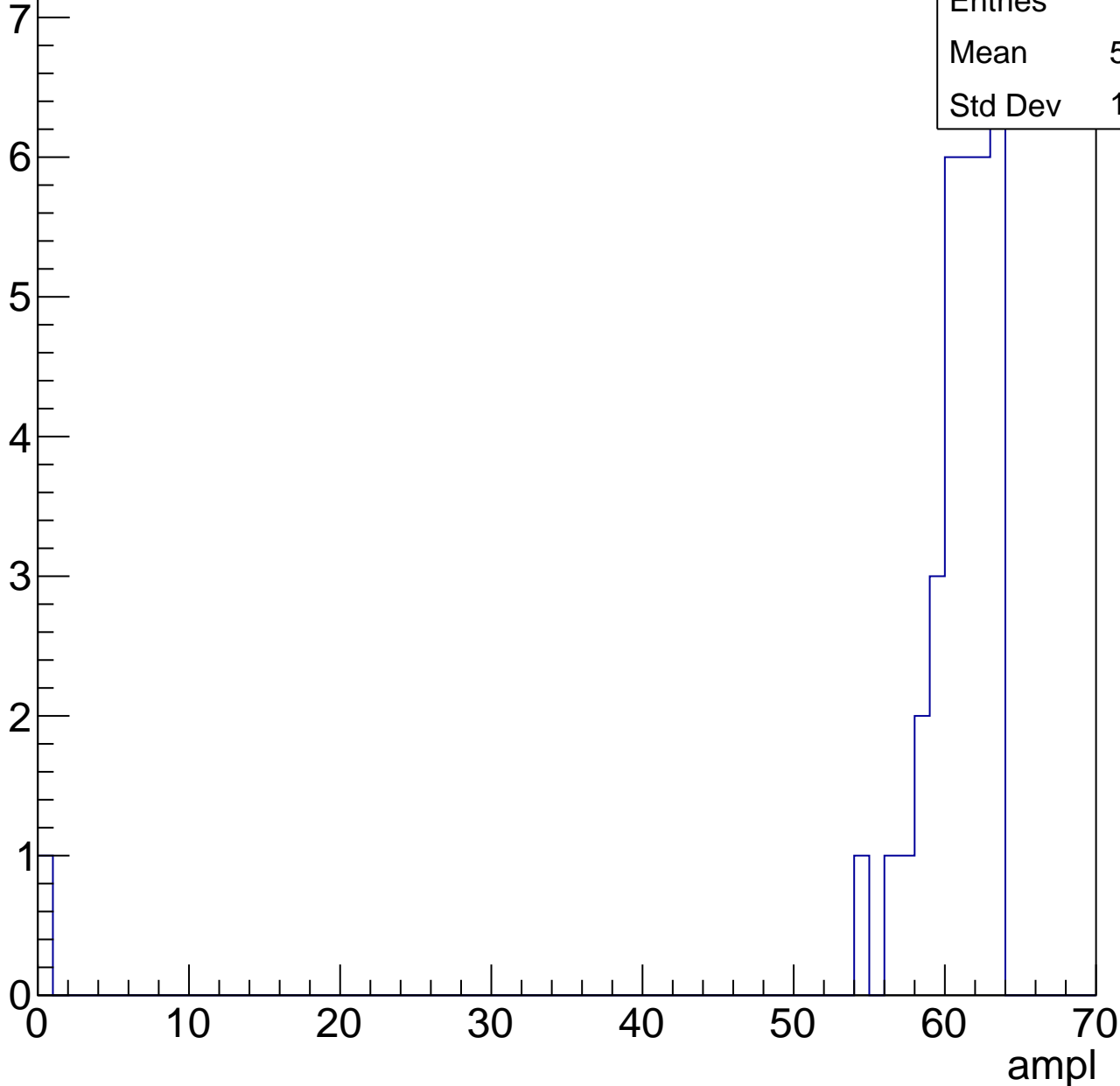


B0L001S, U25-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

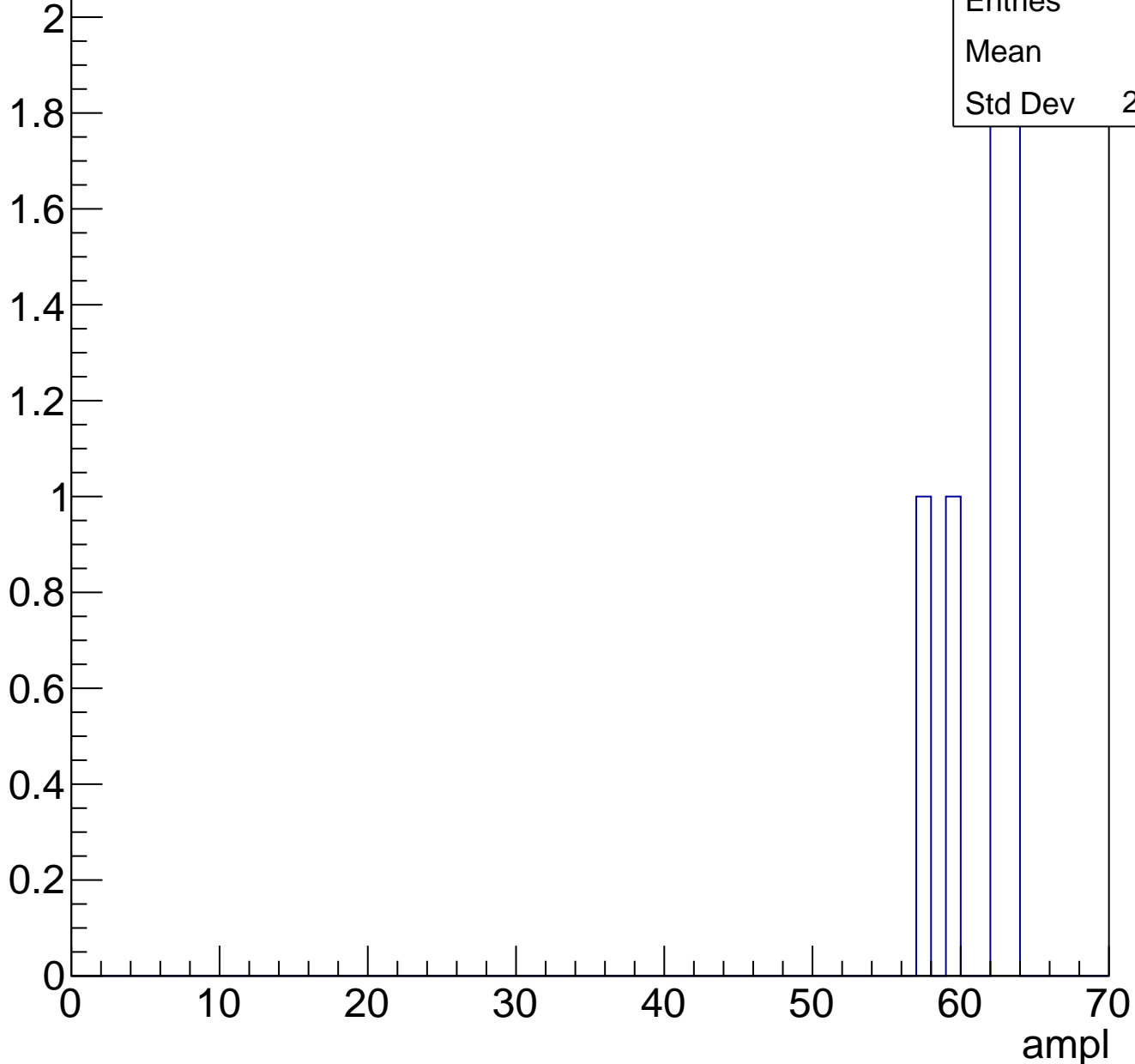
Entries	34
Mean	58.79
Std Dev	10.45



B0L001S, U25-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch65, adc0

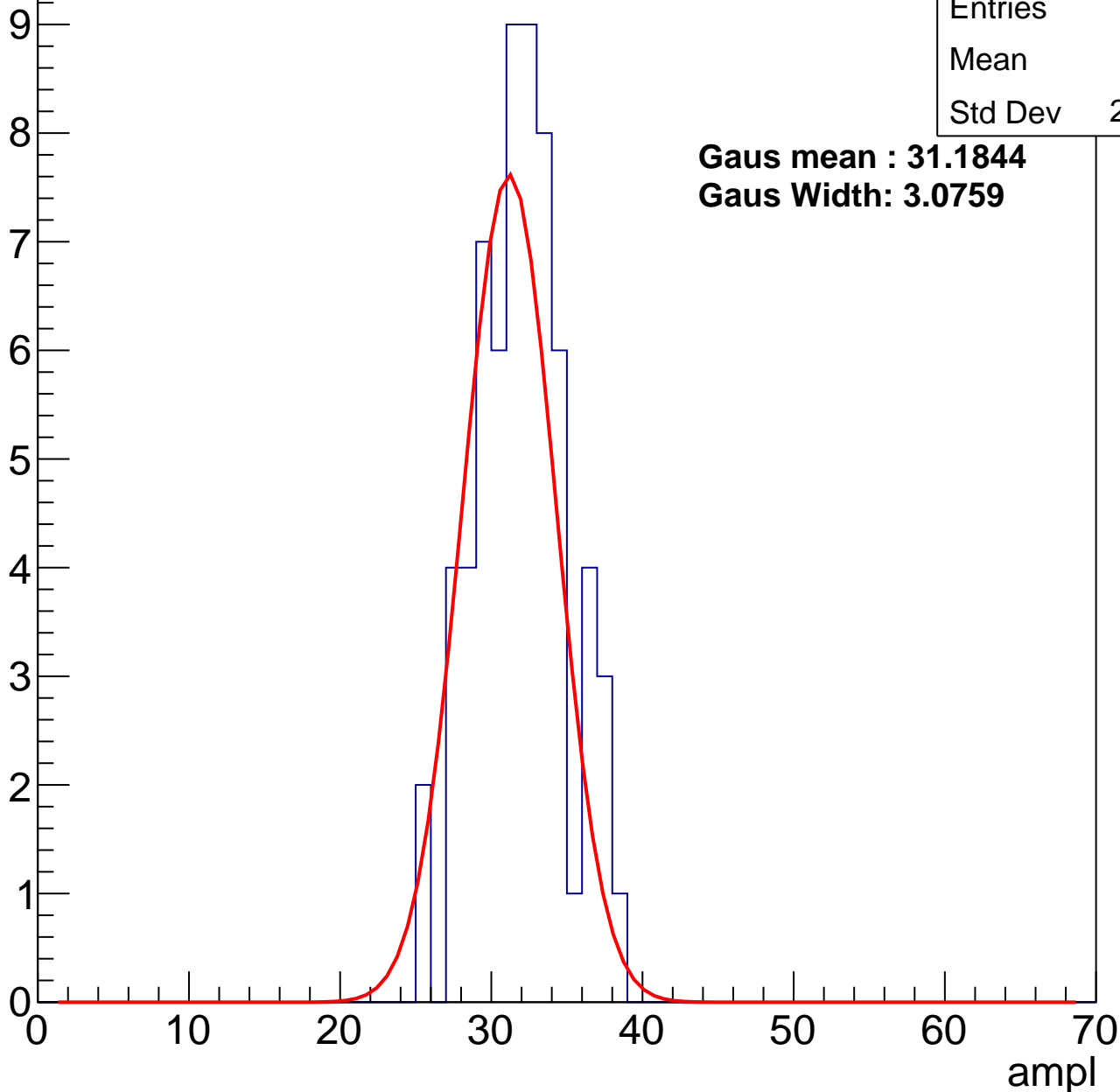
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.5
Std Dev	2.953

Gaus mean : 31.1844

Gaus Width: 3.0759



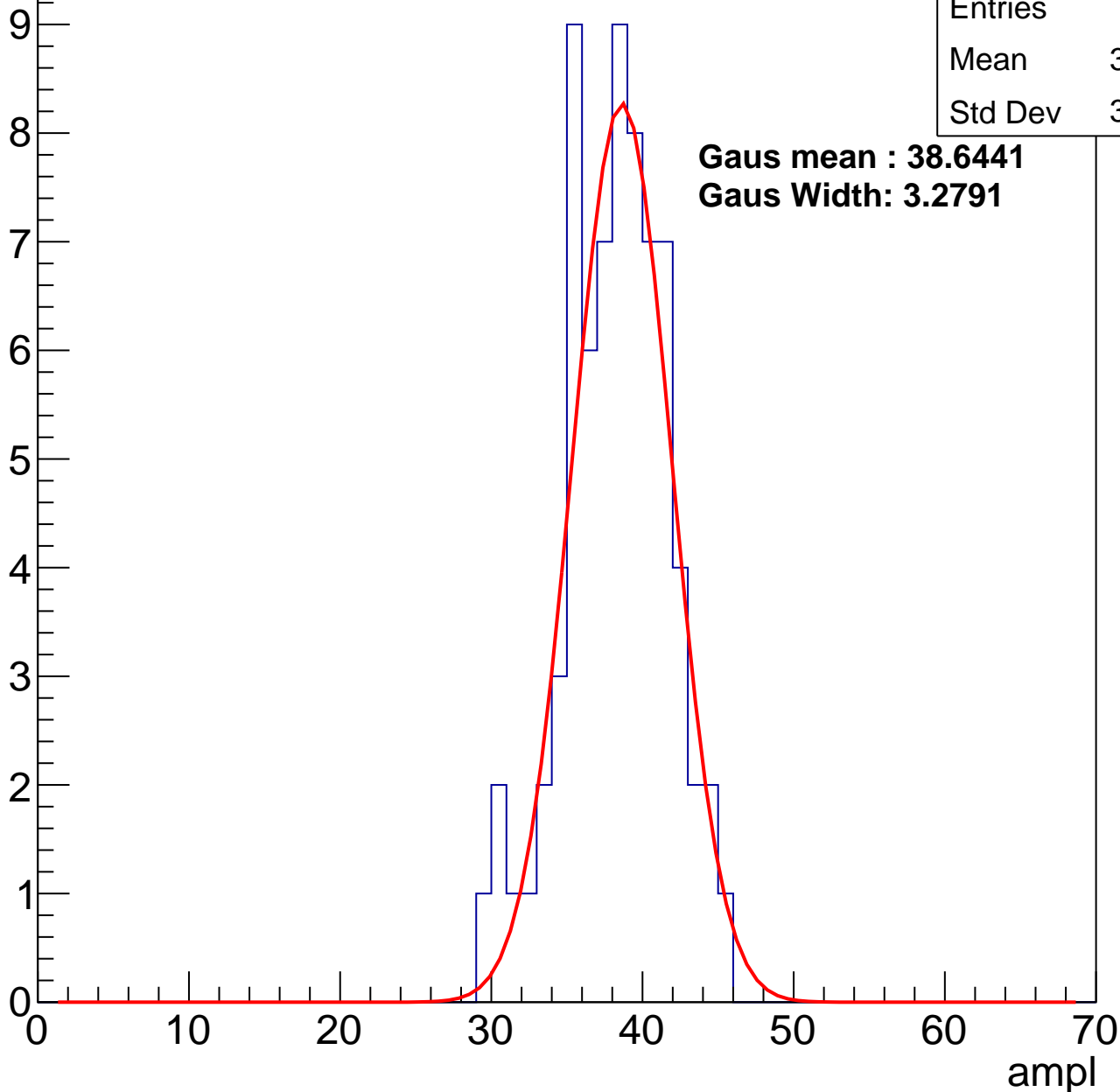
B0L001S, U25-ch65, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.75
Std Dev	3.402

Gaus mean : 38.6441
Gaus Width: 3.2791



B0L001S, U25-ch65, adc2

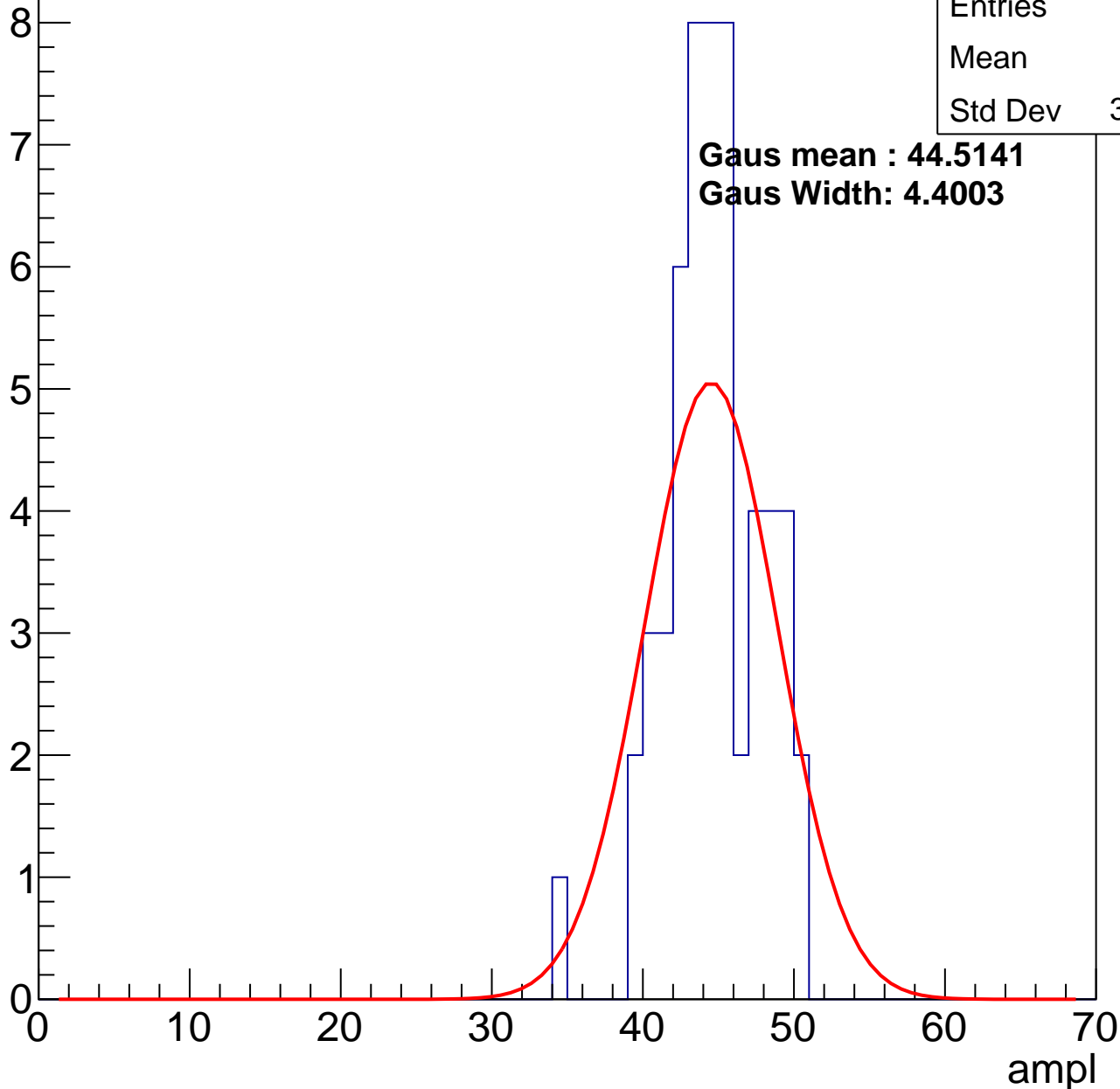
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.2
Std Dev	3.142

Gaus mean : 44.5141

Gaus Width: 4.4003



B0L001S, U25-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

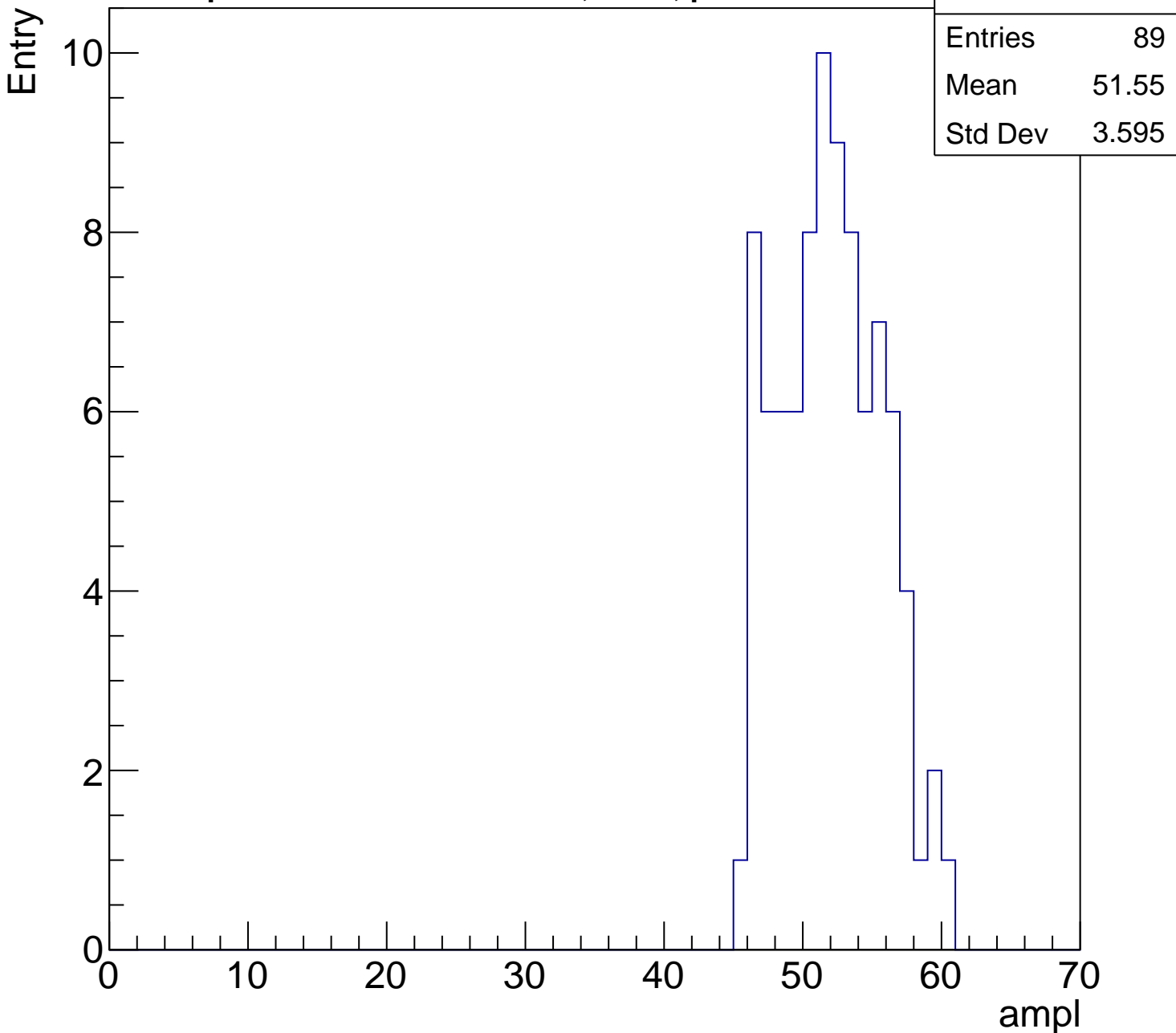
Entries	89
Mean	51.55
Std Dev	3.595

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

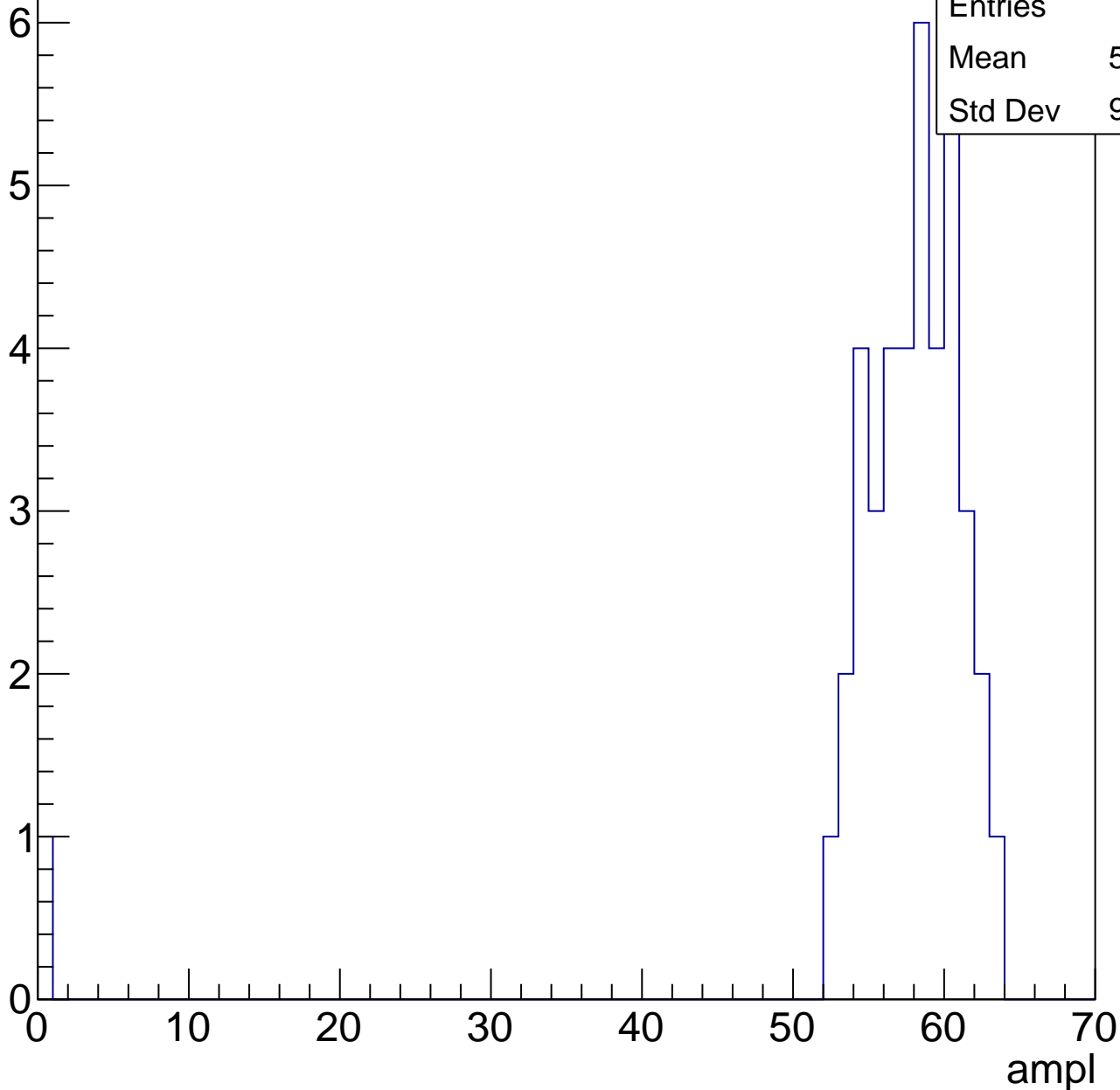


B0L001S, U25-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	56.22
Std Dev	9.296

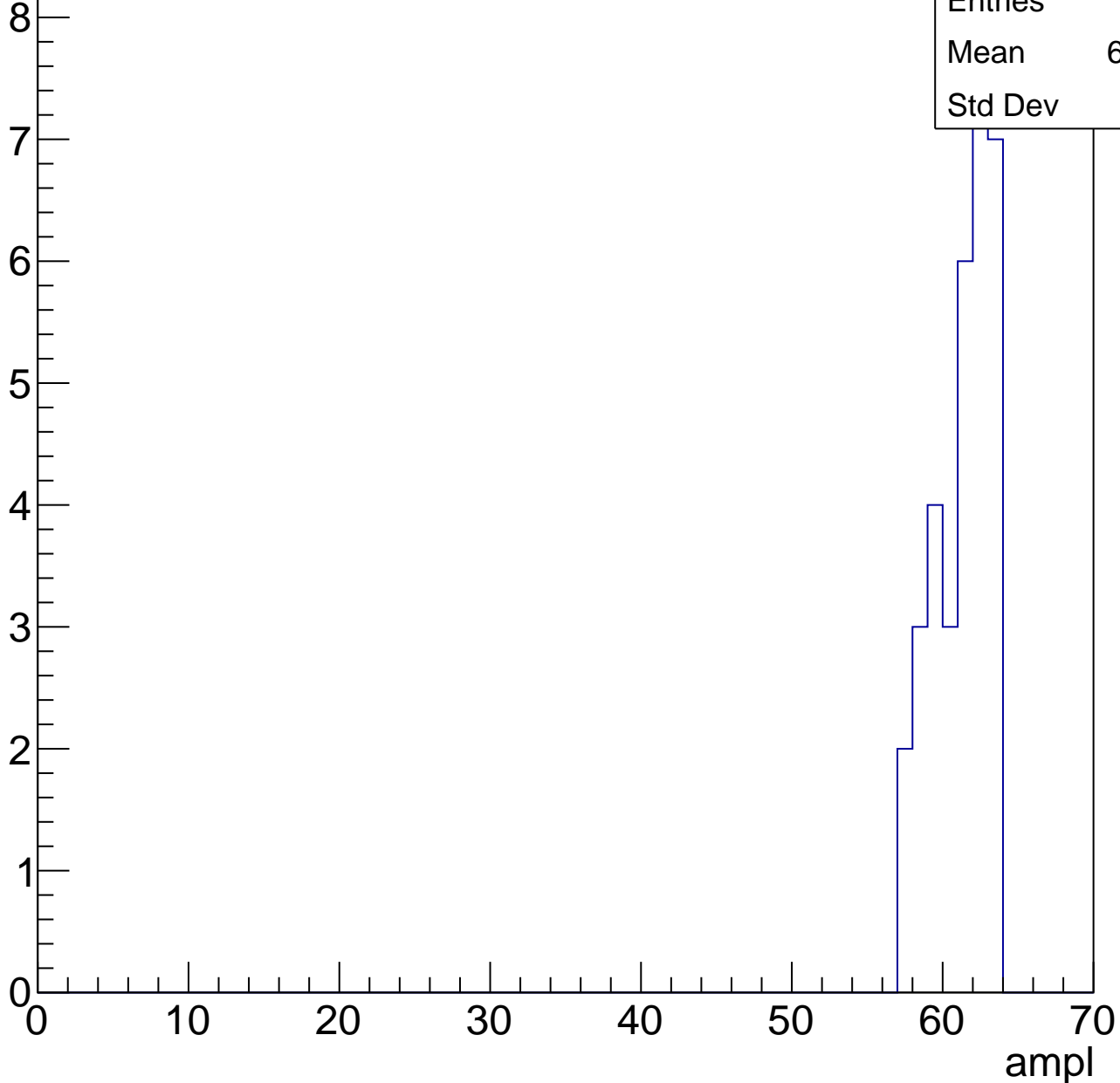


B0L001S, U25-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	60.82
Std Dev	1.85



B0L001S, U25-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B0L001S, U25-ch66, adc0

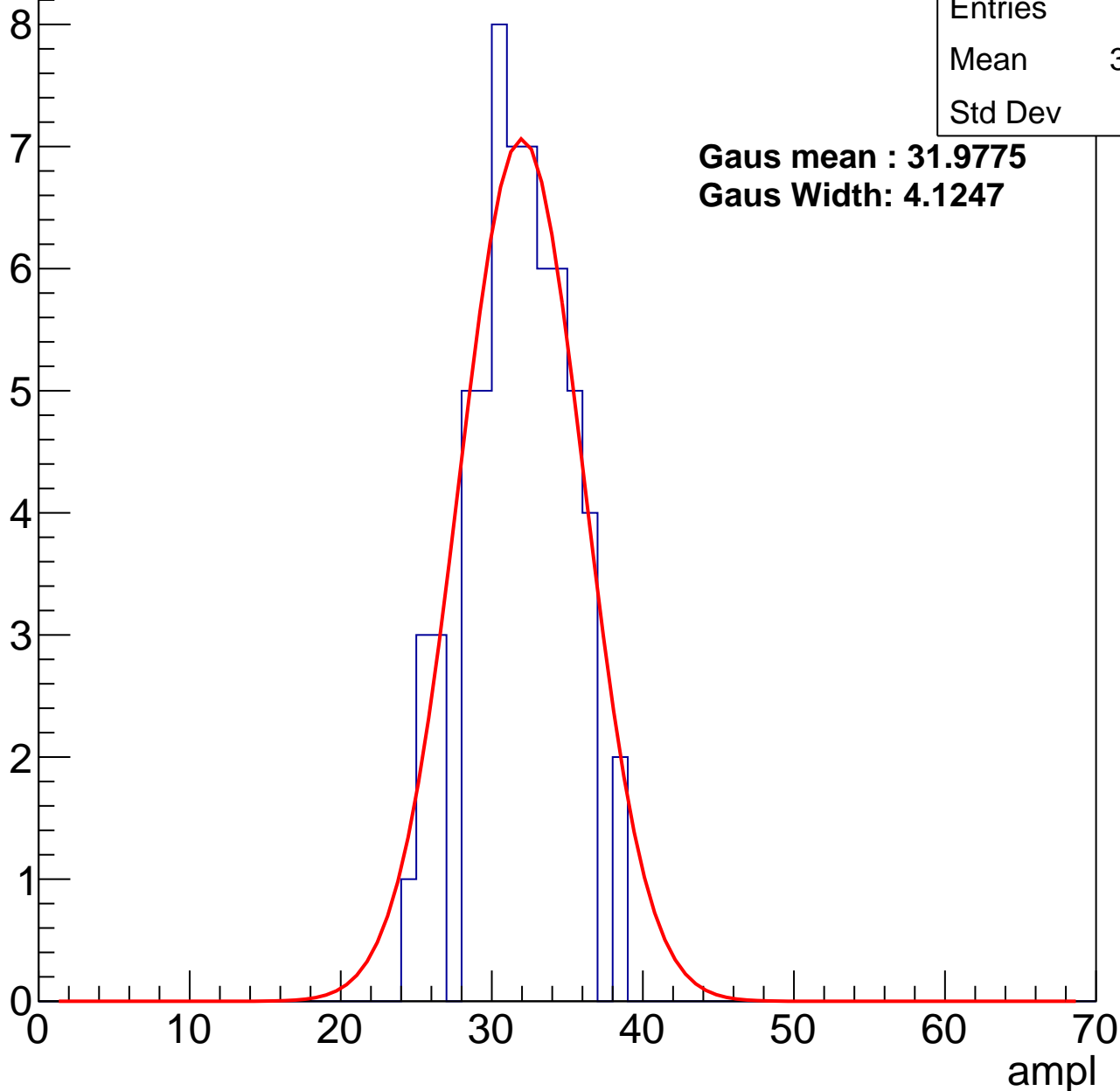
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.29
Std Dev	3.27

Gaus mean : 31.9775

Gaus Width: 4.1247



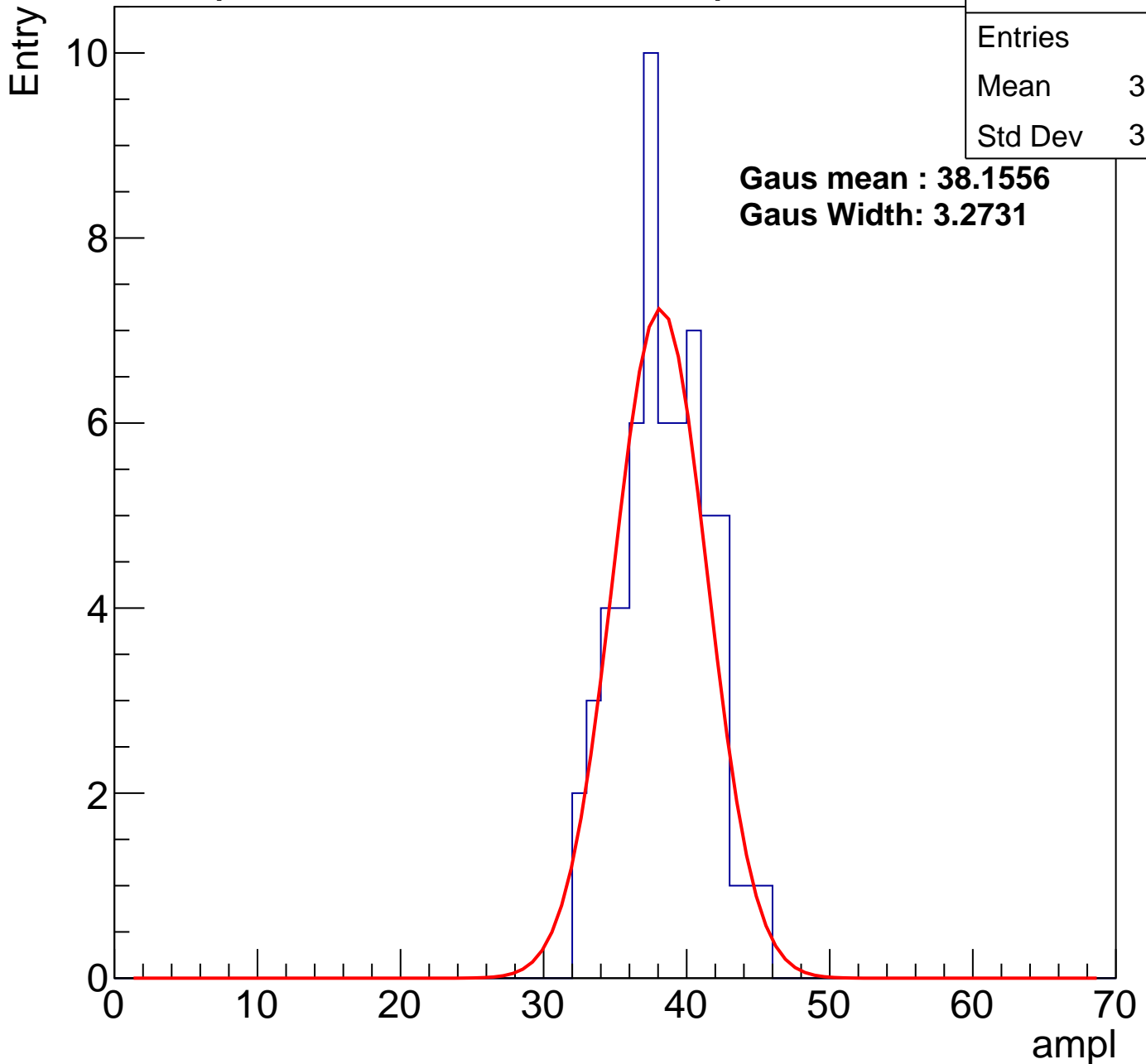
B0L001S, U25-ch66, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	37.93
Std Dev	3.002

Gaus mean : 38.1556

Gaus Width: 3.2731



B0L001S, U25-ch66, adc2

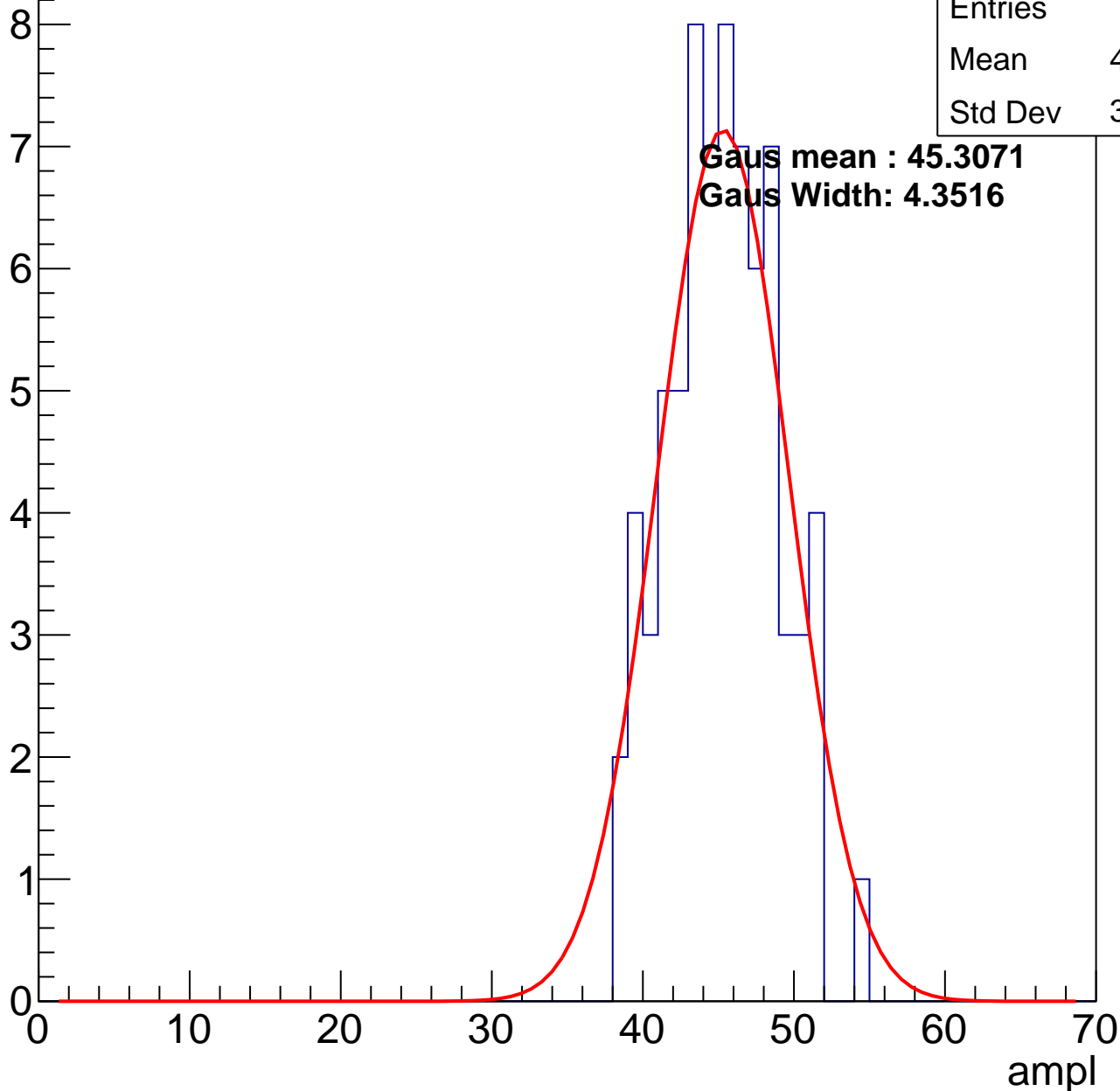
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.85
Std Dev	3.553

Gaus mean : 45.3071

Gaus Width: 4.3516

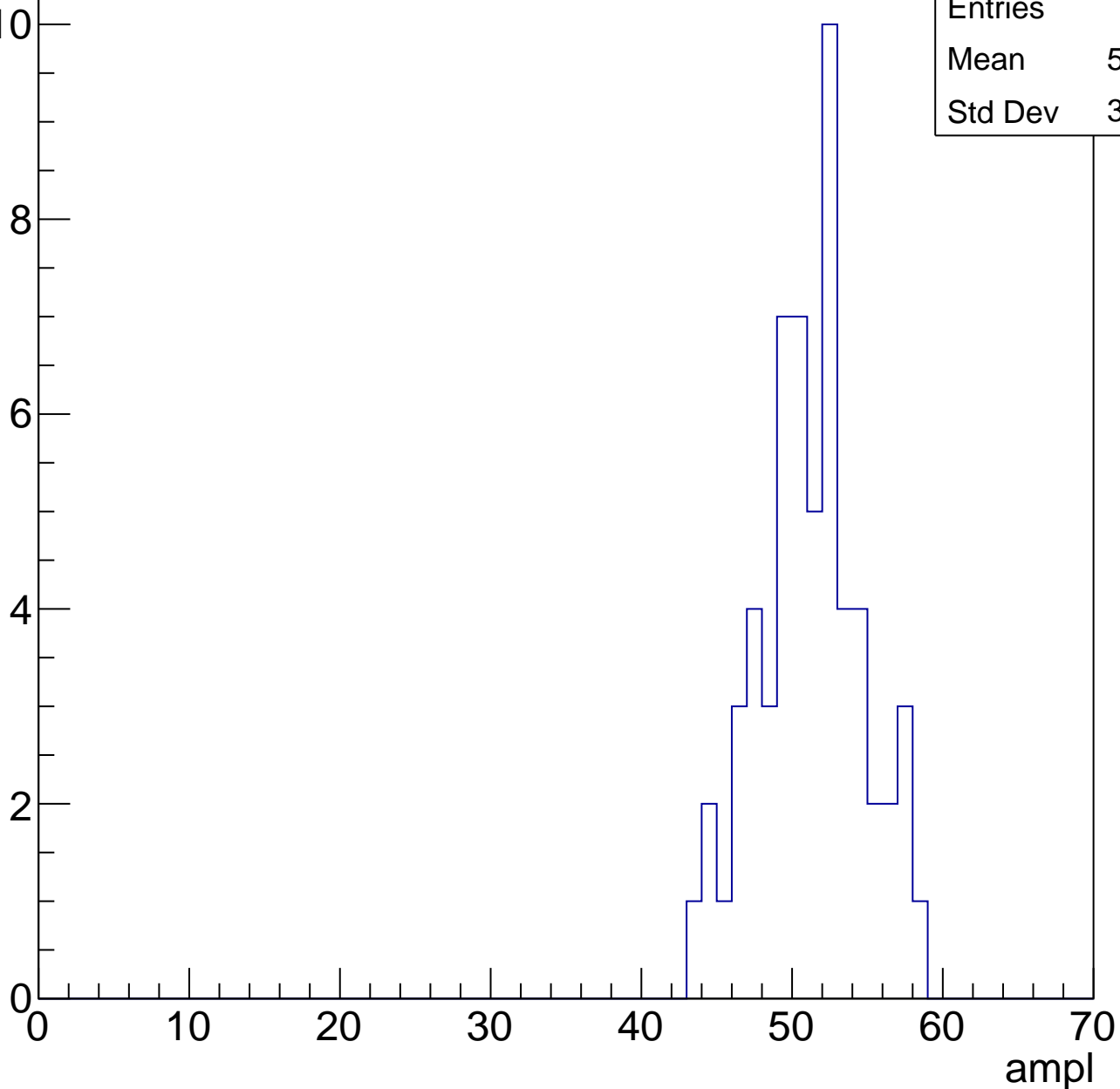


B0L001S, U25-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

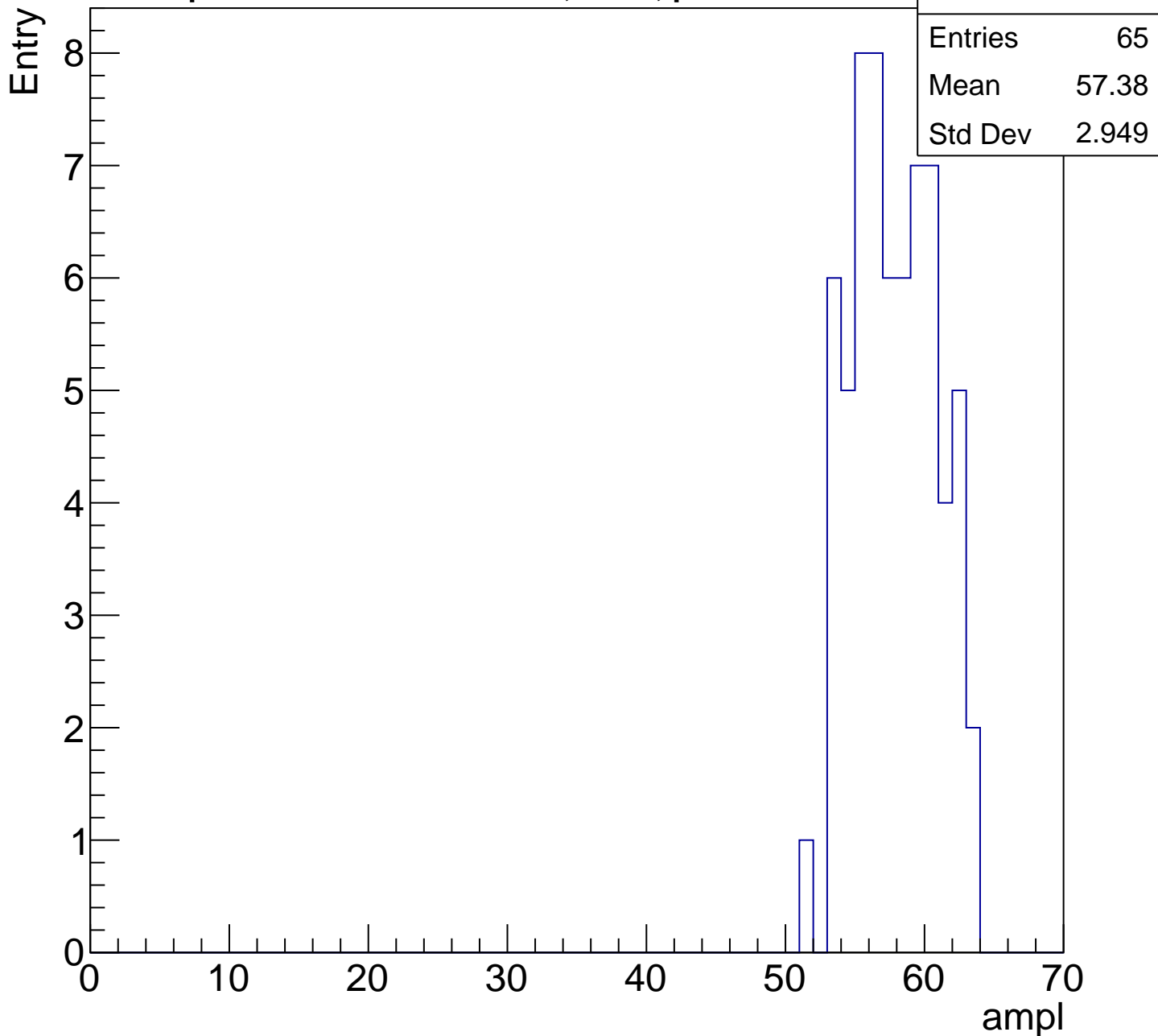
Entry

Entries	59
Mean	50.73
Std Dev	3.434



B0L001S, U25-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

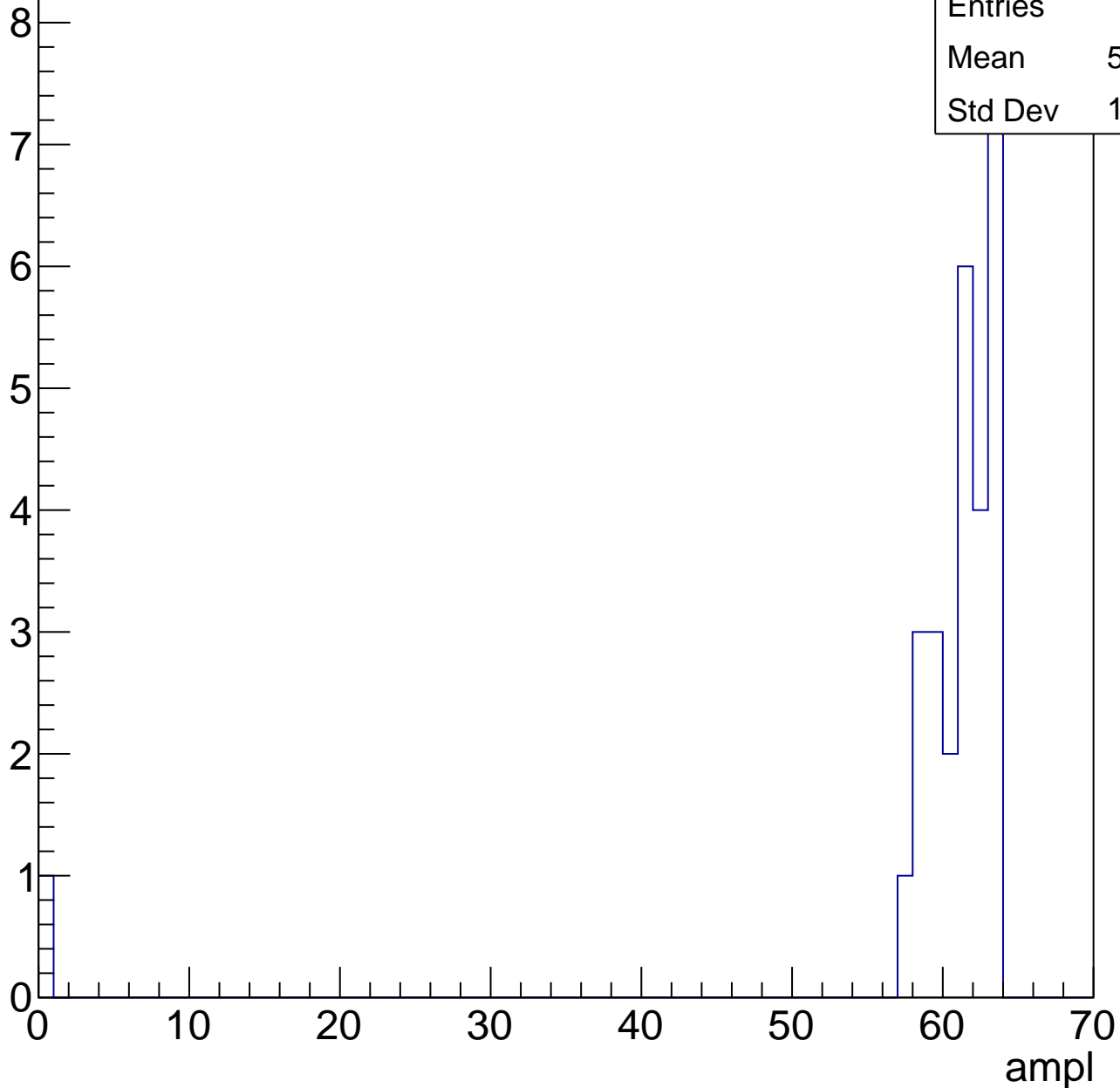


B0L001S, U25-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.79
Std Dev	11.46



B0L001S, U25-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

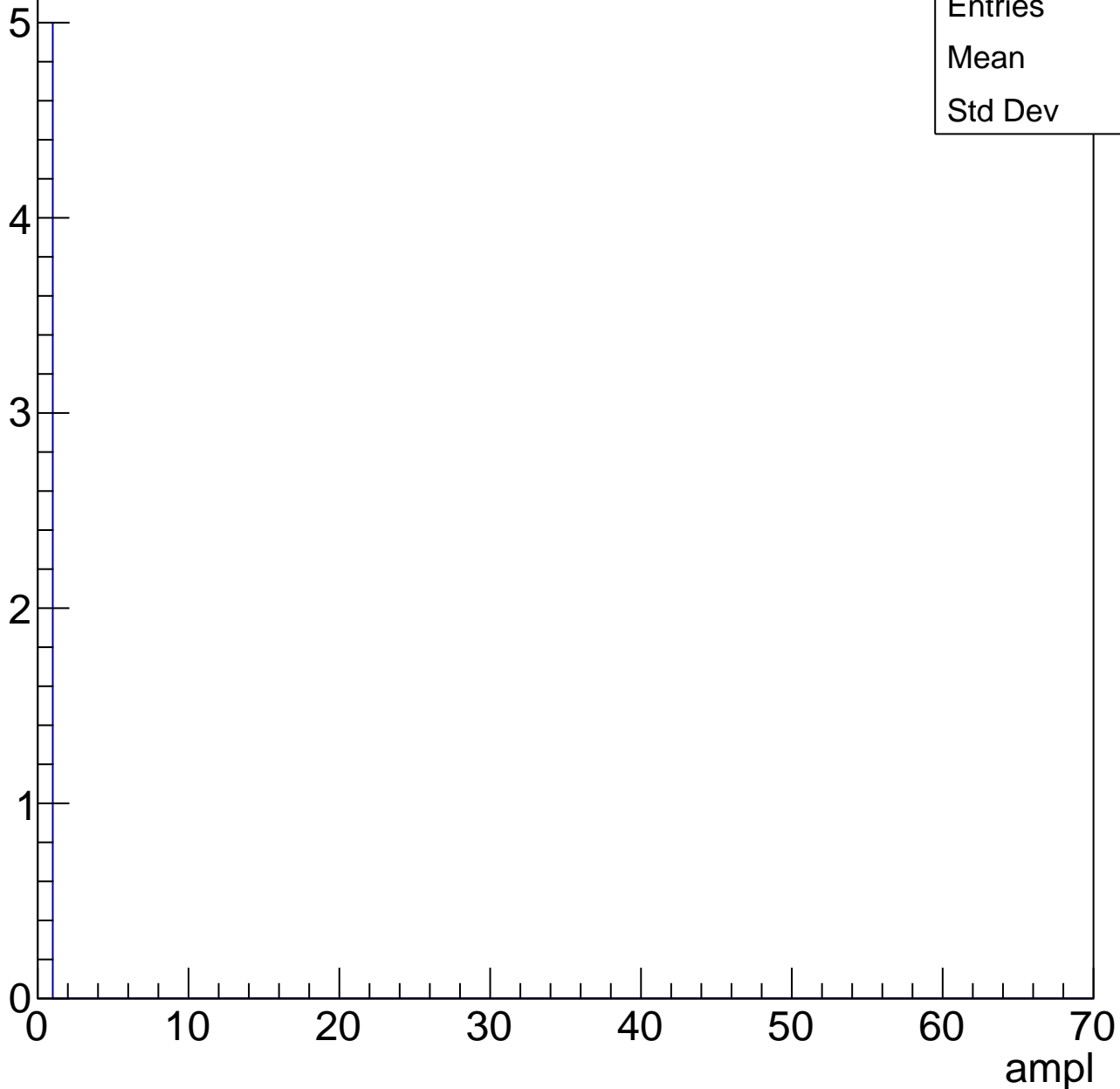


B0L001S, U25-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	5
Mean	0
Std Dev	0



B0L001S, U25-ch67, adc0

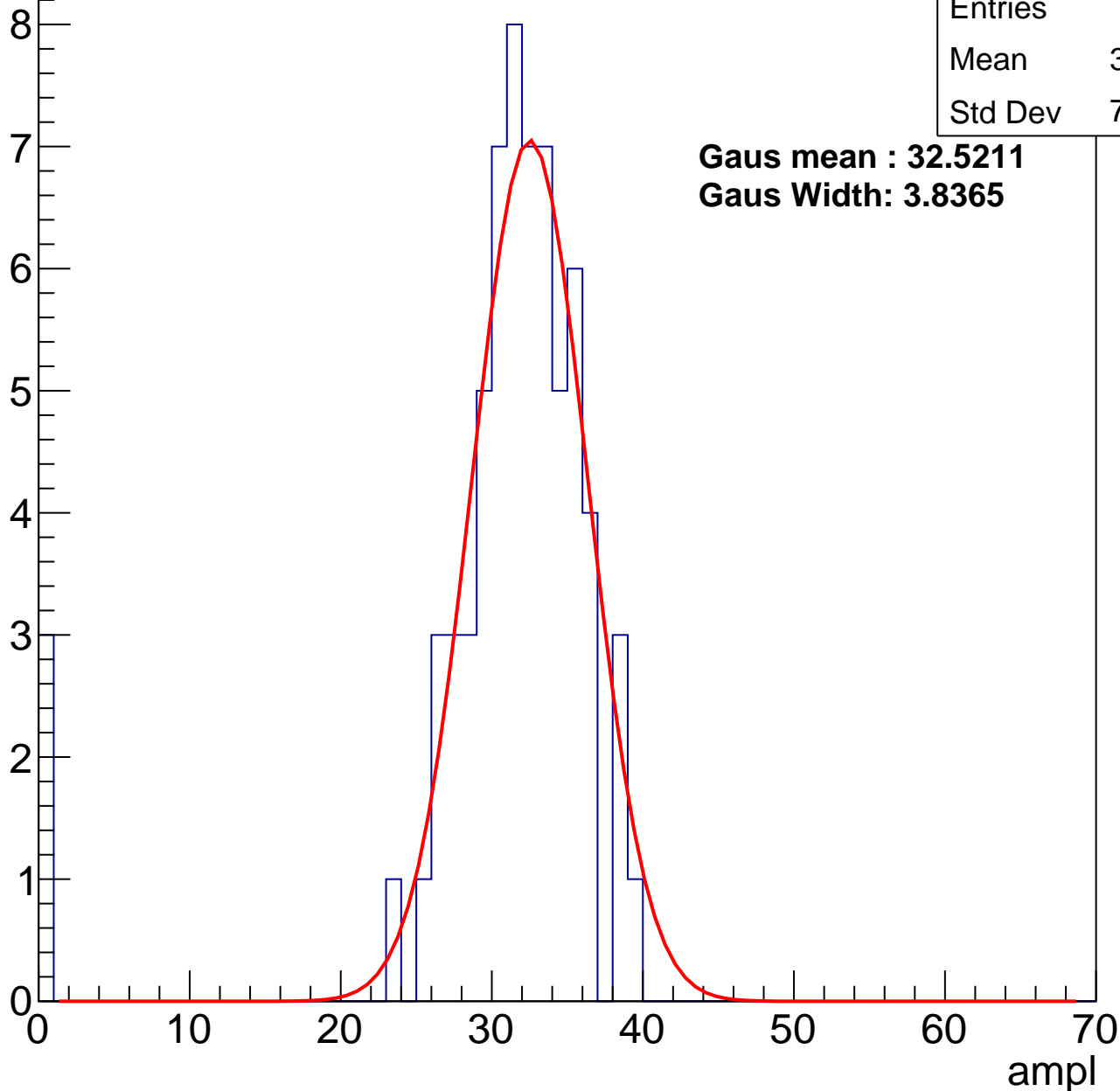
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.24
Std Dev	7.338

Gaus mean : 32.5211

Gaus Width: 3.8365



B0L001S, U25-ch67, adc1

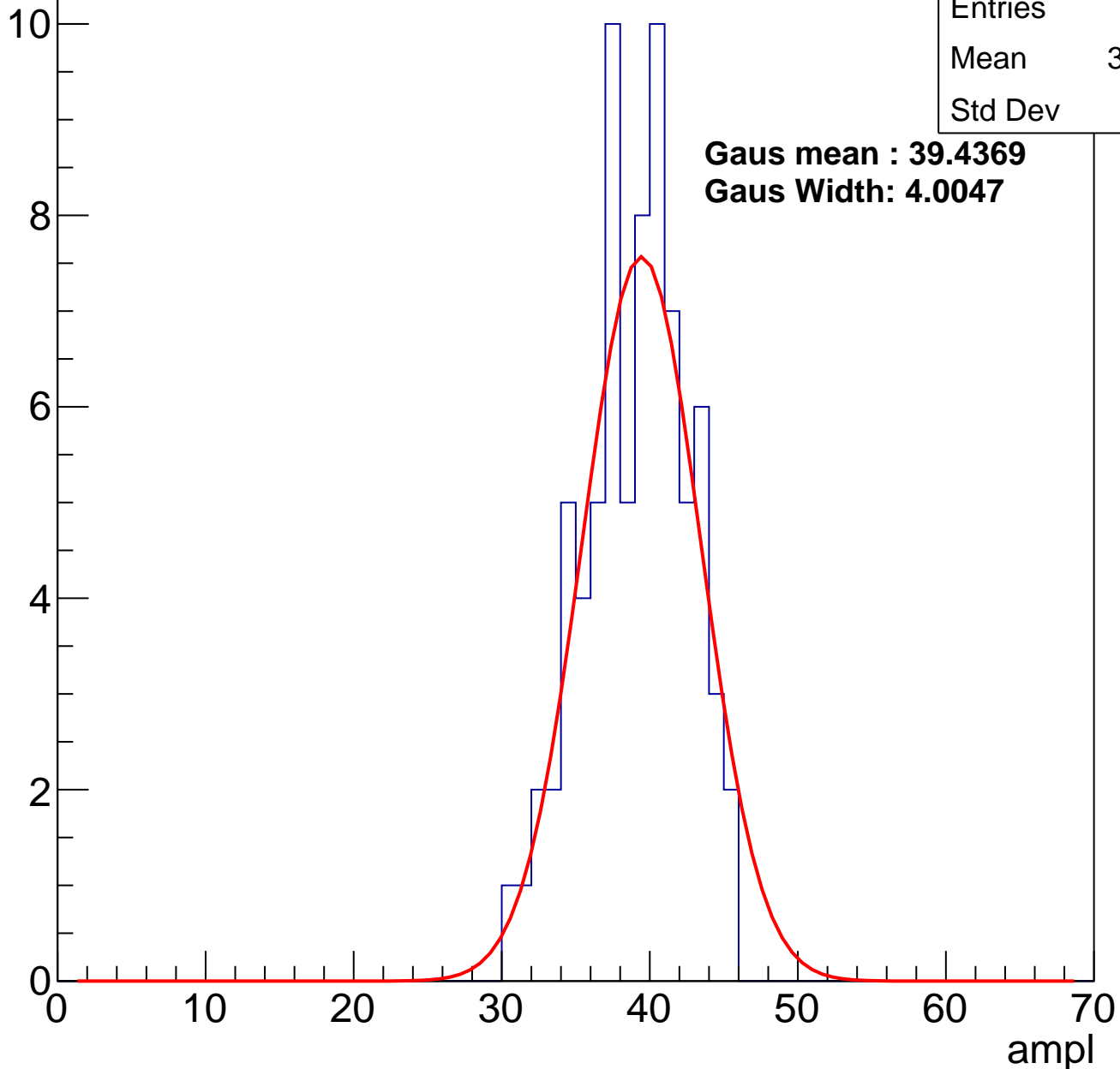
calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	38.55
Std Dev	3.45

Gaus mean : 39.4369

Gaus Width: 4.0047

Entry



B0L001S, U25-ch67, adc2

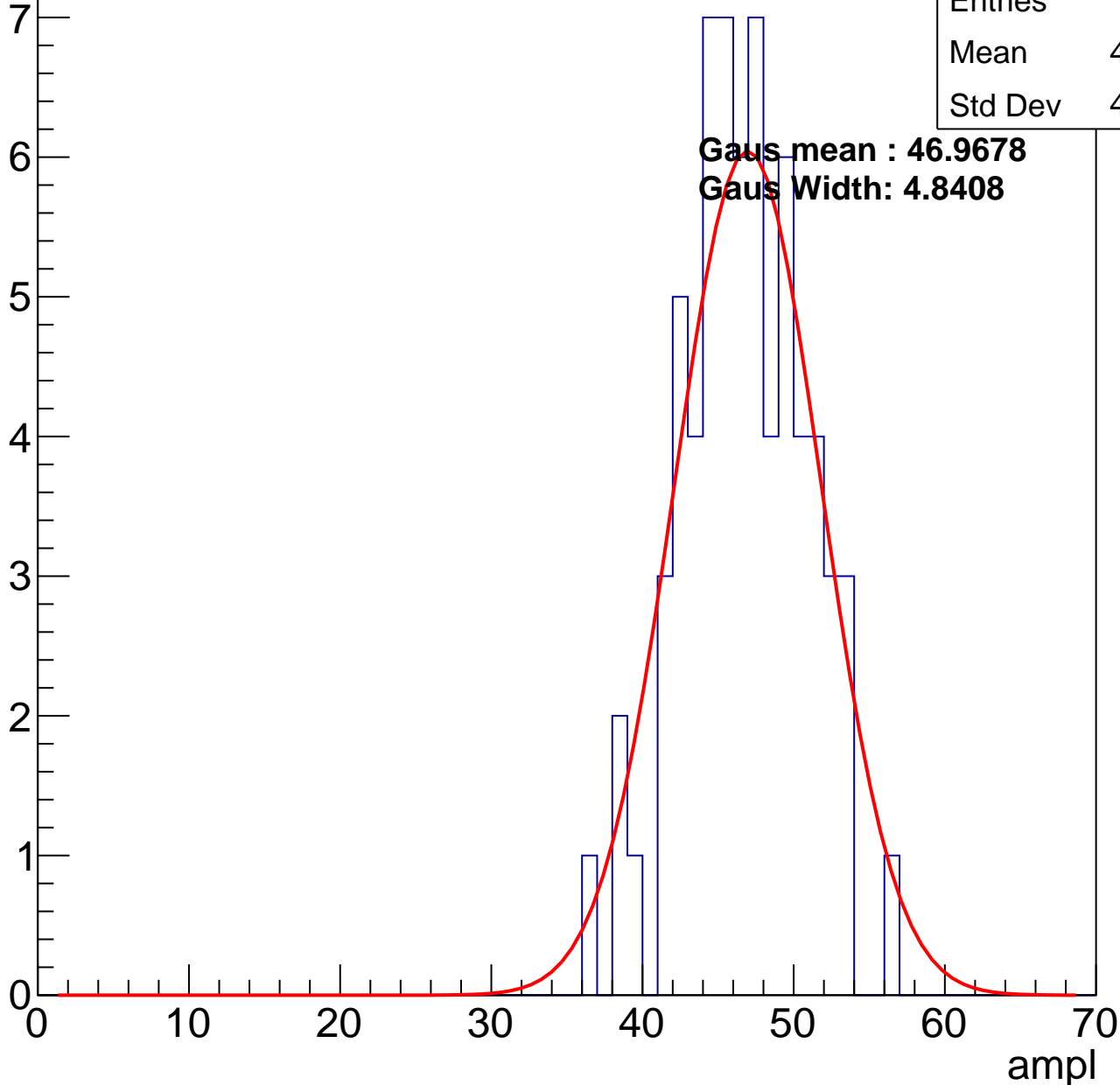
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	46.25
Std Dev	4.012

Gaus mean : 46.9678

Gaus Width: 4.8408

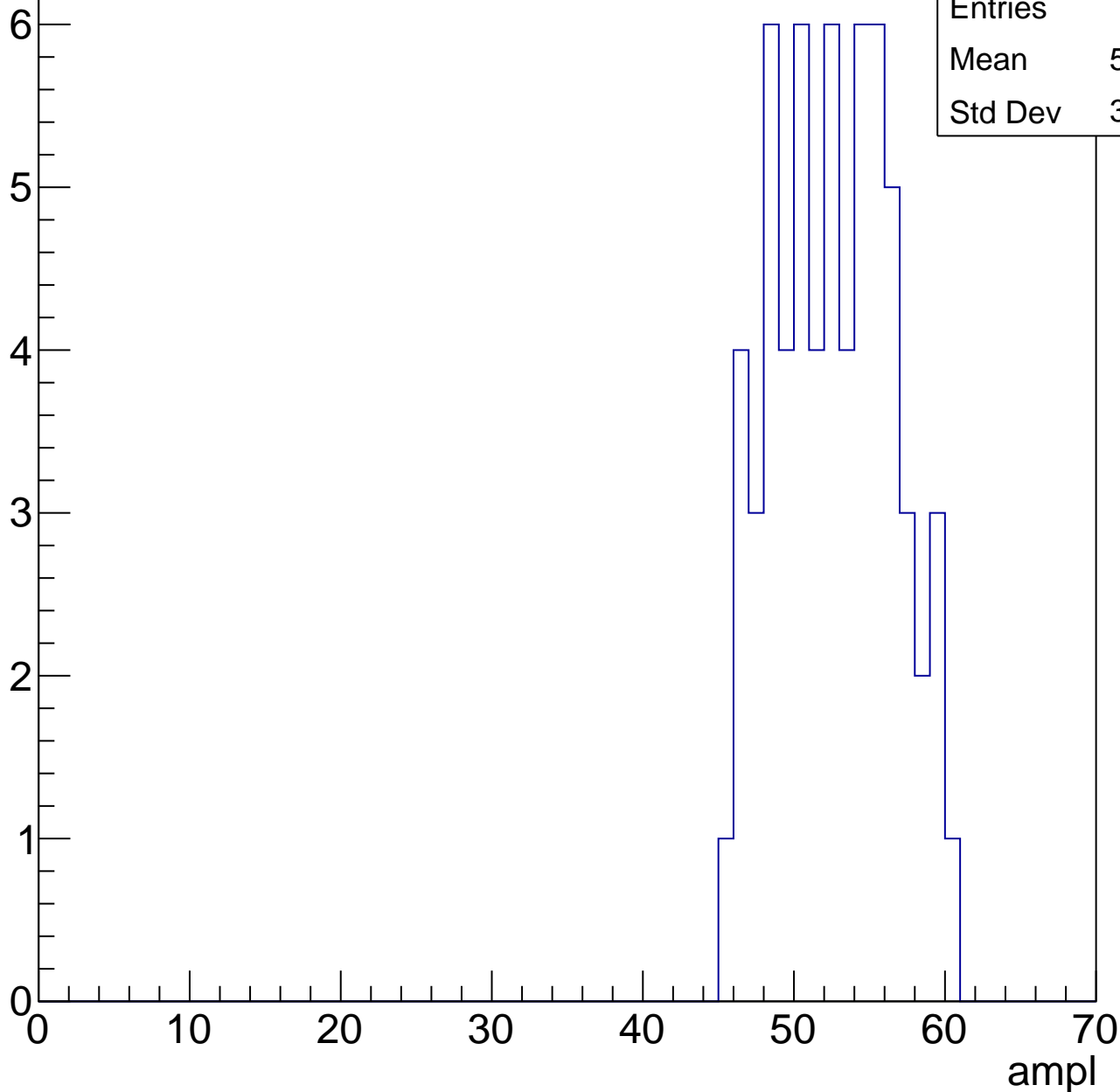


B0L001S, U25-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	52.19
Std Dev	3.844

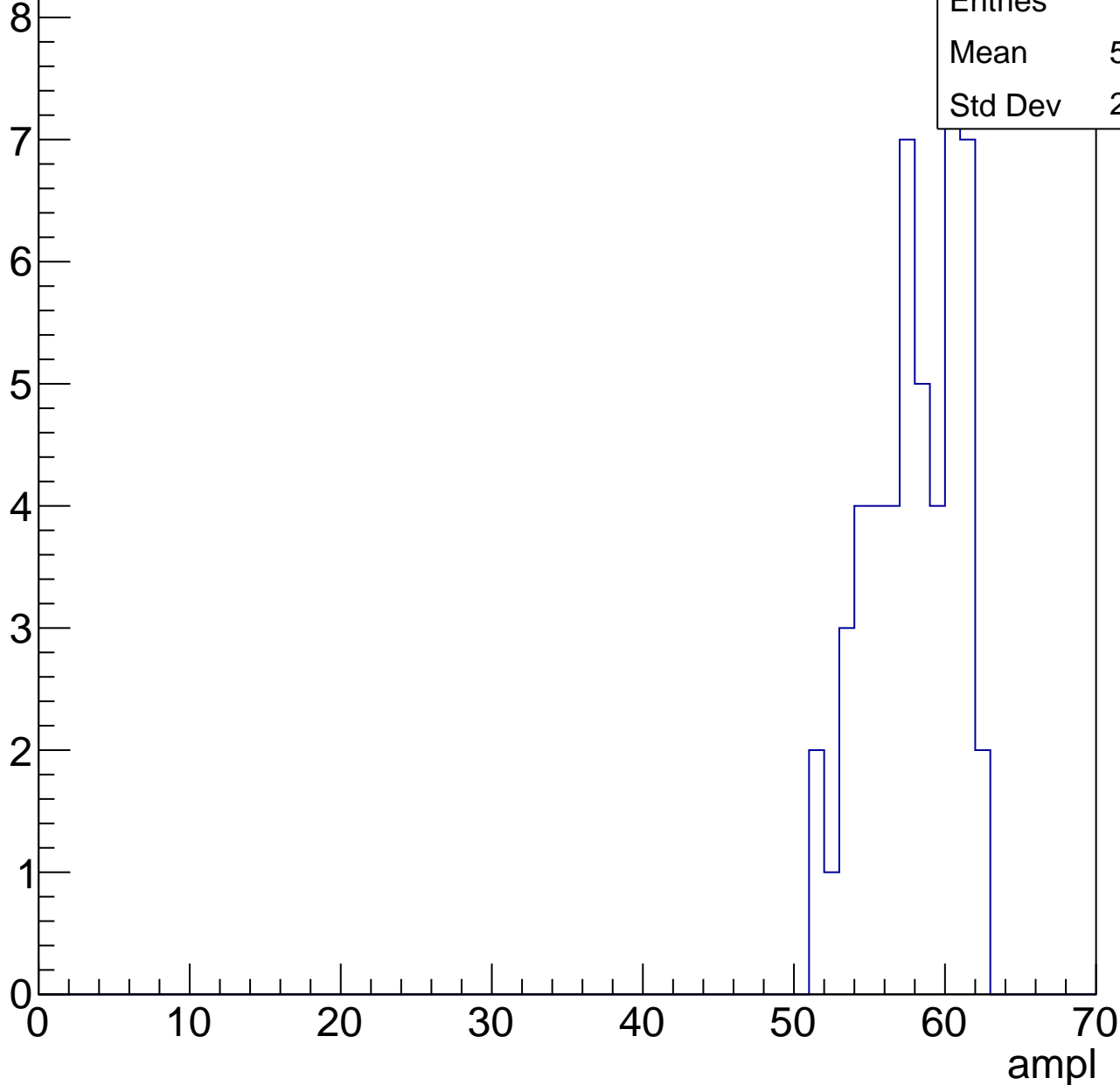


B0L001S, U25-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.43
Std Dev	2.959

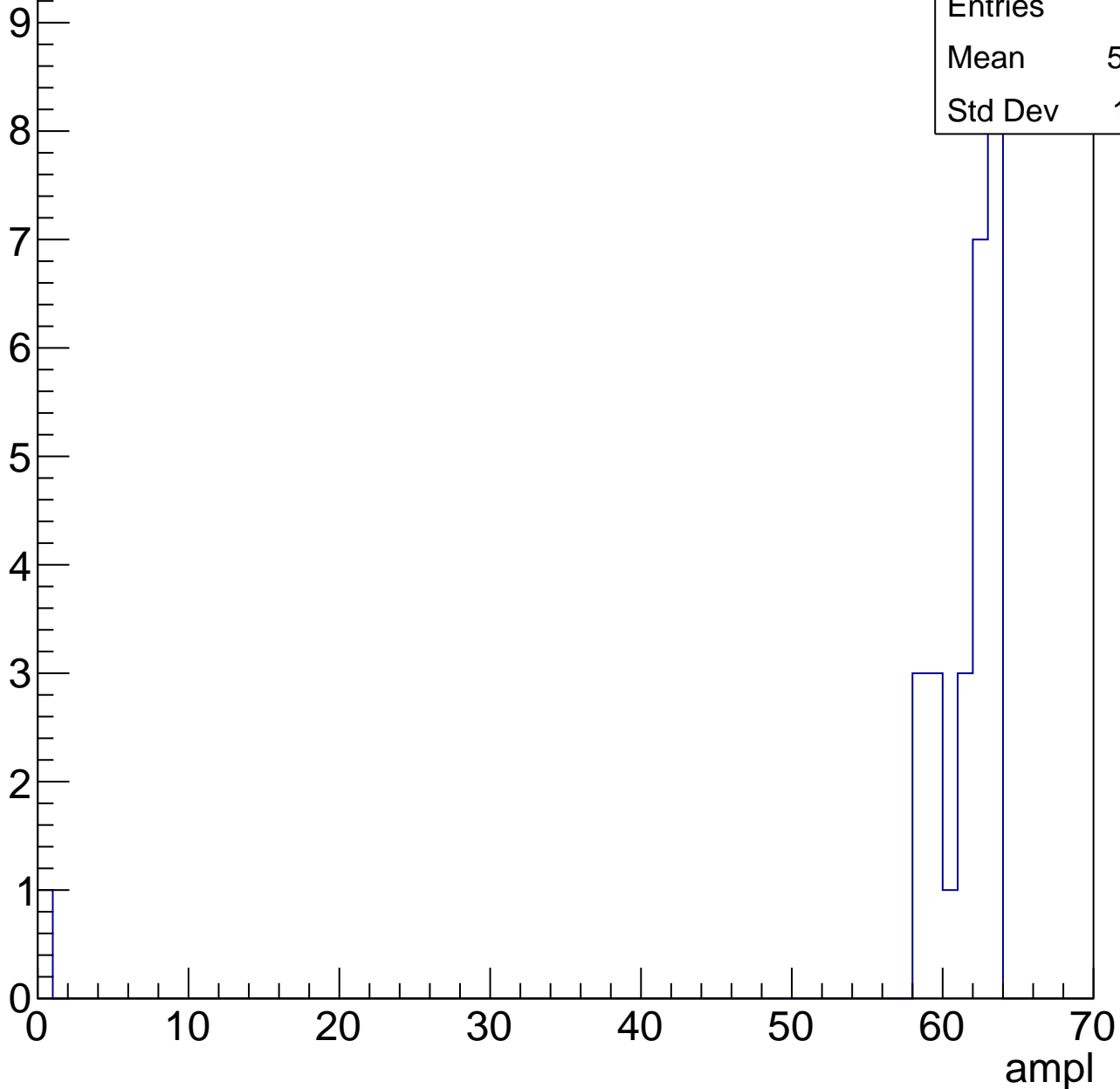


B0L001S, U25-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	59.07
Std Dev	11.71



B0L001S, U25-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch68, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	30.51
Std Dev	3.729

Gaus mean : 30.6467

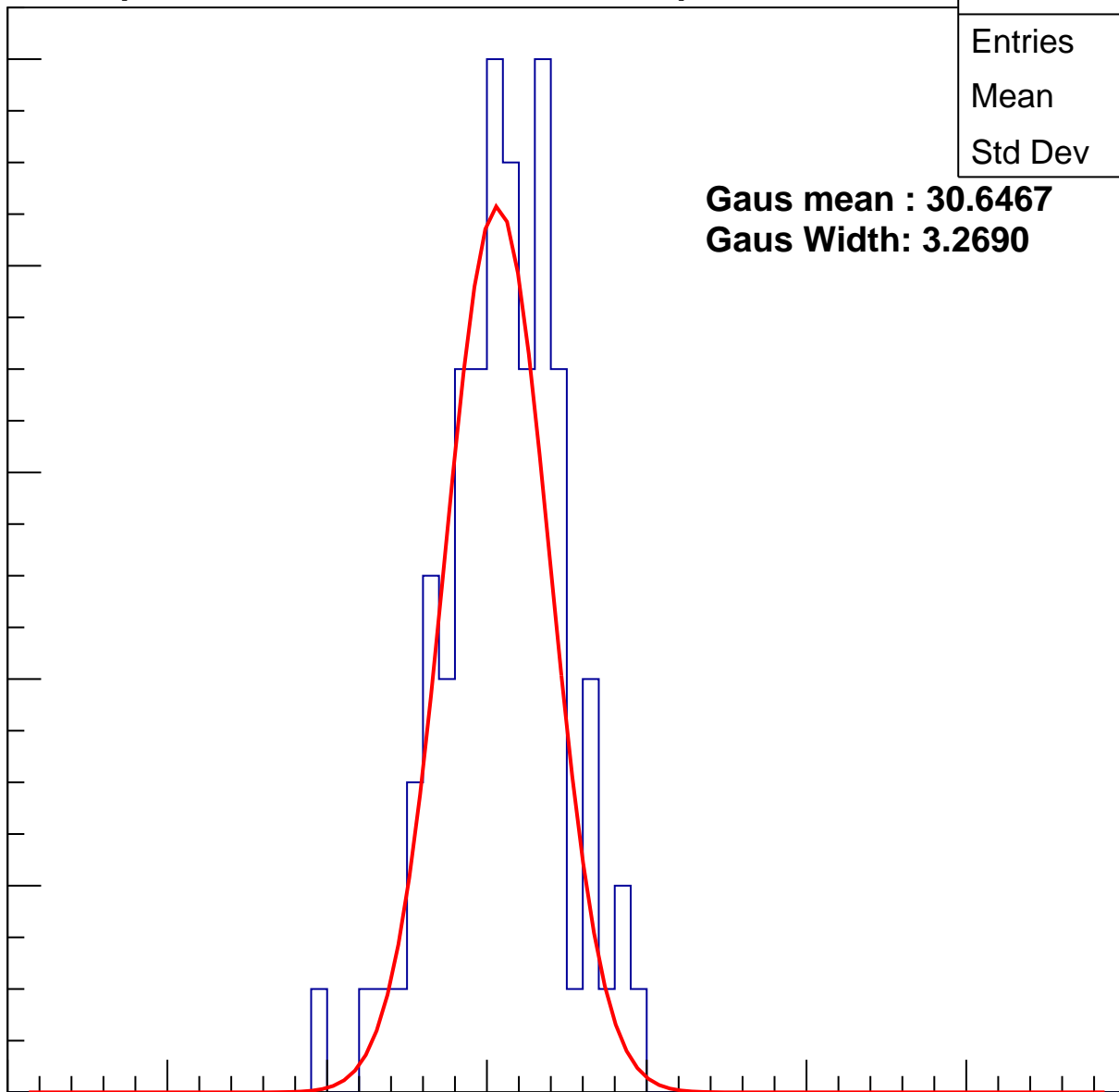
Gaus Width: 3.2690

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch68, adc1

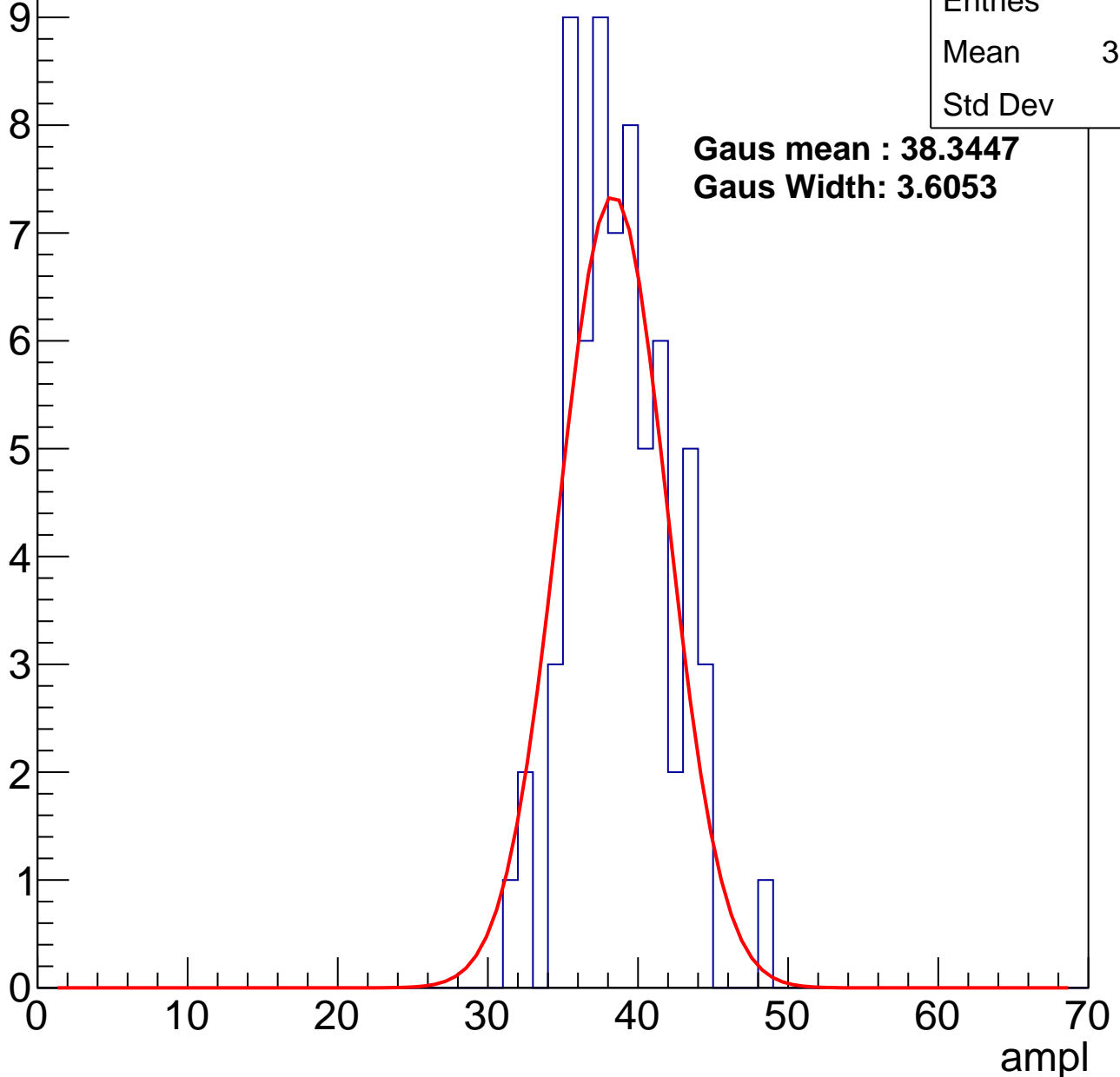
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.27
Std Dev	3.29

Gaus mean : 38.3447

Gaus Width: 3.6053



B0L001S, U25-ch68, adc2

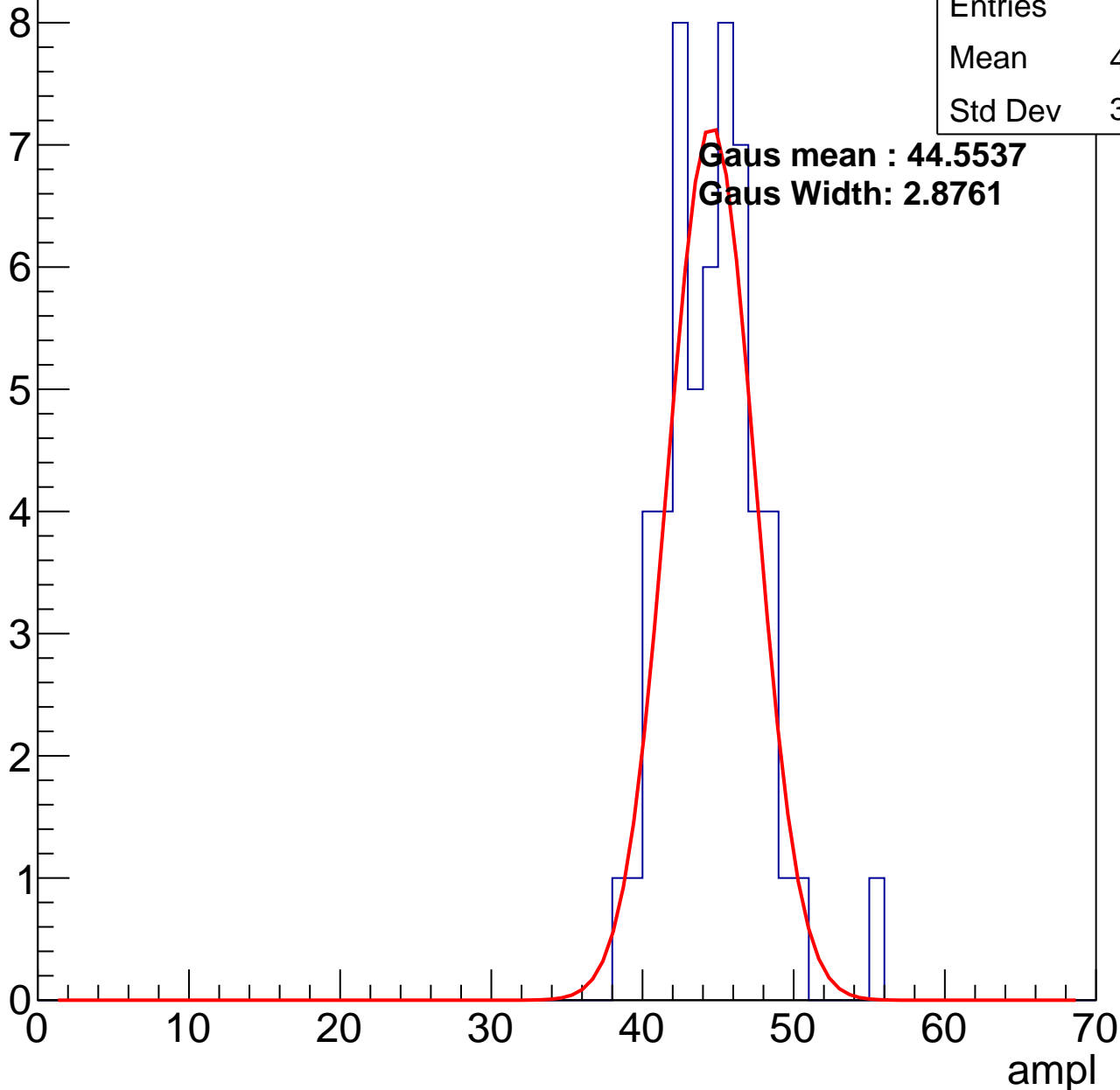
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.22
Std Dev	3.055

Gaus mean : 44.5537

Gaus Width: 2.8761

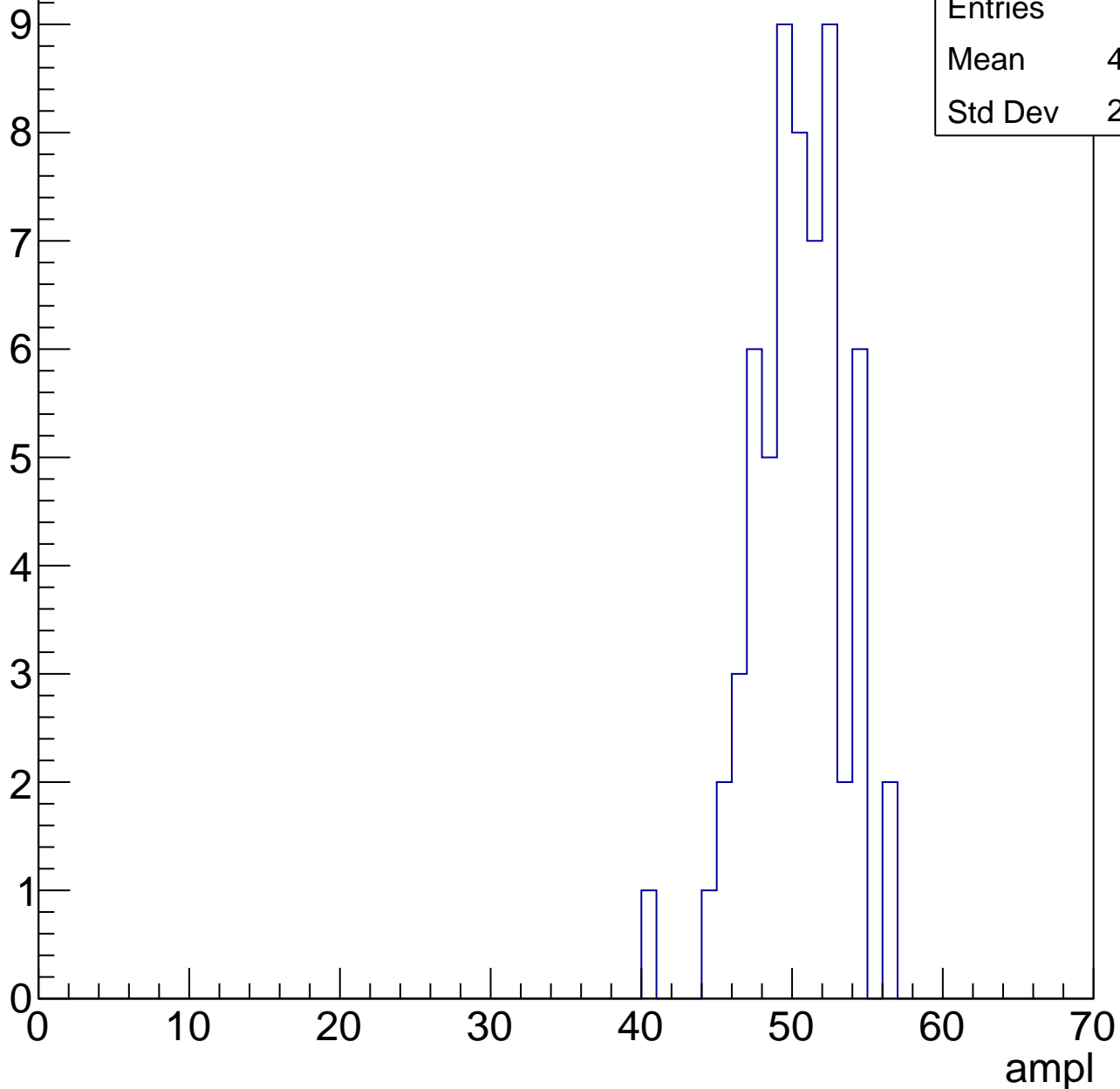


B0L001S, U25-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

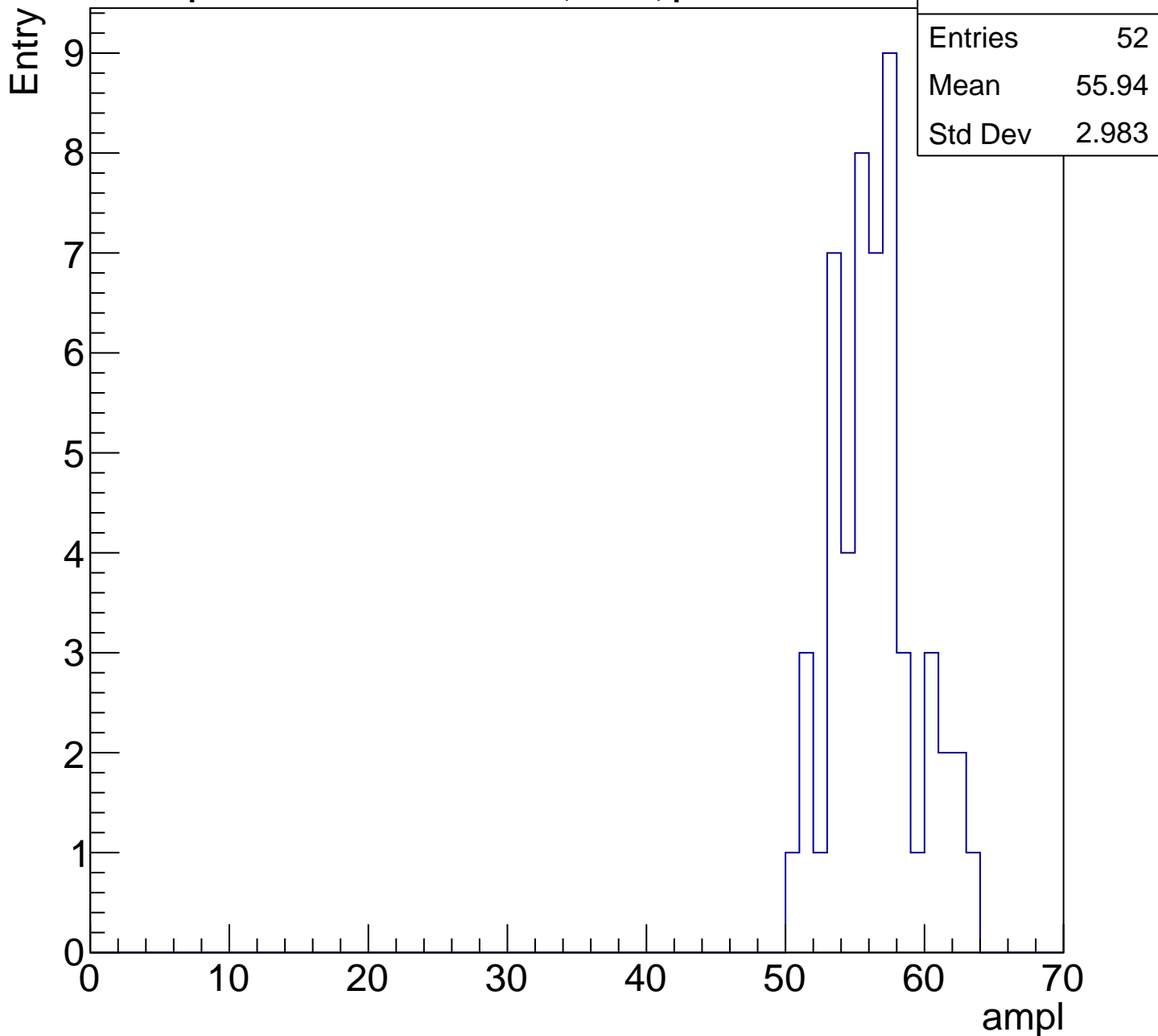
Entry

Entries	61
Mean	49.87
Std Dev	2.989



B0L001S, U25-ch68, adc4

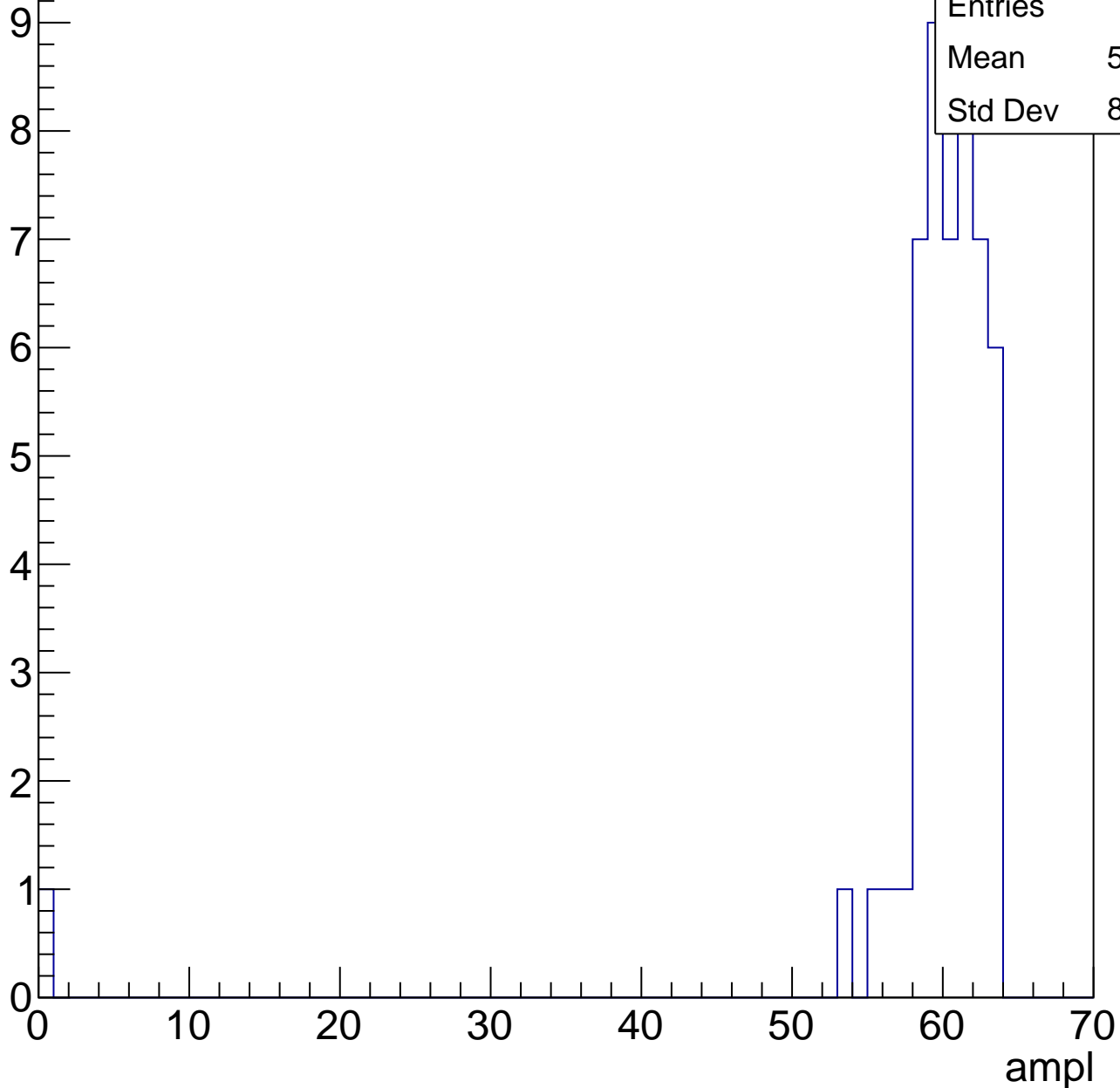
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch69, adc0

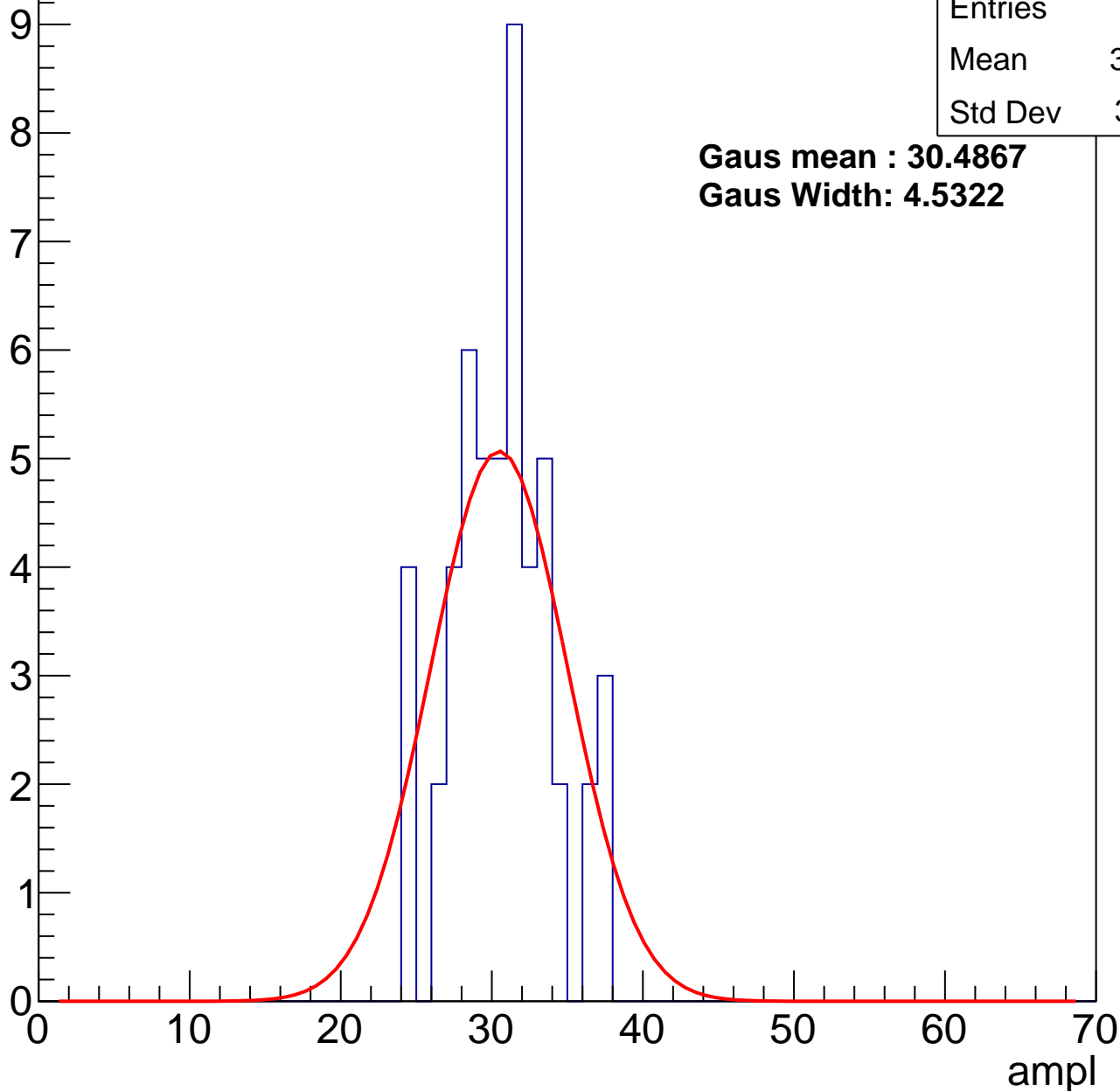
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30.24
Std Dev	3.311

Gaus mean : 30.4867

Gaus Width: 4.5322



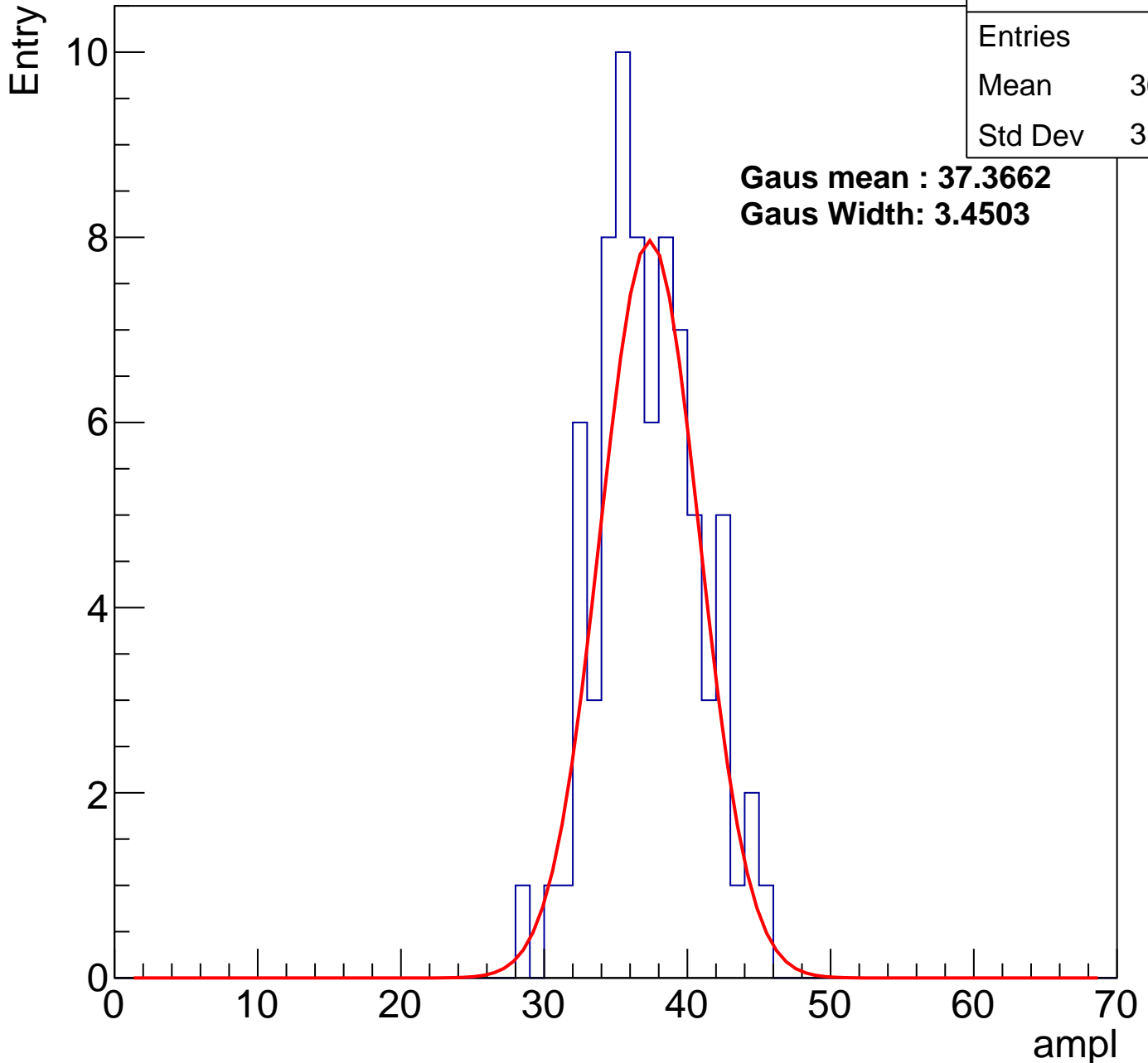
B0L001S, U25-ch69, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	36.82
Std Dev	3.505

Gaus mean : 37.3662

Gaus Width: 3.4503



B0L001S, U25-ch69, adc2

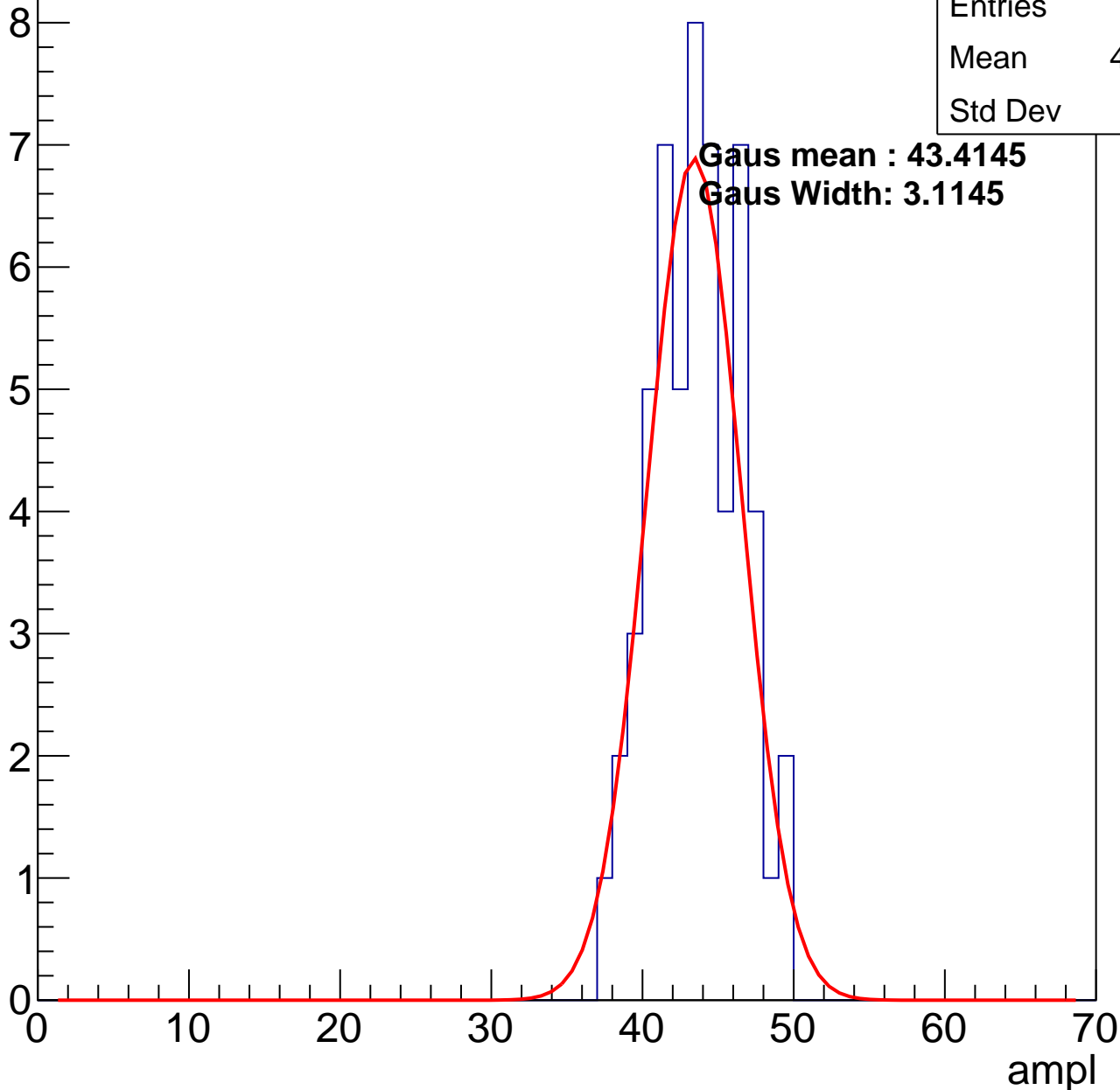
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.12
Std Dev	2.86

Gaus mean : 43.4145

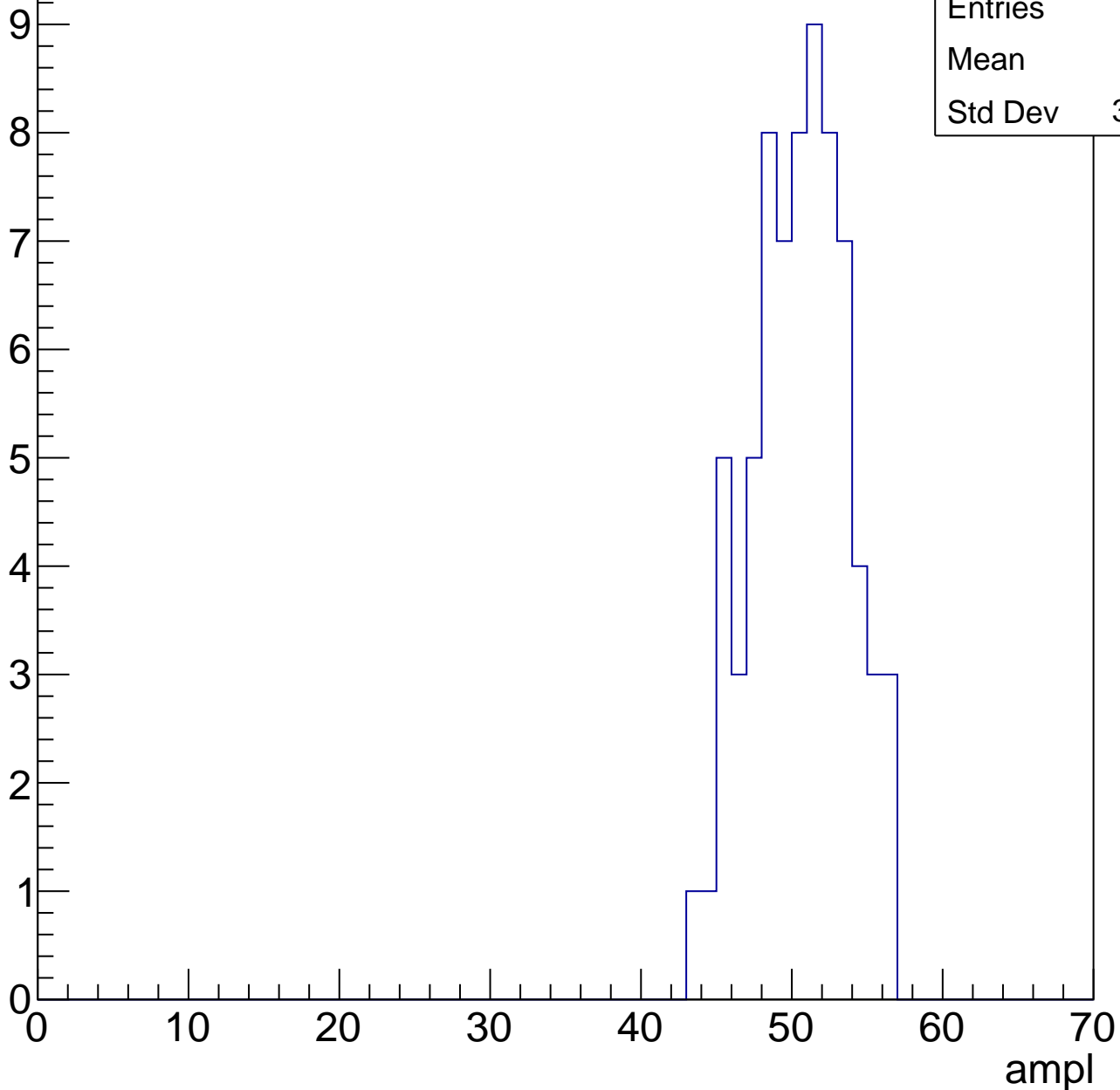
Gaus Width: 3.1145



B0L001S, U25-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

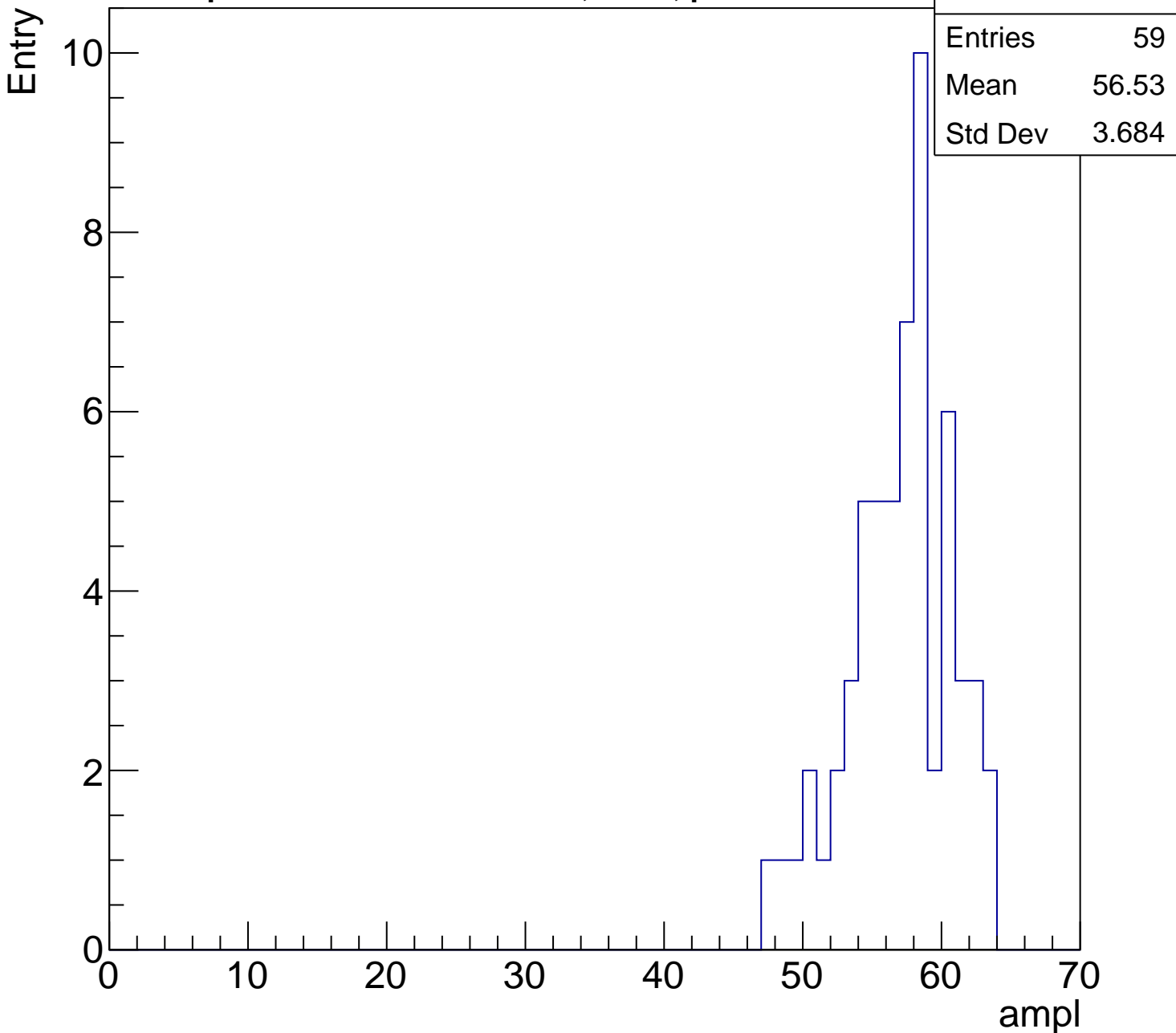
Entries	59
Mean	56.53
Std Dev	3.684

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	59.97
Std Dev	2.486

0

10

20

30

40

50

60

70

ampl

B0L001S, U25-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

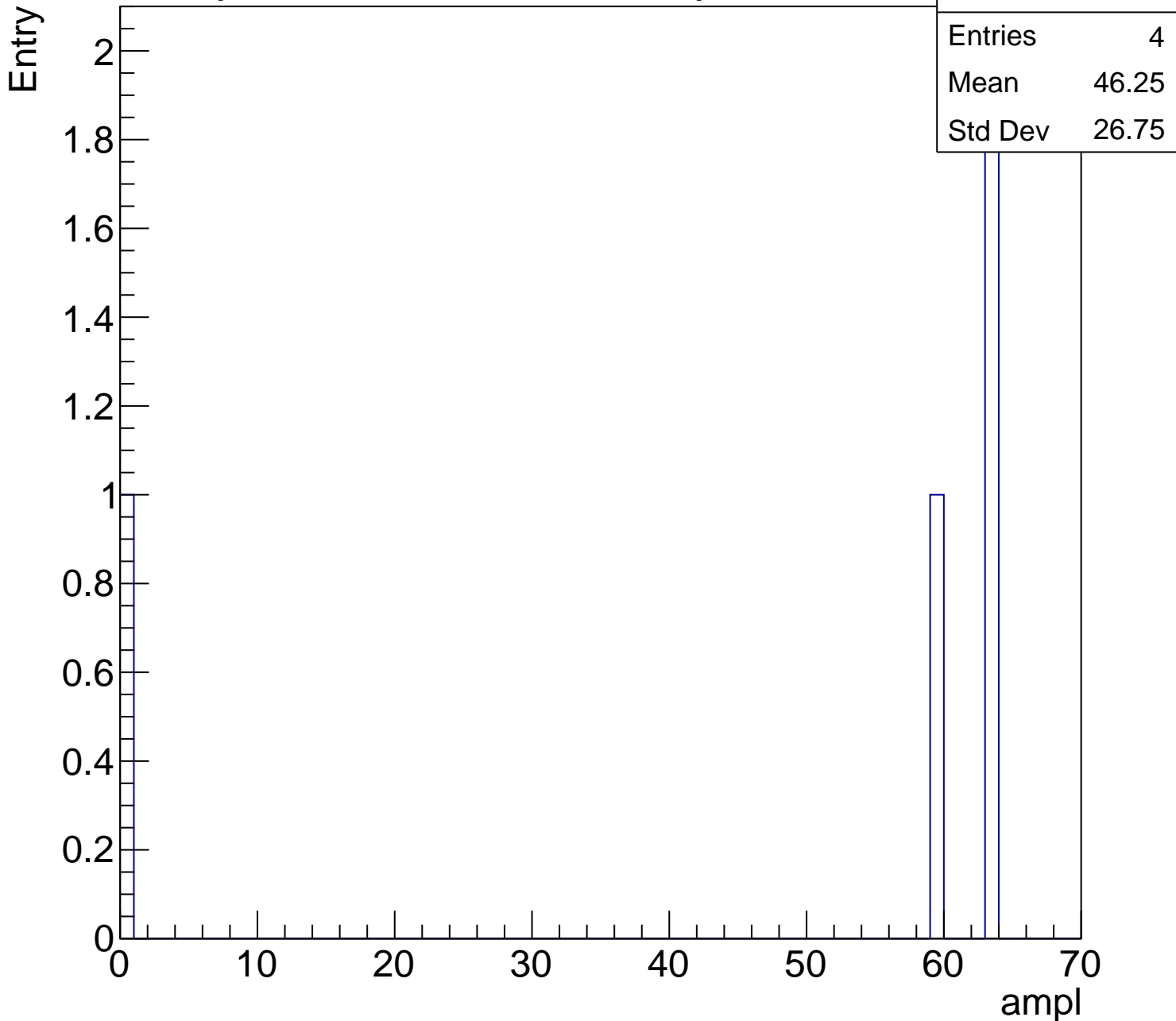
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.25
Std Dev	26.75

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L001S, U25-ch70, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	29.89
Std Dev	5.025

Gaus mean : 31.3958

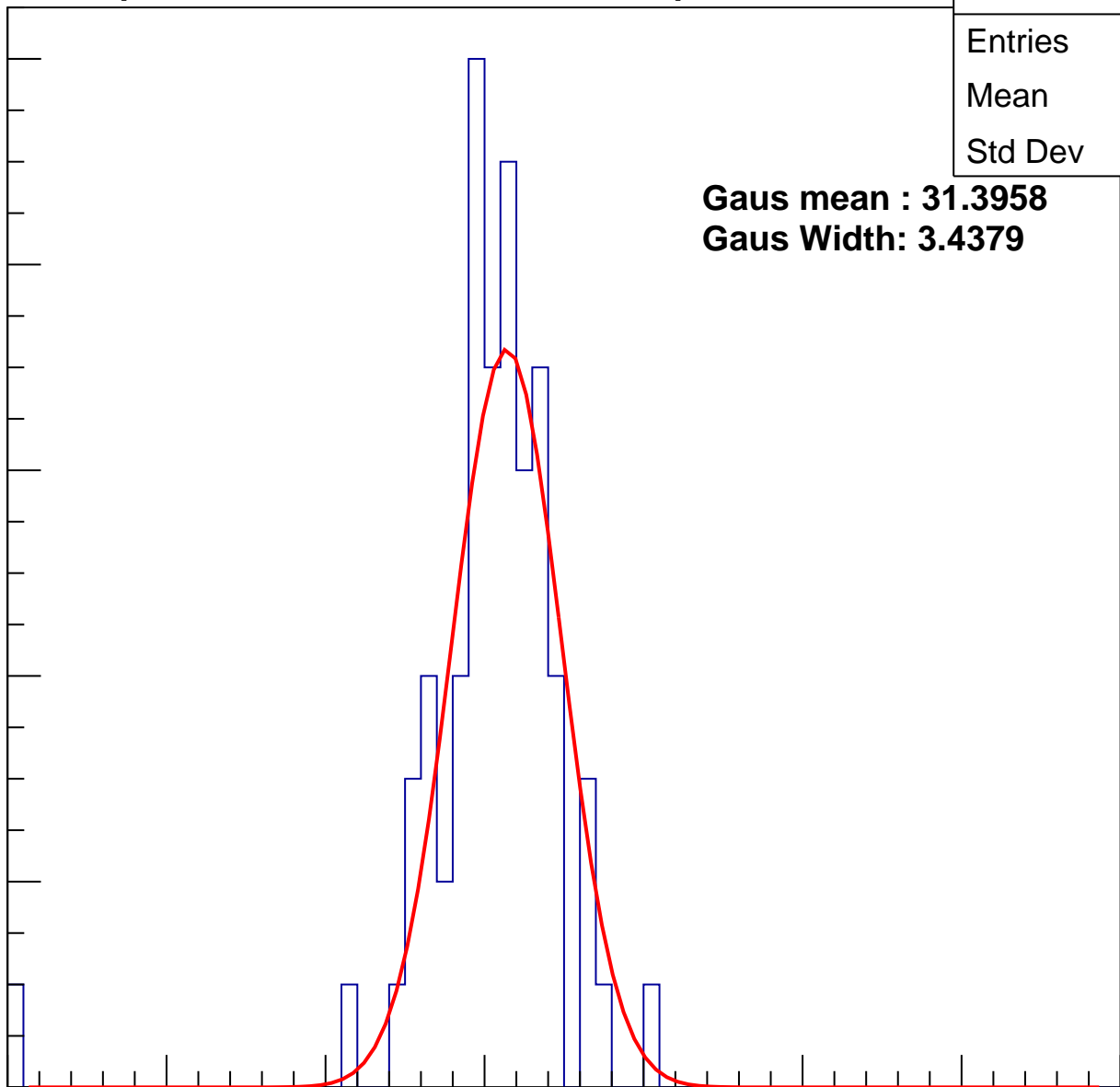
Gaus Width: 3.4379

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch70, adc1

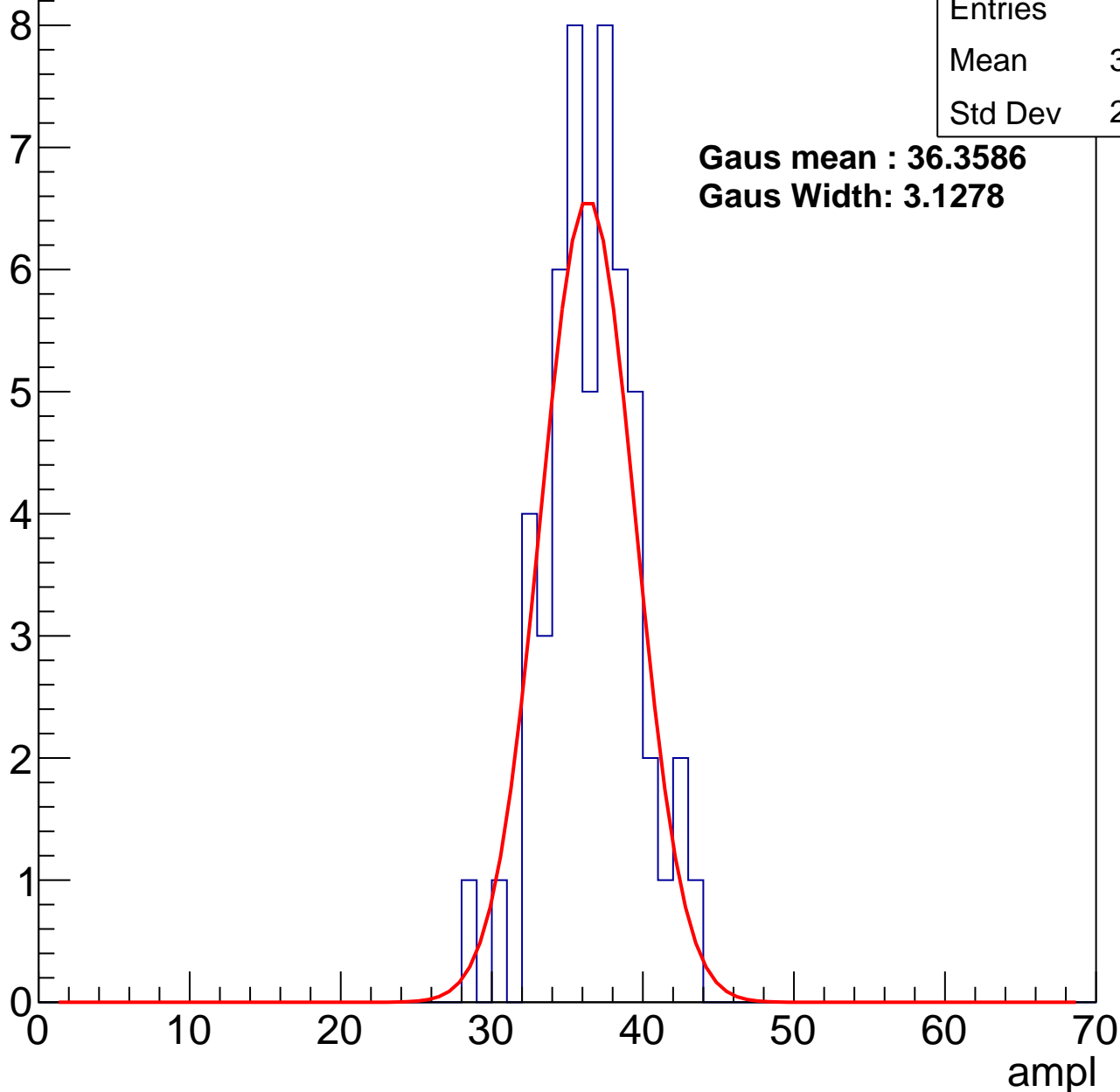
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	36.15
Std Dev	2.999

Gaus mean : 36.3586

Gaus Width: 3.1278



B0L001S, U25-ch70, adc2

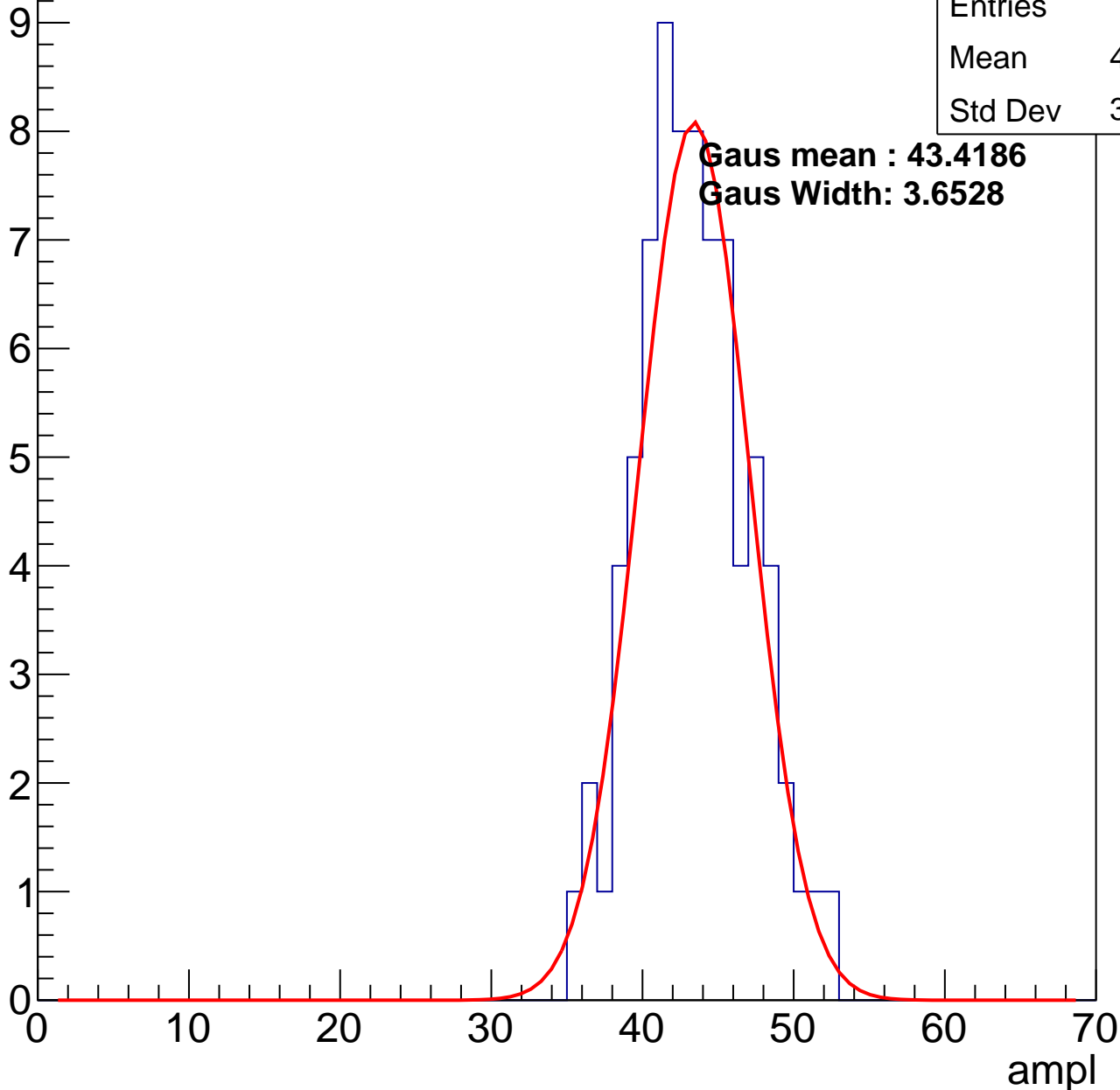
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	42.92
Std Dev	3.607

Gaus mean : 43.4186

Gaus Width: 3.6528

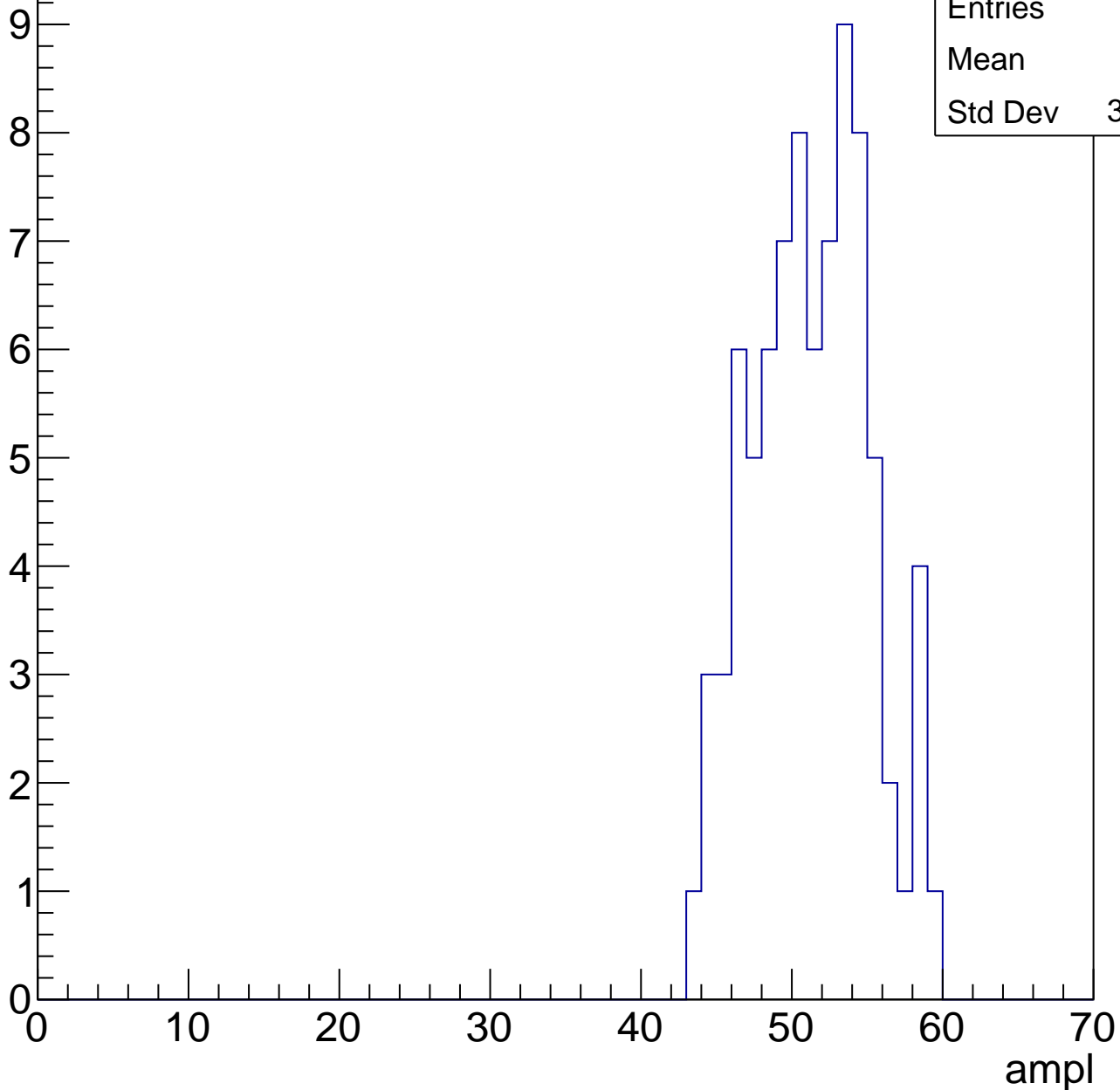


B0L001S, U25-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	50.8
Std Dev	3.788

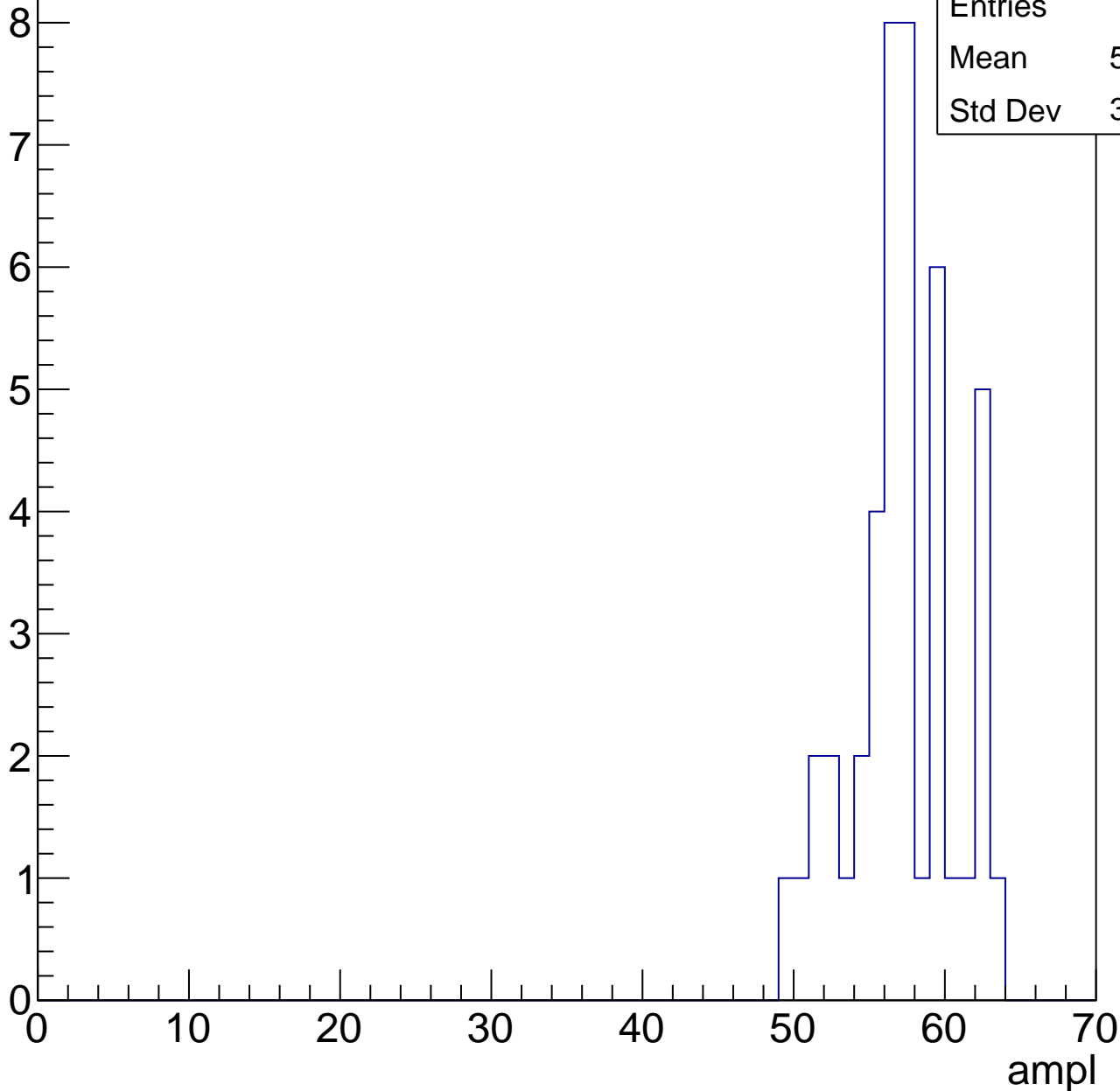


B0L001S, U25-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	56.73
Std Dev	3.373



B0L001S, U25-ch70, adc5

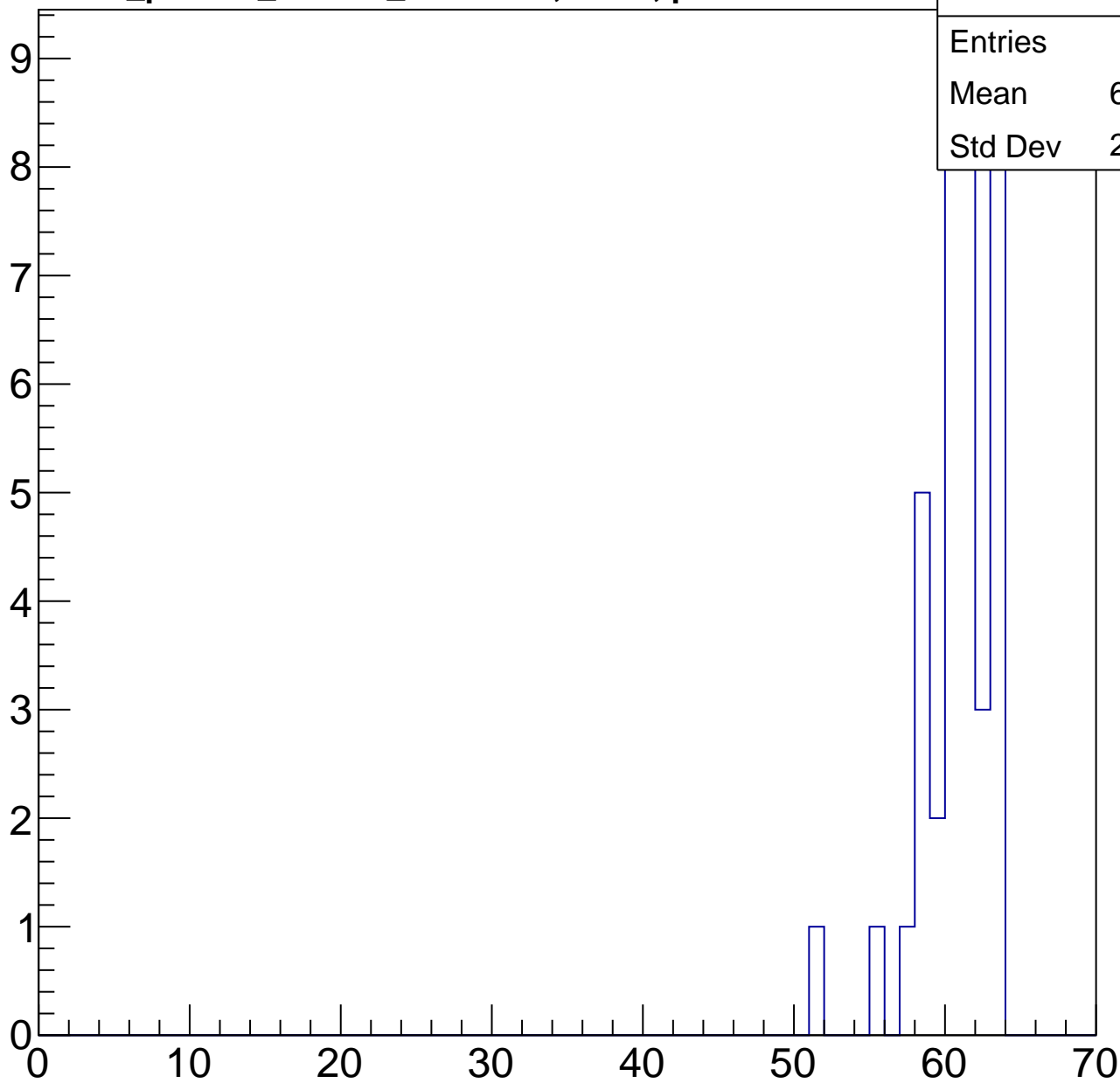
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.26
Std Dev	2.415

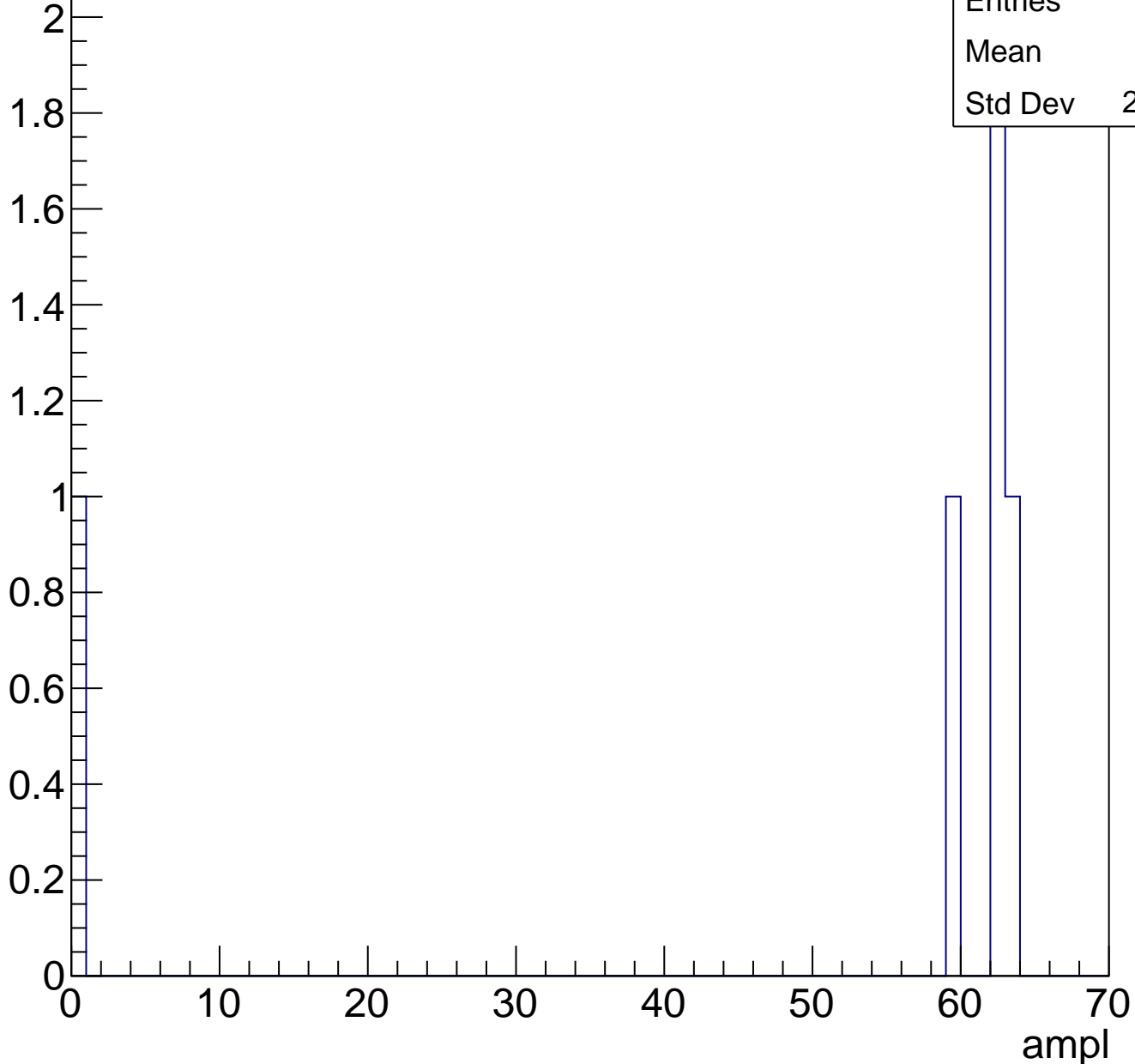
ampl



B0L001S, U25-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch71, adc0

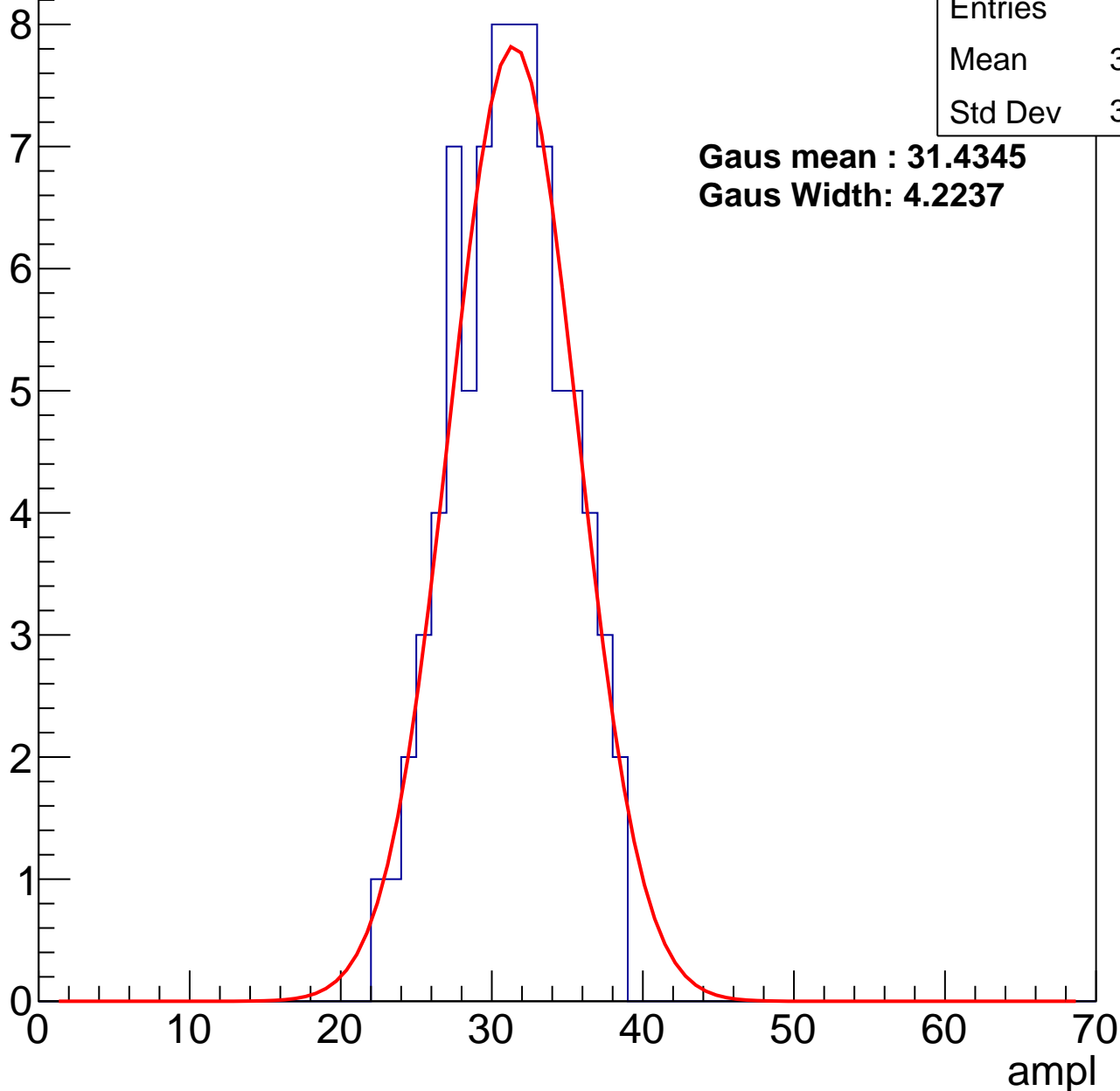
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.69
Std Dev	3.713

Gaus mean : 31.4345

Gaus Width: 4.2237



B0L001S, U25-ch71, adc1

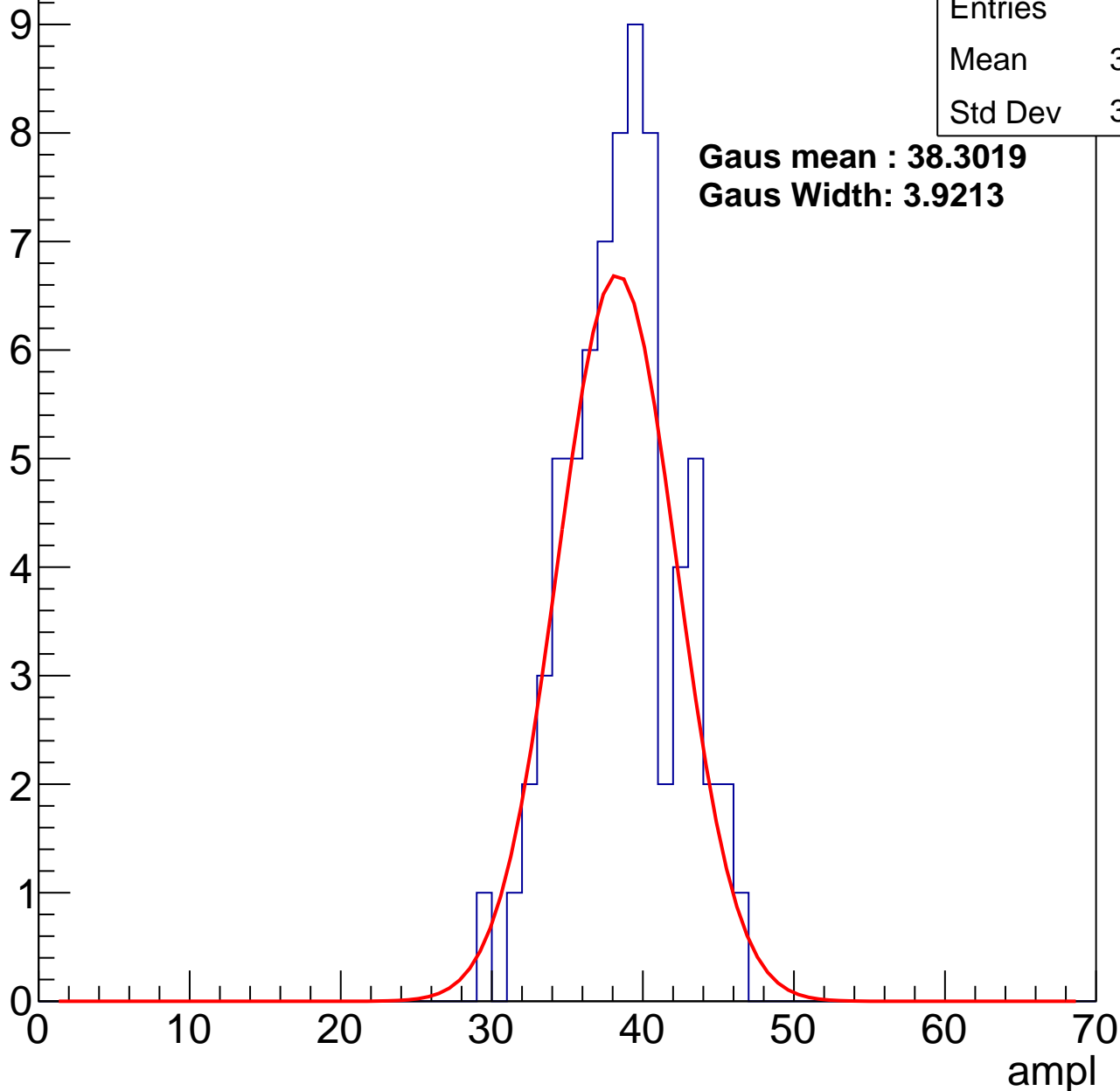
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.13
Std Dev	3.588

Gaus mean : 38.3019

Gaus Width: 3.9213



B0L001S, U25-ch71, adc2

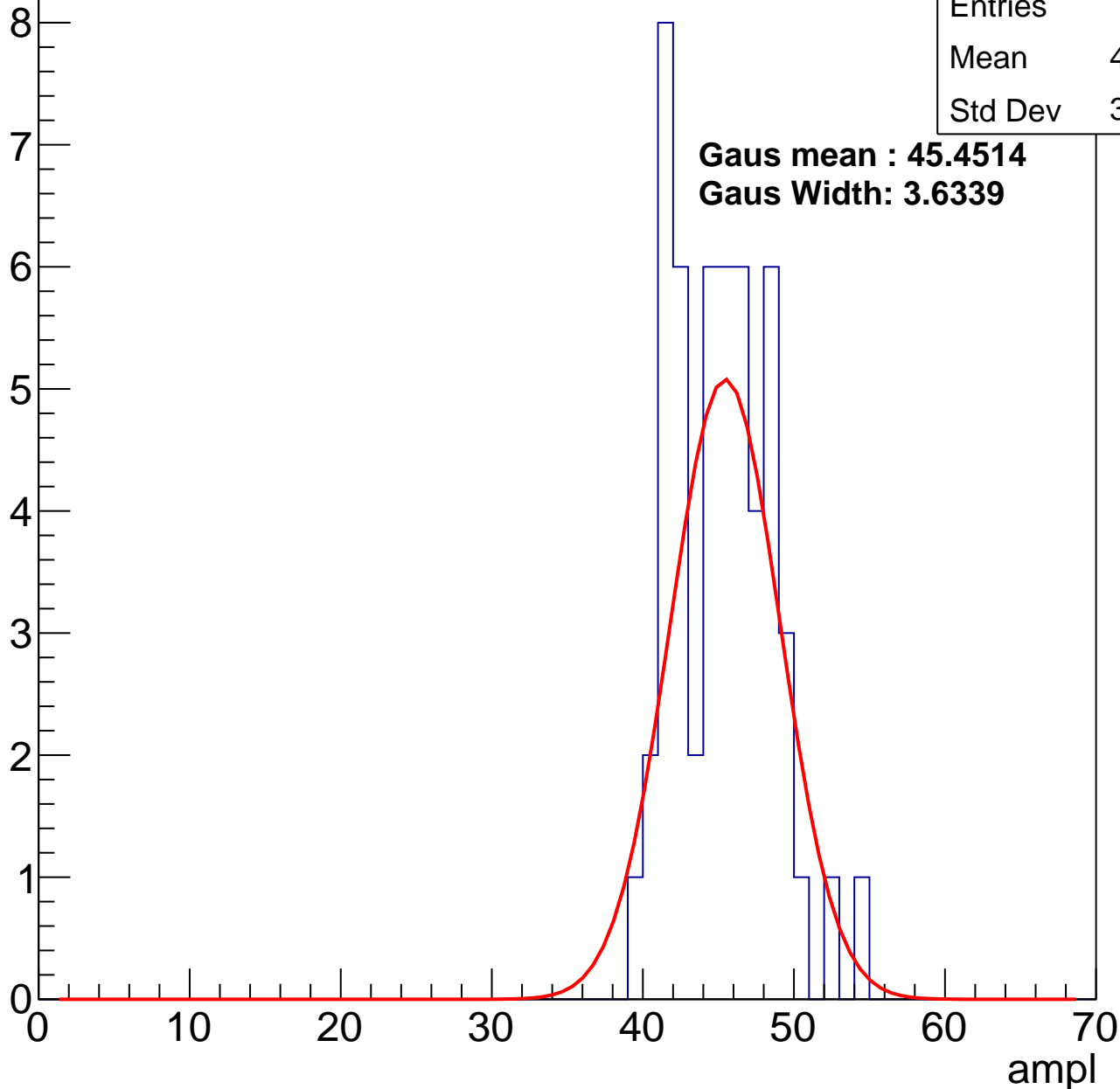
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.79
Std Dev	3.247

Gaus mean : 45.4514

Gaus Width: 3.6339

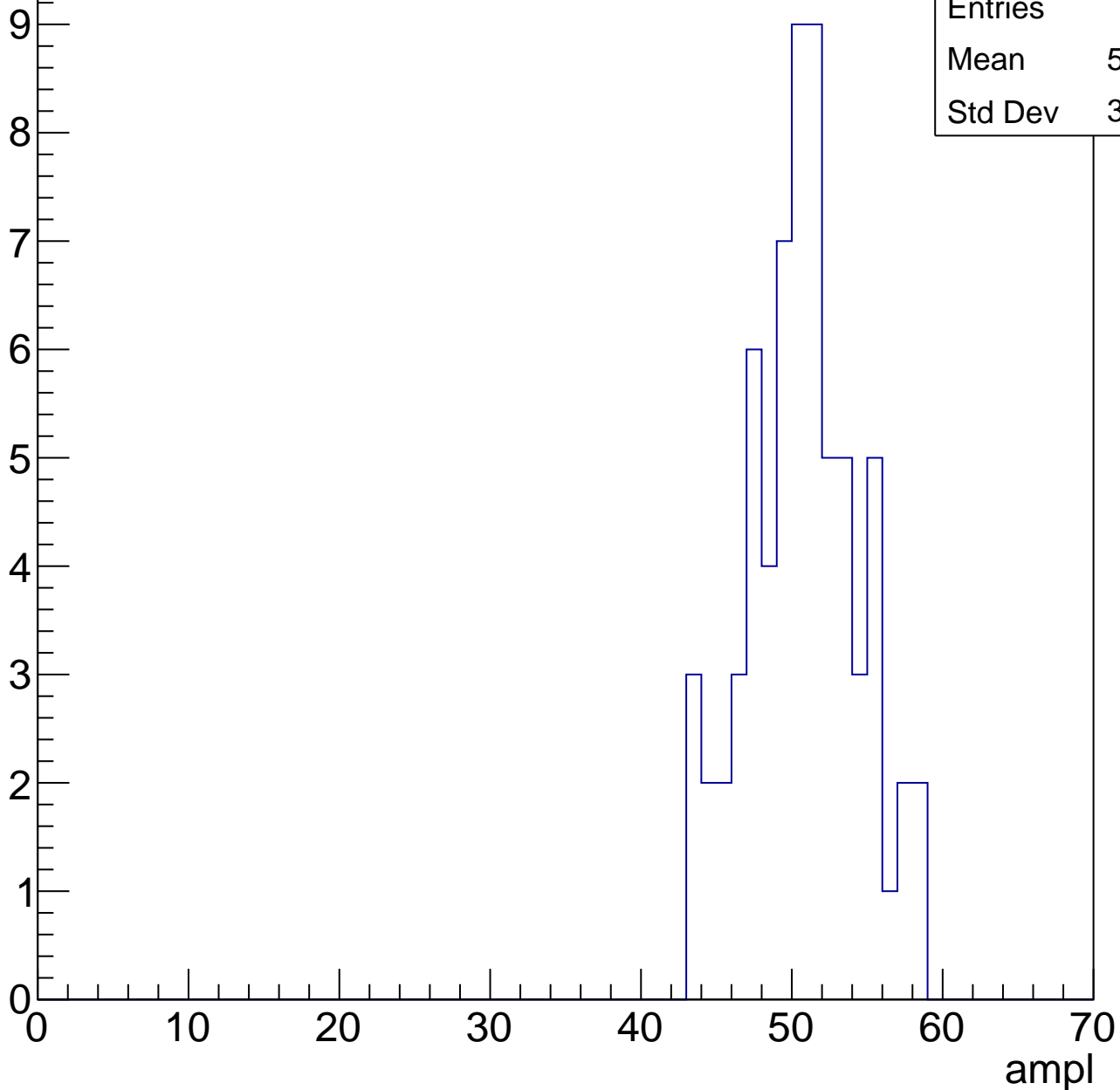


B0L001S, U25-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.28
Std Dev	3.633

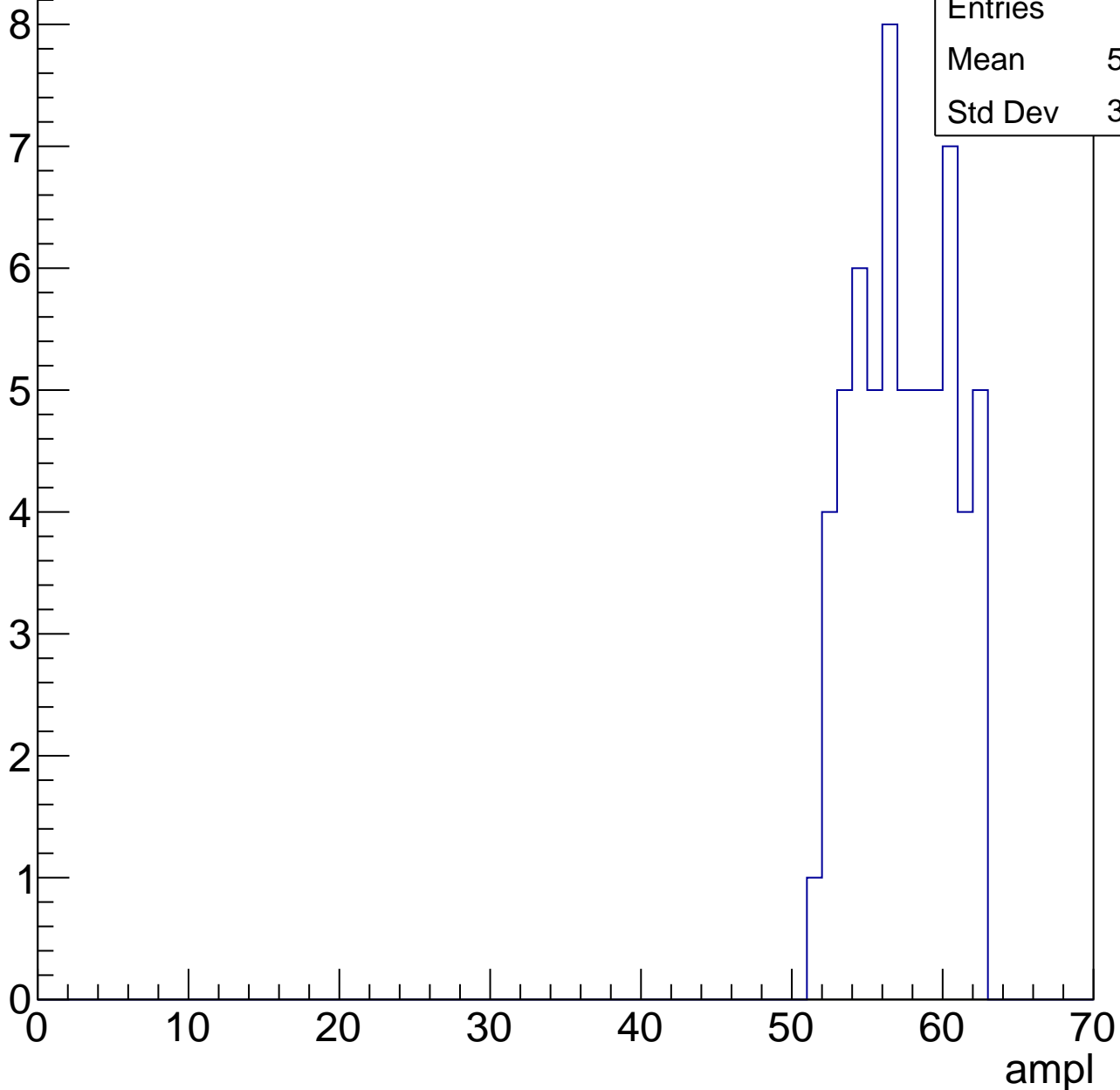


B0L001S, U25-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.92
Std Dev	3.095

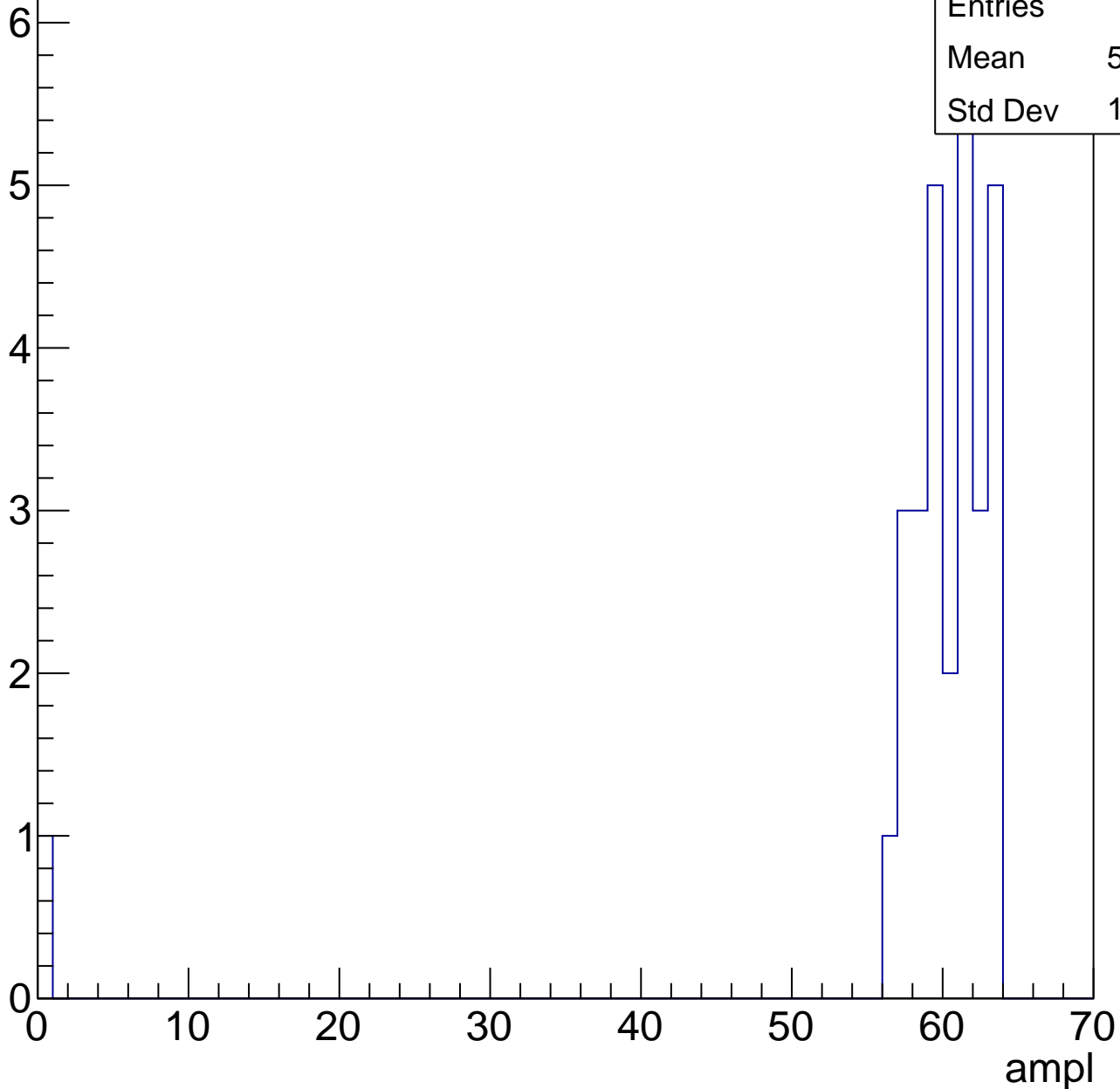


B0L001S, U25-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.03
Std Dev	11.16

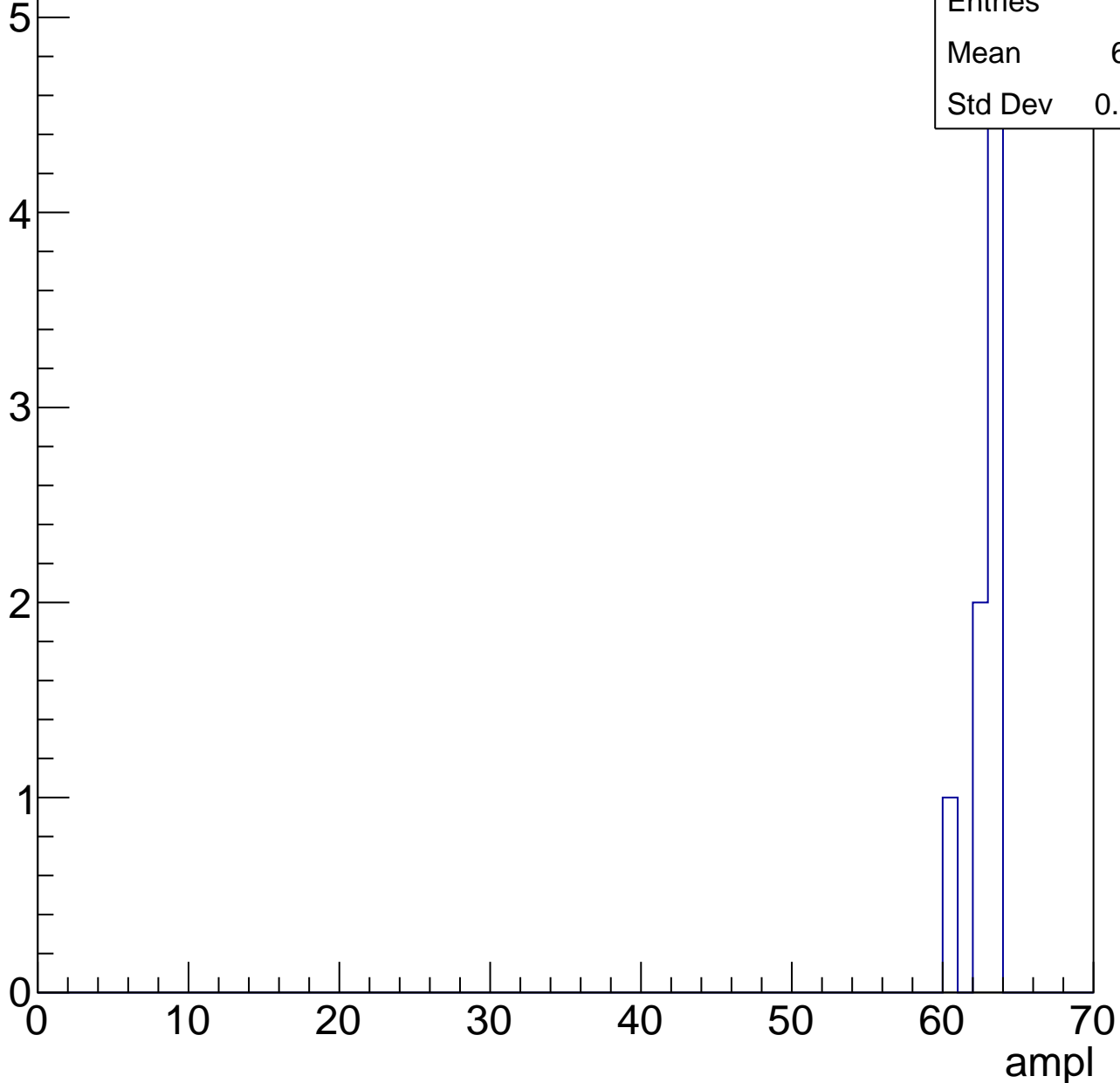


B0L001S, U25-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B0L001S, U25-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch72, adc0

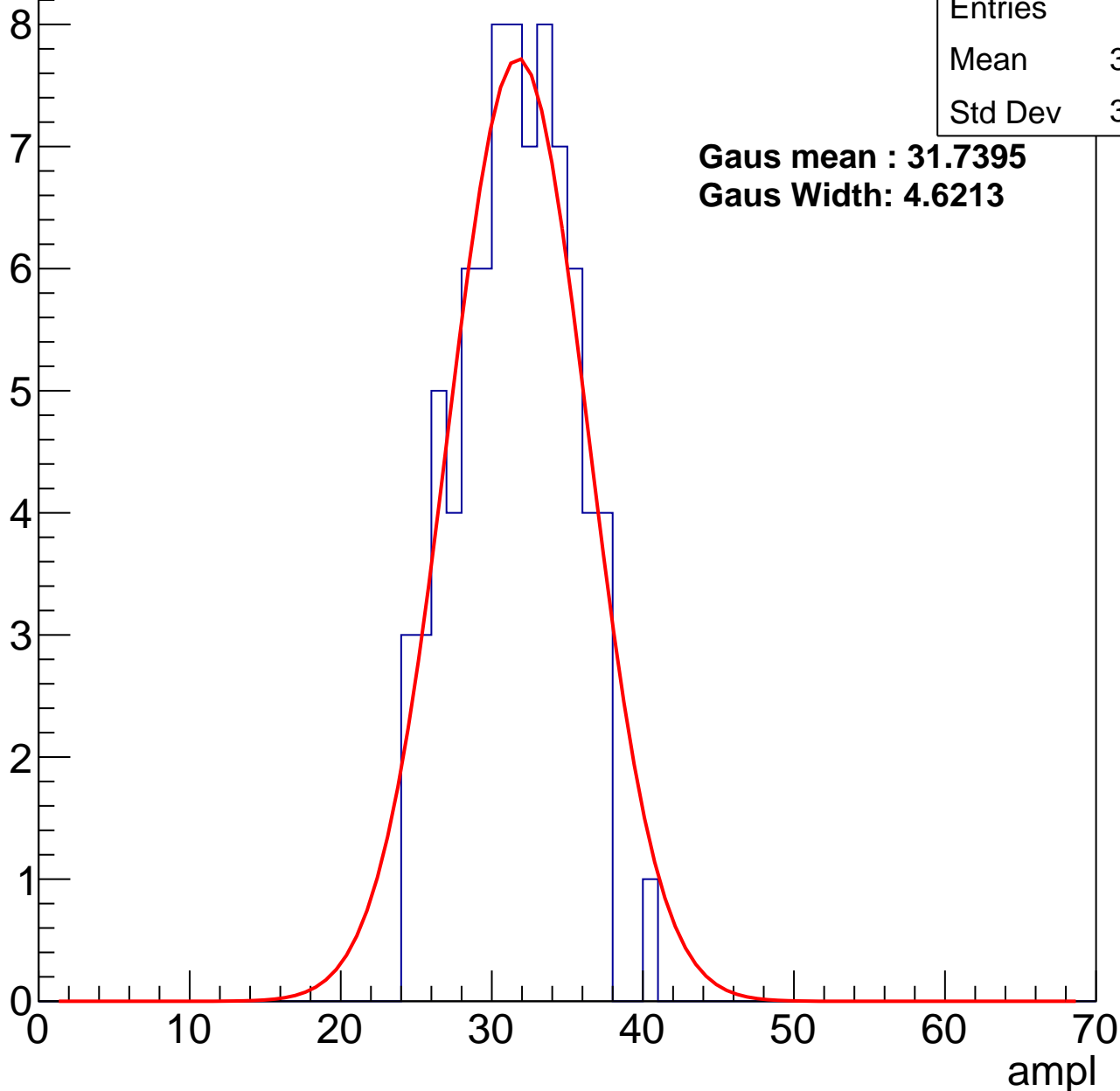
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	31.04
Std Dev	3.628

Gaus mean : 31.7395

Gaus Width: 4.6213



B0L001S, U25-ch72, adc1

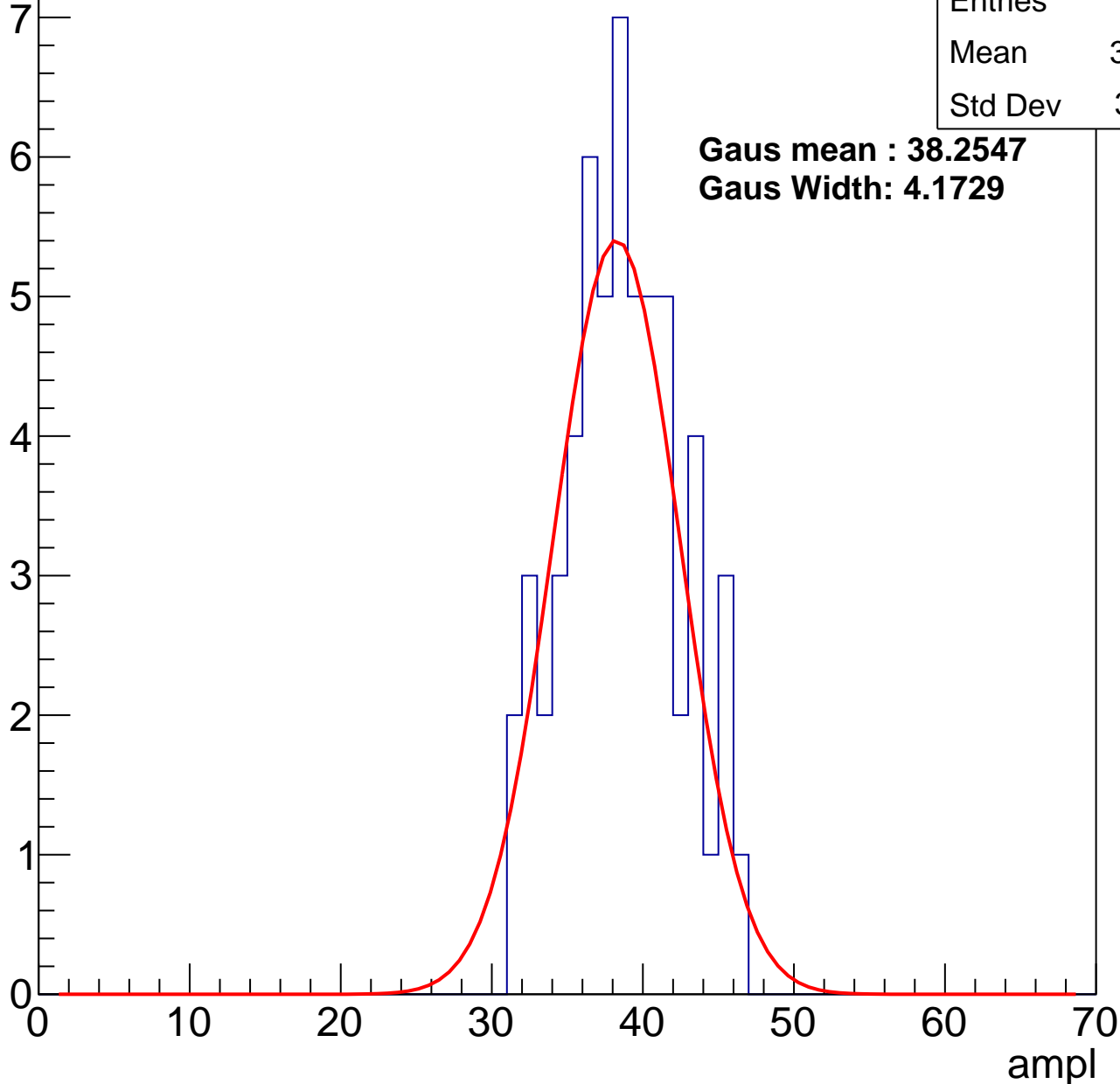
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	38.17
Std Dev	3.751

Gaus mean : 38.2547

Gaus Width: 4.1729



B0L001S, U25-ch72, adc2

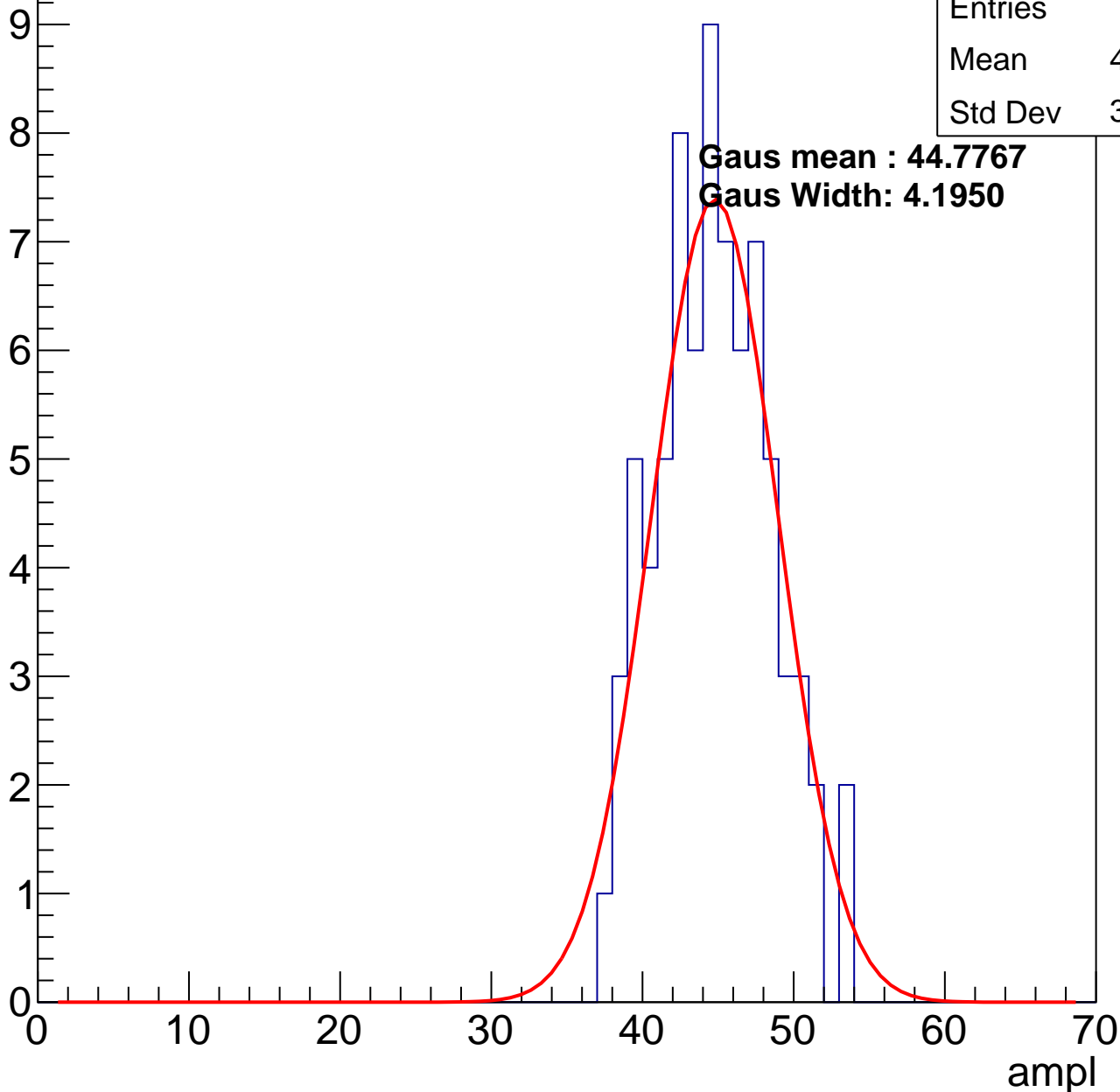
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.29
Std Dev	3.699

Gaus mean : 44.7767

Gaus Width: 4.1950

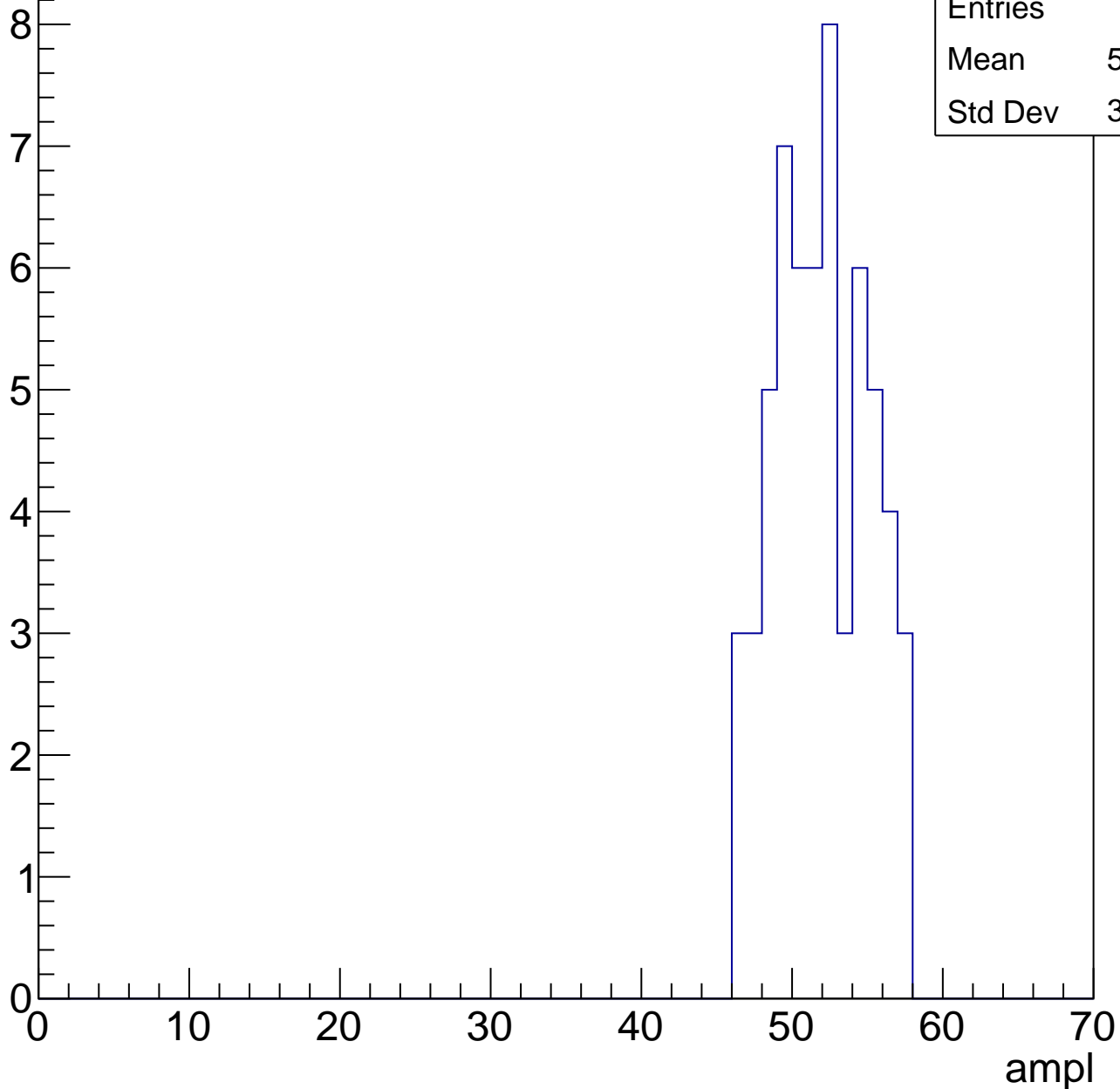


B0L001S, U25-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	51.47
Std Dev	3.055

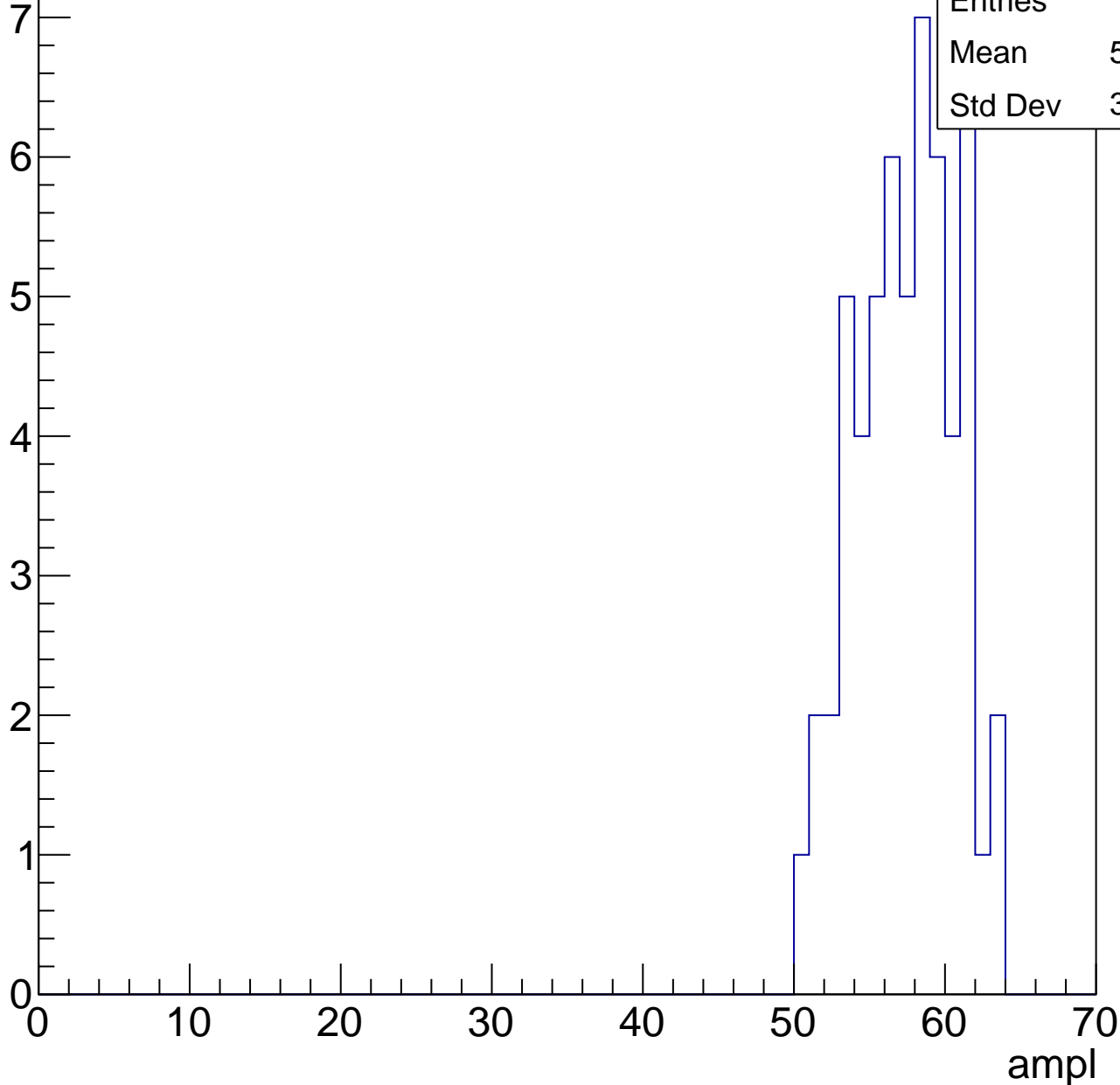


B0L001S, U25-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.98
Std Dev	3.215

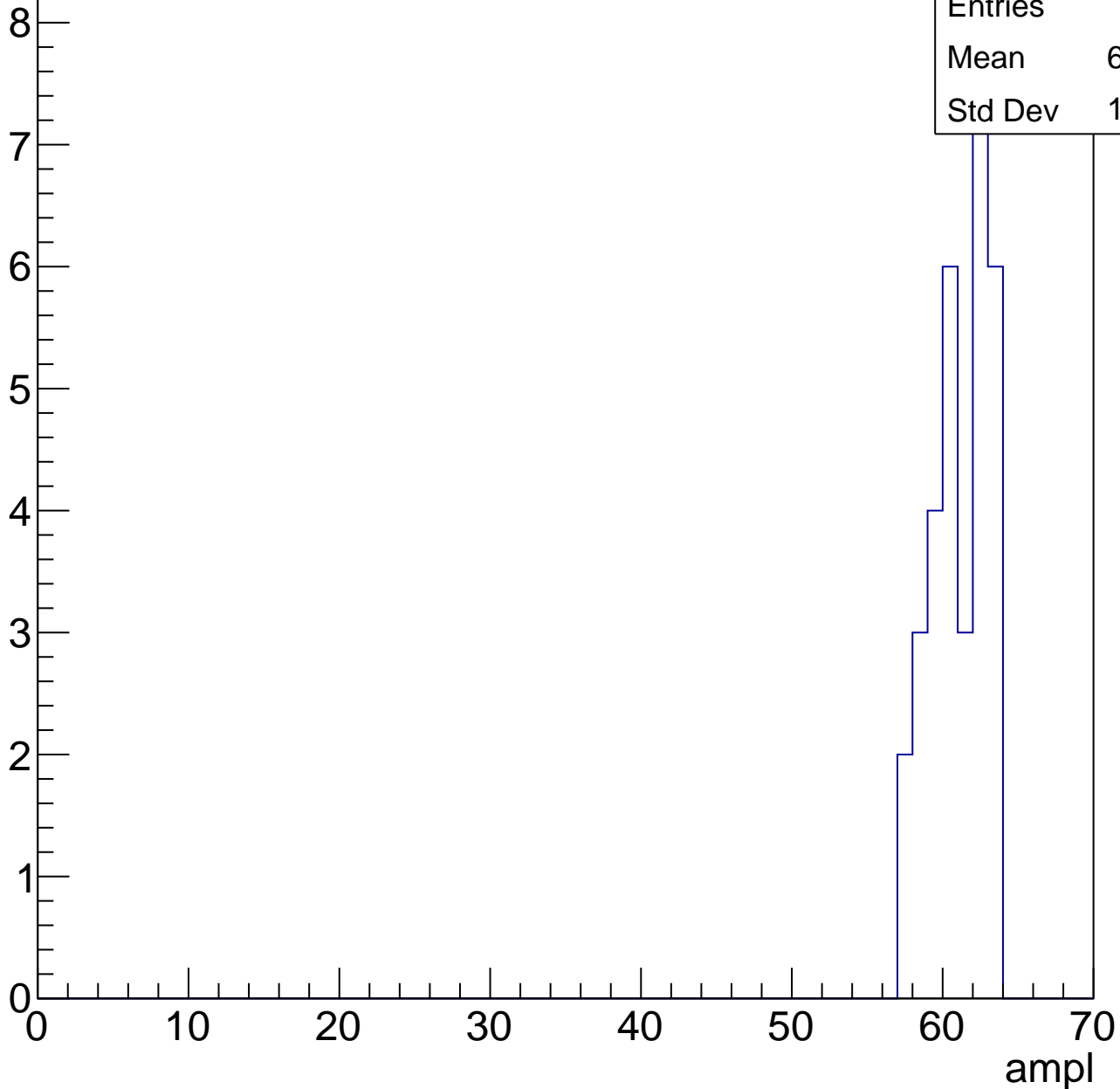


B0L001S, U25-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.66
Std Dev	1.847



B0L001S, U25-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch73, adc0

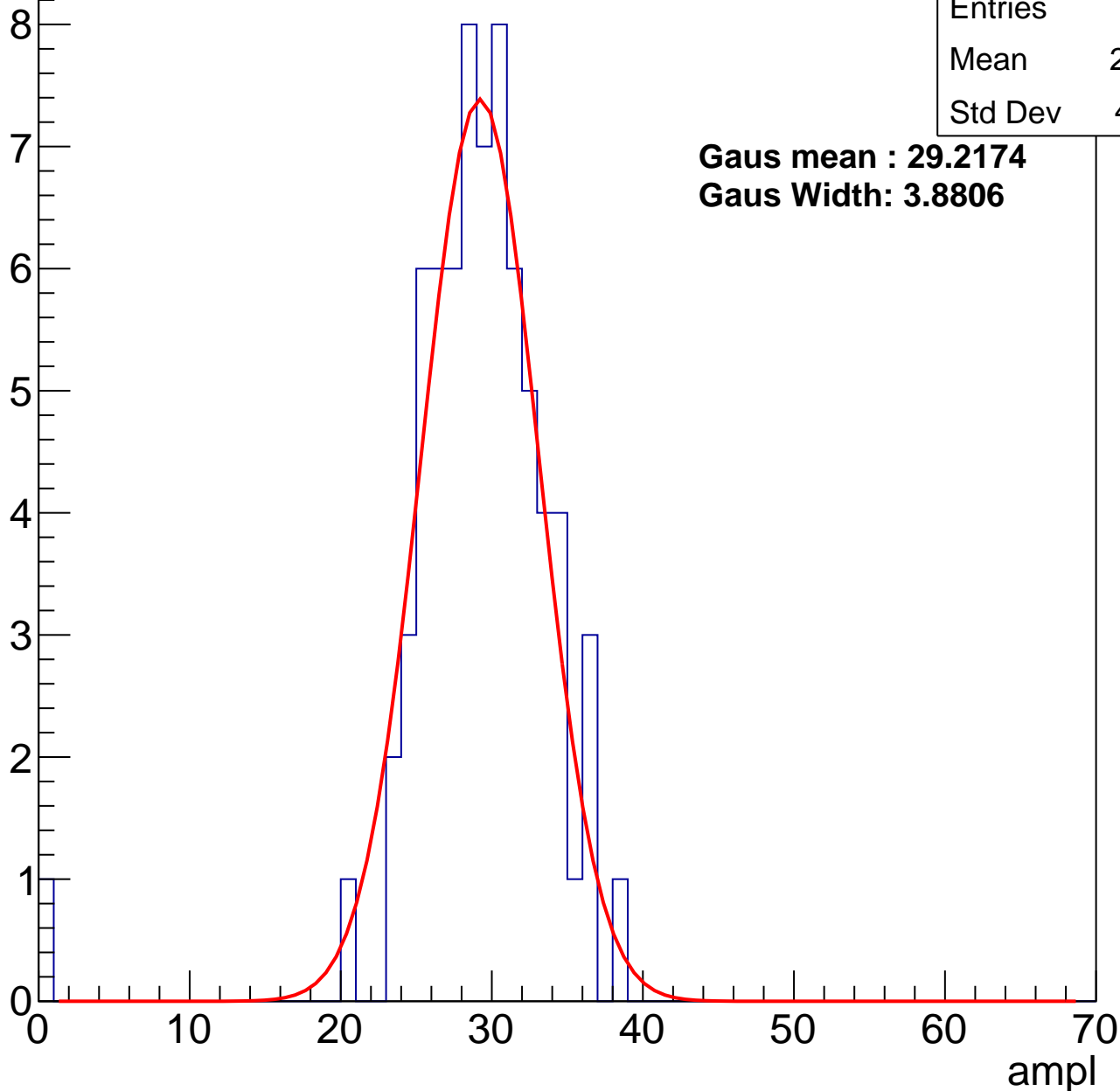
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	28.72
Std Dev	4.931

Gaus mean : 29.2174

Gaus Width: 3.8806



B0L001S, U25-ch73, adc1

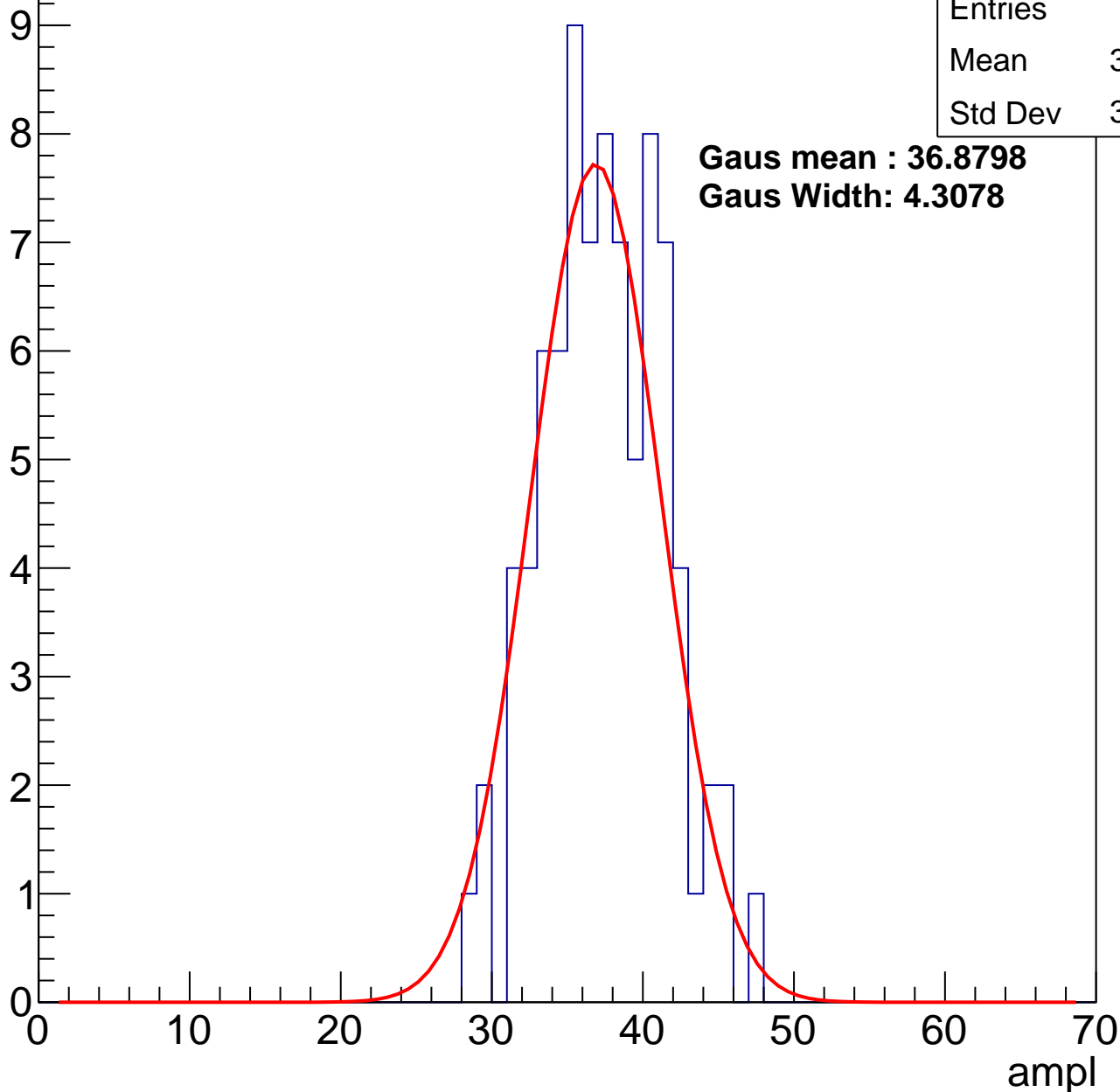
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	36.99
Std Dev	3.963

Gaus mean : 36.8798

Gaus Width: 4.3078



B0L001S, U25-ch73, adc2

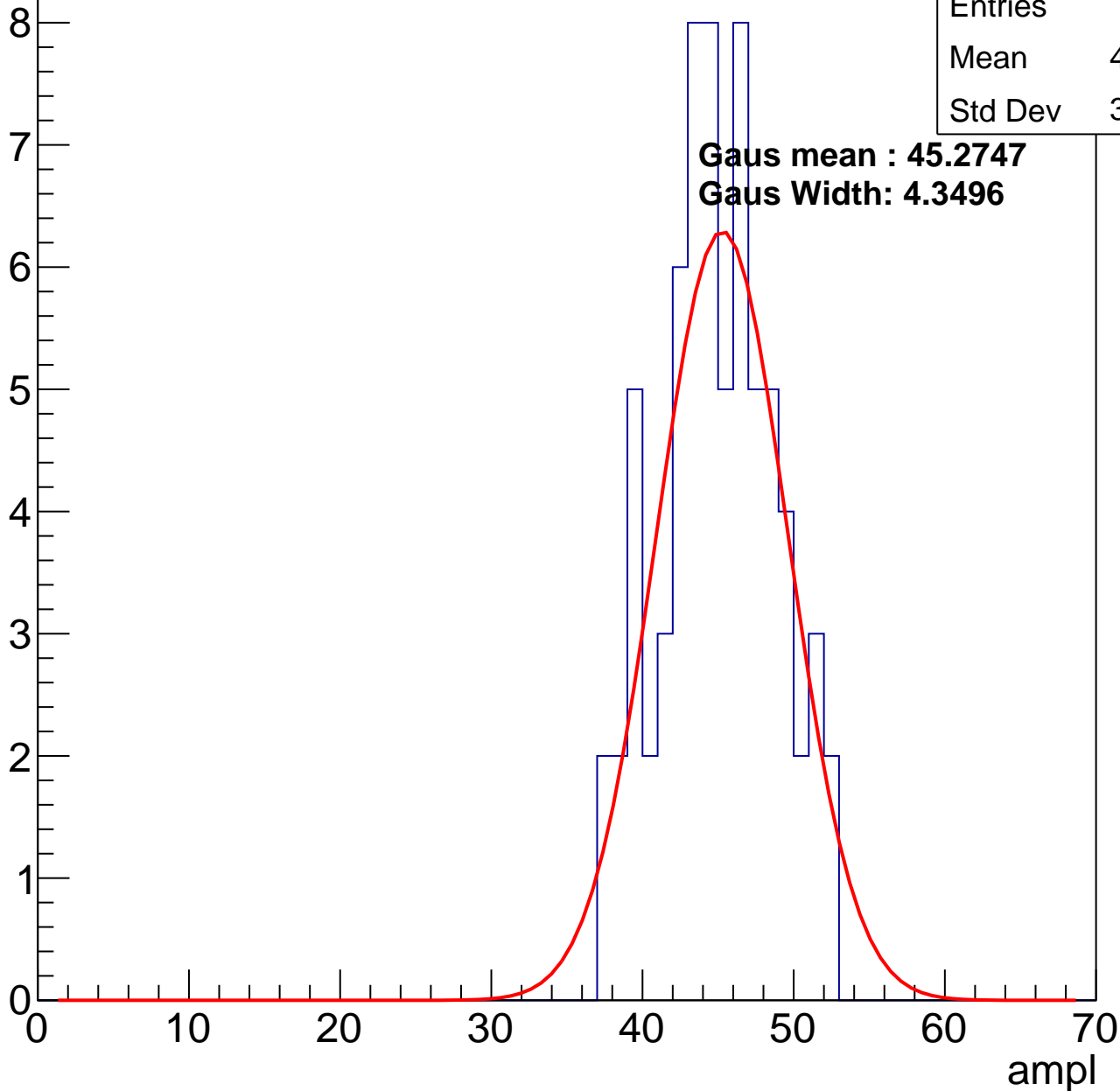
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.53
Std Dev	3.733

Gaus mean : 45.2747

Gaus Width: 4.3496

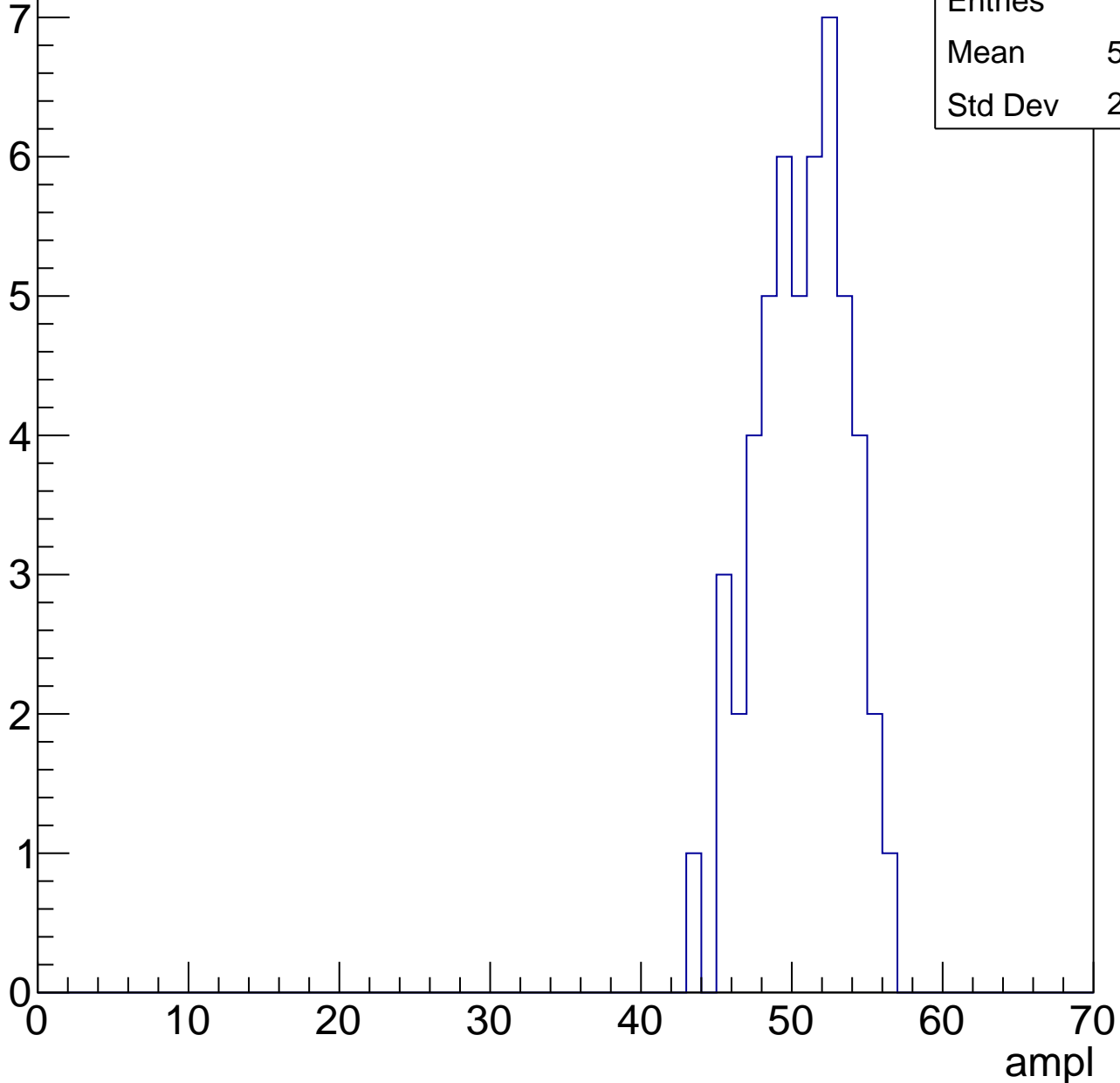


B0L001S, U25-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	50.18
Std Dev	2.955

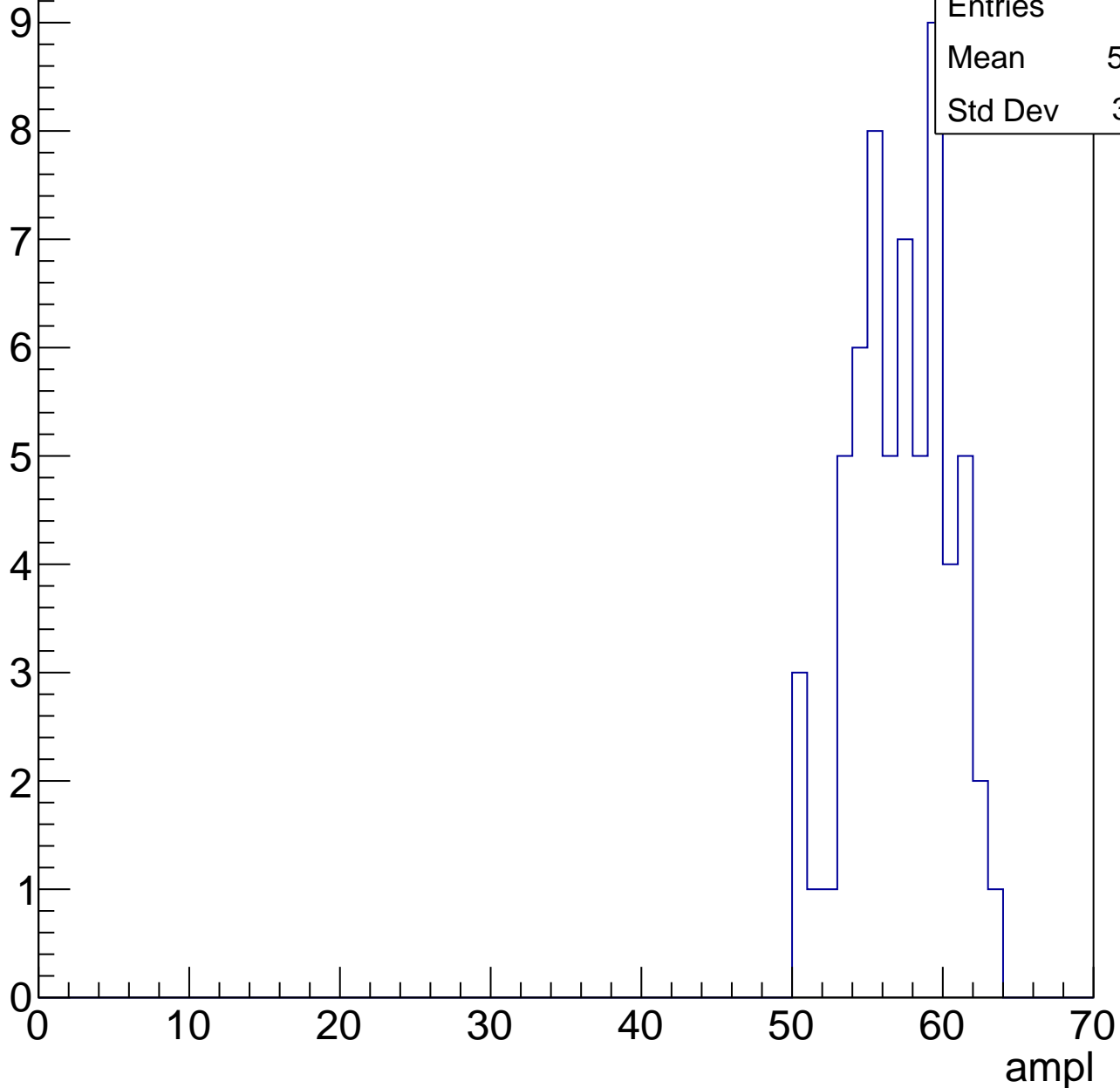


B0L001S, U25-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.68
Std Dev	3.151

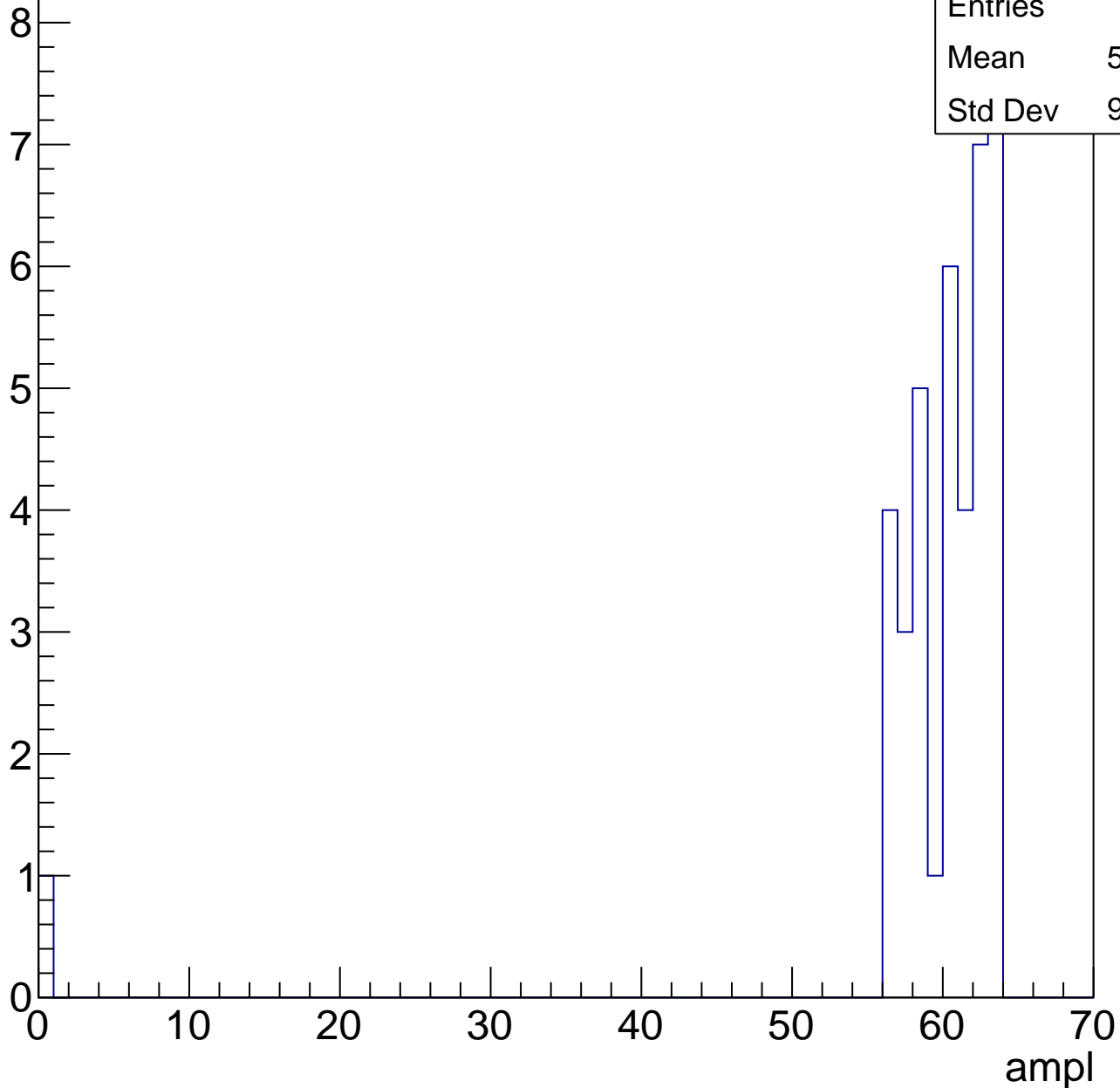


B0L001S, U25-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.62
Std Dev	9.794



B0L001S, U25-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch74, adc0

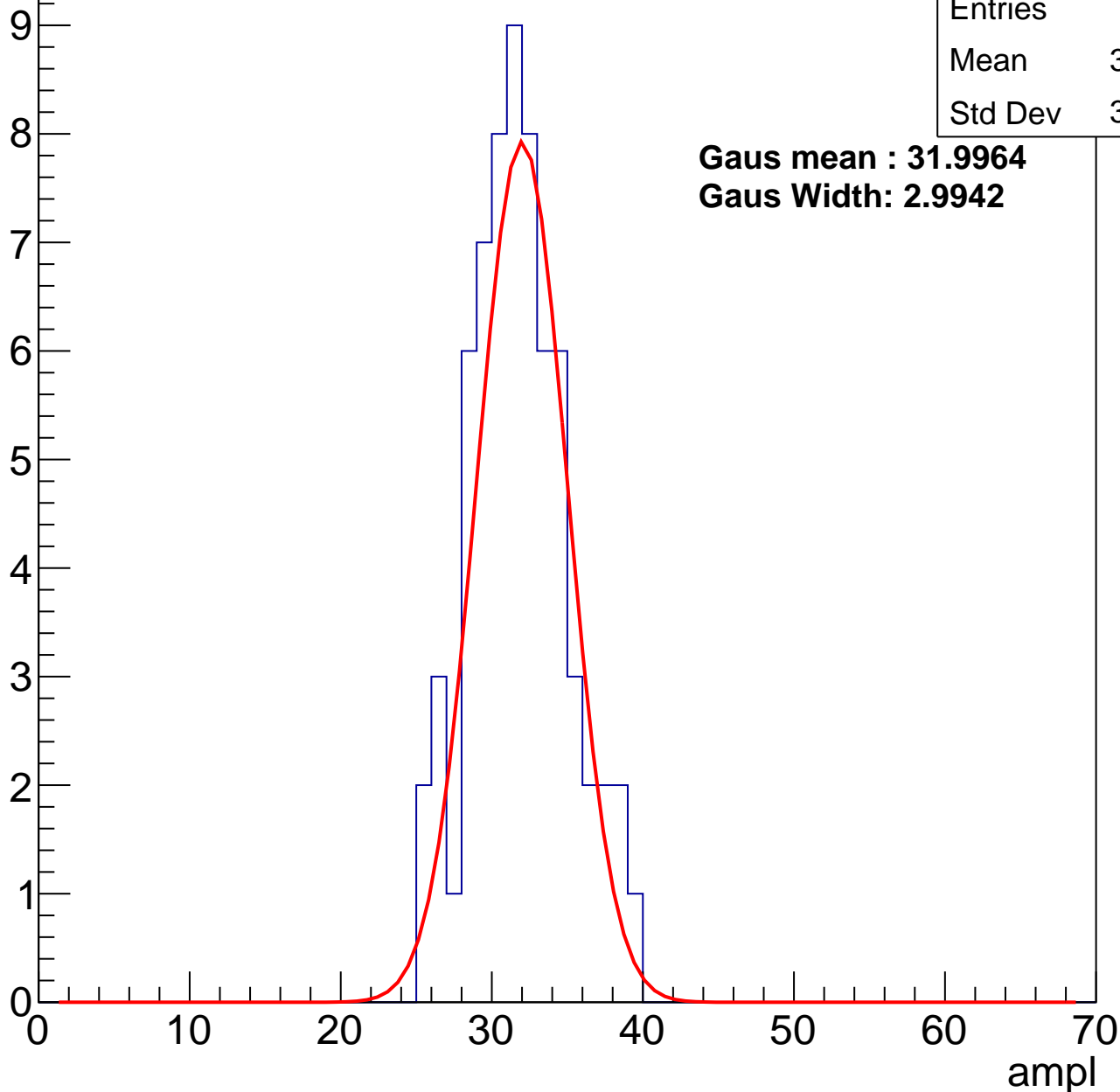
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	31.35
Std Dev	3.169

Gaus mean : 31.9964

Gaus Width: 2.9942



B0L001S, U25-ch74, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	38.94
Std Dev	3.426

Gaus mean : 39.2273

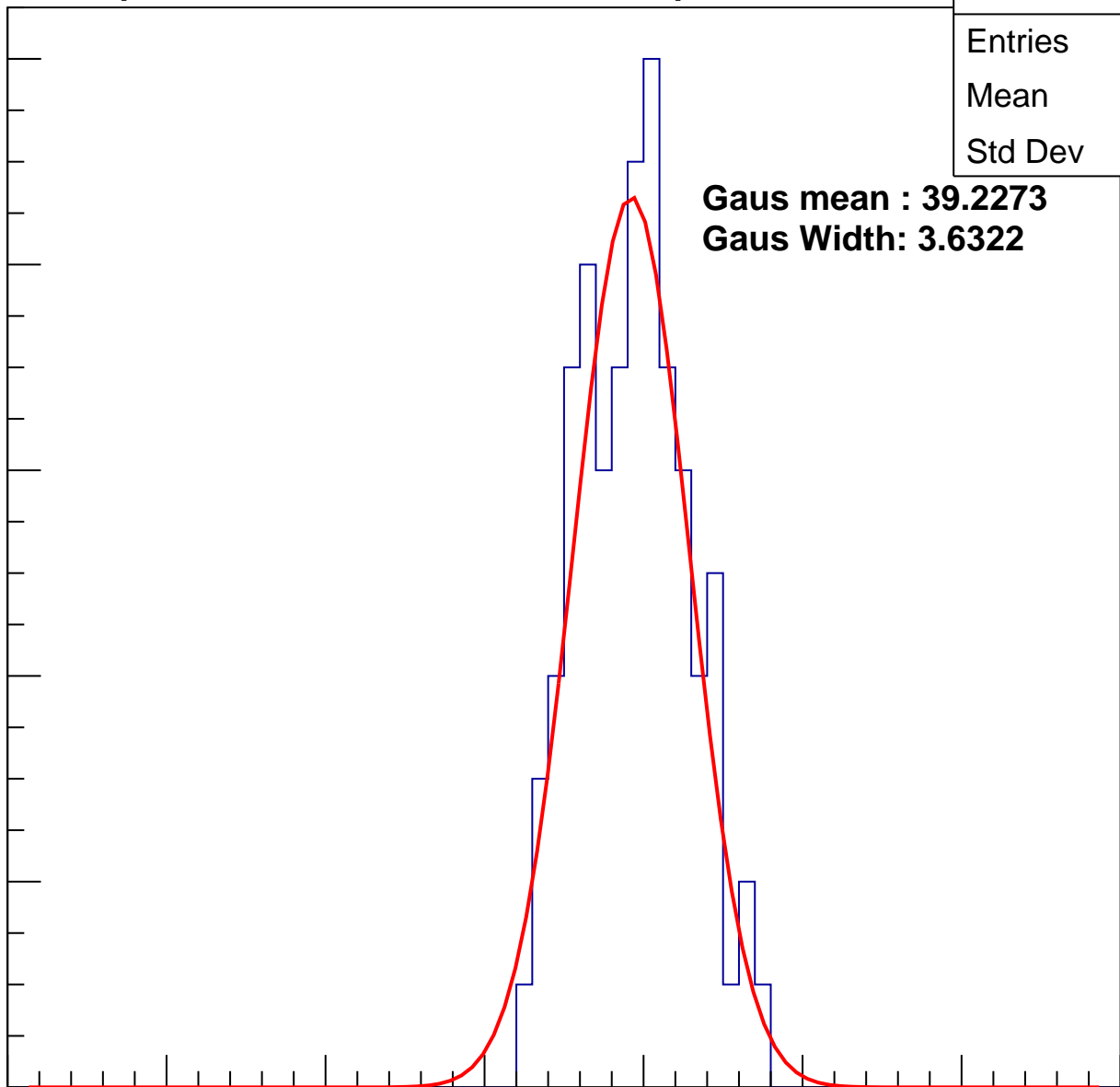
Gaus Width: 3.6322

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch74, adc2

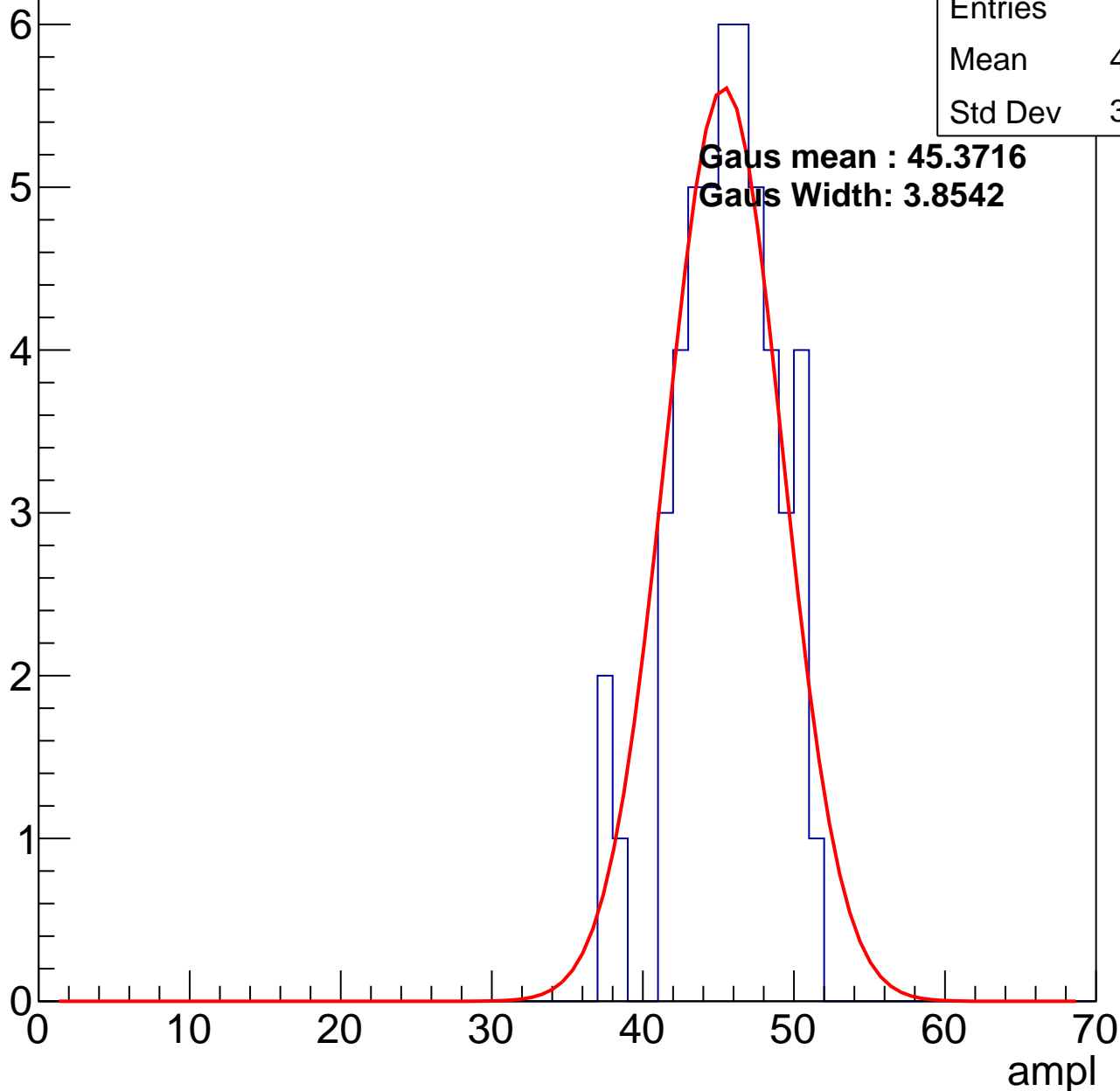
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	45.08
Std Dev	3.294

Gaus mean : 45.3716

Gaus Width: 3.8542

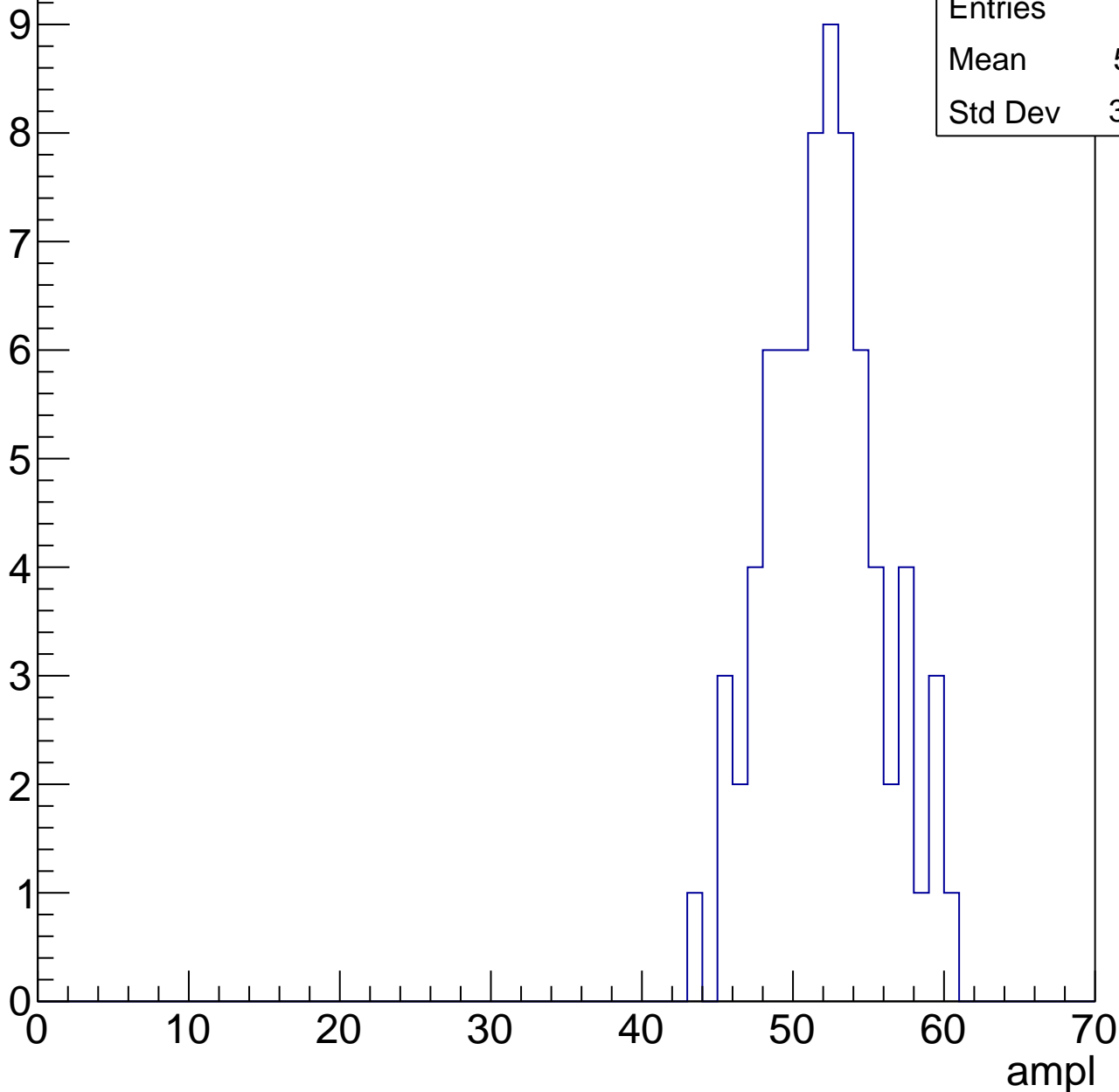


B0L001S, U25-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	51.61
Std Dev	3.708

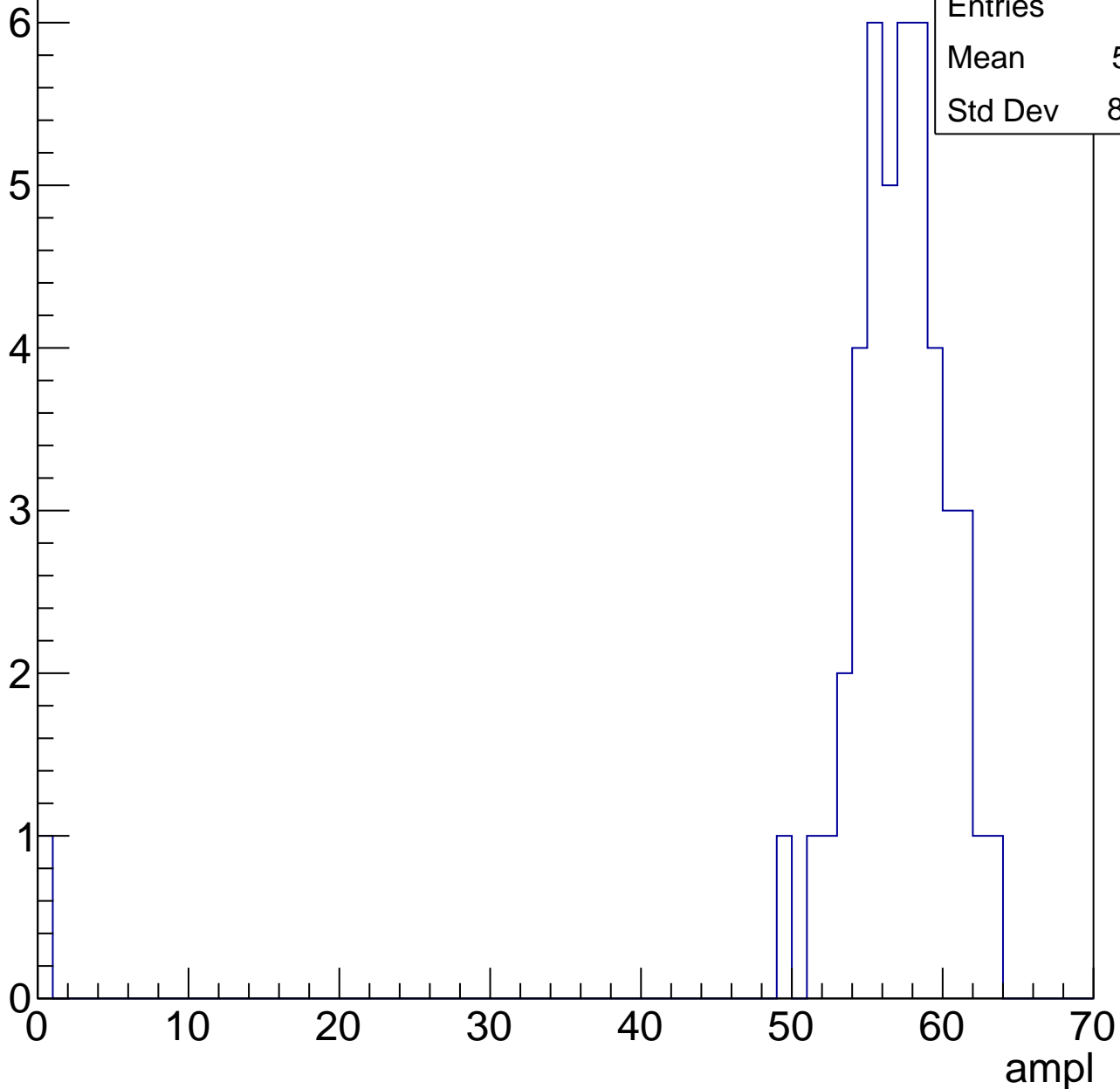


B0L001S, U25-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	55.51
Std Dev	8.856

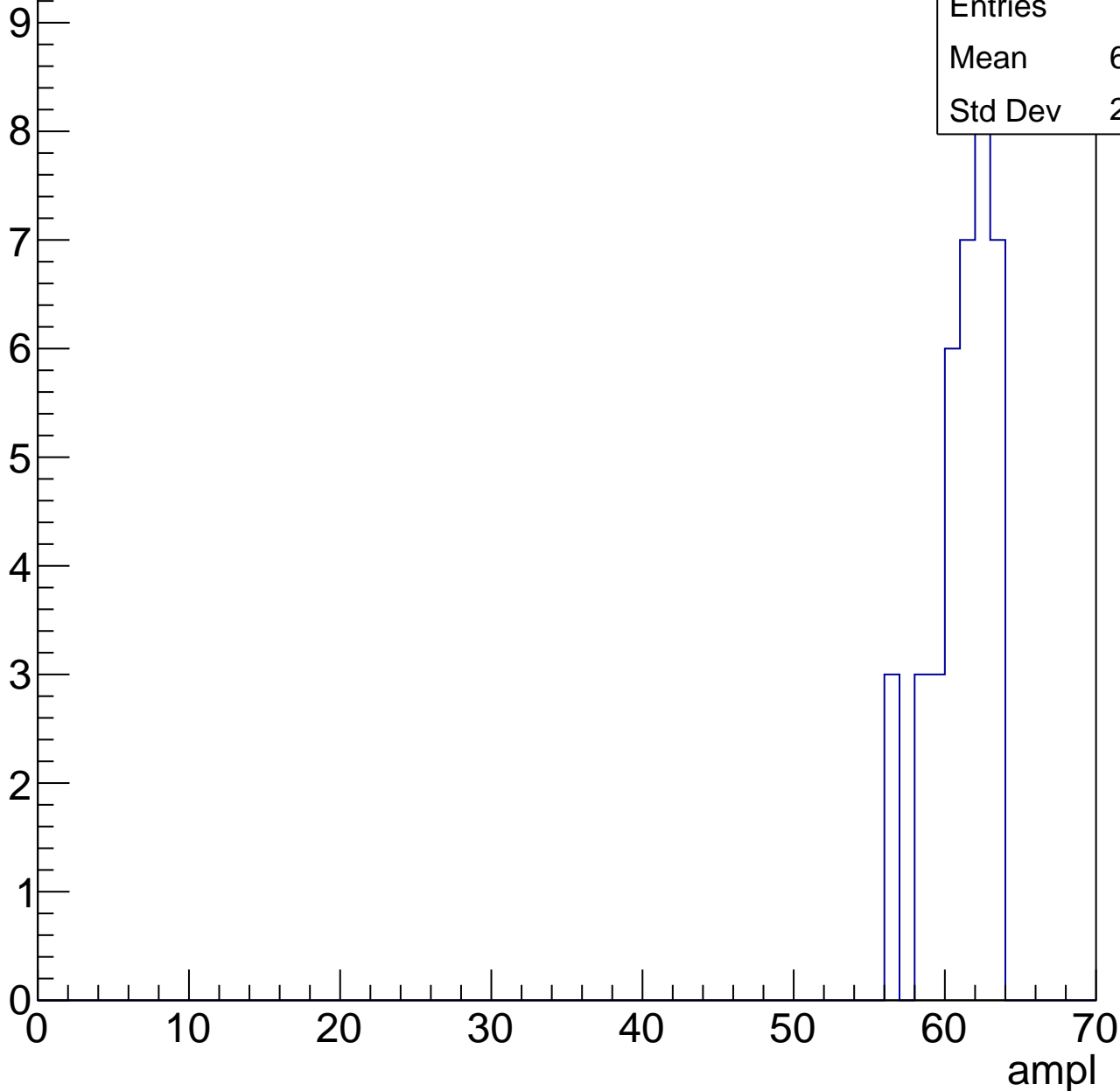


B0L001S, U25-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	60.66
Std Dev	2.004



B0L001S, U25-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch75, adc0

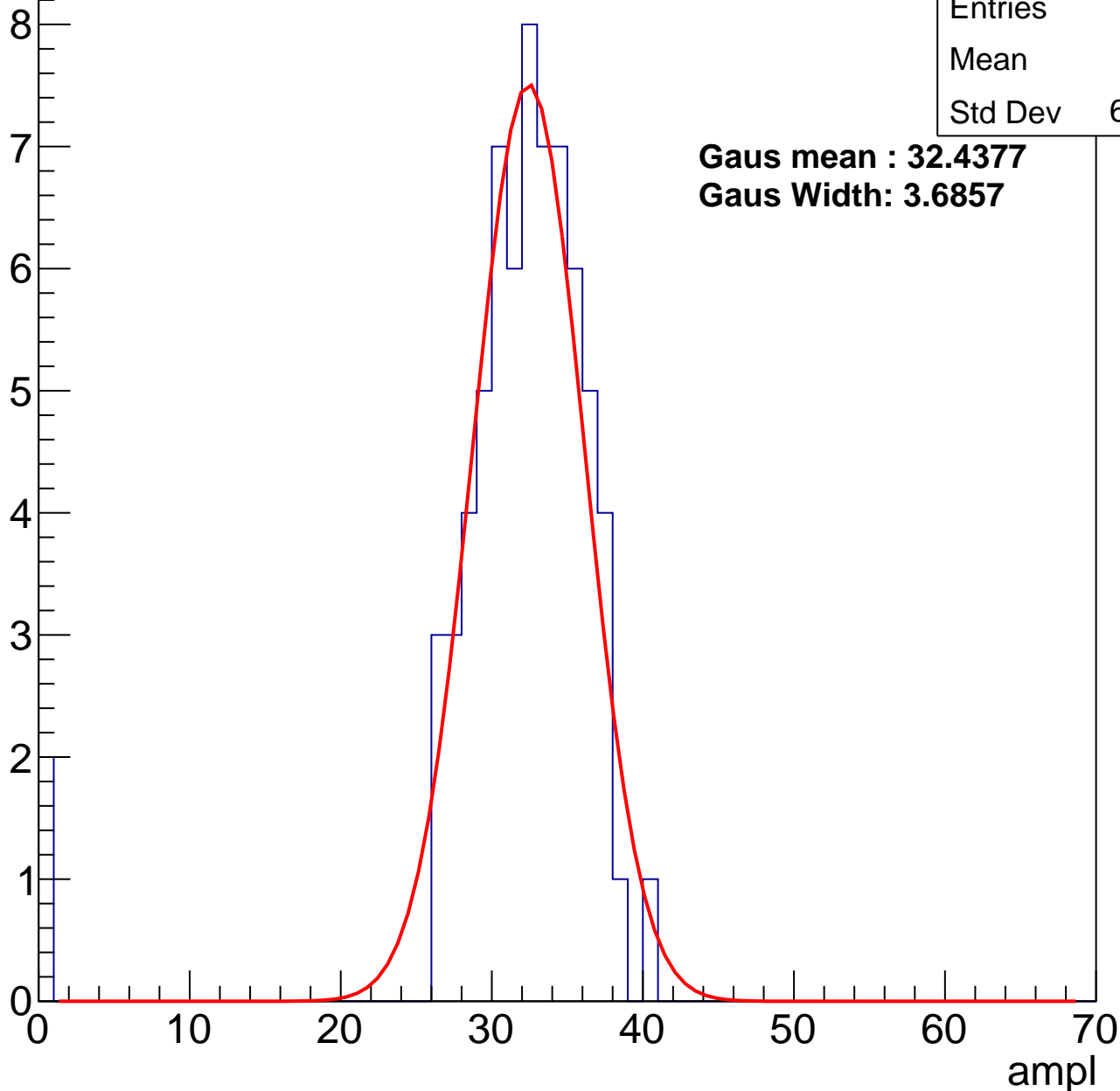
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.2
Std Dev	6.254

Gaus mean : 32.4377

Gaus Width: 3.6857



B0L001S, U25-ch75, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	60
Mean	38.3
Std Dev	3.602

Gaus mean : 38.8226

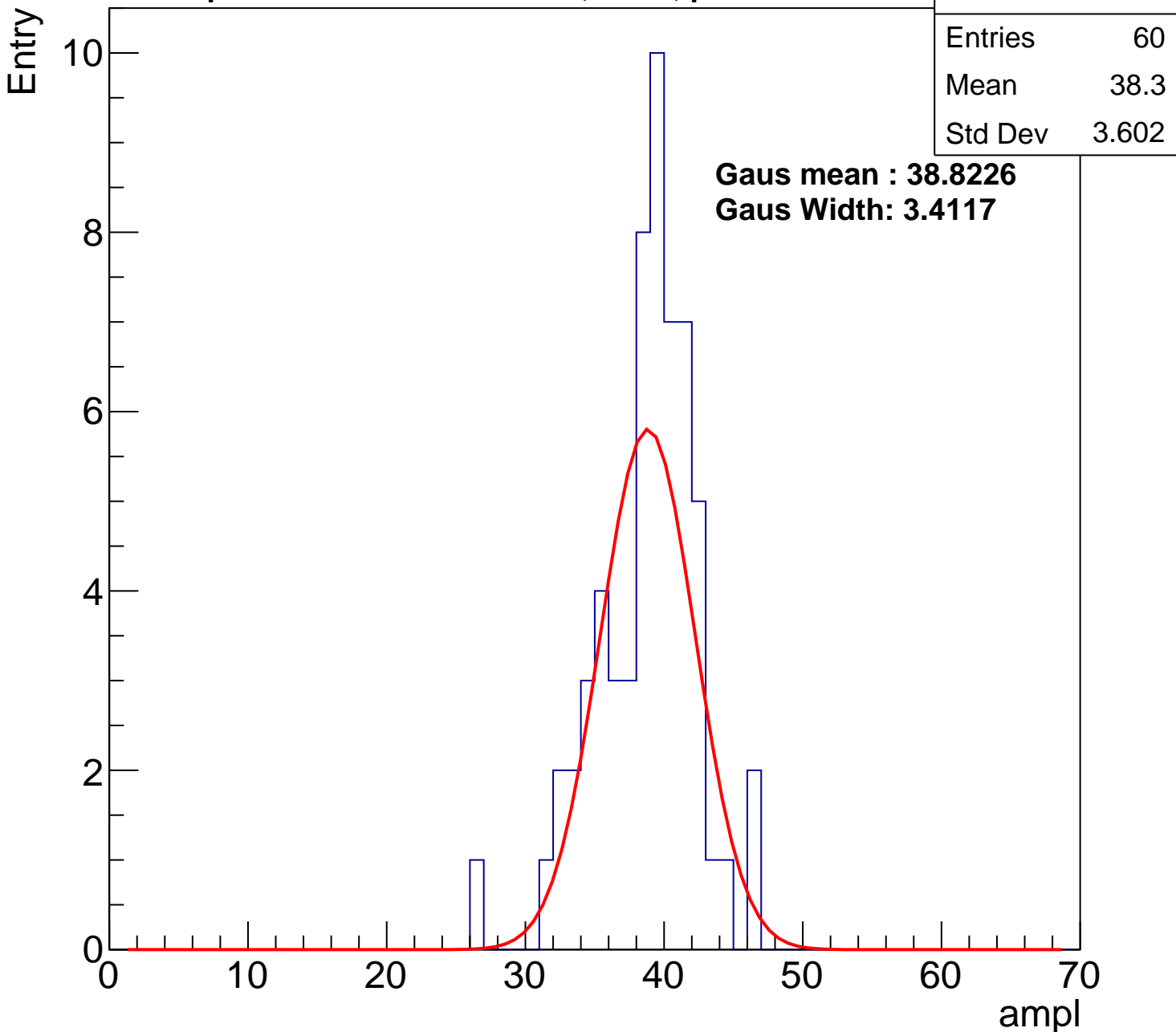
Gaus Width: 3.4117

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch75, adc2

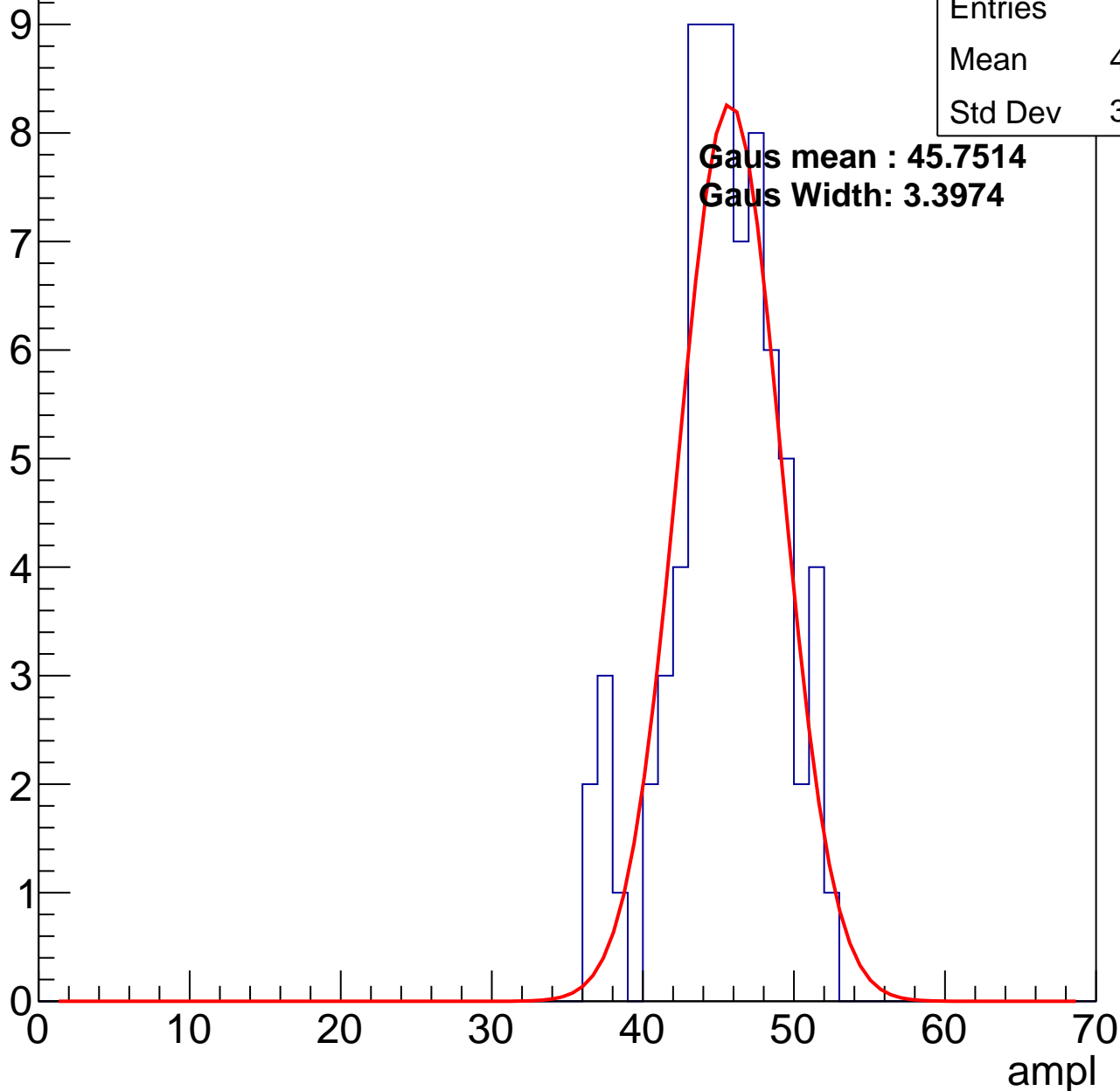
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	44.89
Std Dev	3.646

Gaus mean : 45.7514

Gaus Width: 3.3974

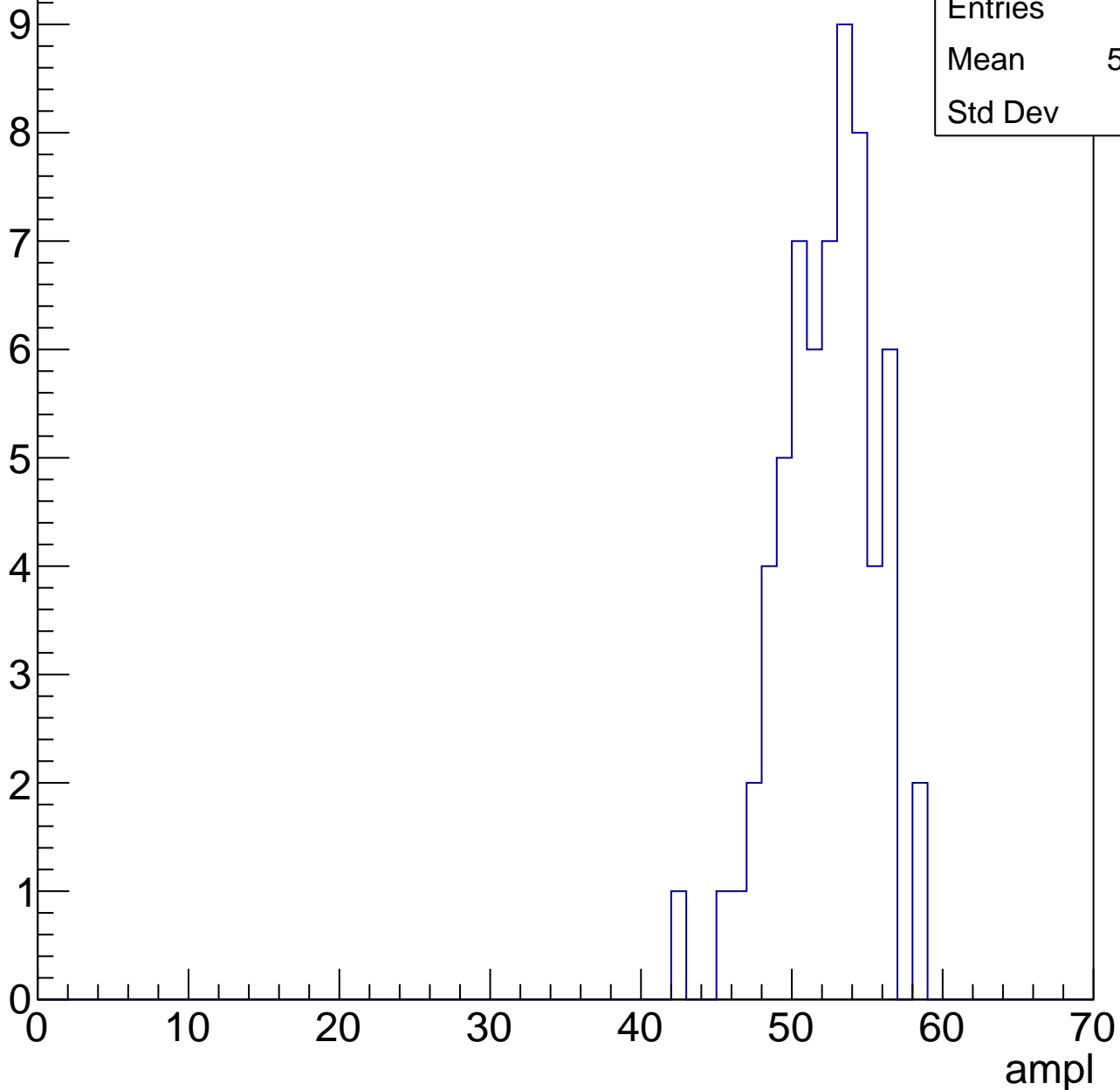


B0L001S, U25-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

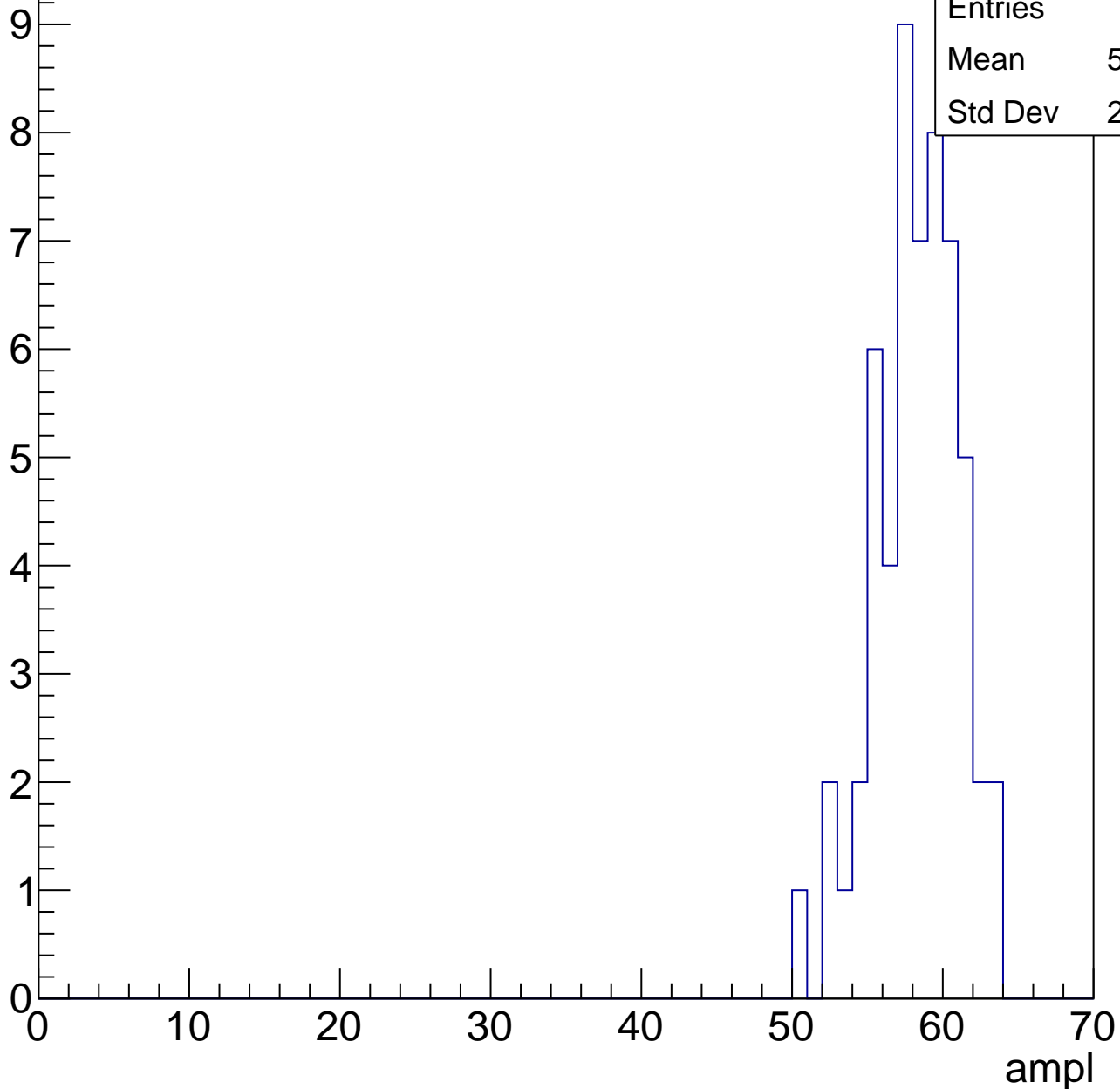
Entries	63
Mean	51.83
Std Dev	3.14



B0L001S, U25-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



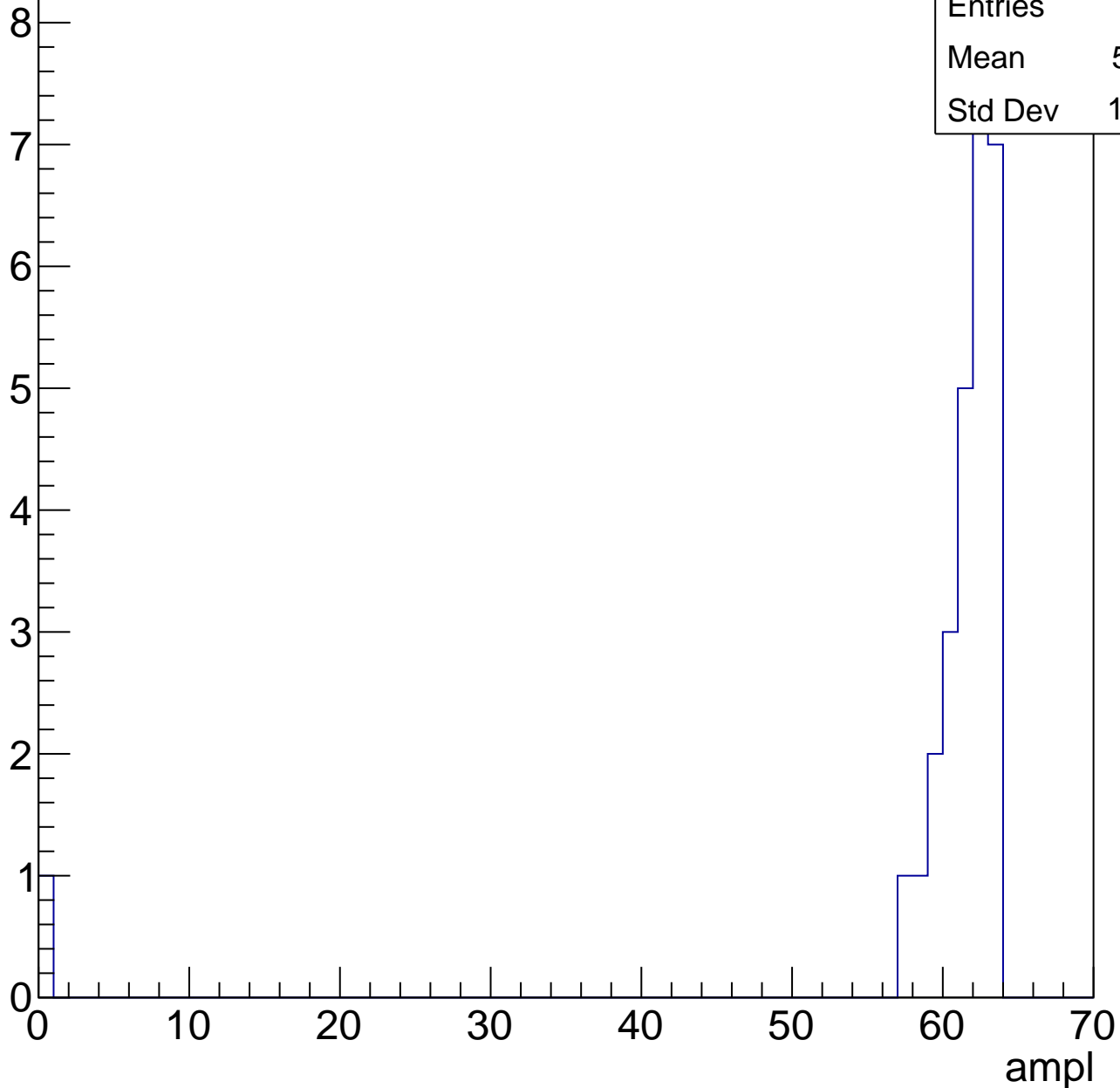
Entries	56
Mean	57.77
Std Dev	2.777

B0L001S, U25-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	59.11
Std Dev	11.48



B0L001S, U25-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch76, adc0

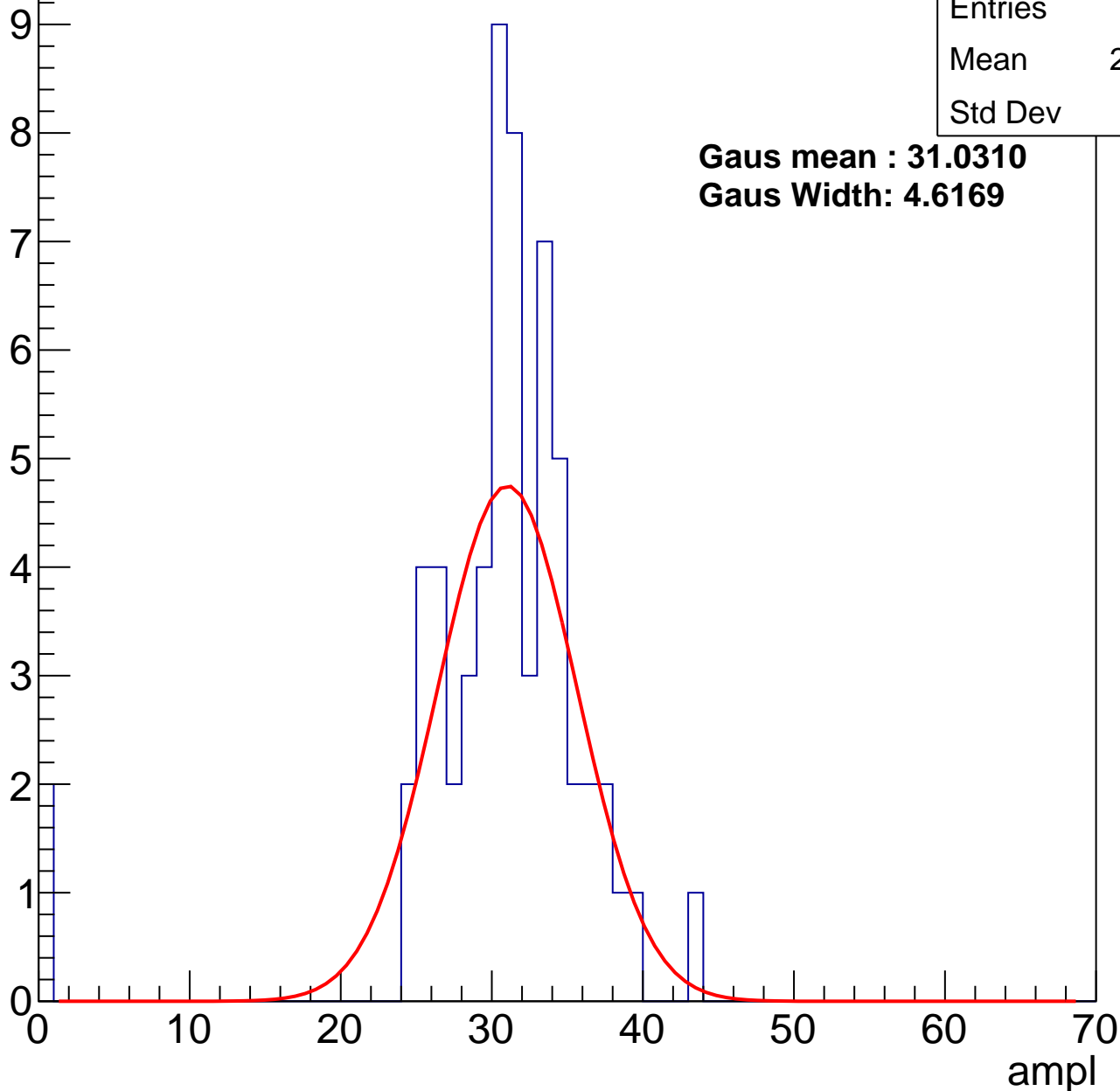
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.95
Std Dev	6.68

Gaus mean : 31.0310

Gaus Width: 4.6169



B0L001S, U25-ch76, adc1

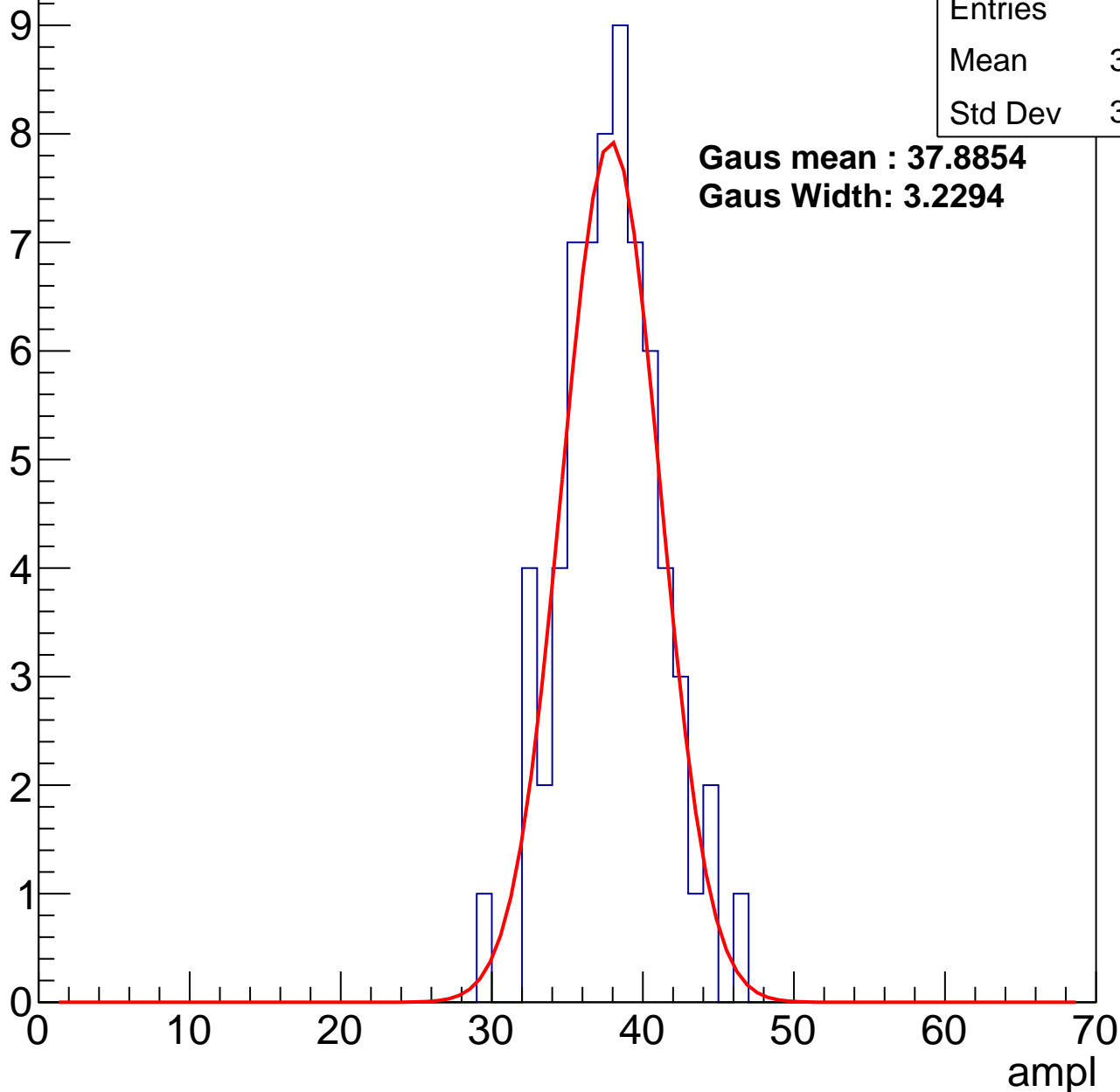
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.48
Std Dev	3.249

Gaus mean : 37.8854

Gaus Width: 3.2294



B0L001S, U25-ch76, adc2

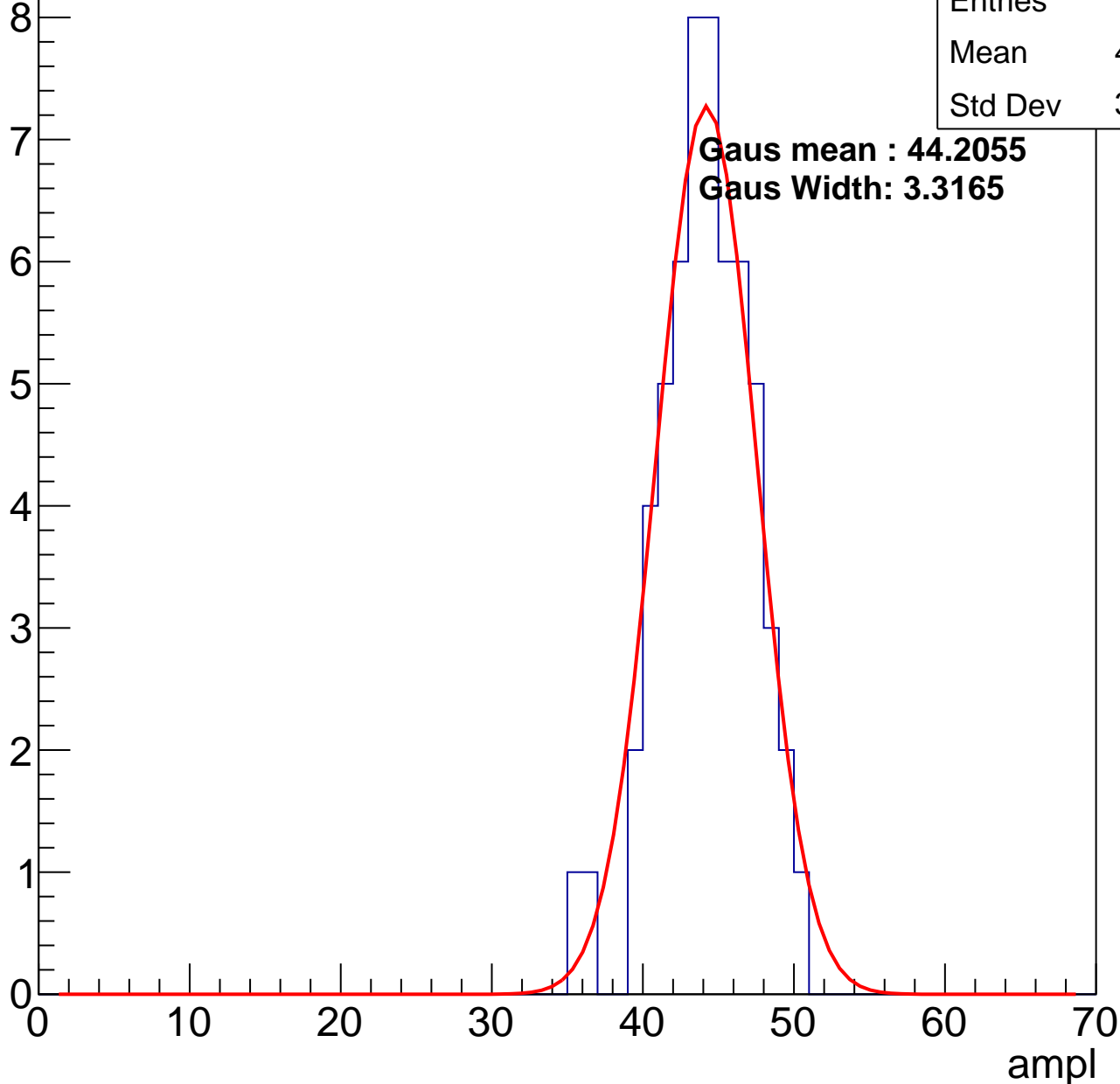
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.71
Std Dev	3.051

Gaus mean : 44.2055

Gaus Width: 3.3165



B0L001S, U25-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

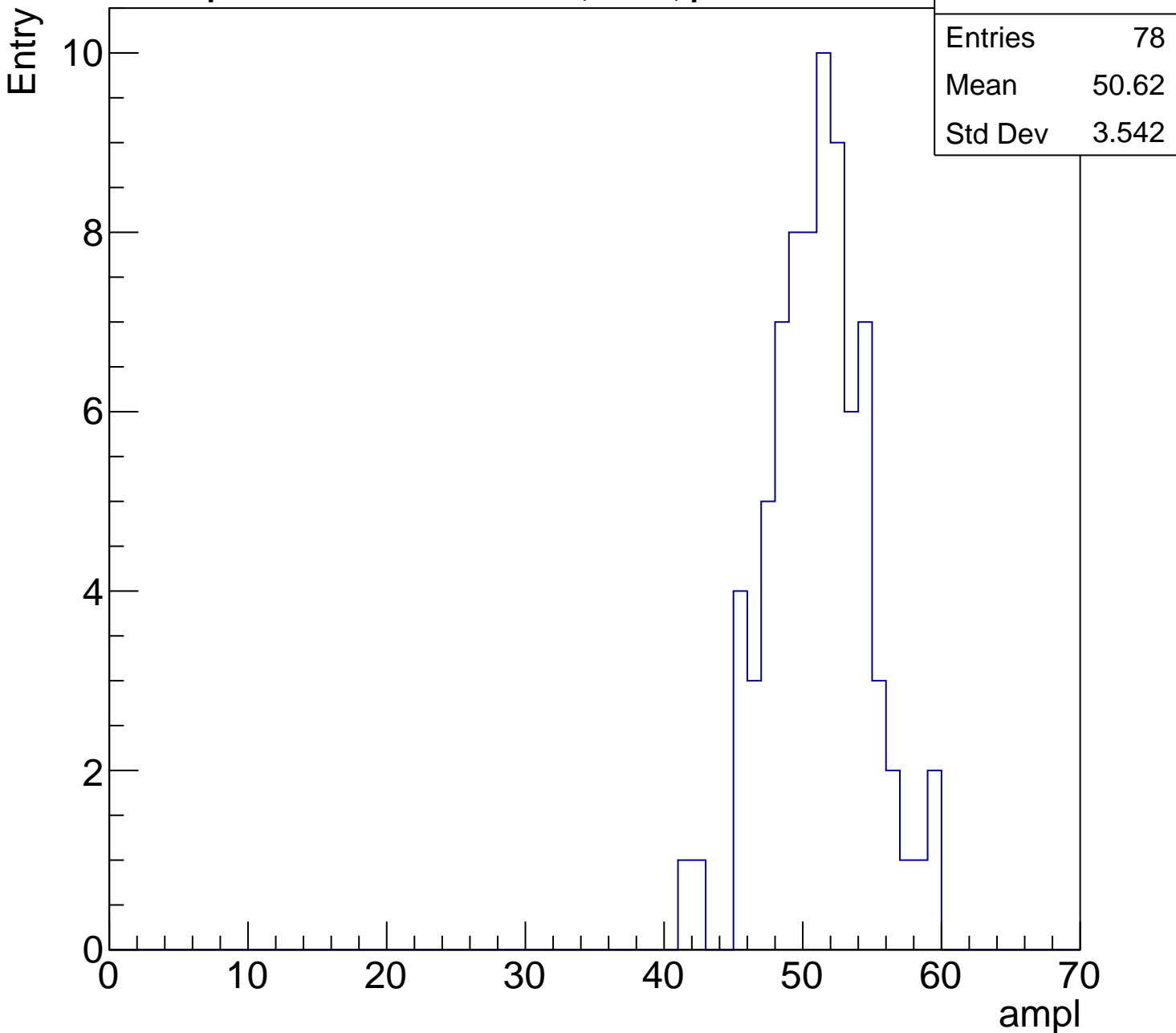
Entries	78
Mean	50.62
Std Dev	3.542

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

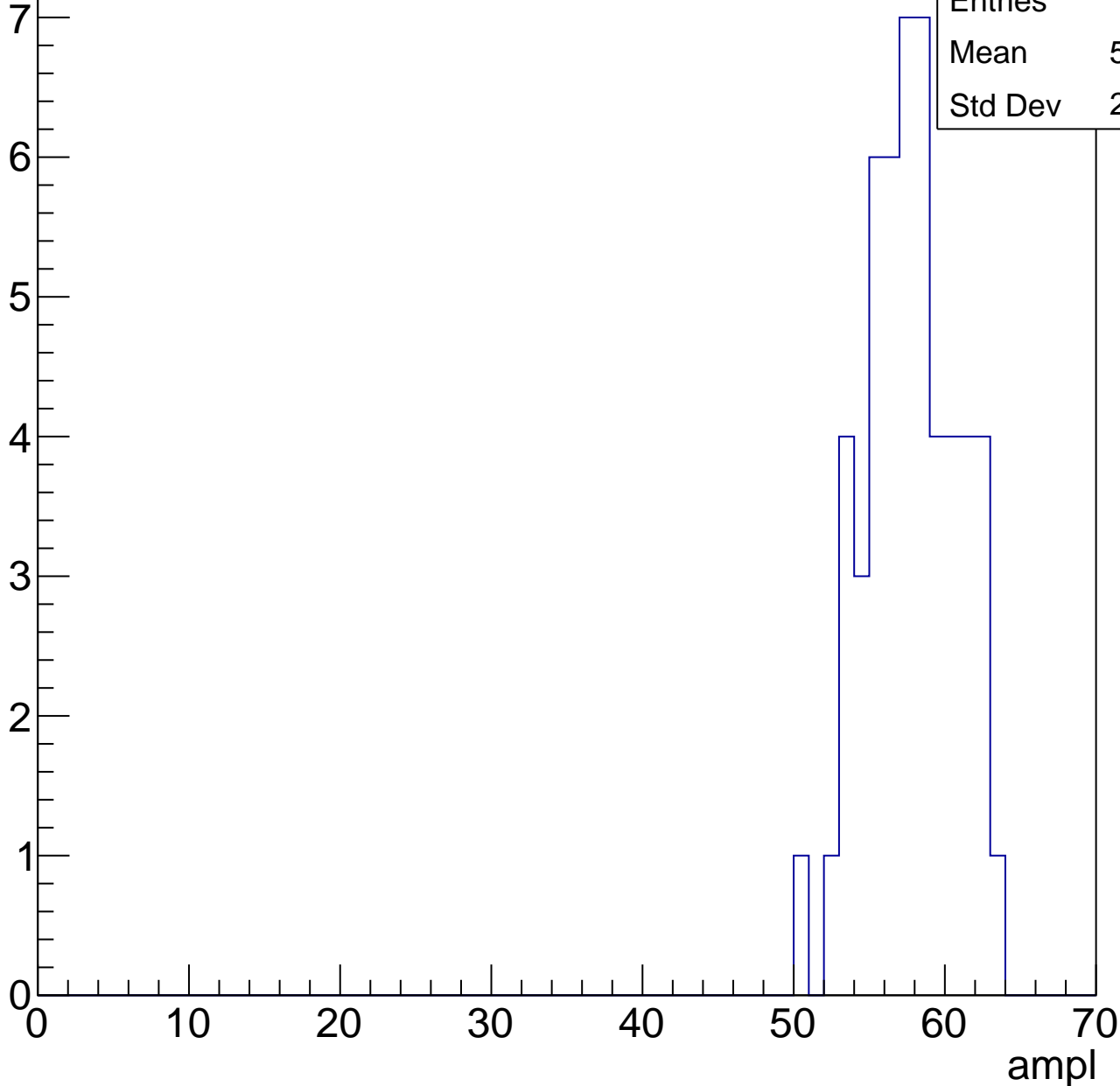


B0L001S, U25-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	57.27
Std Dev	2.942

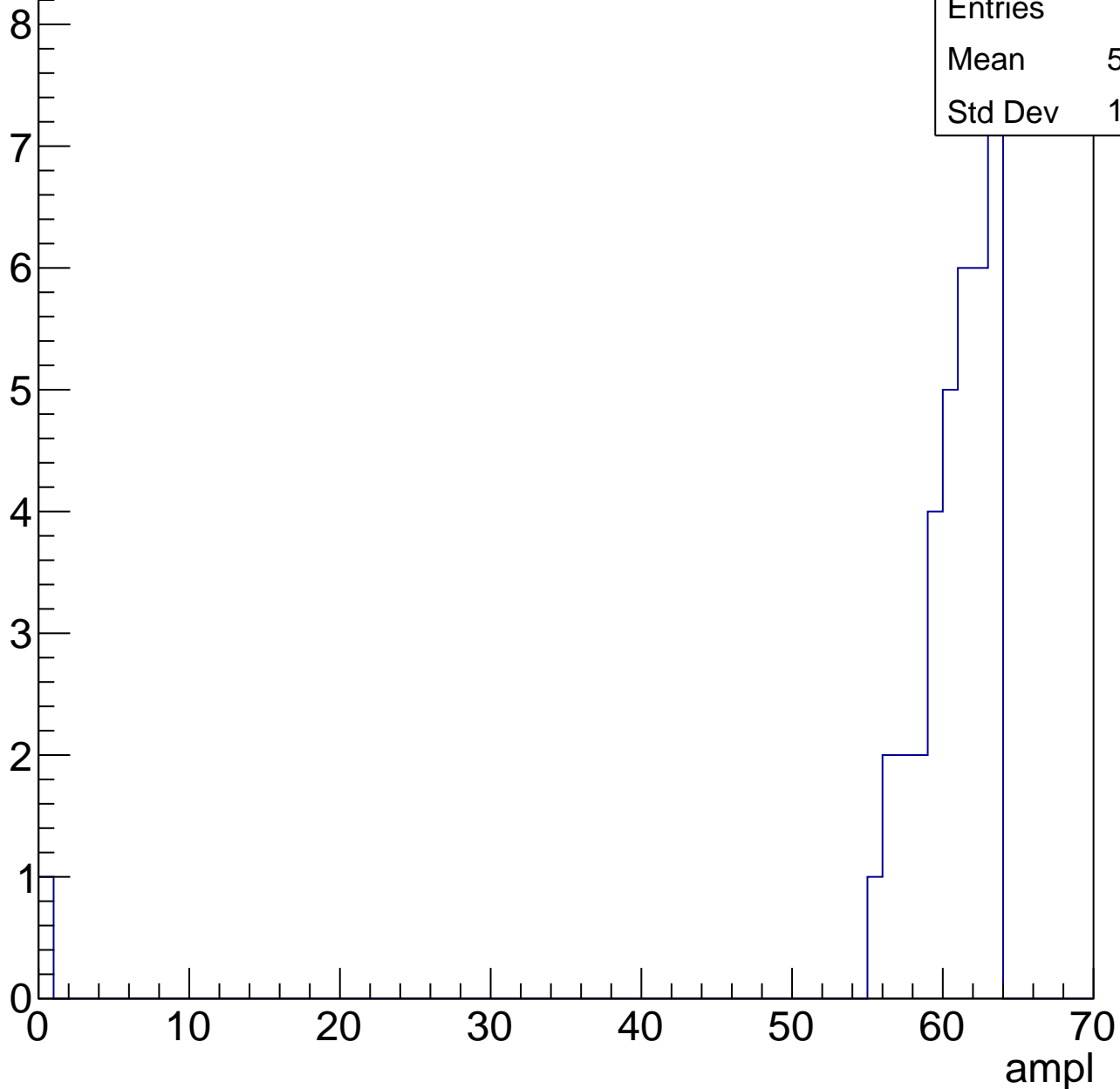


B0L001S, U25-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.78
Std Dev	10.05



B0L001S, U25-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch77, adc0

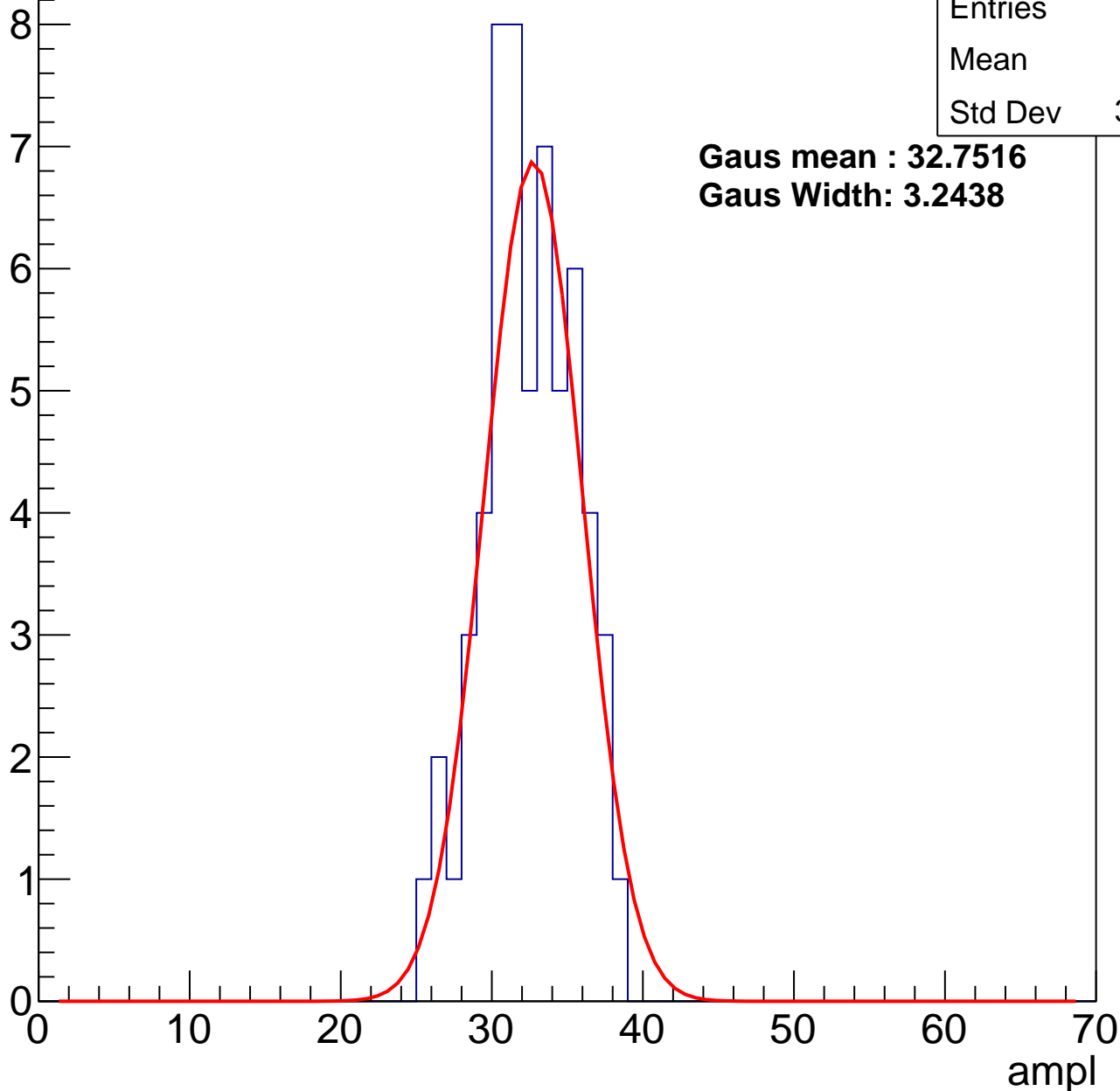
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	32
Std Dev	3.011

Gaus mean : 32.7516

Gaus Width: 3.2438



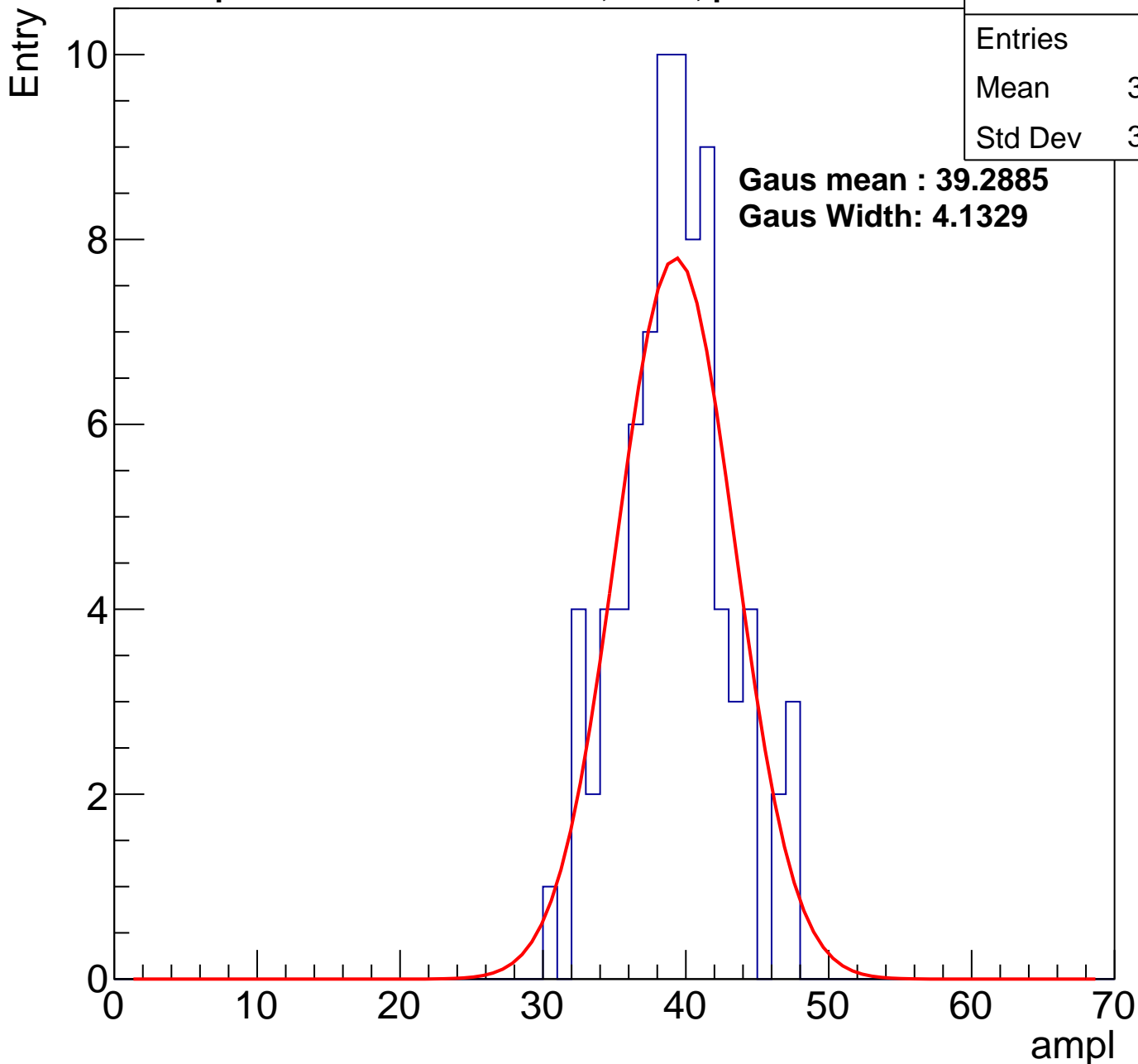
B0L001S, U25-ch77, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	38.77
Std Dev	3.716

Gaus mean : 39.2885

Gaus Width: 4.1329



B0L001S, U25-ch77, adc2

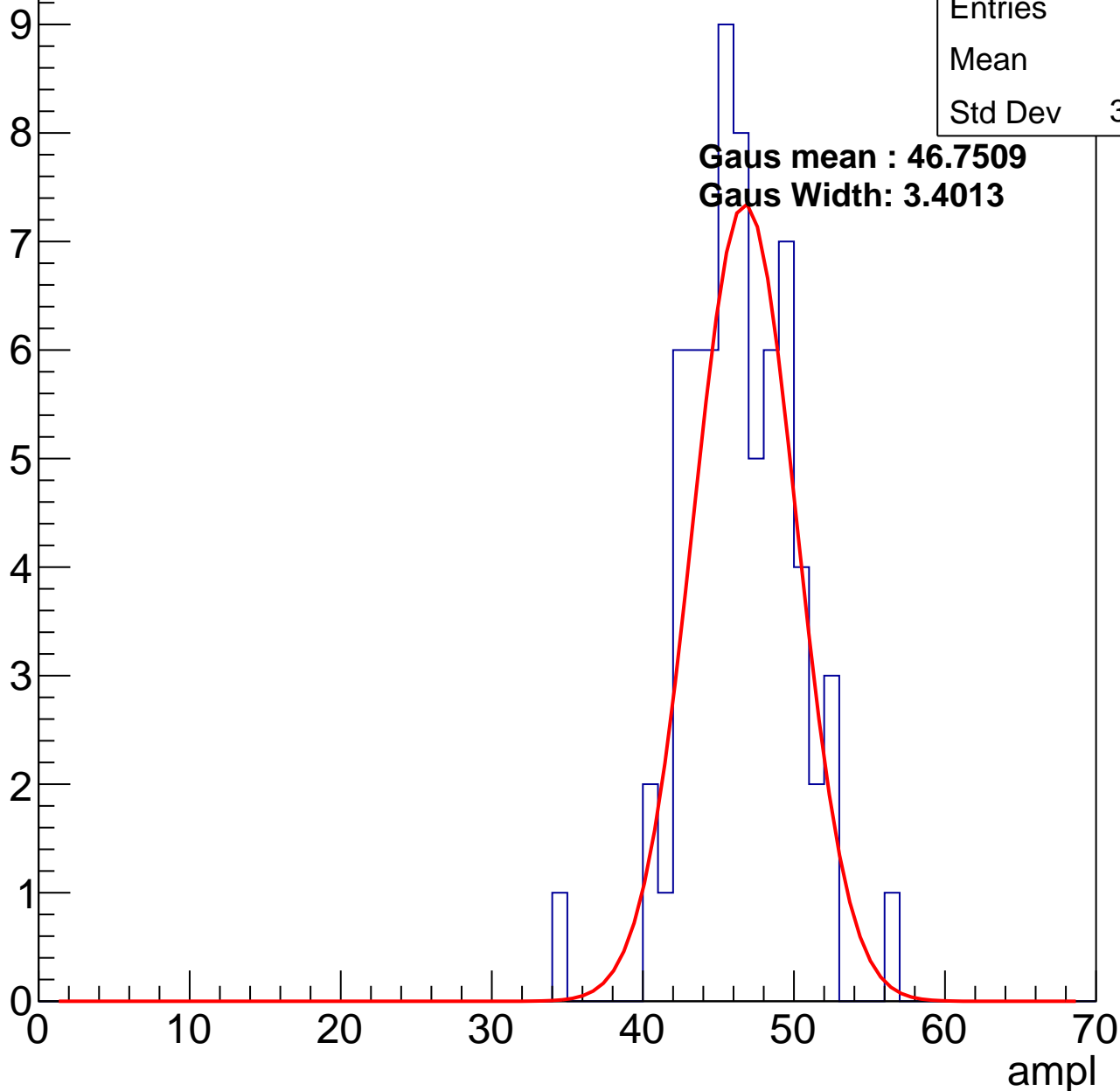
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	46
Std Dev	3.537

Gaus mean : 46.7509

Gaus Width: 3.4013

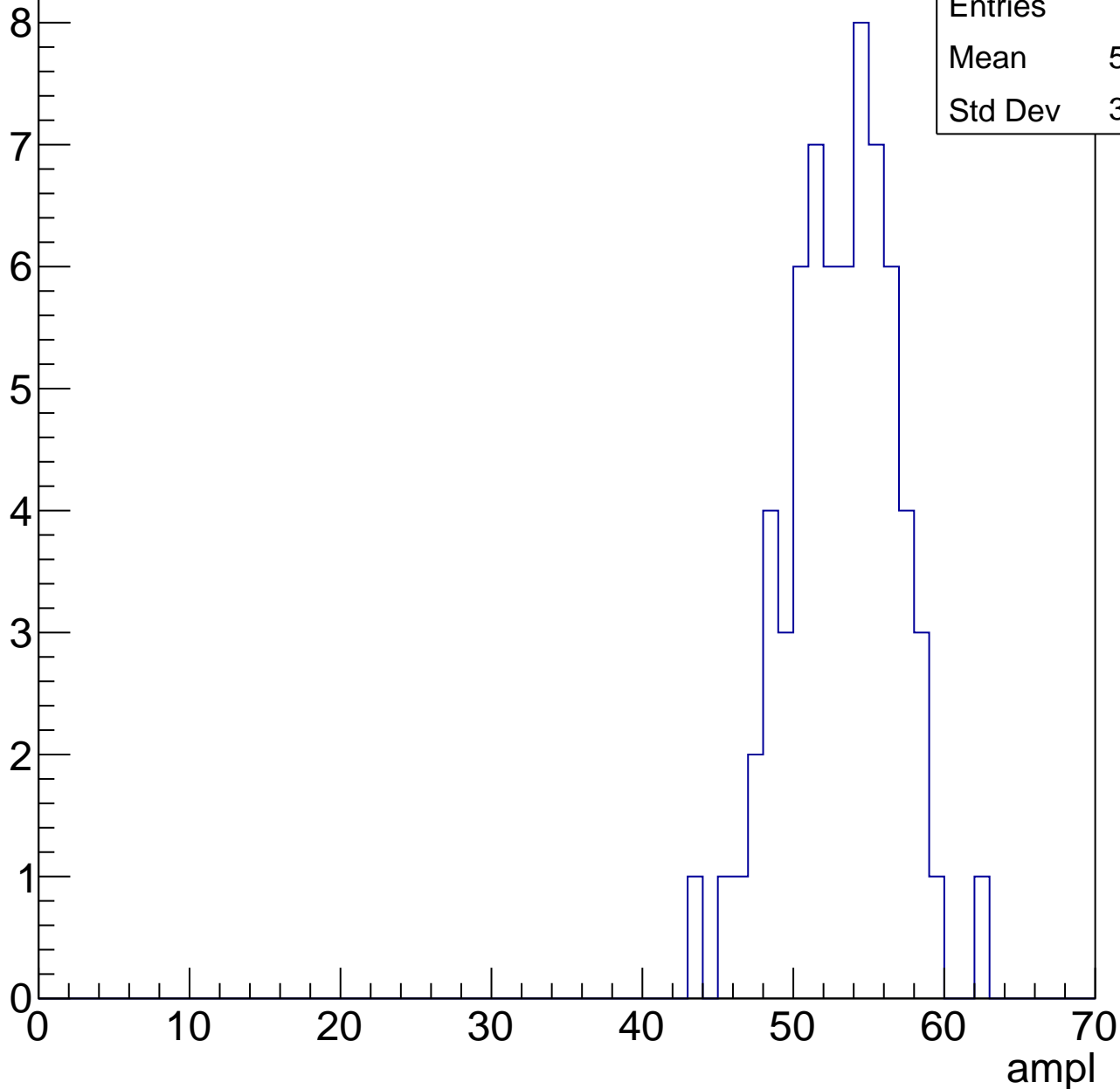


B0L001S, U25-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	52.69
Std Dev	3.584

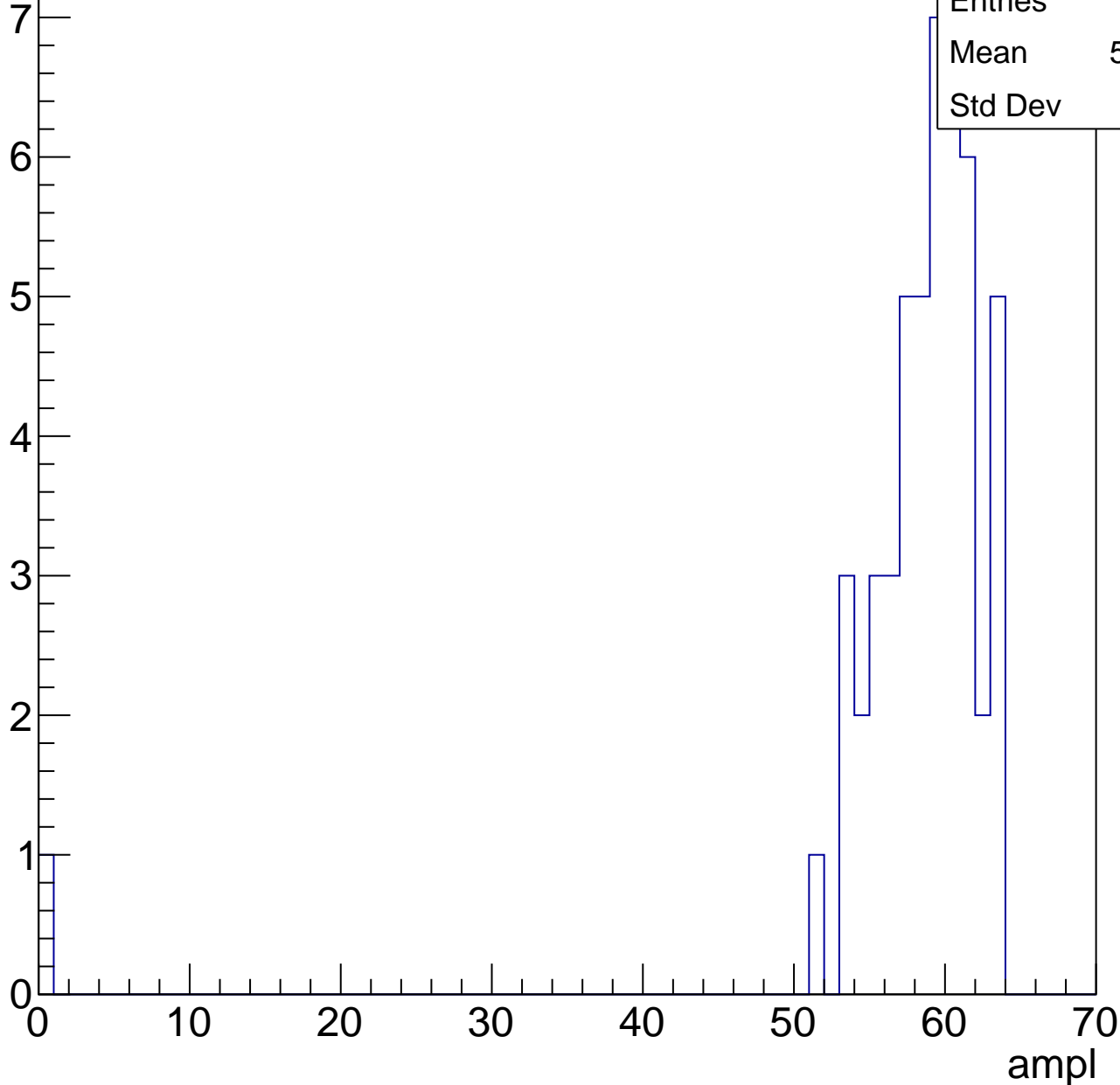


B0L001S, U25-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.28
Std Dev	8.7

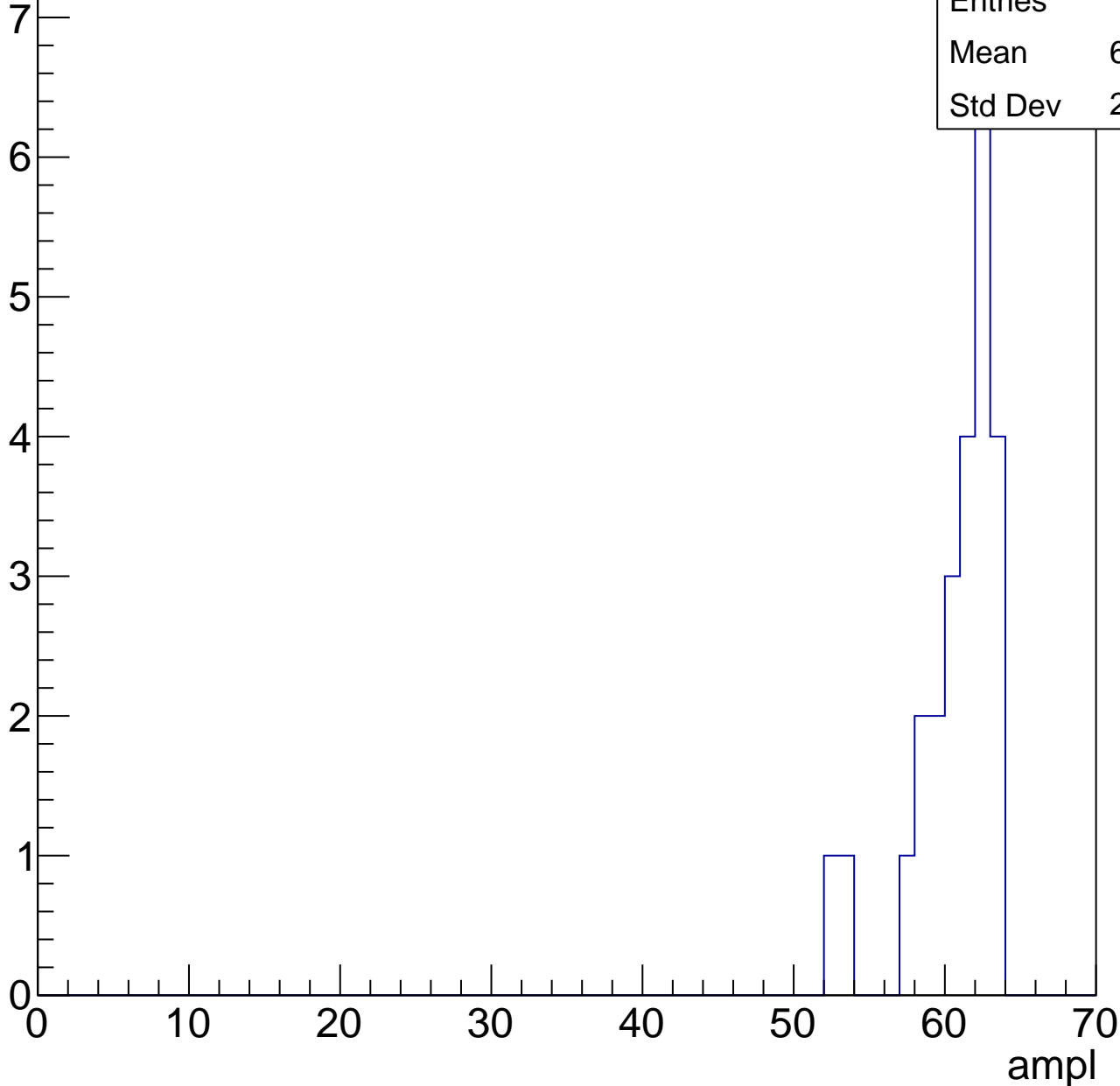


B0L001S, U25-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	60.24
Std Dev	2.818



B0L001S, U25-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch78, adc0

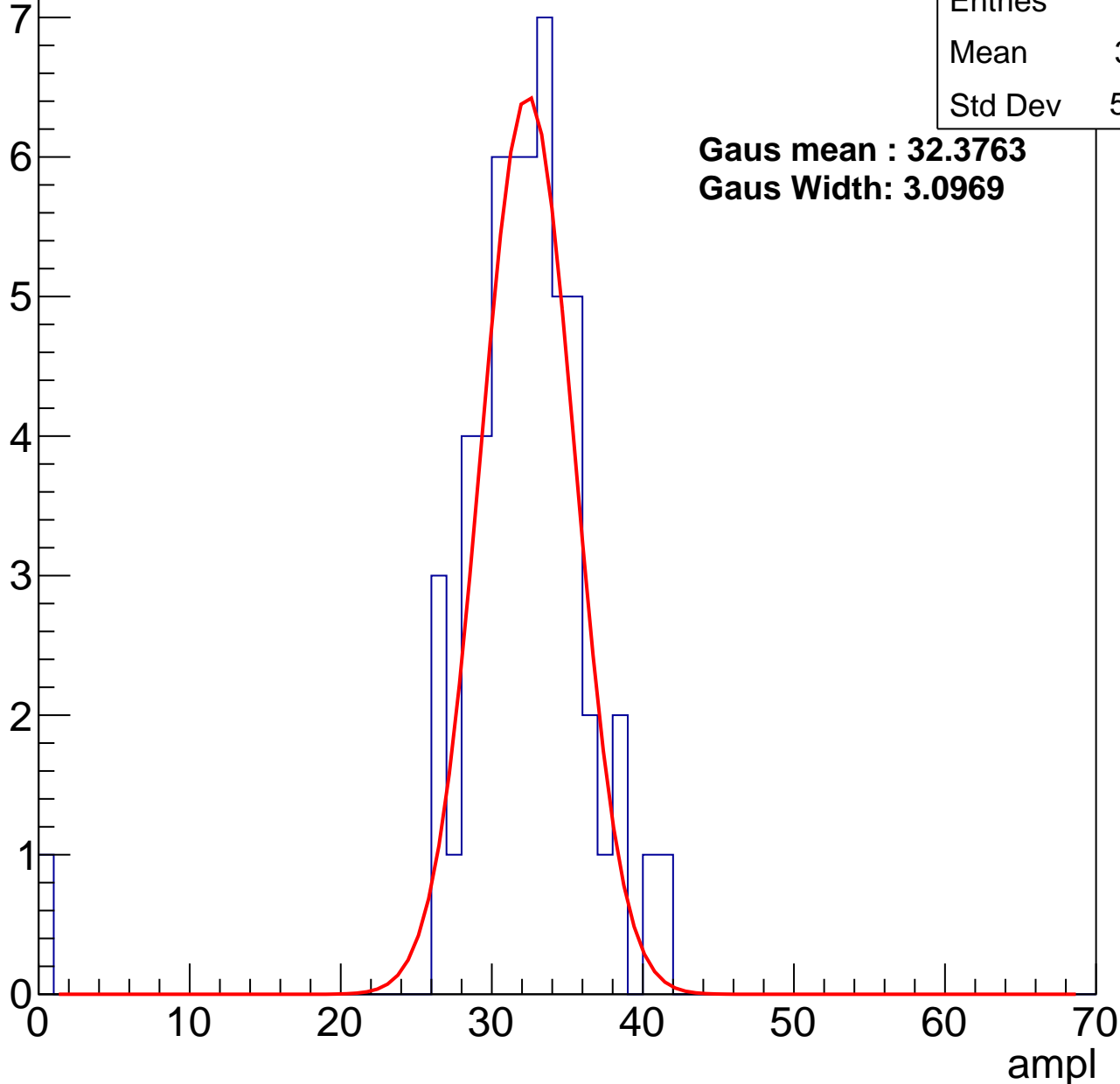
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.51
Std Dev	5.427

Gaus mean : 32.3763

Gaus Width: 3.0969



B0L001S, U25-ch78, adc1

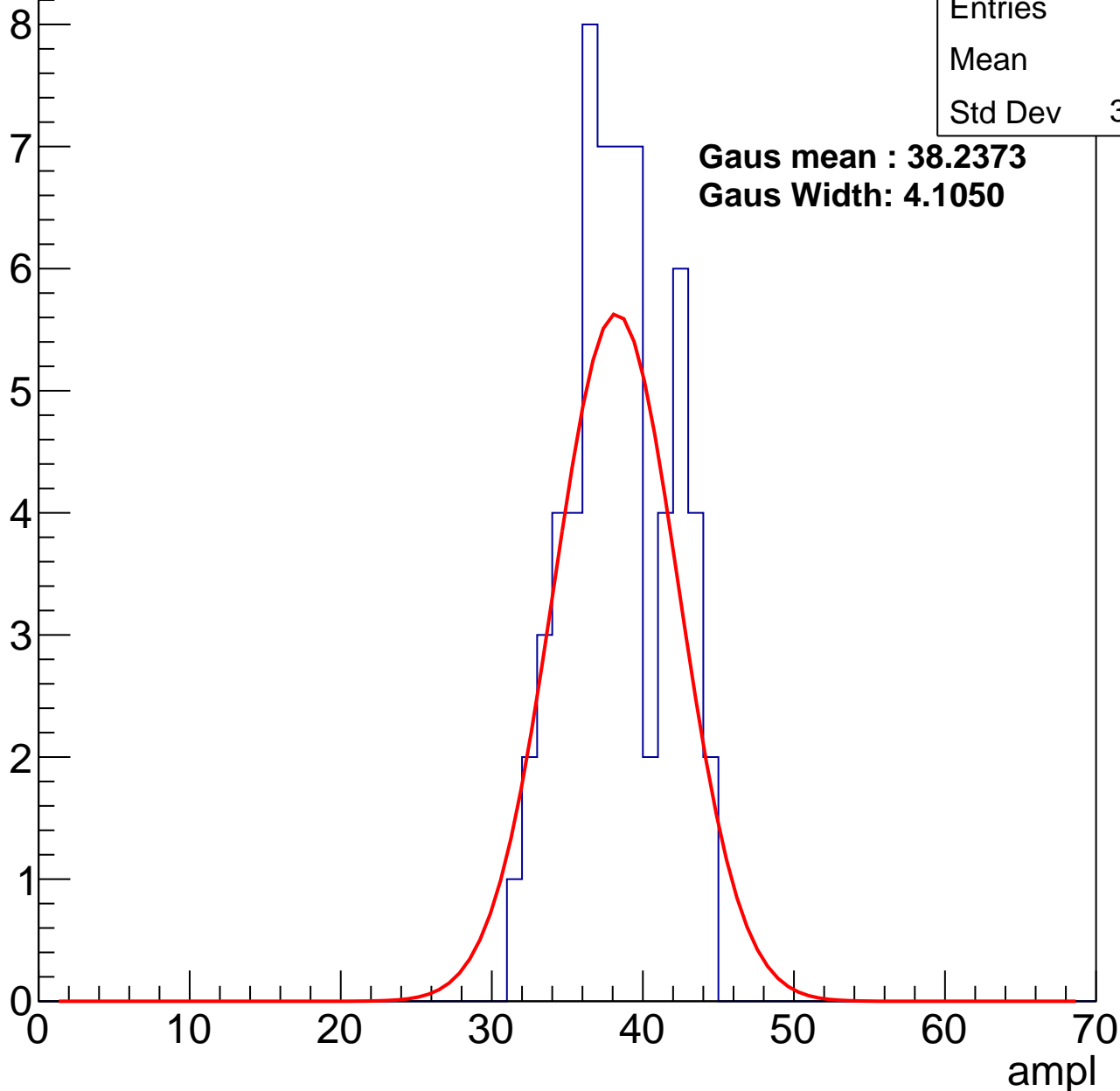
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.9
Std Dev	3.273

Gaus mean : 38.2373

Gaus Width: 4.1050



B0L001S, U25-ch78, adc2

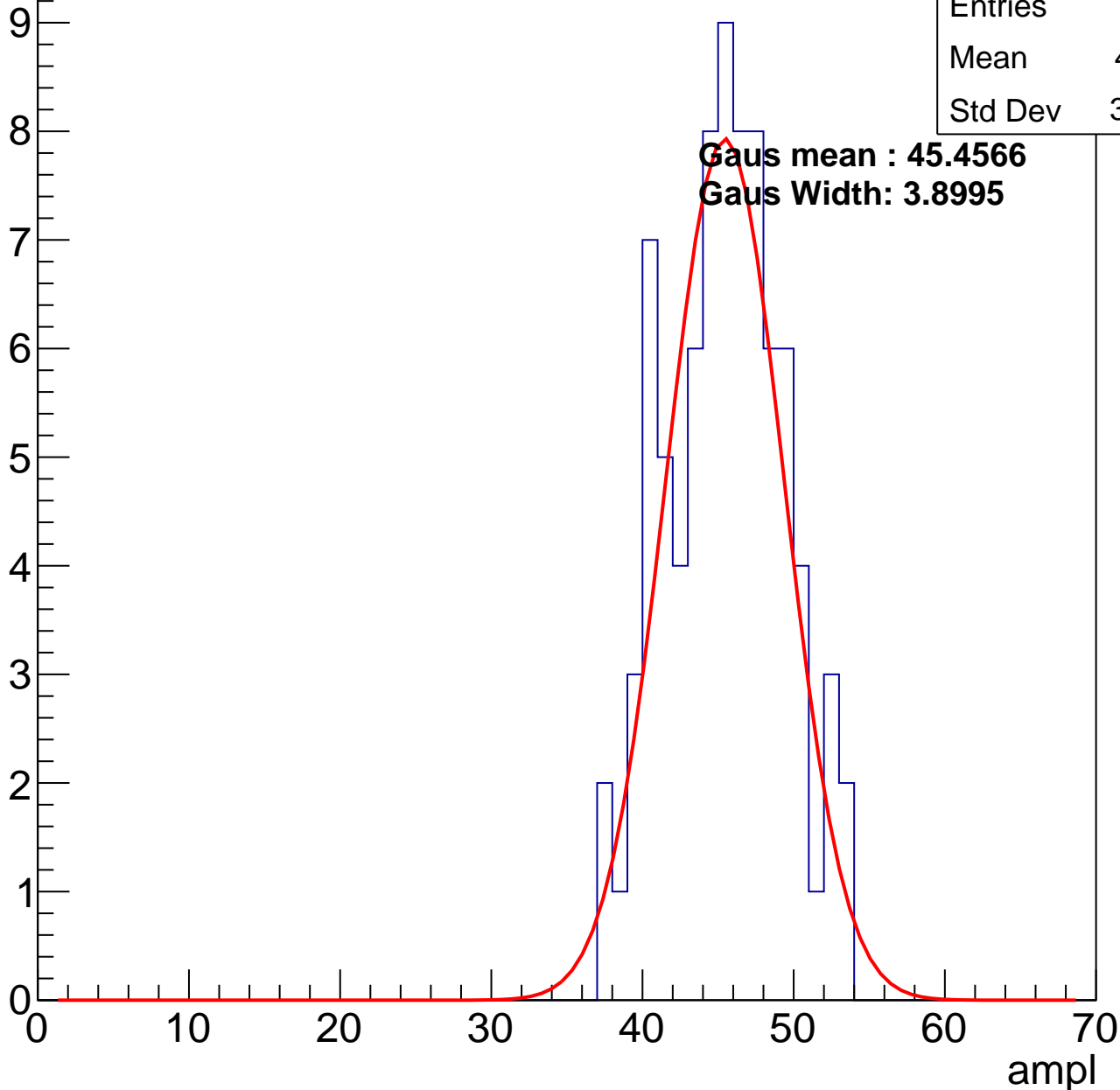
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	45.01
Std Dev	3.817

Gaus mean : 45.4566

Gaus Width: 3.8995

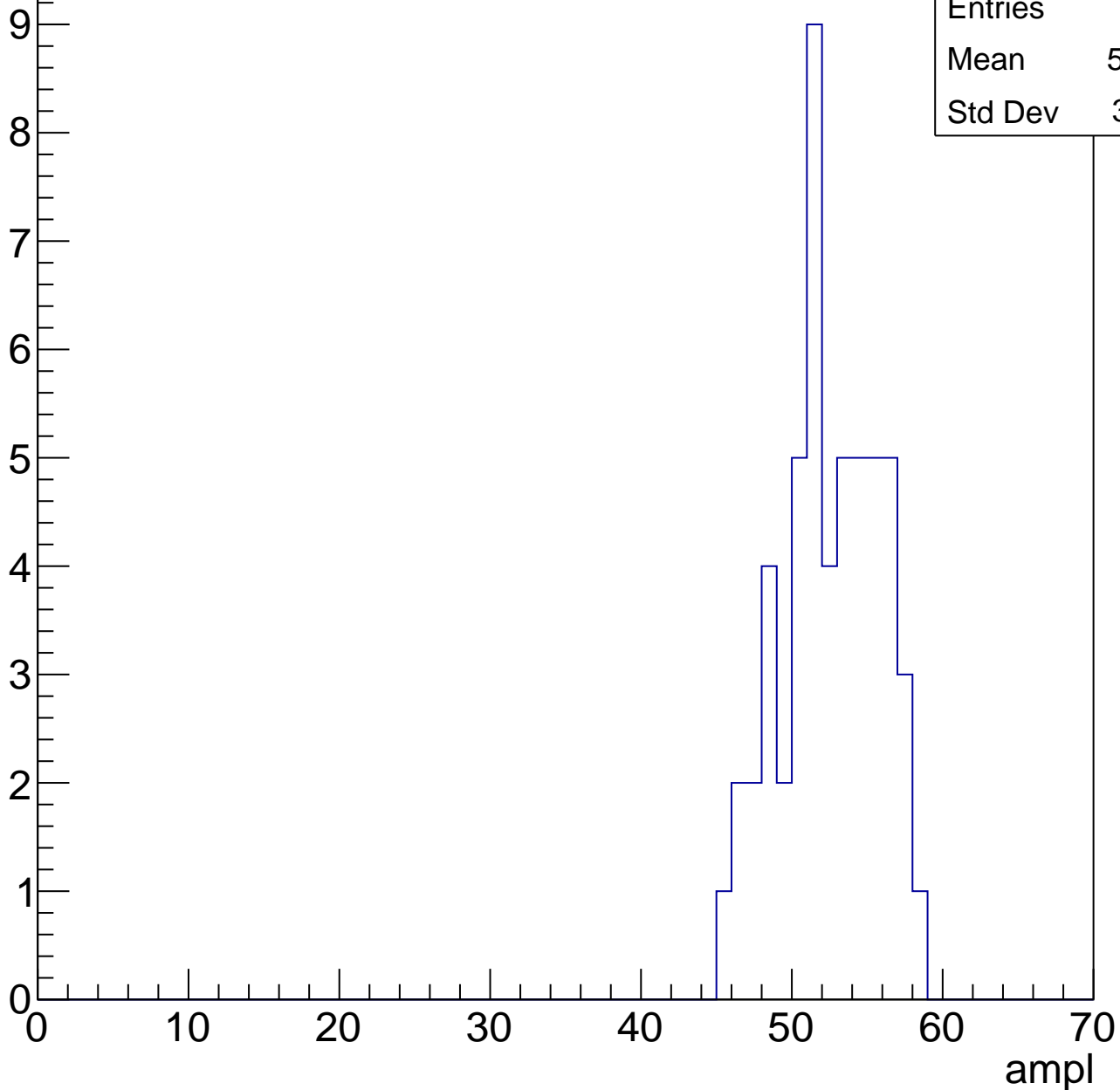


B0L001S, U25-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	52.02
Std Dev	3.201

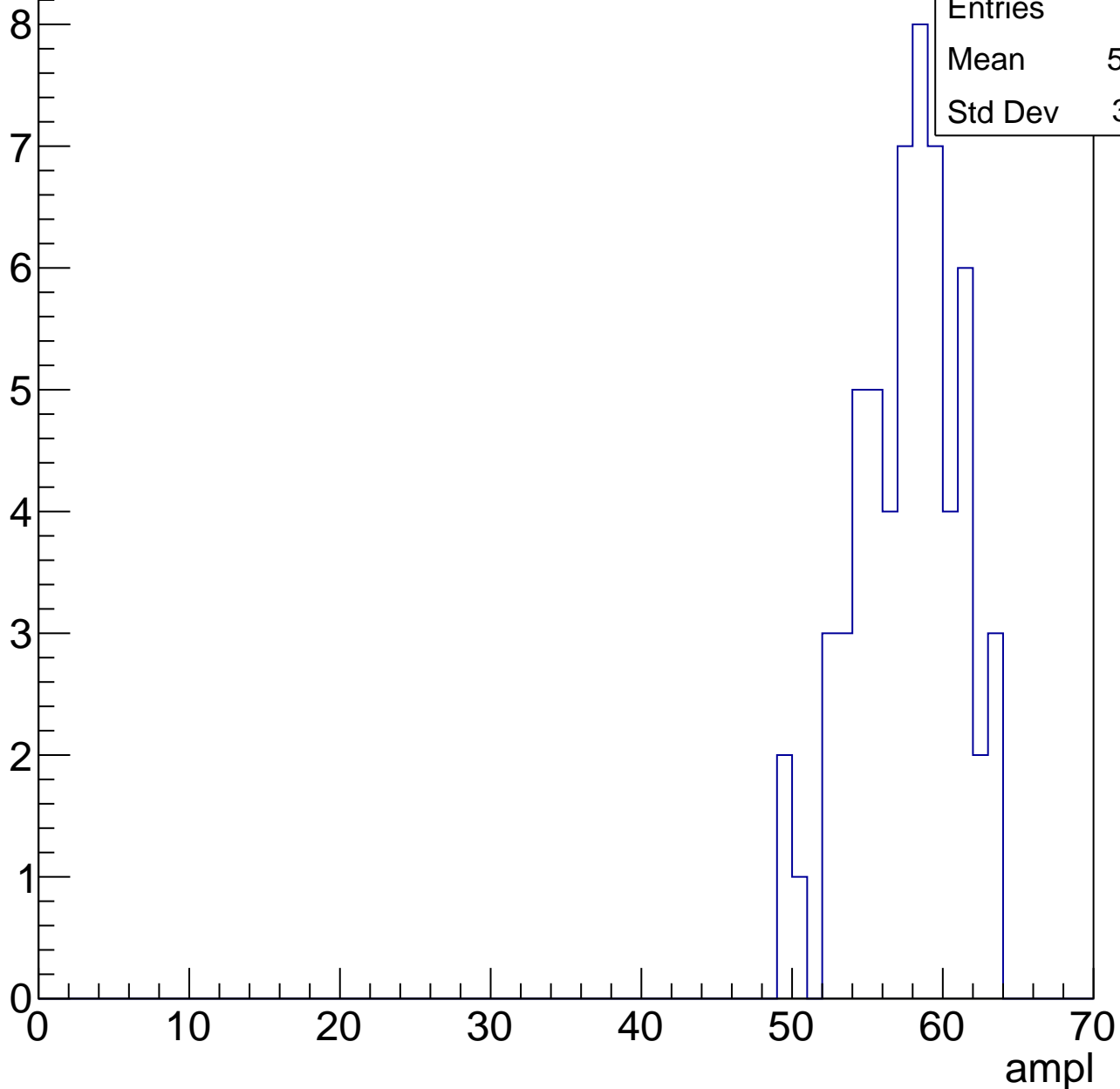


B0L001S, U25-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	57.12
Std Dev	3.401

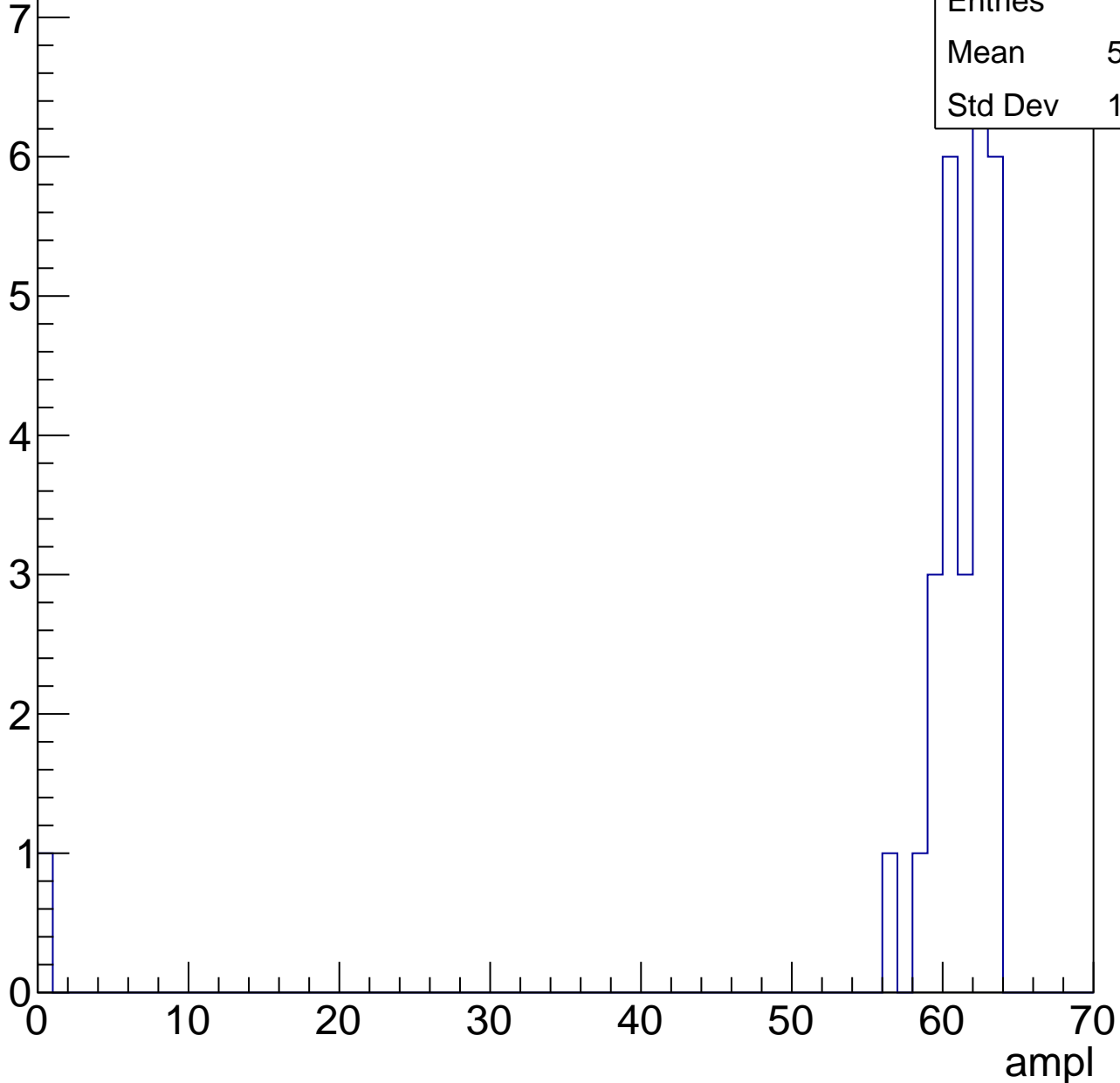


B0L001S, U25-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.79
Std Dev	11.44



B0L001S, U25-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch79, adc0

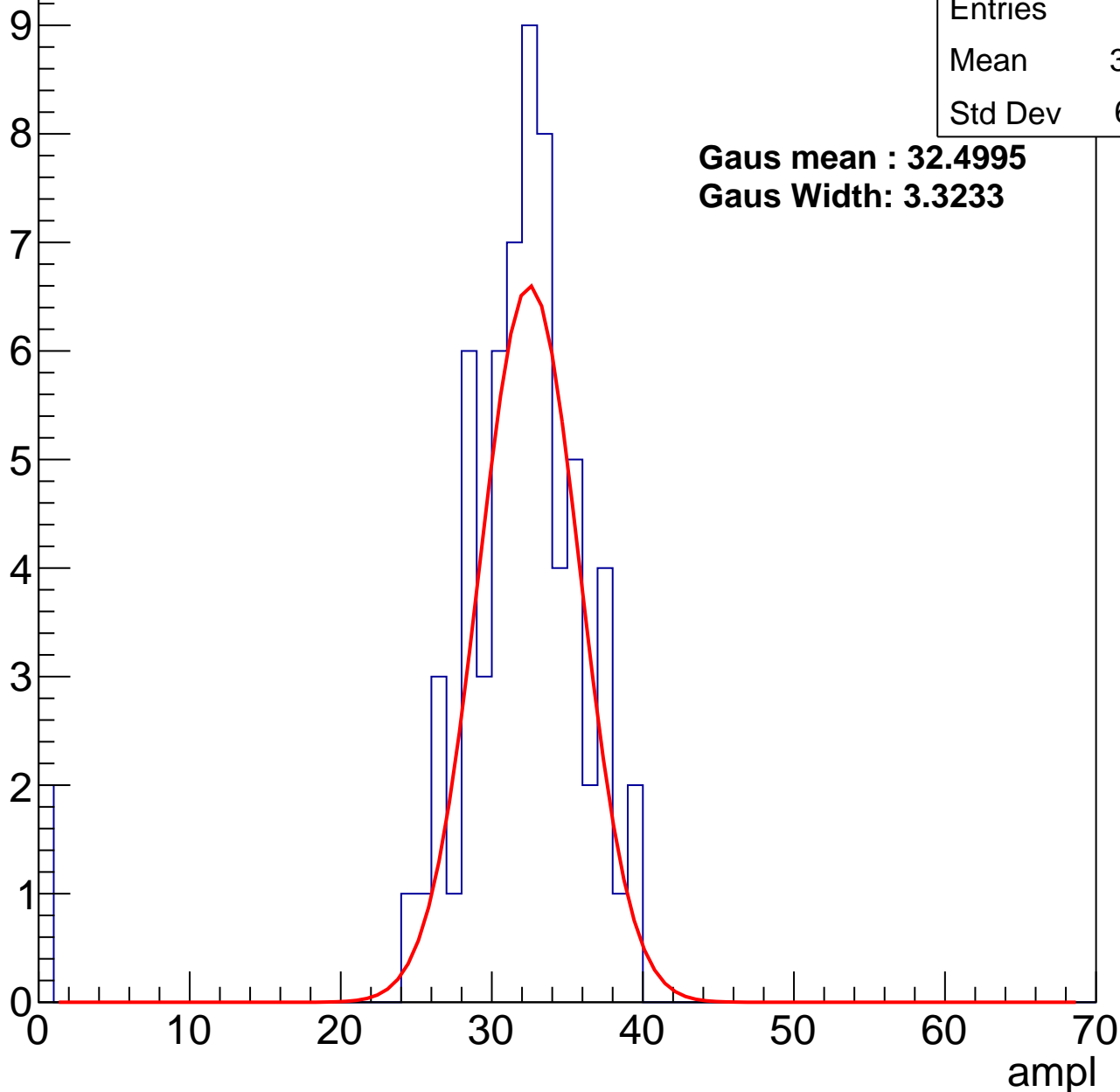
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.85
Std Dev	6.441

Gaus mean : 32.4995

Gaus Width: 3.3233



B0L001S, U25-ch79, adc1

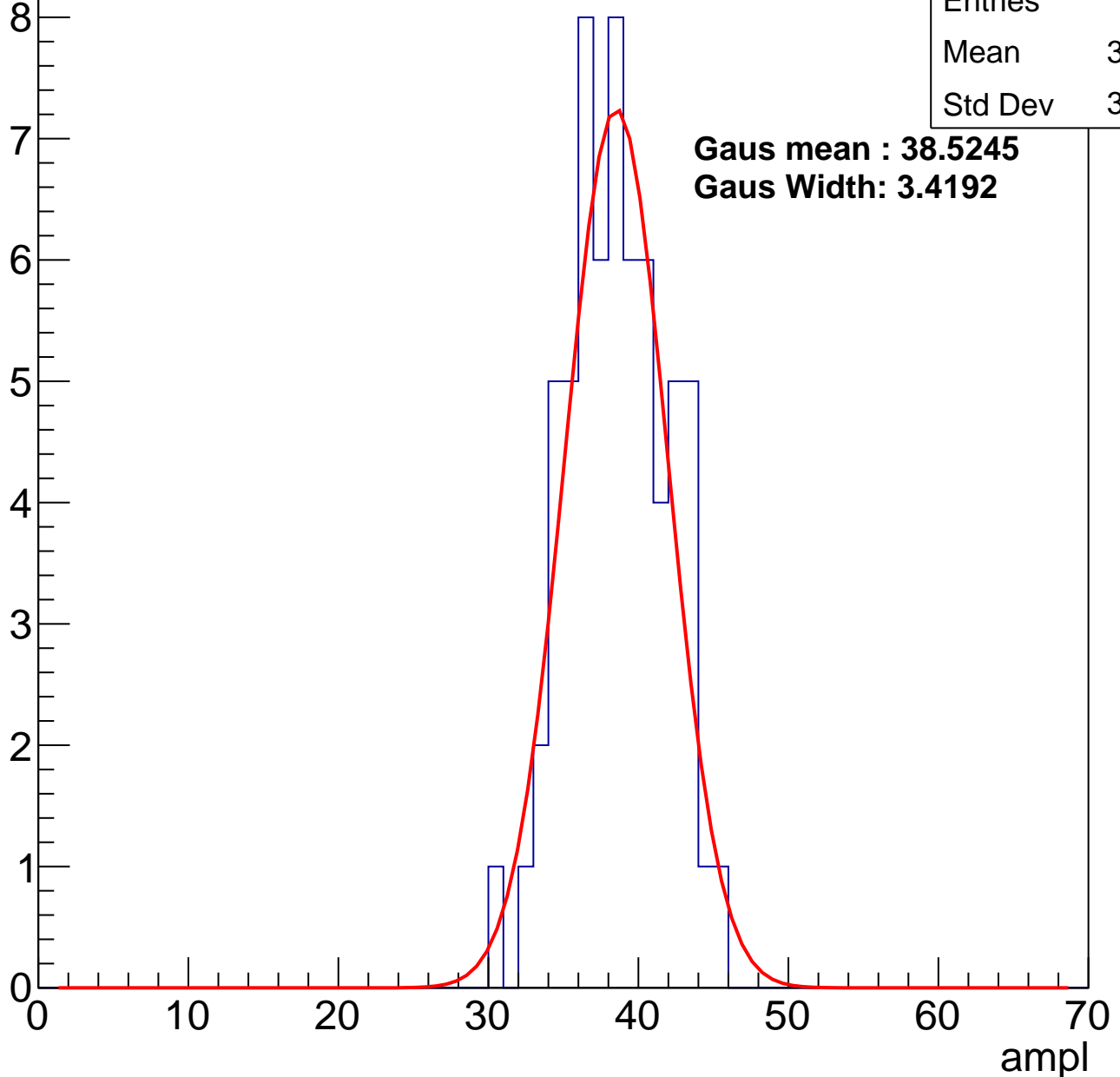
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	38.11
Std Dev	3.231

Gaus mean : 38.5245

Gaus Width: 3.4192

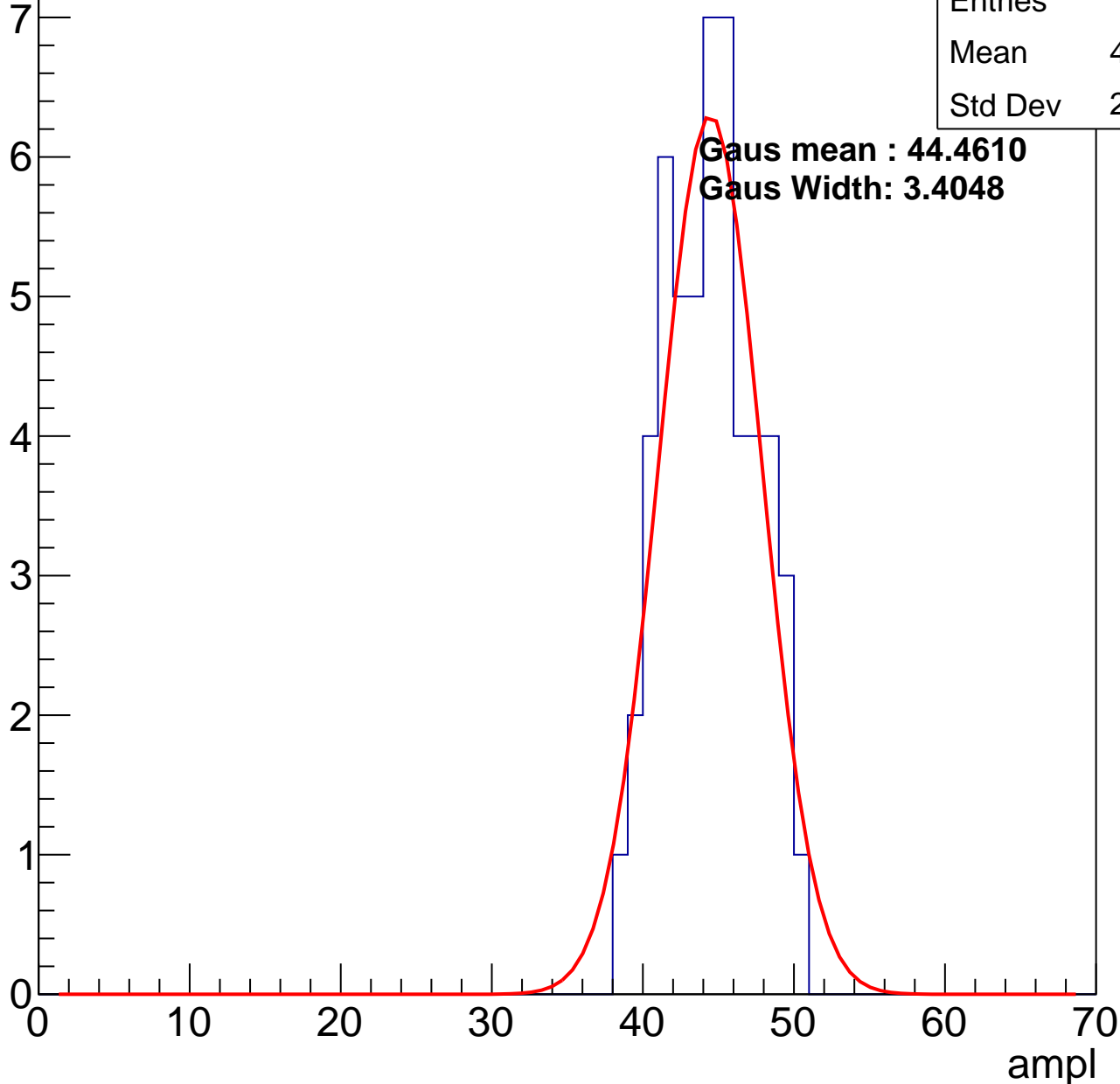


B0L001S, U25-ch79, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	43.98
Std Dev	2.956

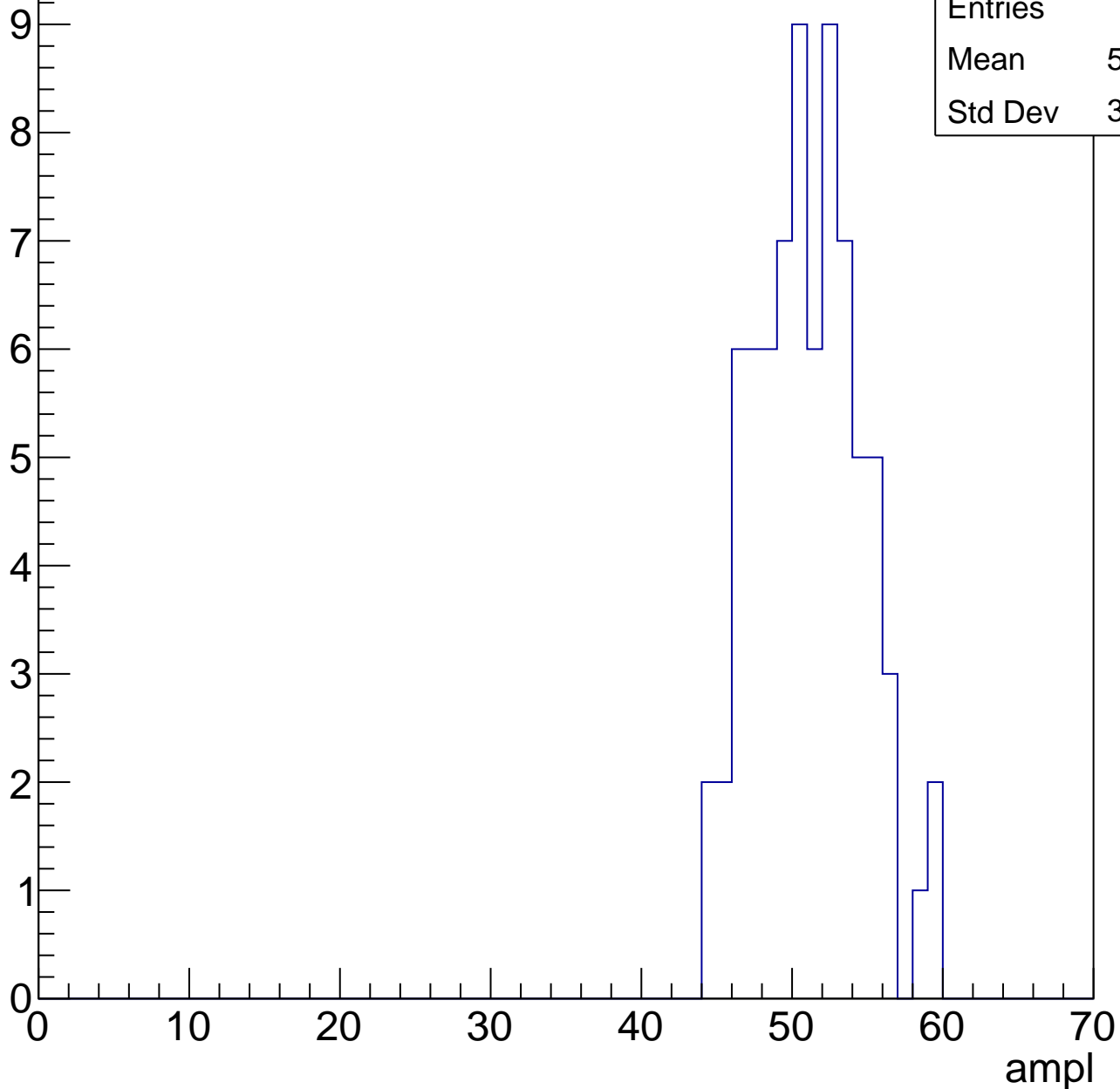


B0L001S, U25-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

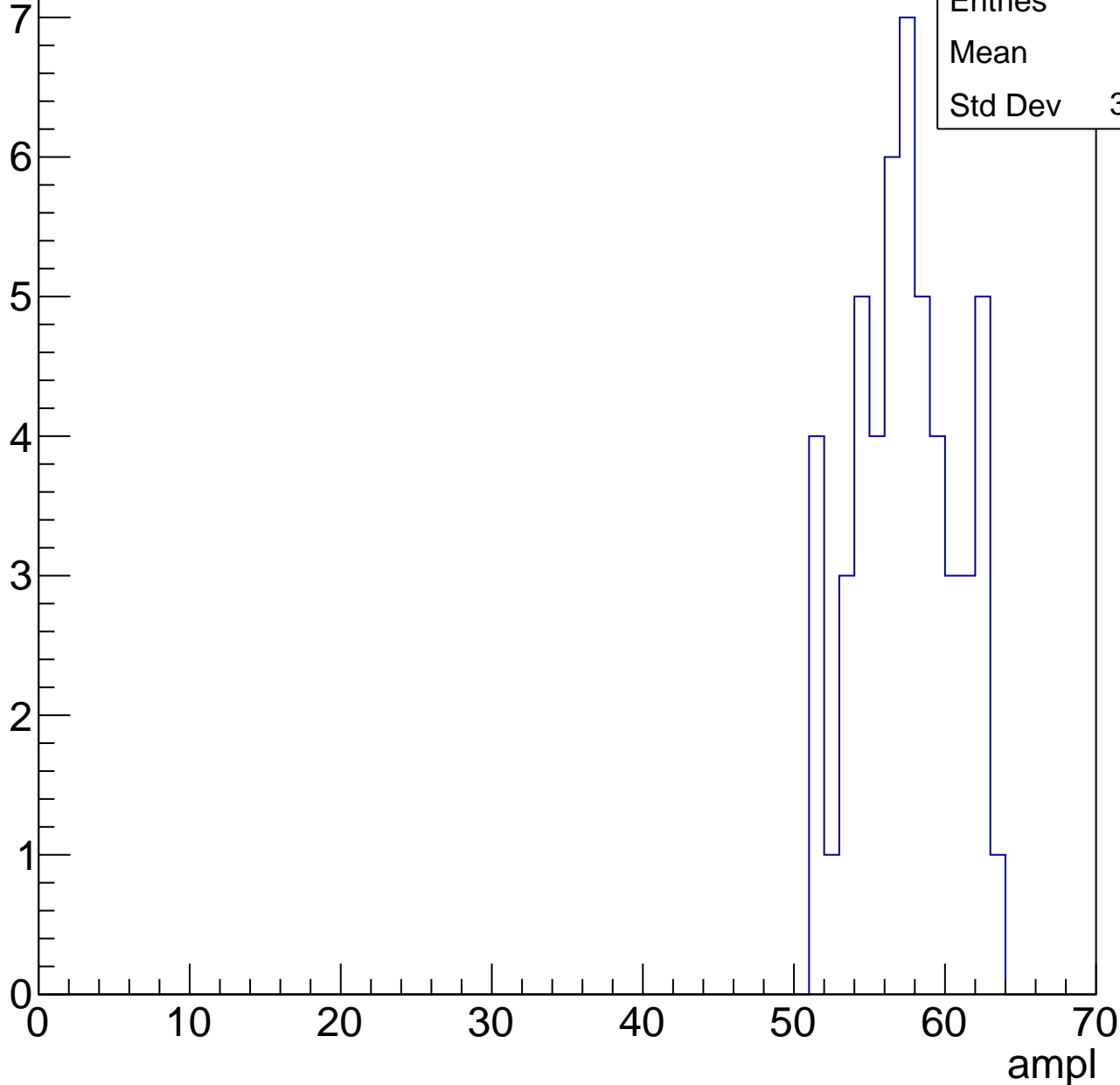
Entries	76
Mean	50.67
Std Dev	3.466



B0L001S, U25-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

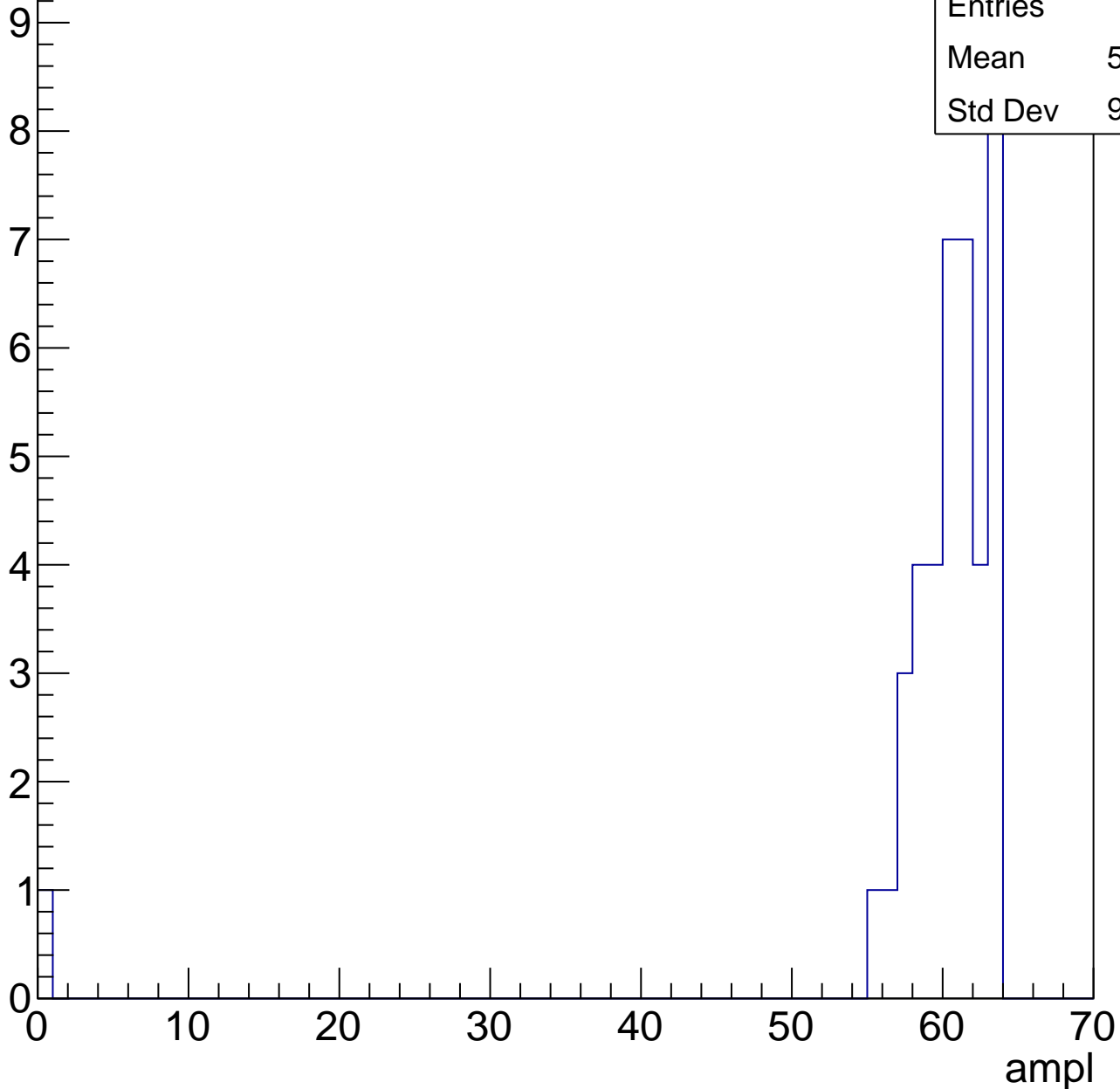


B0L001S, U25-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.83
Std Dev	9.546



B0L001S, U25-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	62
Std Dev	0

B0L001S, U25-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch80, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	30.39
Std Dev	6.865

Gaus mean : 31.4676

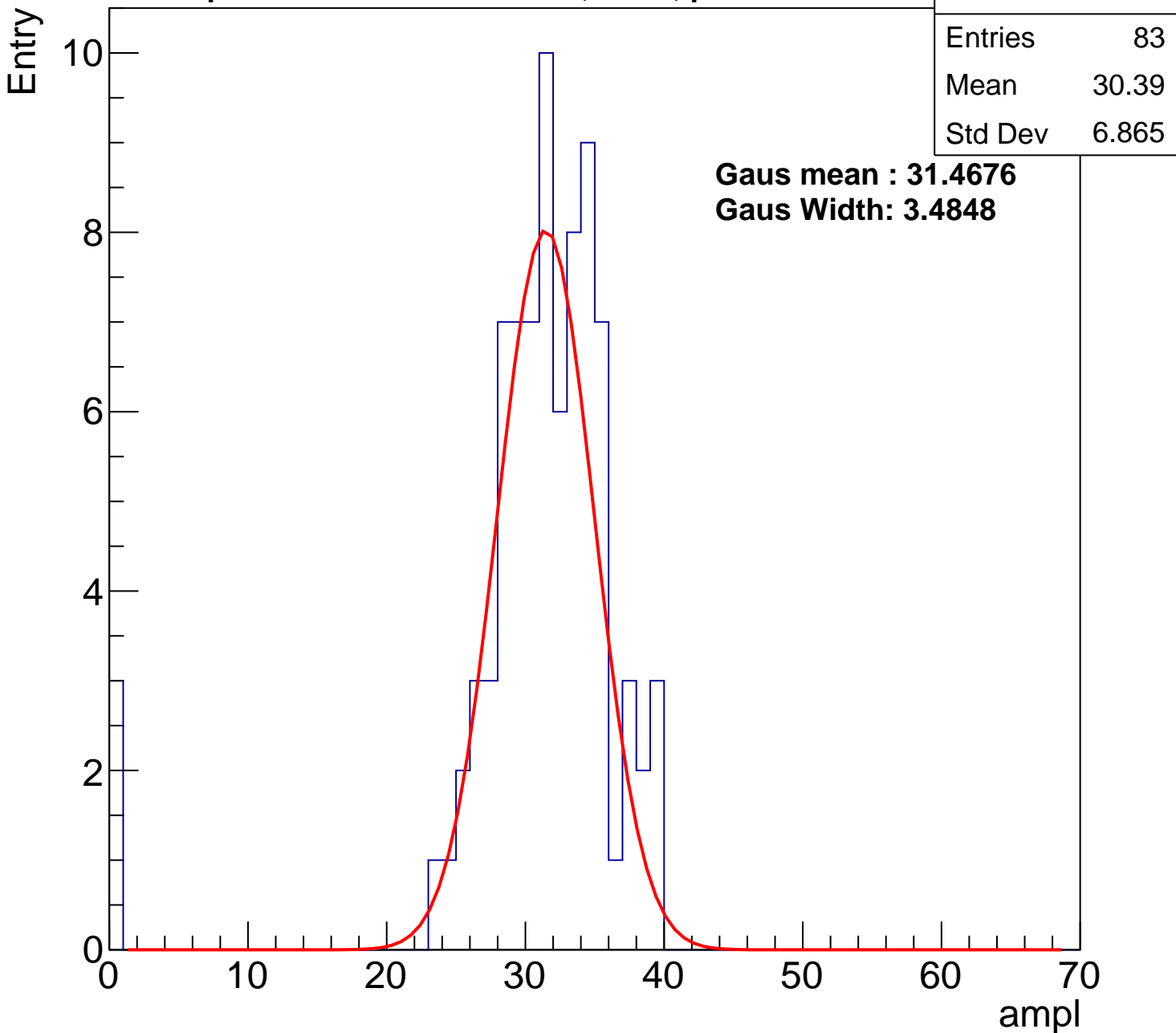
Gaus Width: 3.4848

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch80, adc1

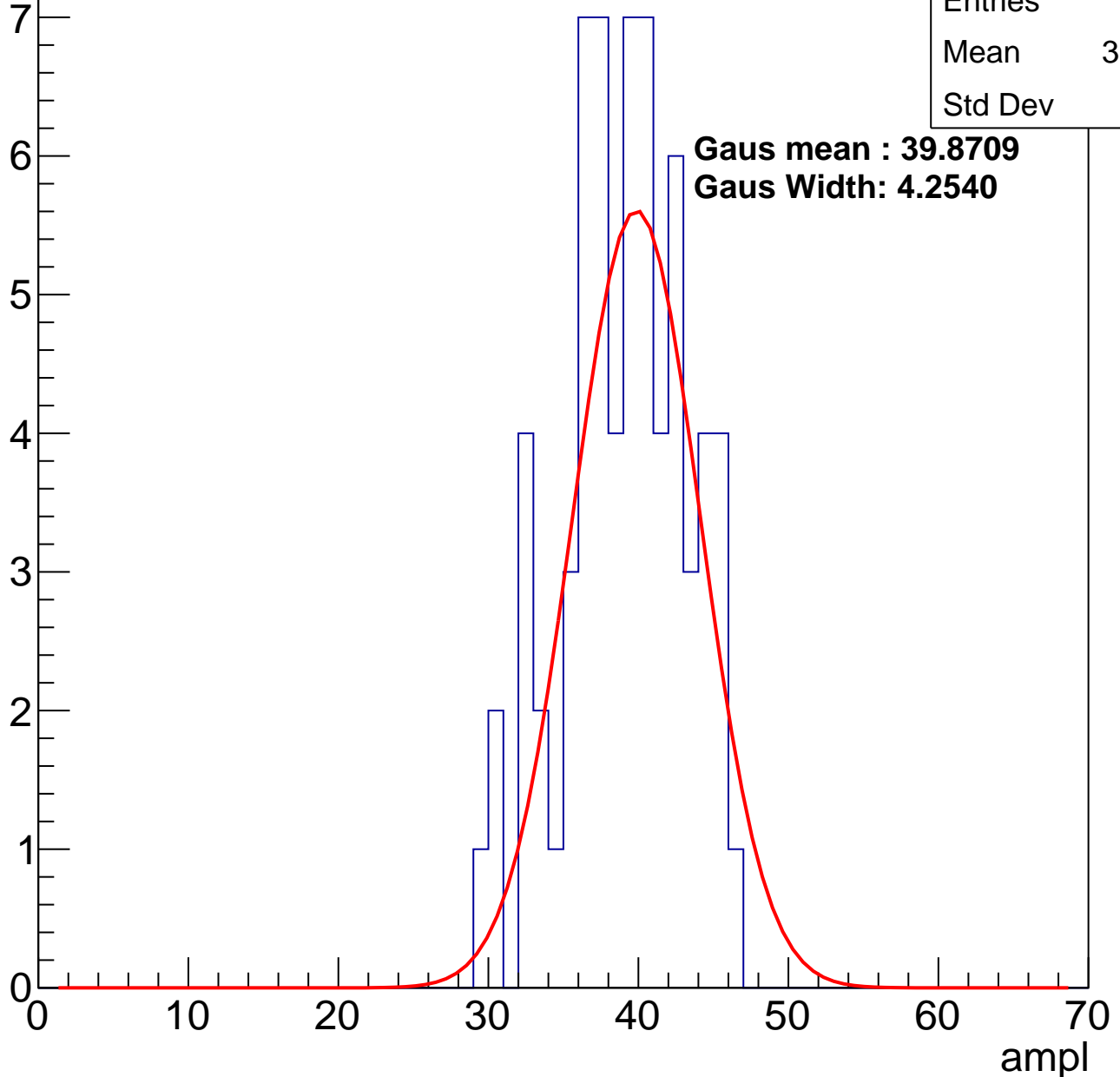
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.58
Std Dev	4.06

Gaus mean : 39.8709

Gaus Width: 4.2540



B0L001S, U25-ch80, adc2

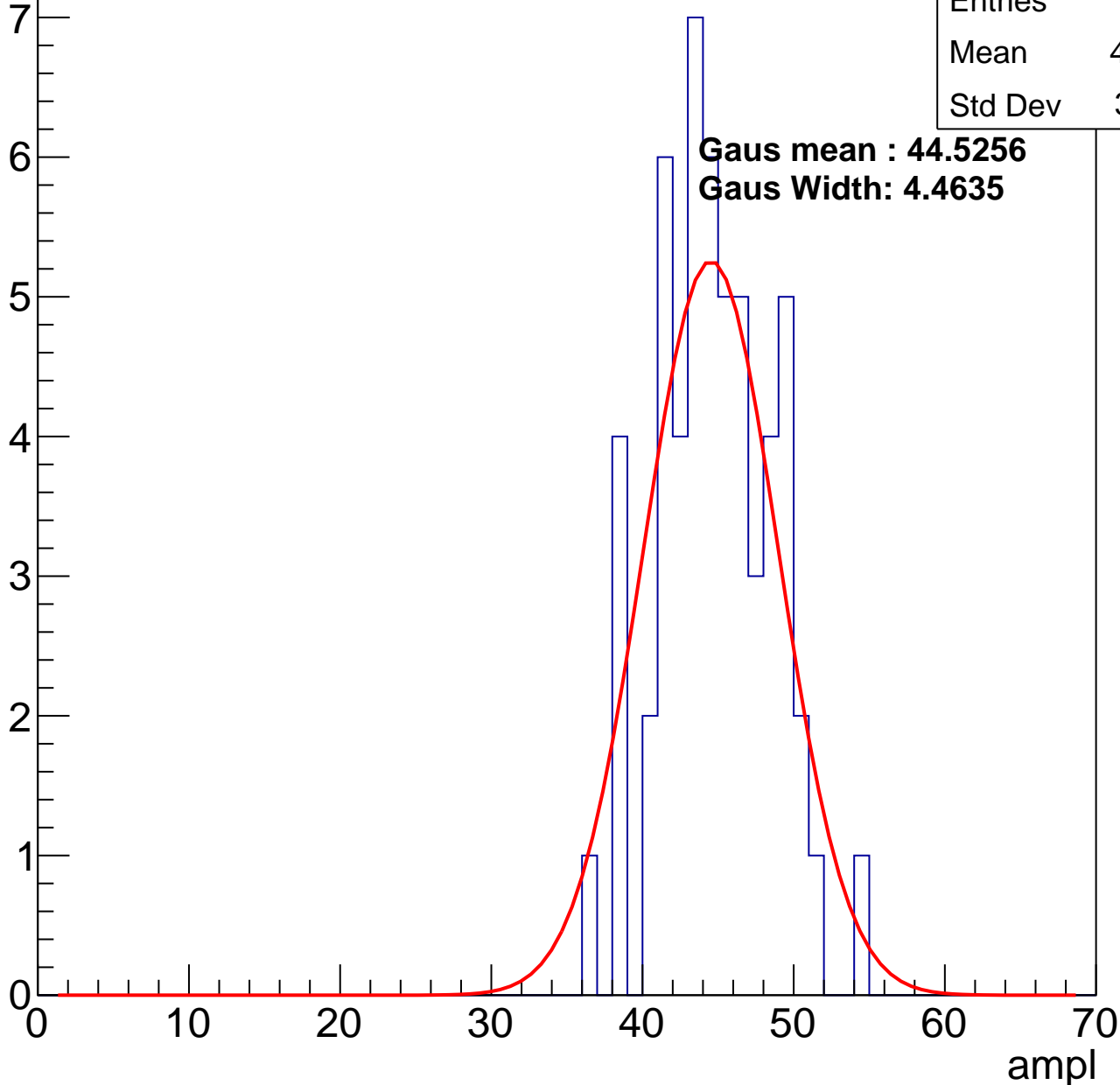
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.38
Std Dev	3.711

Gaus mean : 44.5256

Gaus Width: 4.4635

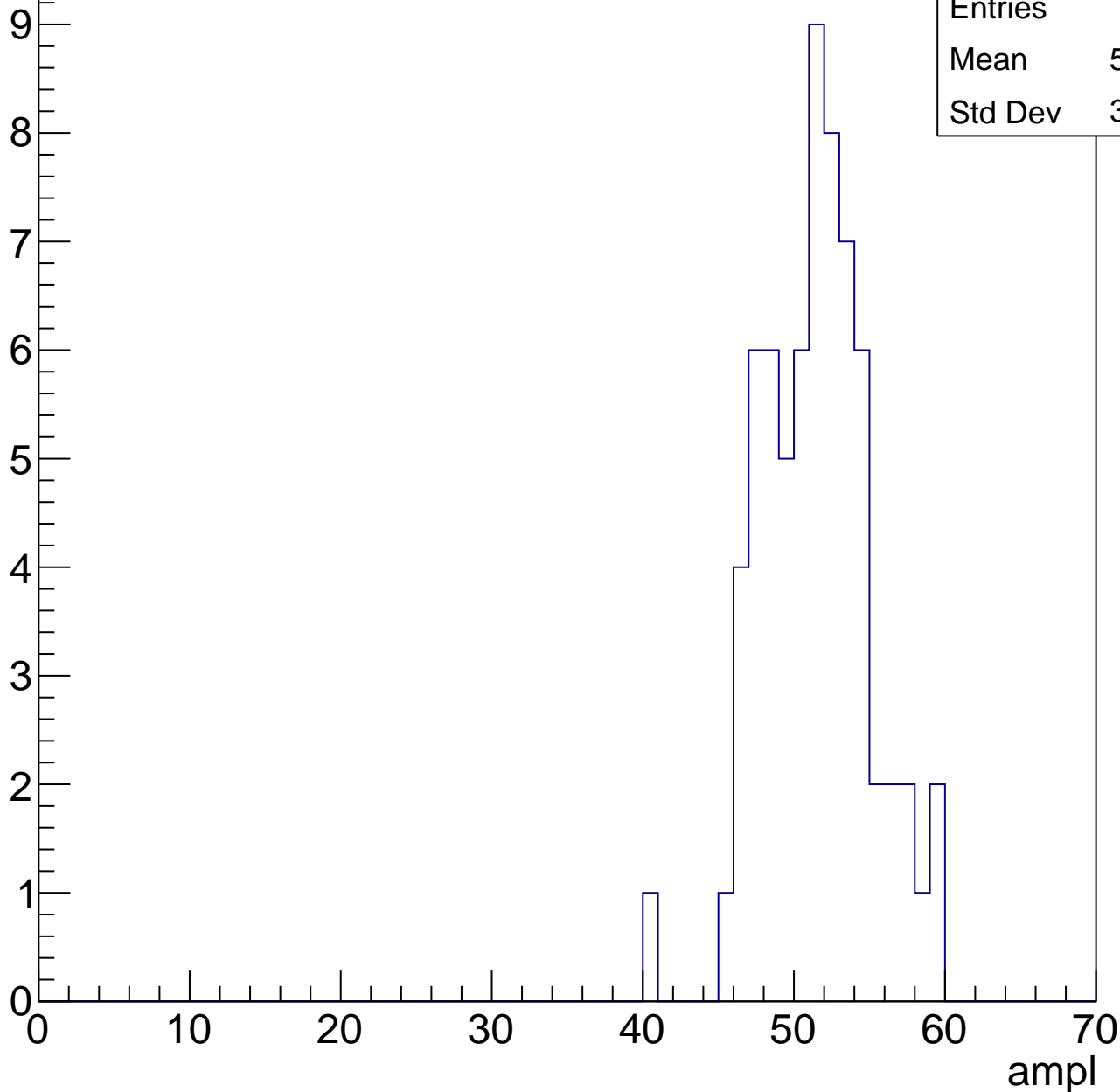


B0L001S, U25-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.97
Std Dev	3.544

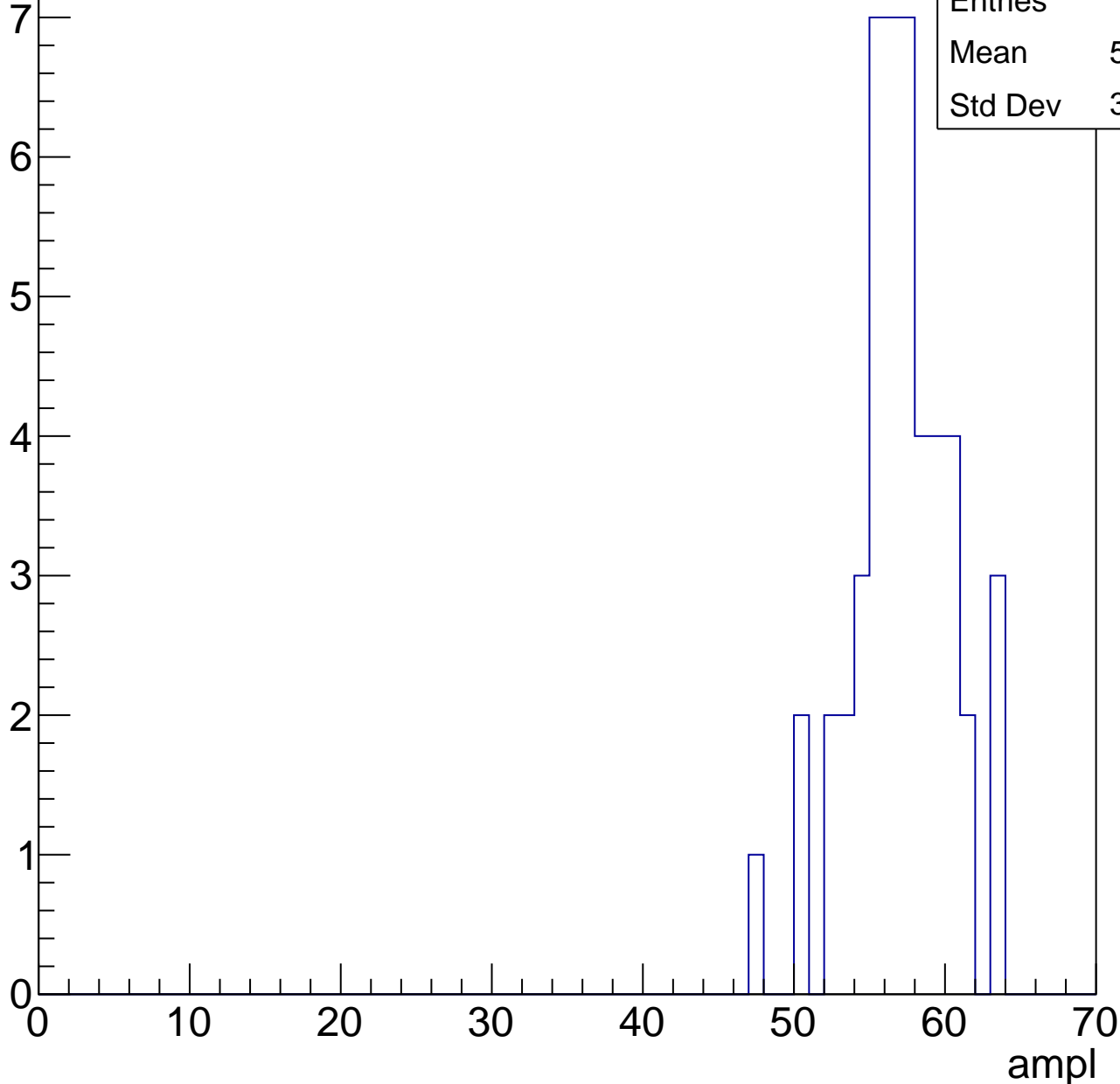


B0L001S, U25-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.54
Std Dev	3.316



B0L001S, U25-ch80, adc5

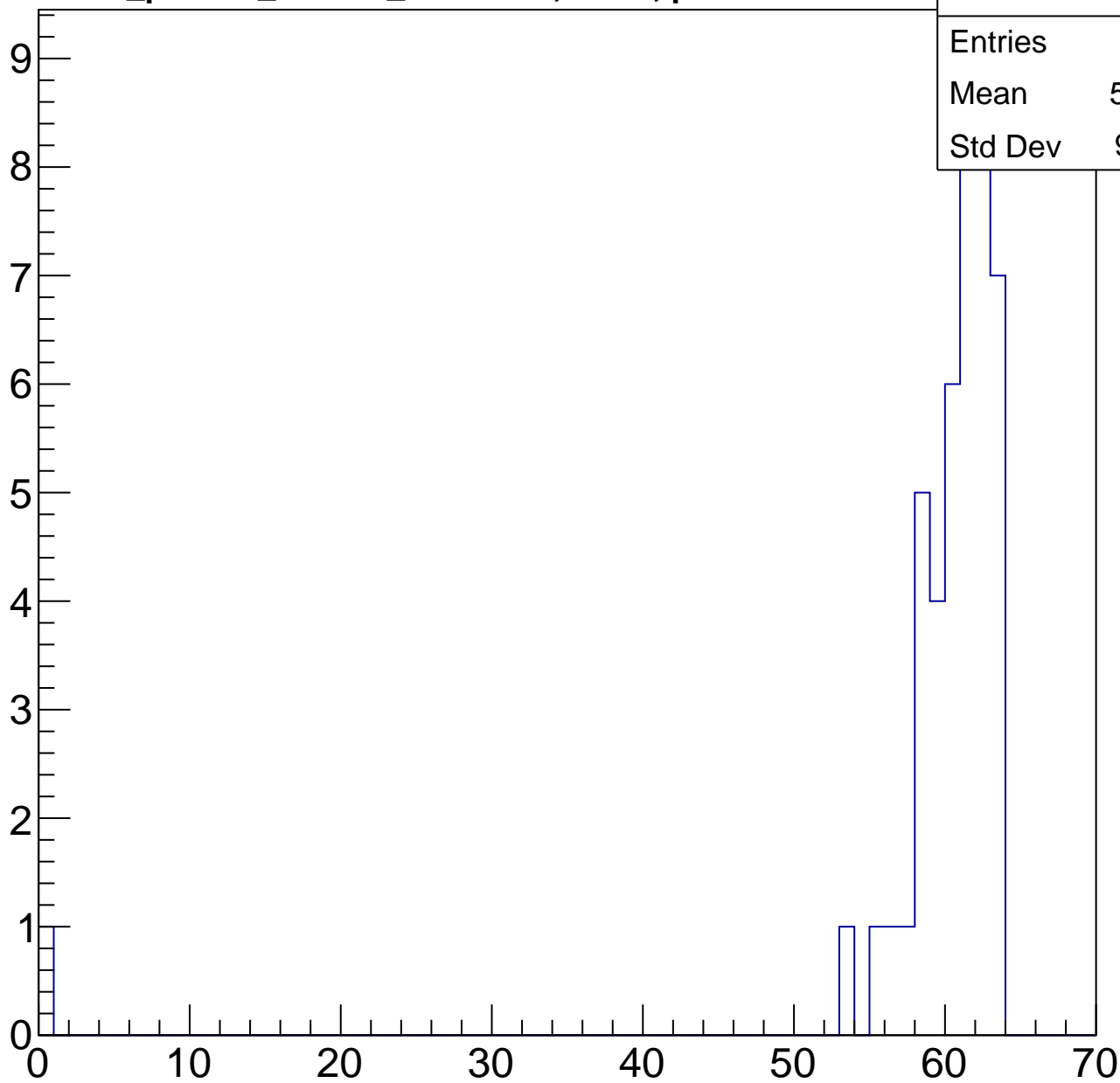
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.95
Std Dev	9.271

ampl



B0L001S, U25-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L001S, U25-ch81, adc0

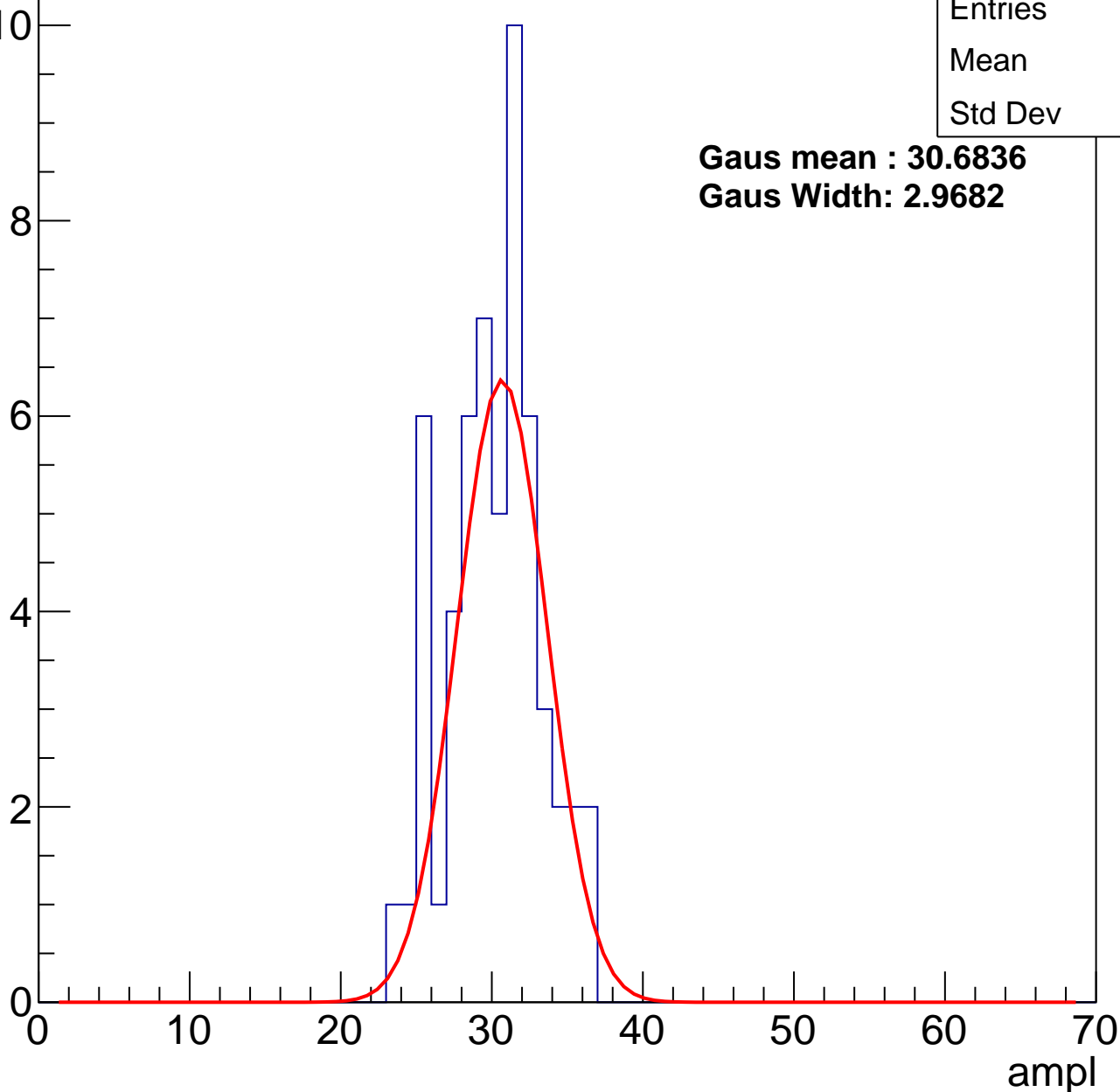
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.7
Std Dev	3.07

Gaus mean : 30.6836

Gaus Width: 2.9682



B0L001S, U25-ch81, adc1

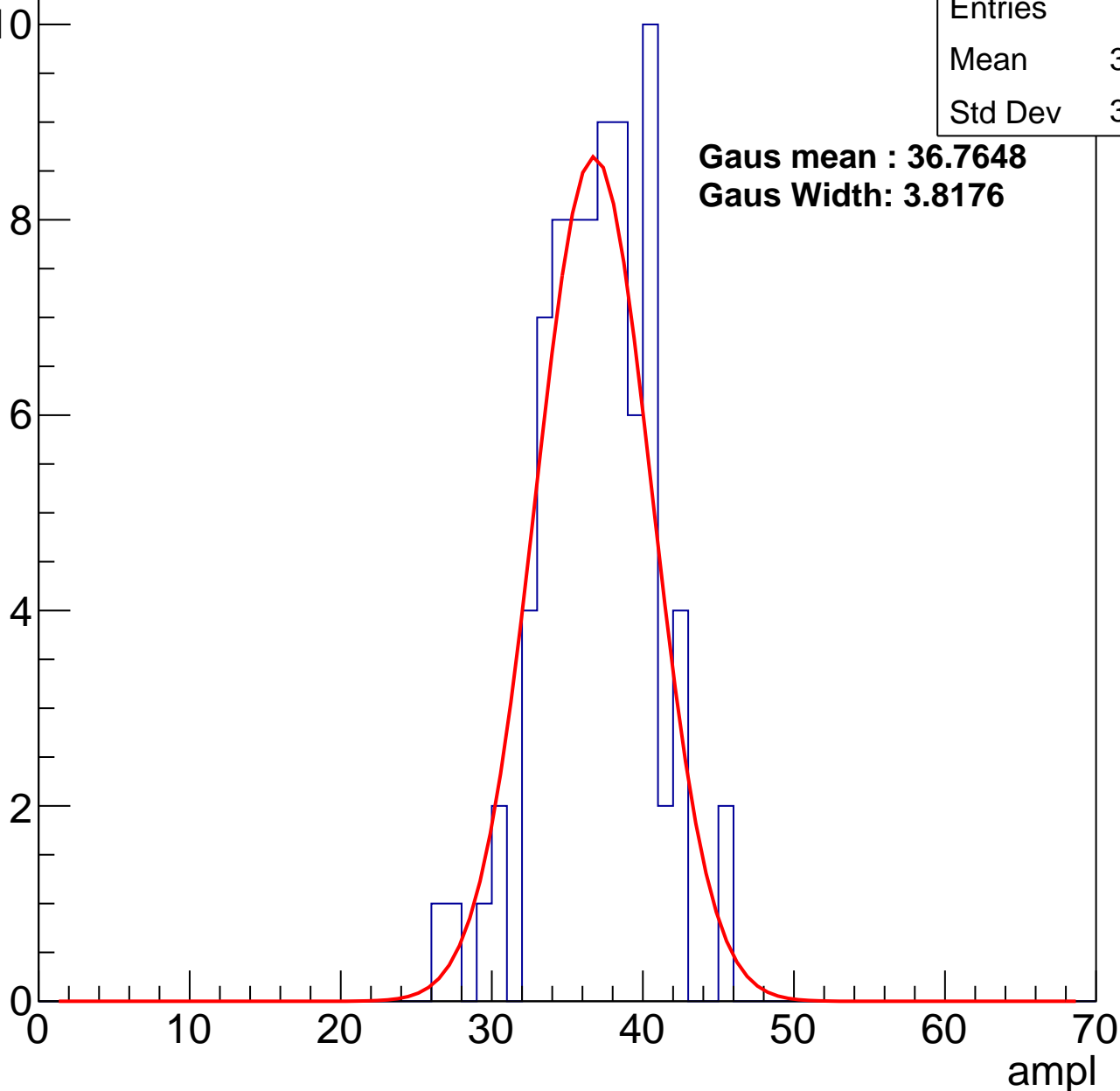
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	36.46
Std Dev	3.606

Gaus mean : 36.7648

Gaus Width: 3.8176



B0L001S, U25-ch81, adc2

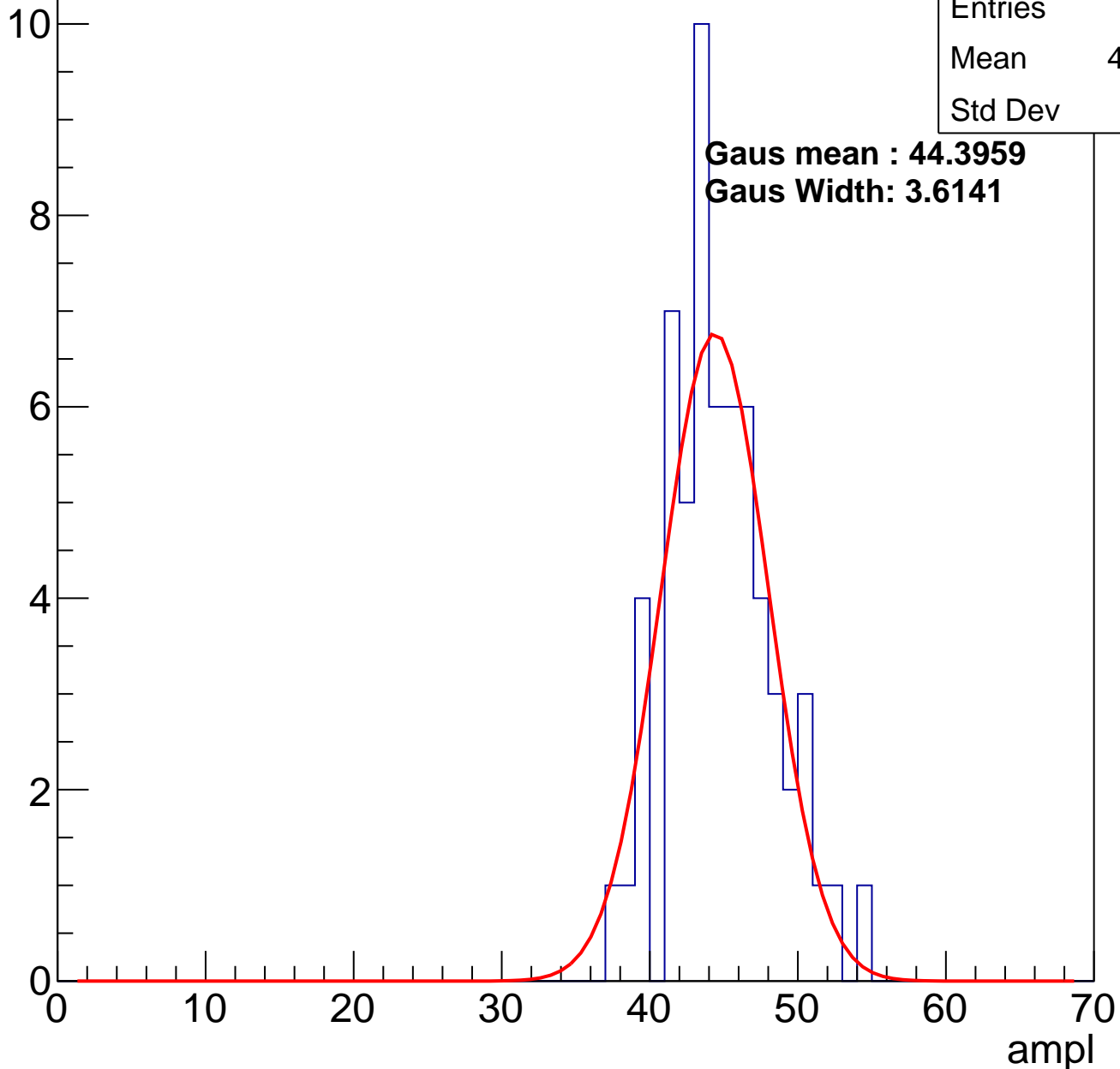
calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	44.34
Std Dev	3.52

Gaus mean : 44.3959

Gaus Width: 3.6141

Entry

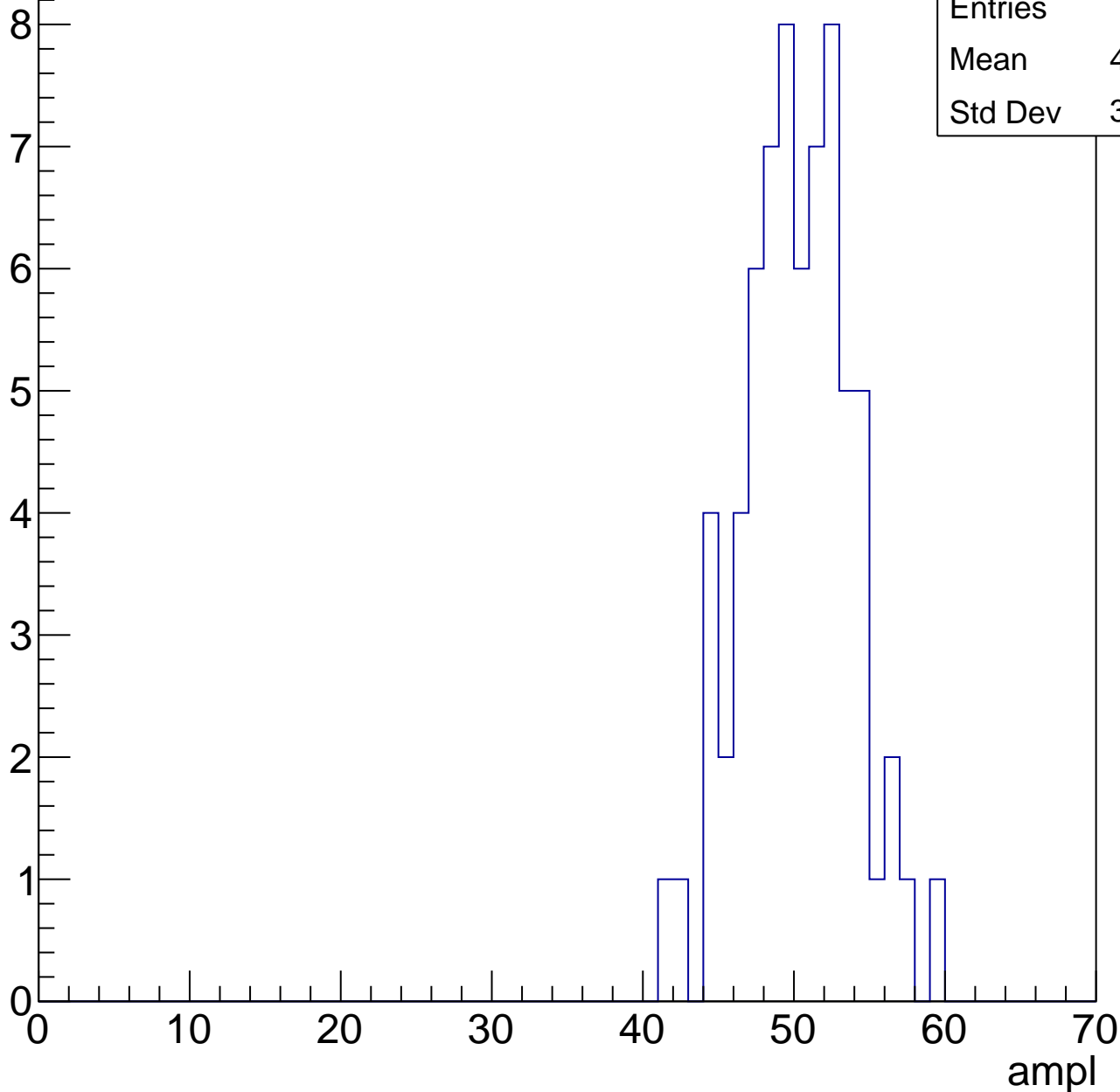


B0L001S, U25-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

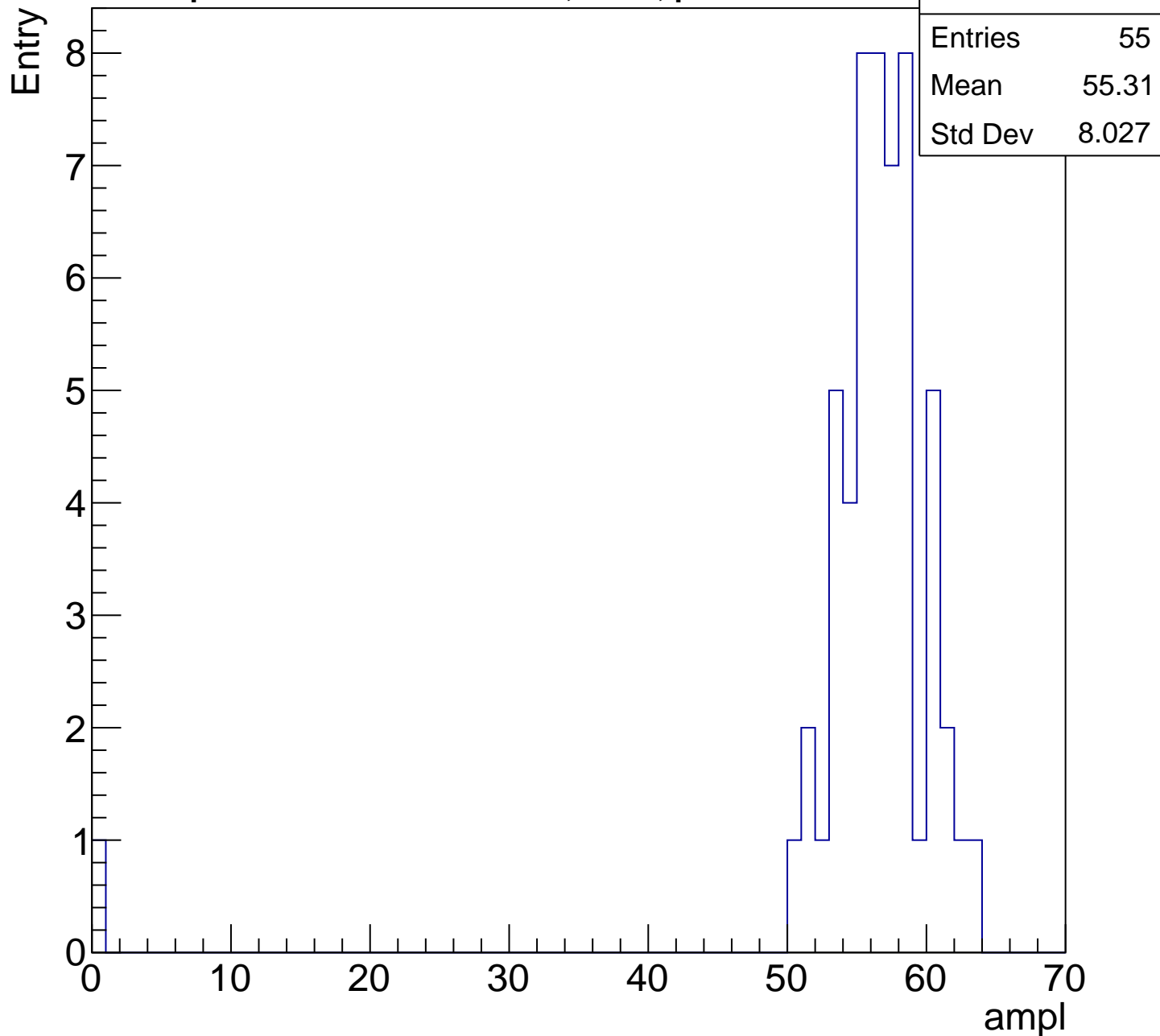
Entry

Entries	69
Mean	49.77
Std Dev	3.572



B0L001S, U25-ch81, adc4

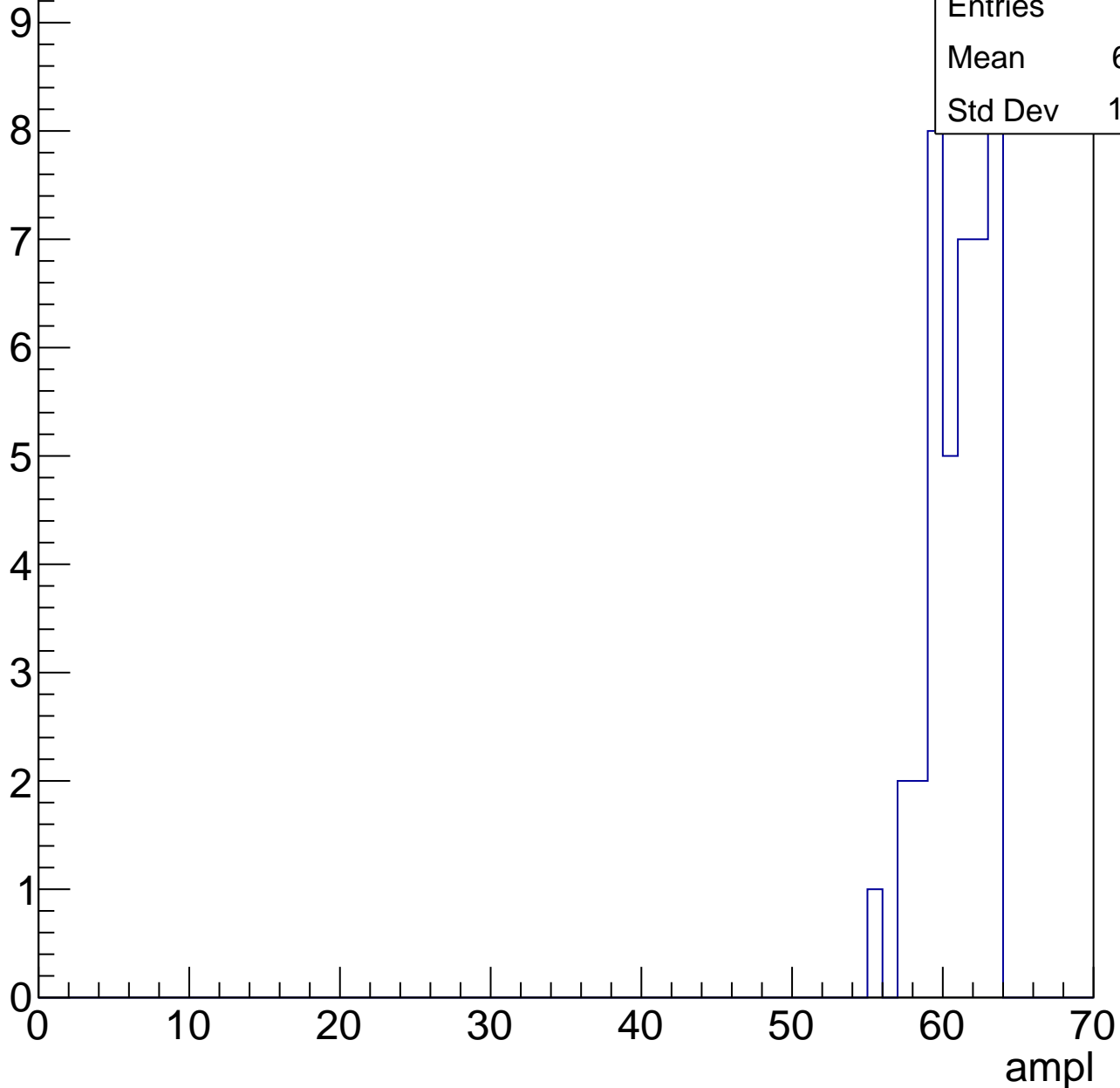
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U25-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U25-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch82, adc0

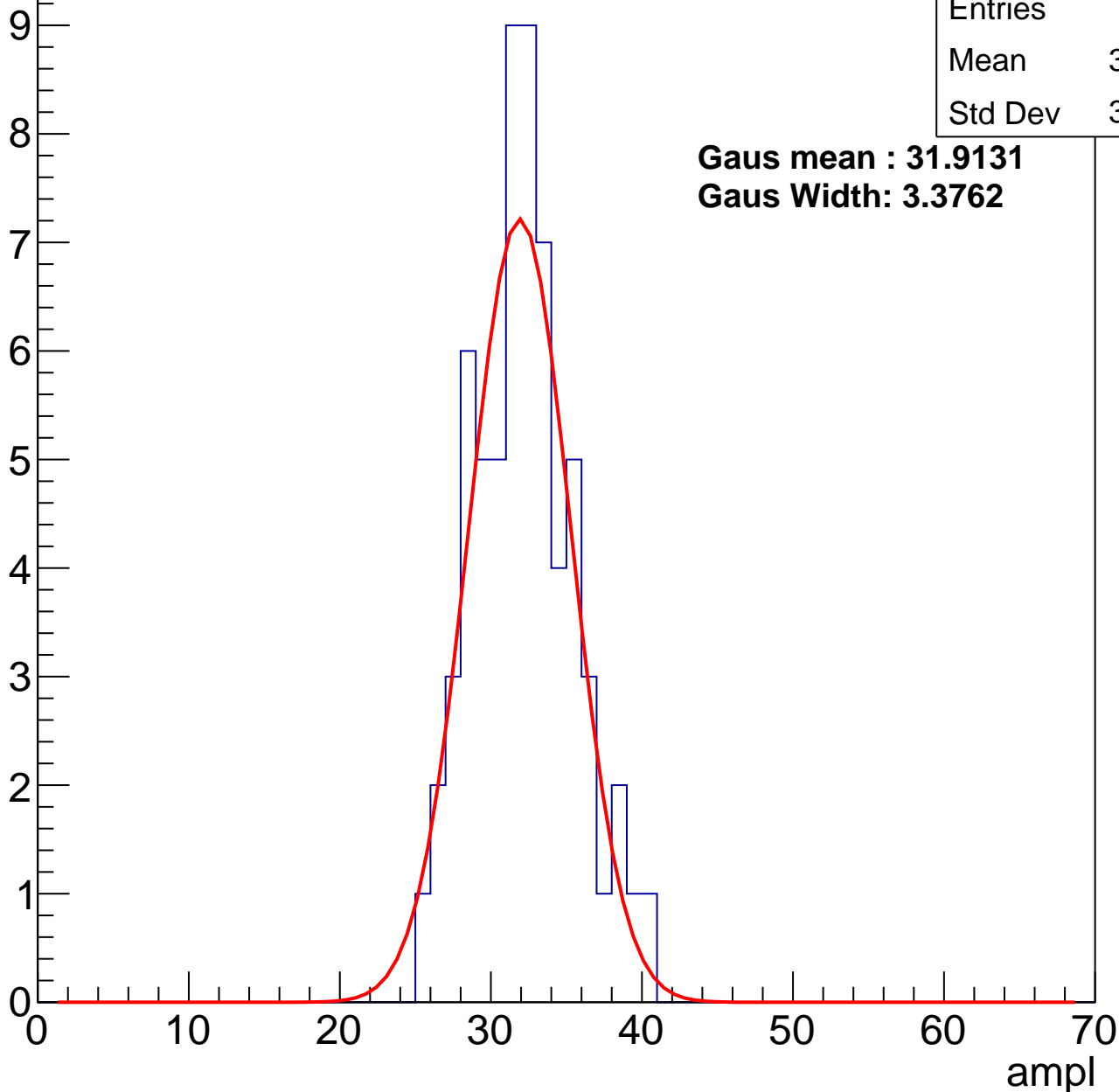
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.72
Std Dev	3.276

Gaus mean : 31.9131

Gaus Width: 3.3762



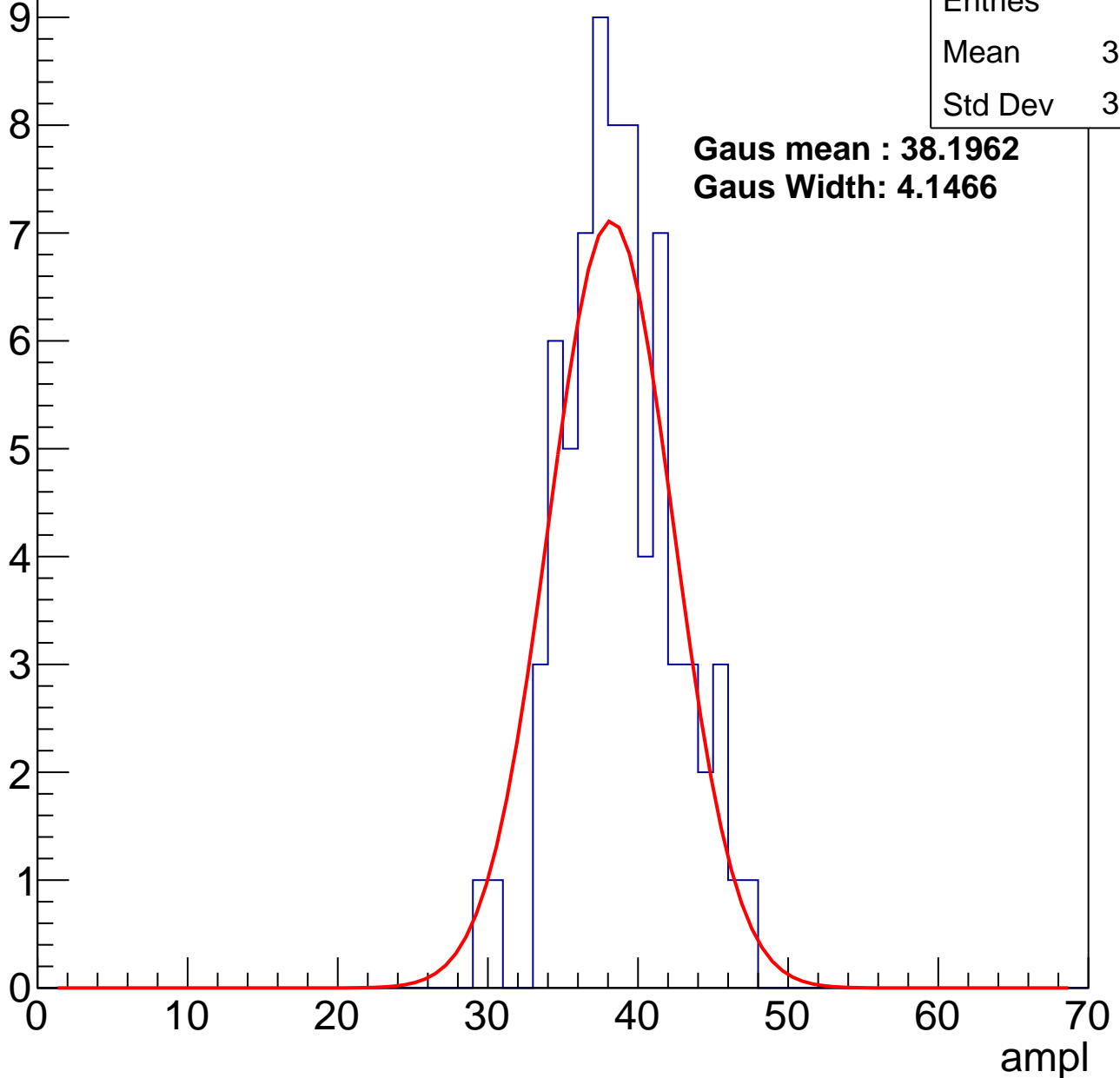
B0L001S, U25-ch82, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.28
Std Dev	3.664

Gaus mean : 38.1962
Gaus Width: 4.1466



B0L001S, U25-ch82, adc2

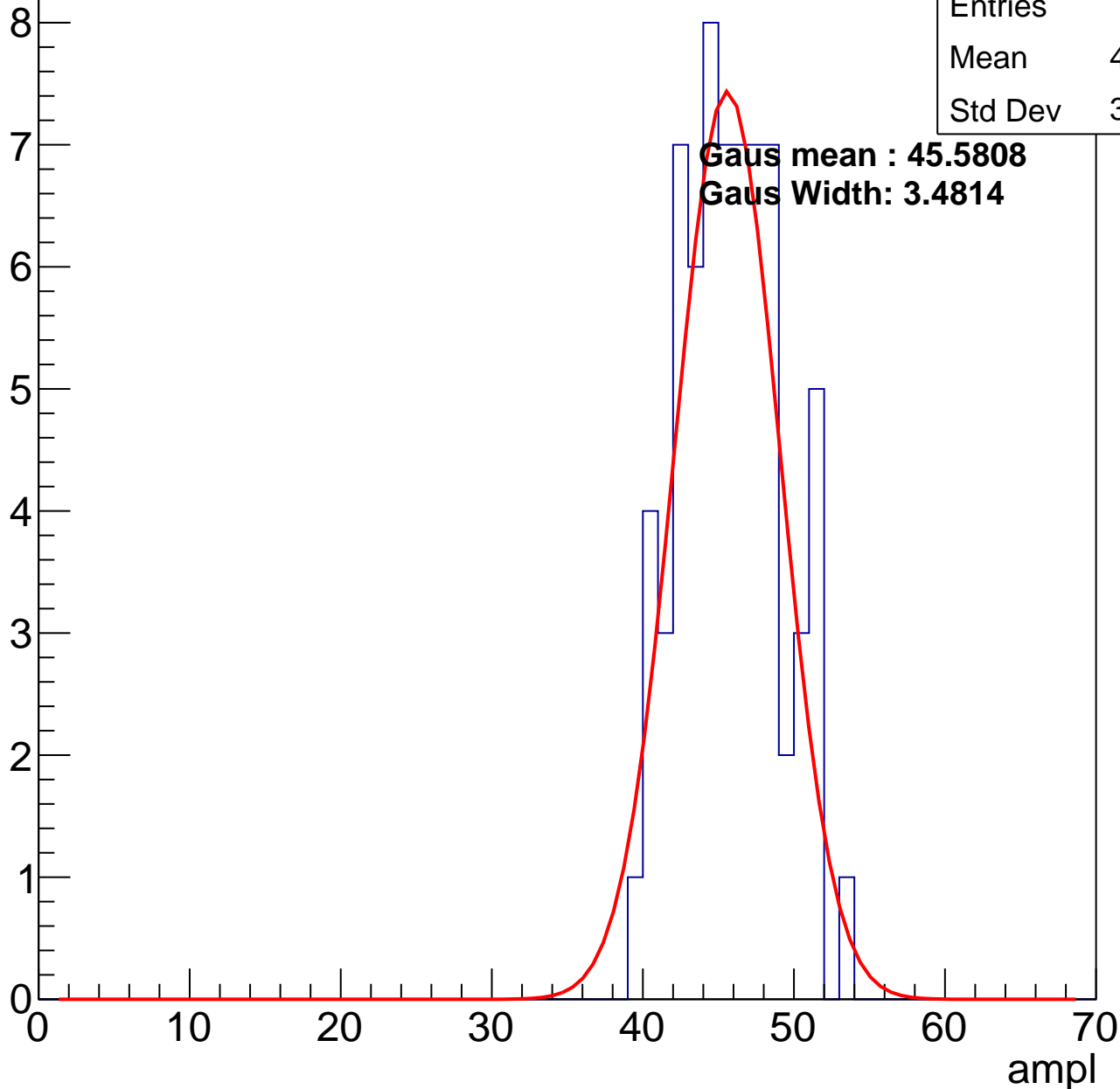
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45.35
Std Dev	3.253

Gaus mean : 45.5808

Gaus Width: 3.4814

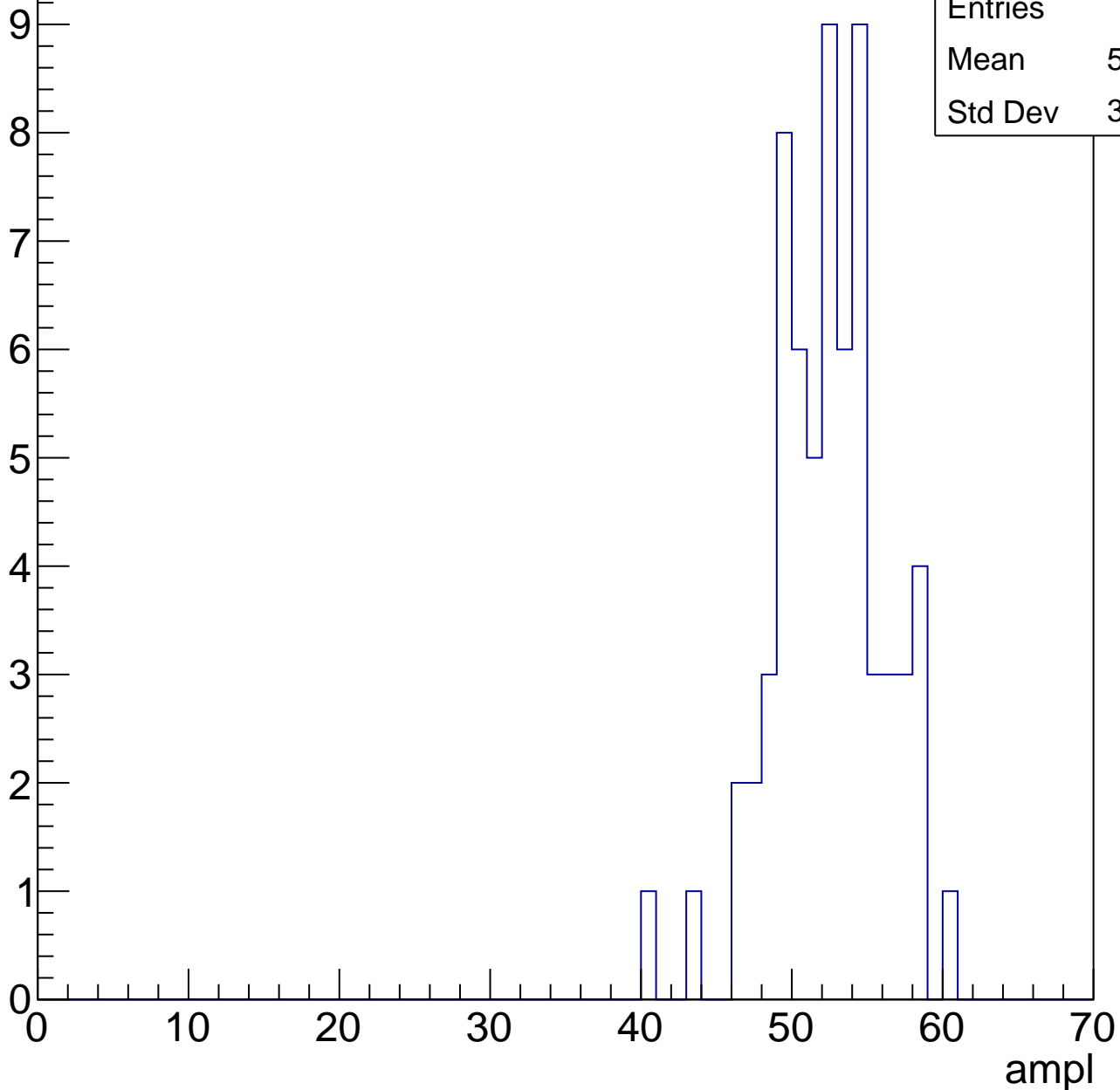


B0L001S, U25-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.94
Std Dev	3.684

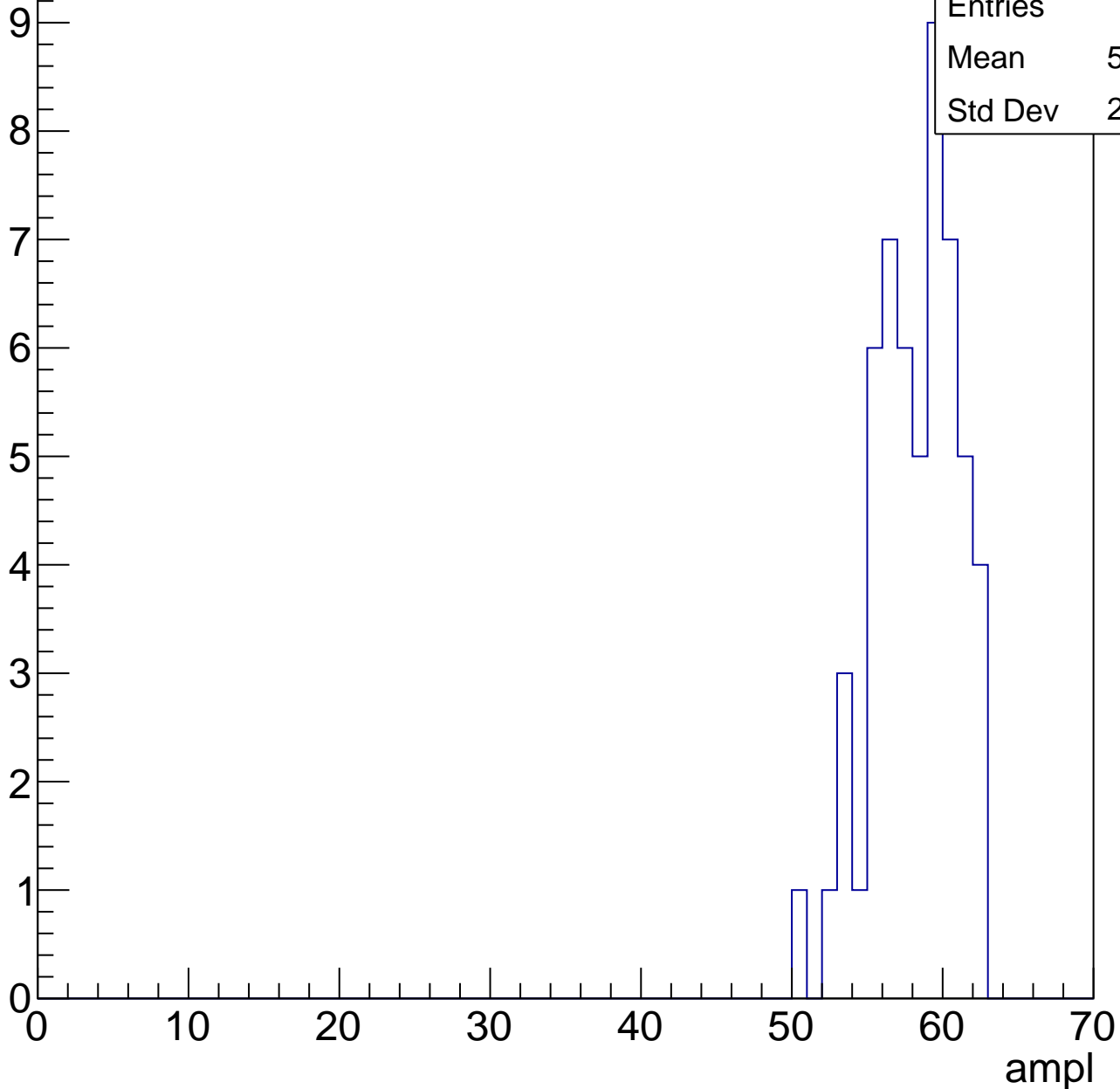


B0L001S, U25-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.69
Std Dev	2.763

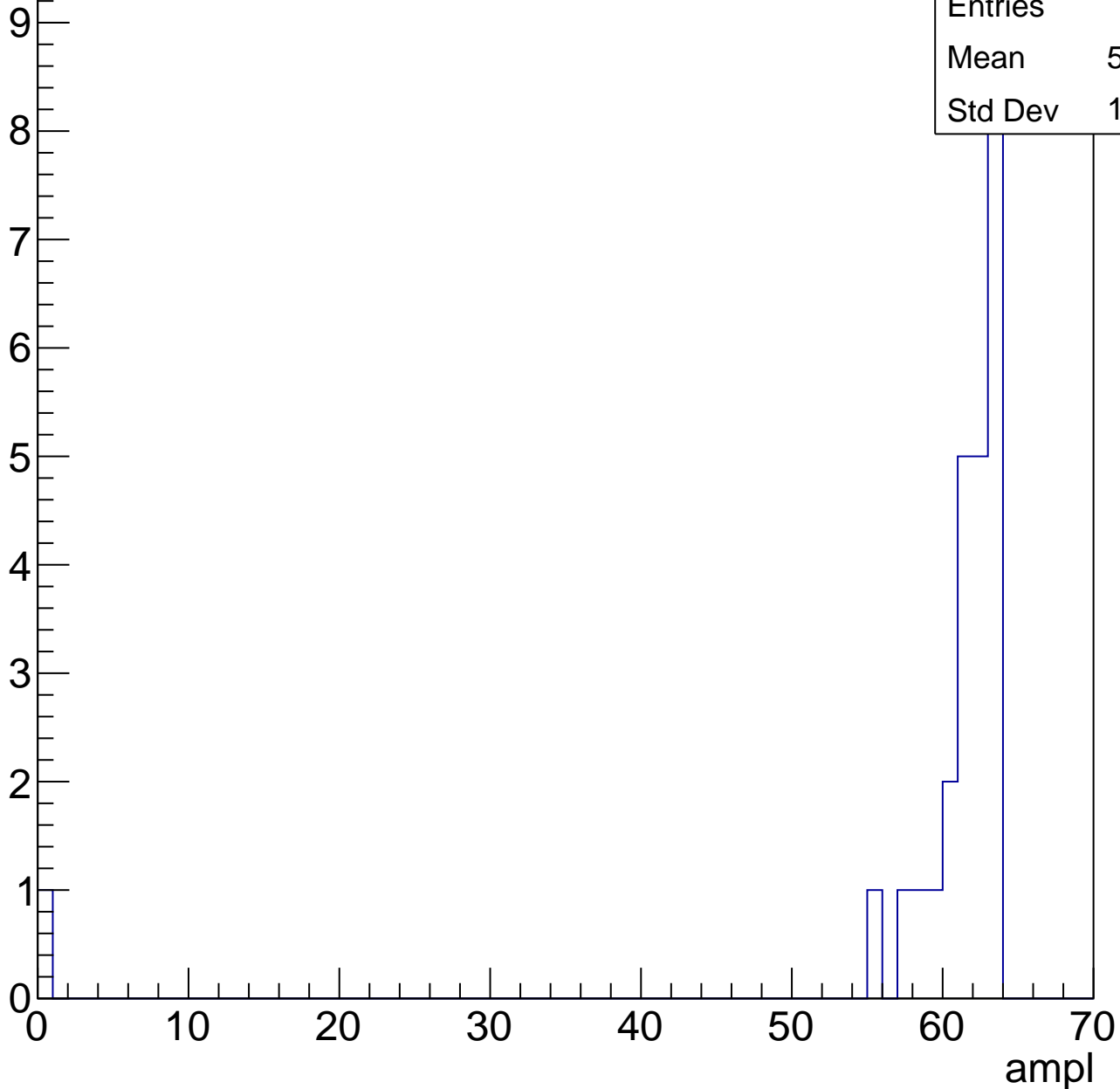


B0L001S, U25-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.88
Std Dev	11.95



B0L001S, U25-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U25-ch83, adc0

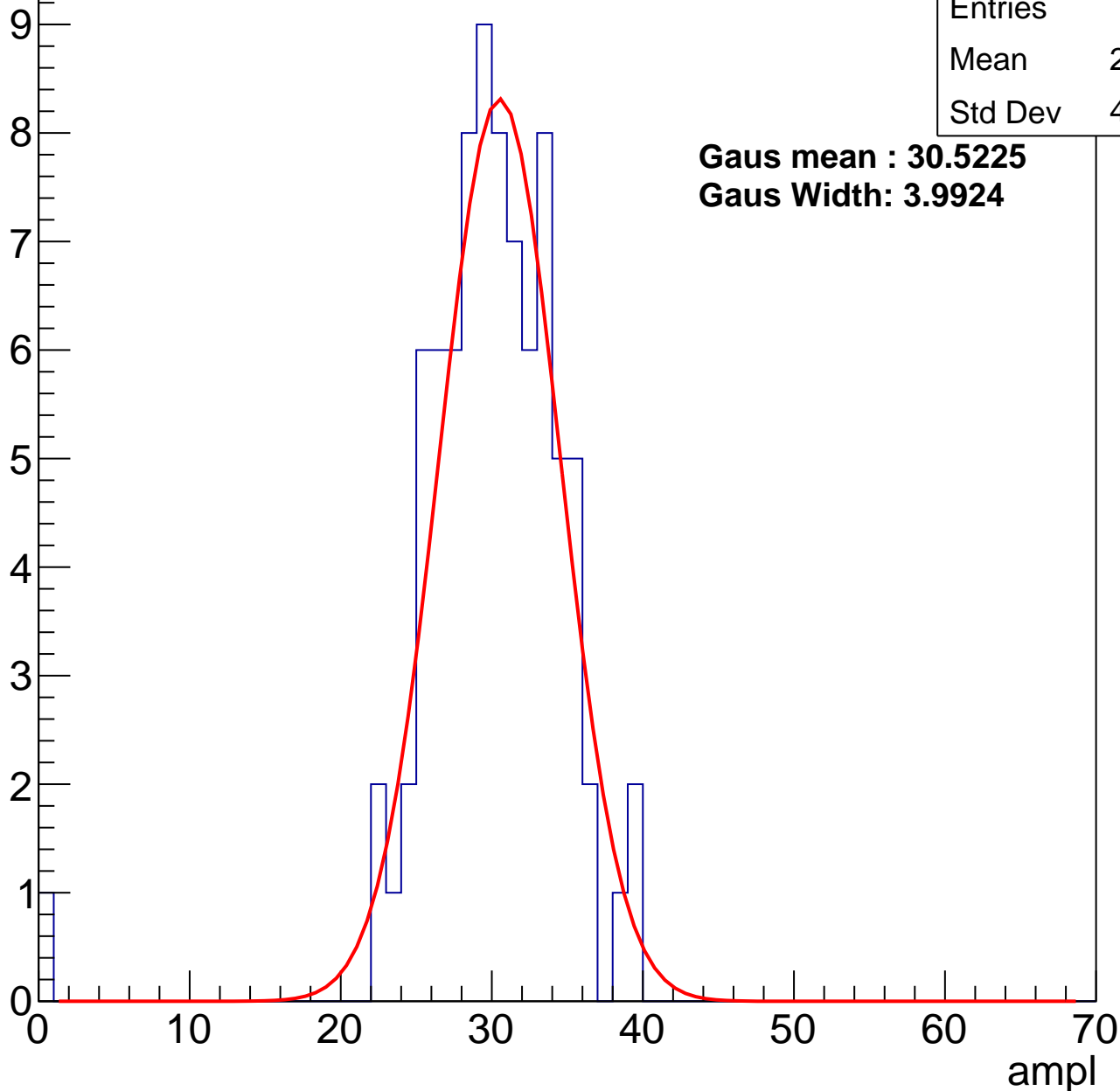
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	29.58
Std Dev	4.948

Gaus mean : 30.5225

Gaus Width: 3.9924



B0L001S, U25-ch83, adc1

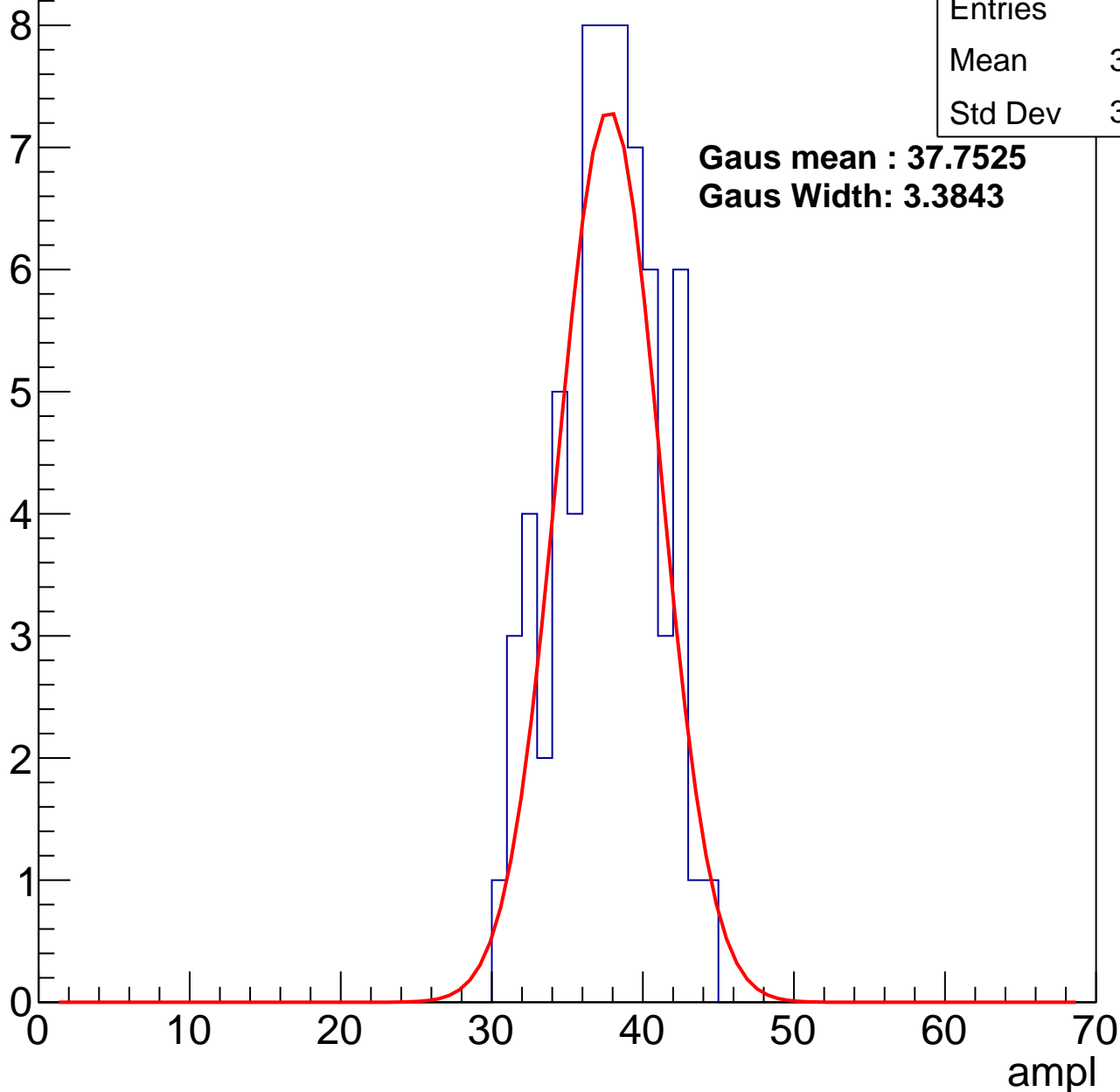
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.16
Std Dev	3.299

Gaus mean : 37.7525

Gaus Width: 3.3843



B0L001S, U25-ch83, adc2

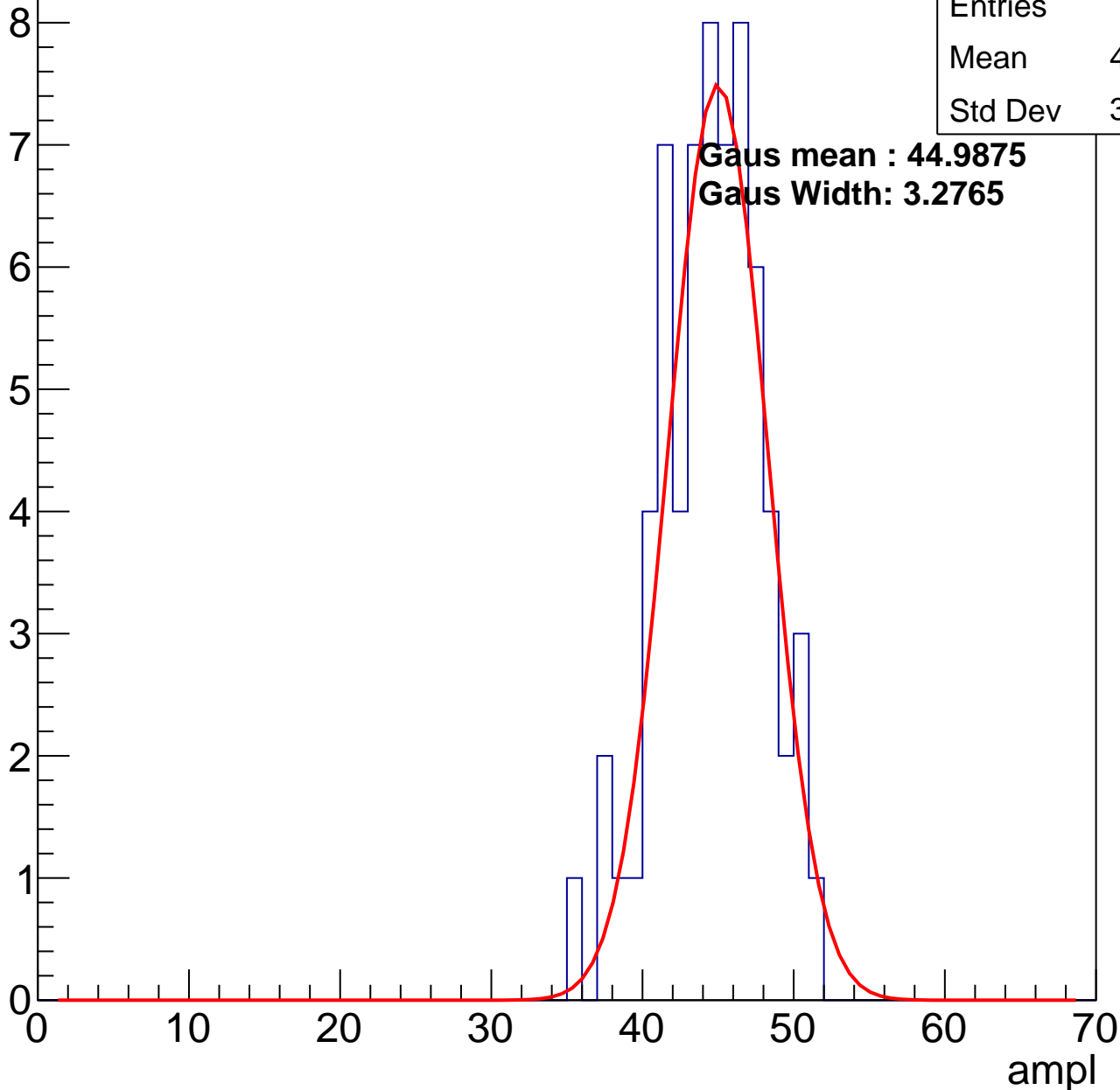
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.09
Std Dev	3.379

Gaus mean : 44.9875

Gaus Width: 3.2765

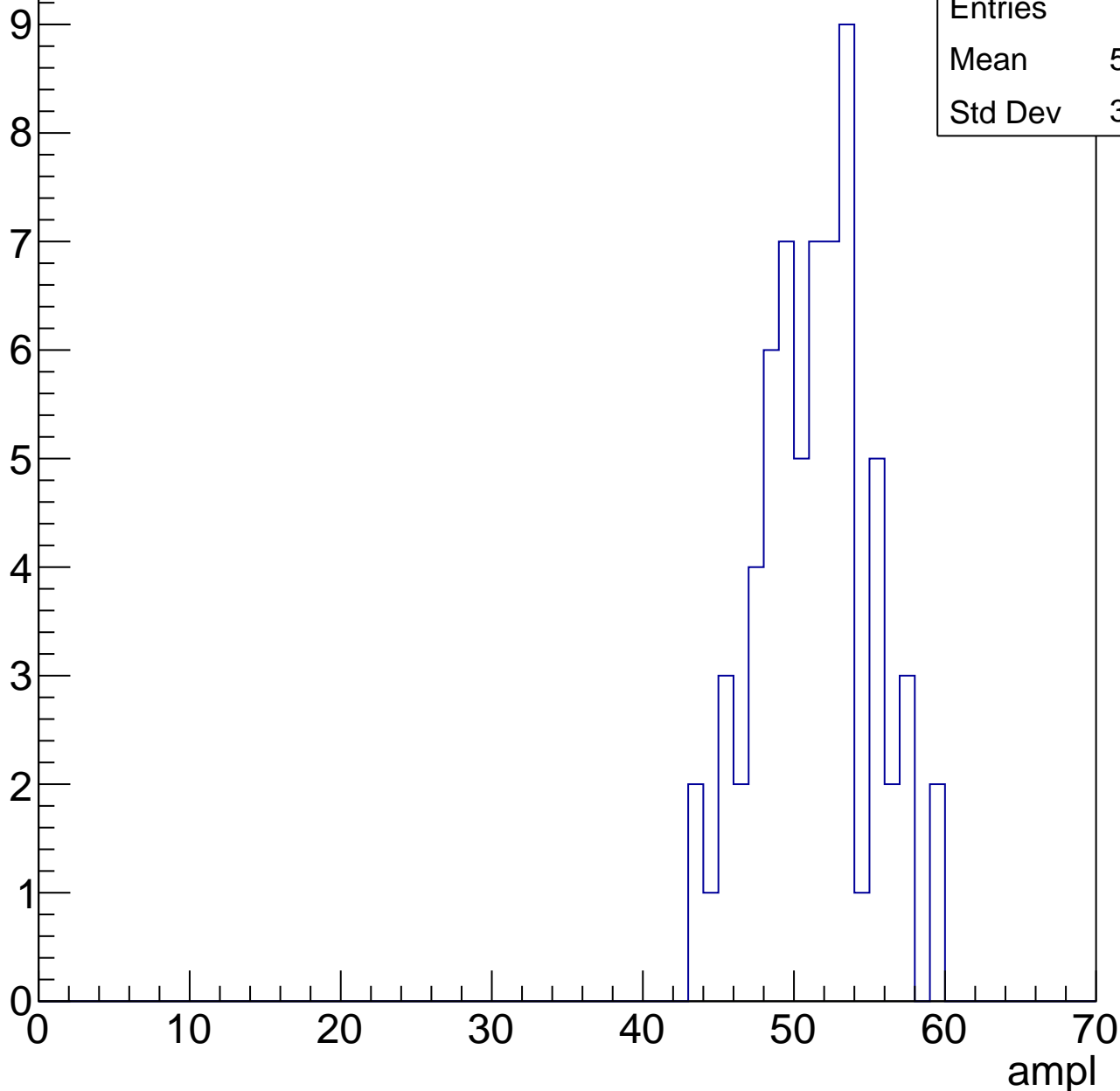


B0L001S, U25-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.82
Std Dev	3.696

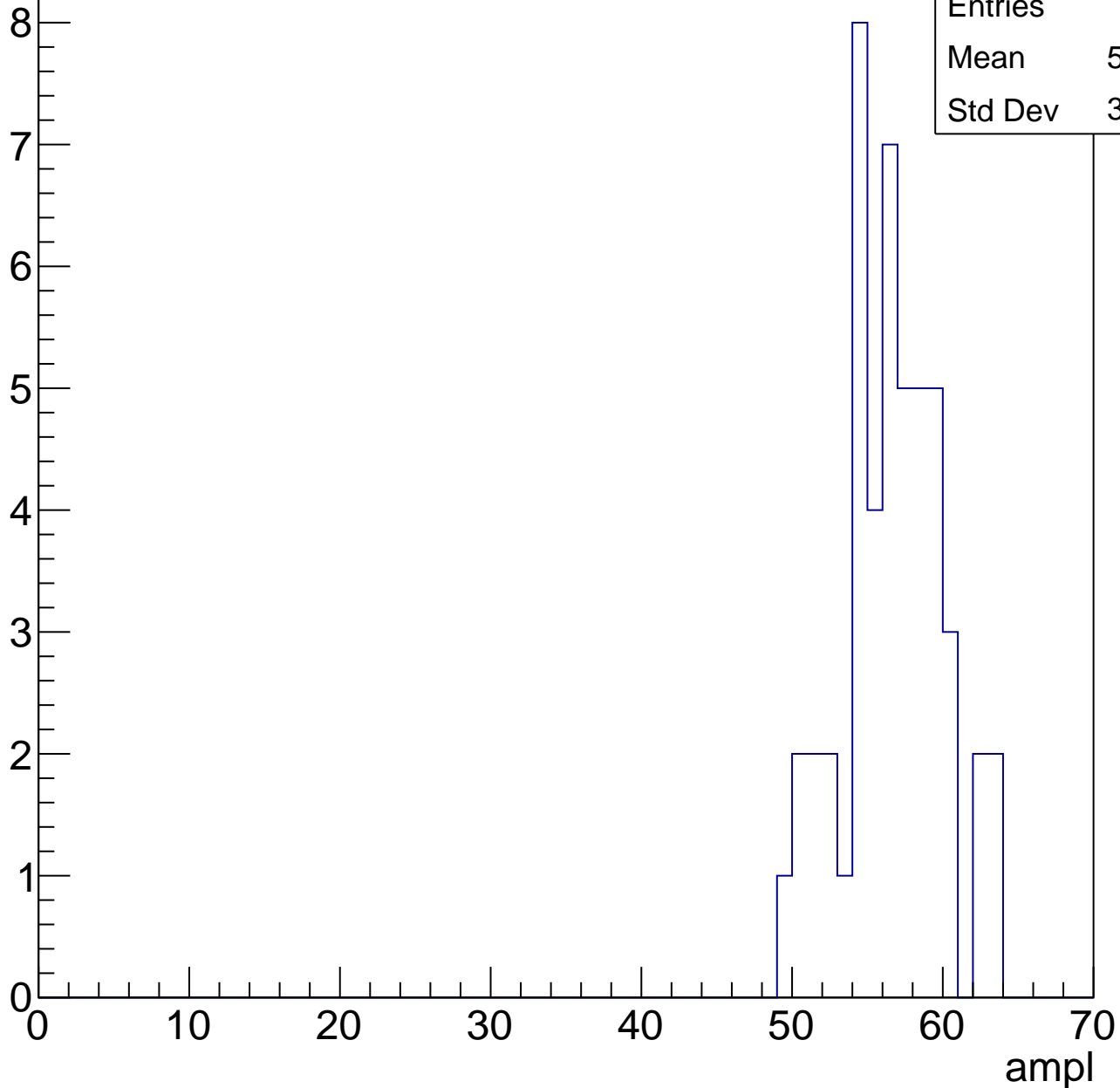


B0L001S, U25-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.16
Std Dev	3.303

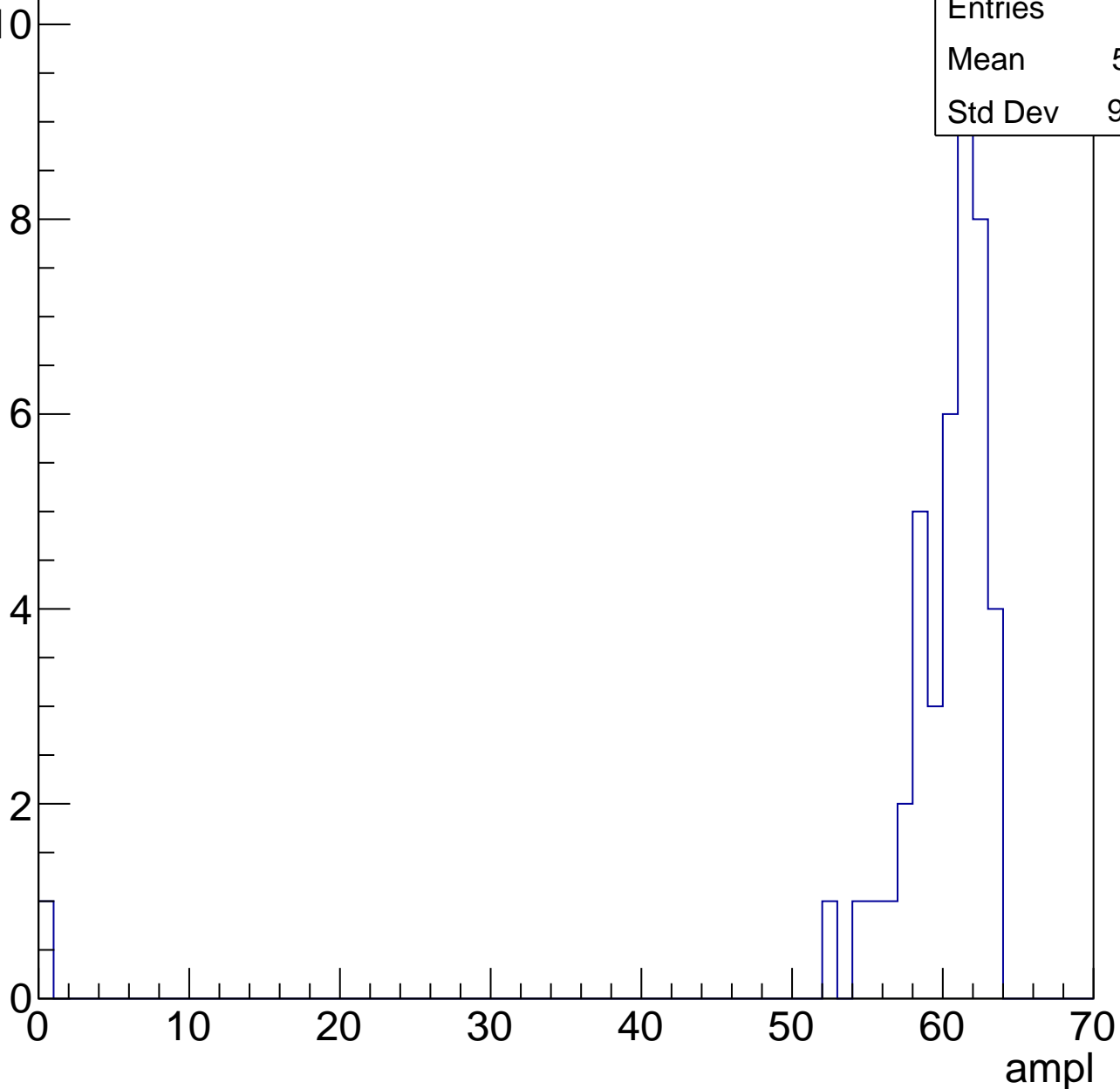


B0L001S, U25-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

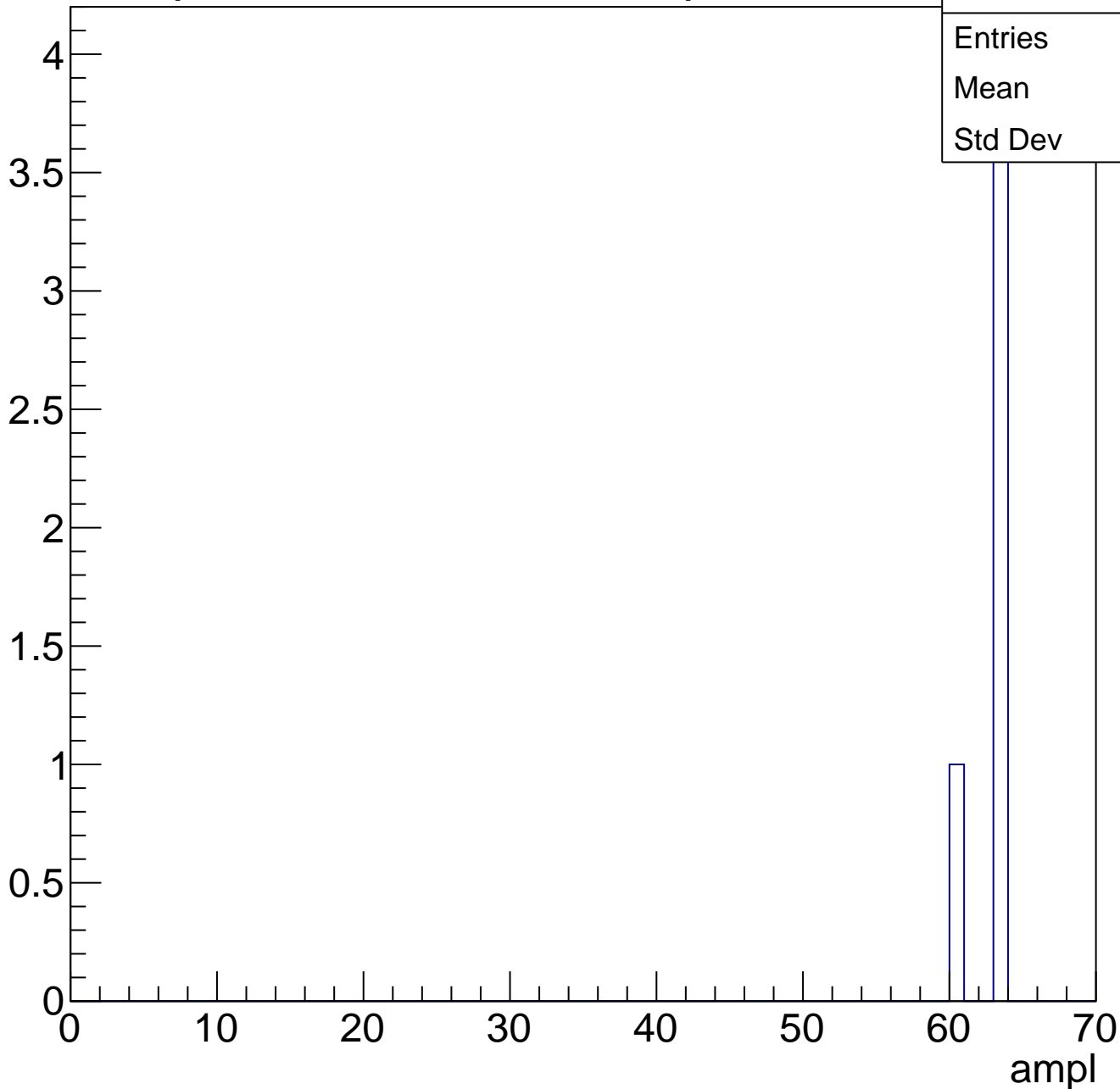
Entries	43
Mean	58.51
Std Dev	9.357



B0L001S, U25-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch84, adc0

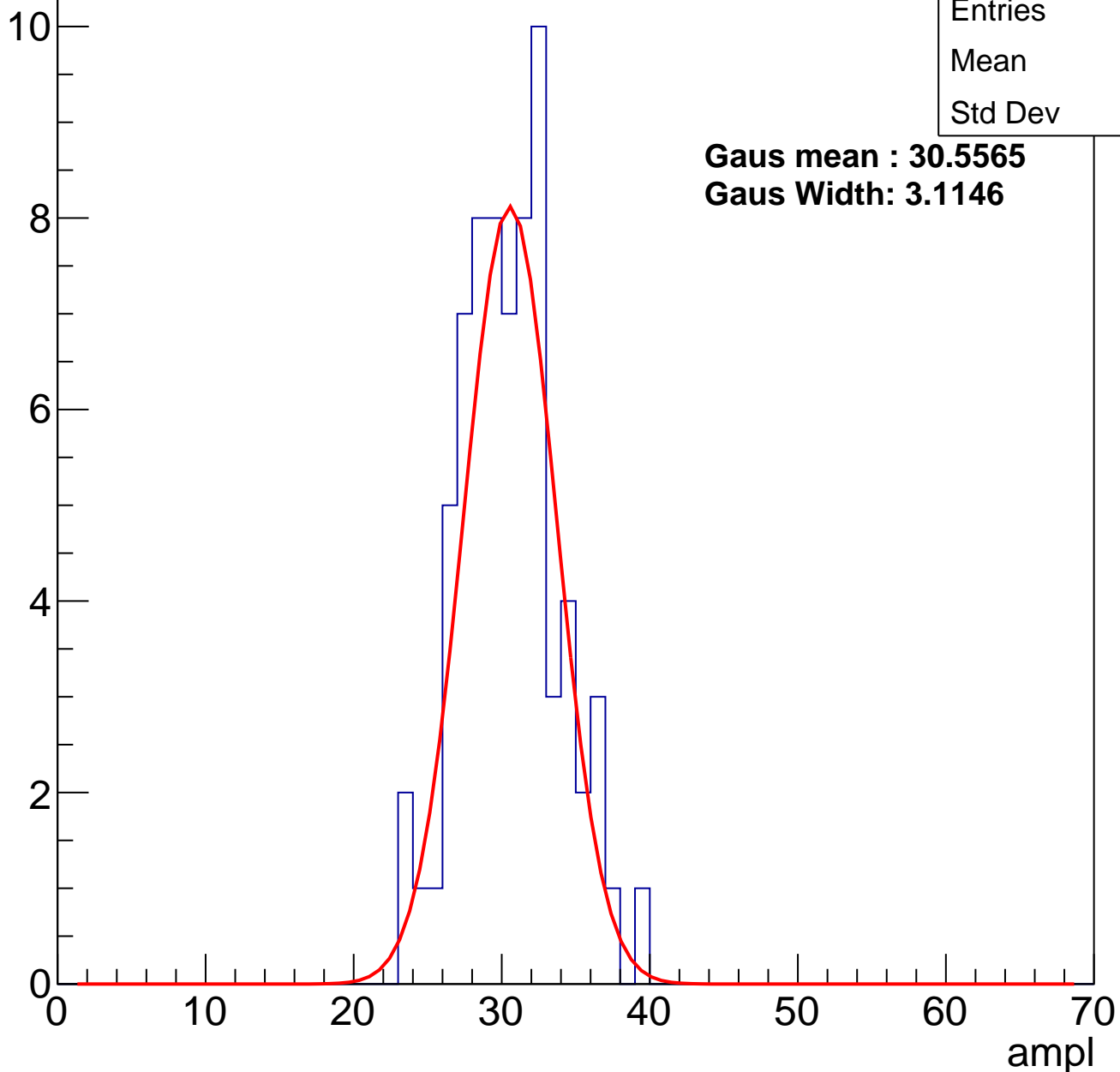
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	30.1
Std Dev	3.29

Gaus mean : 30.5565

Gaus Width: 3.1146

Entry



B0L001S, U25-ch84, adc1

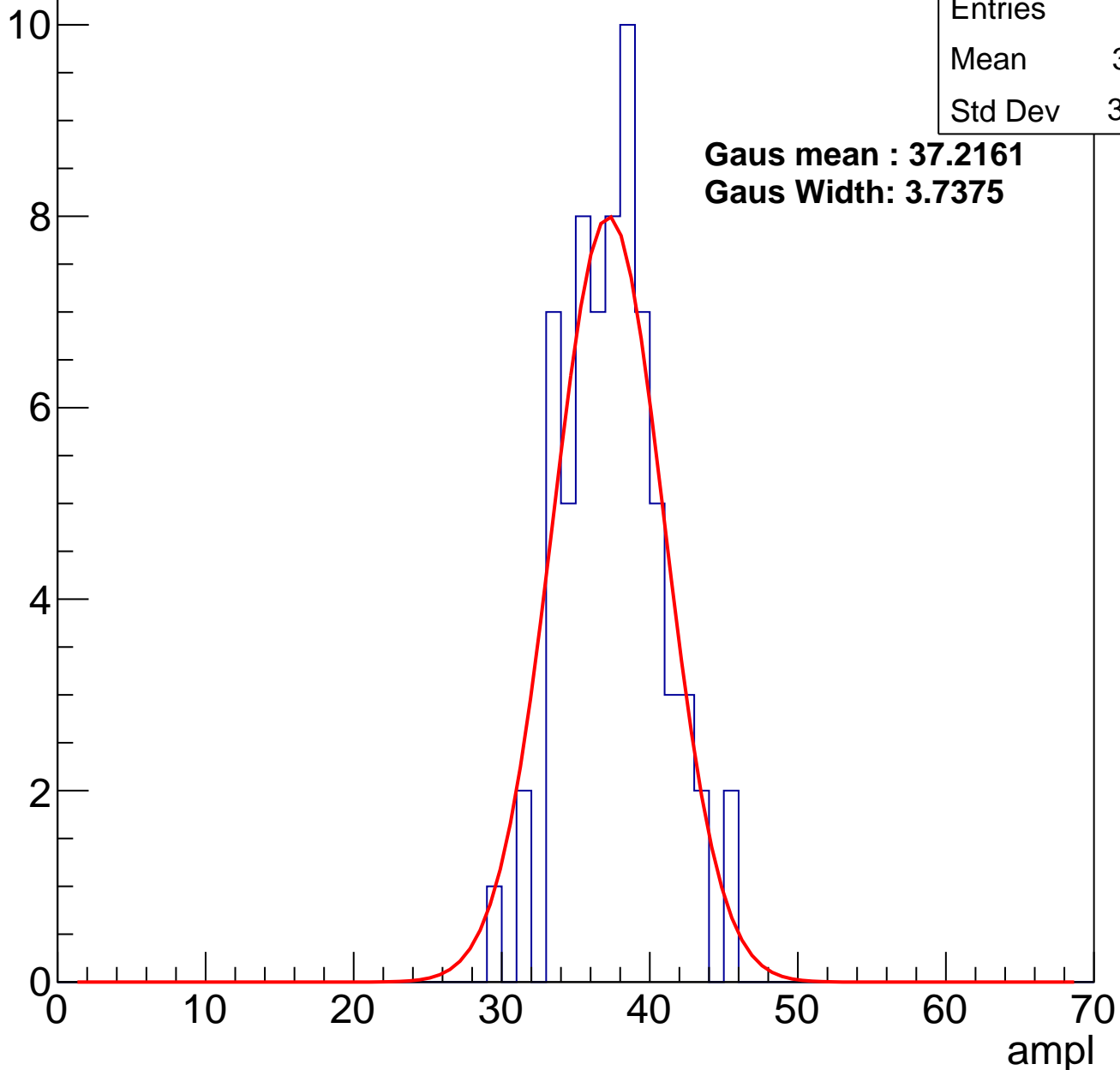
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	37.11
Std Dev	3.245

Gaus mean : 37.2161

Gaus Width: 3.7375

Entry



B0L001S, U25-ch84, adc2

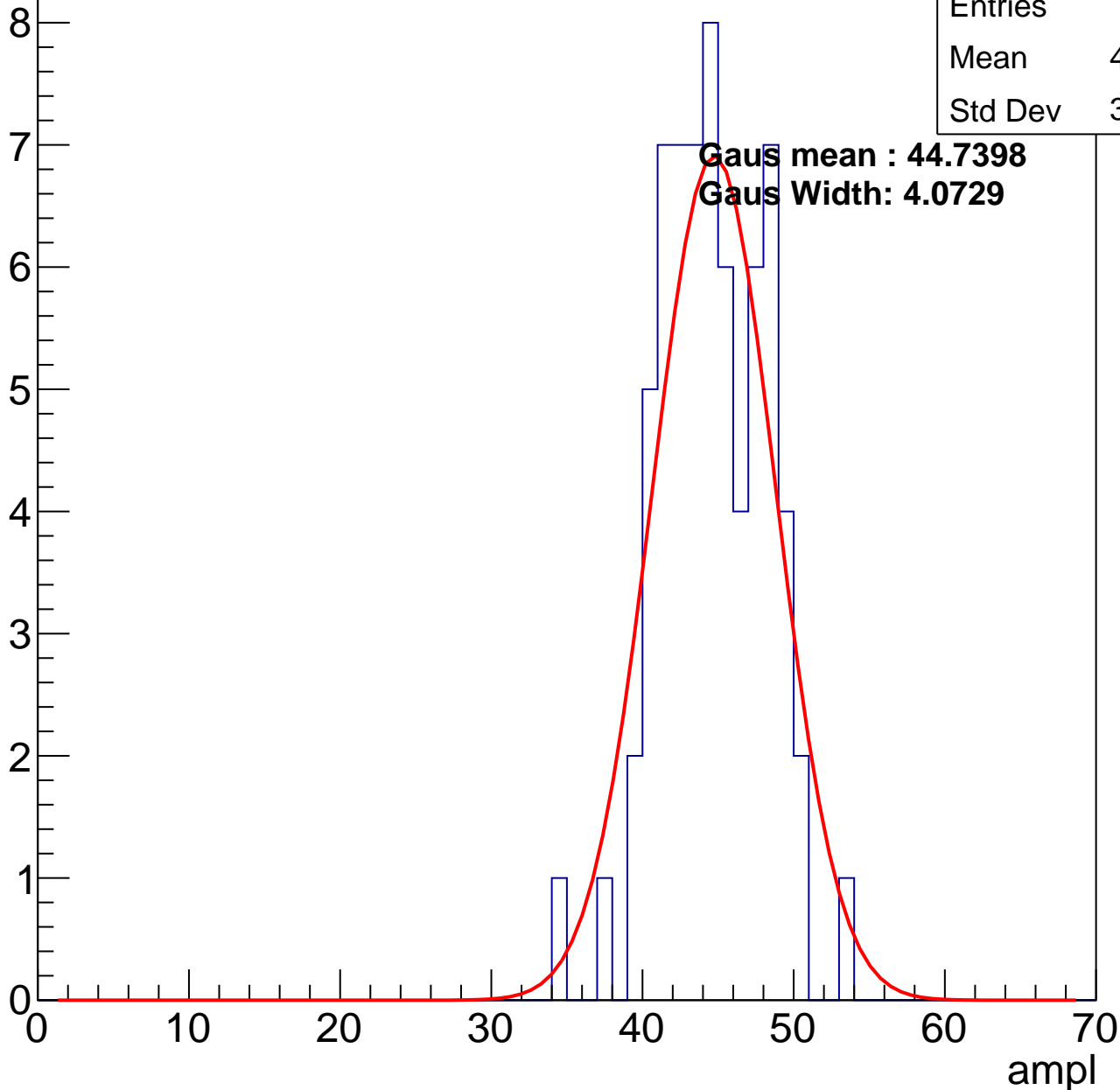
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.18
Std Dev	3.464

Gaus mean : 44.7398

Gaus Width: 4.0729

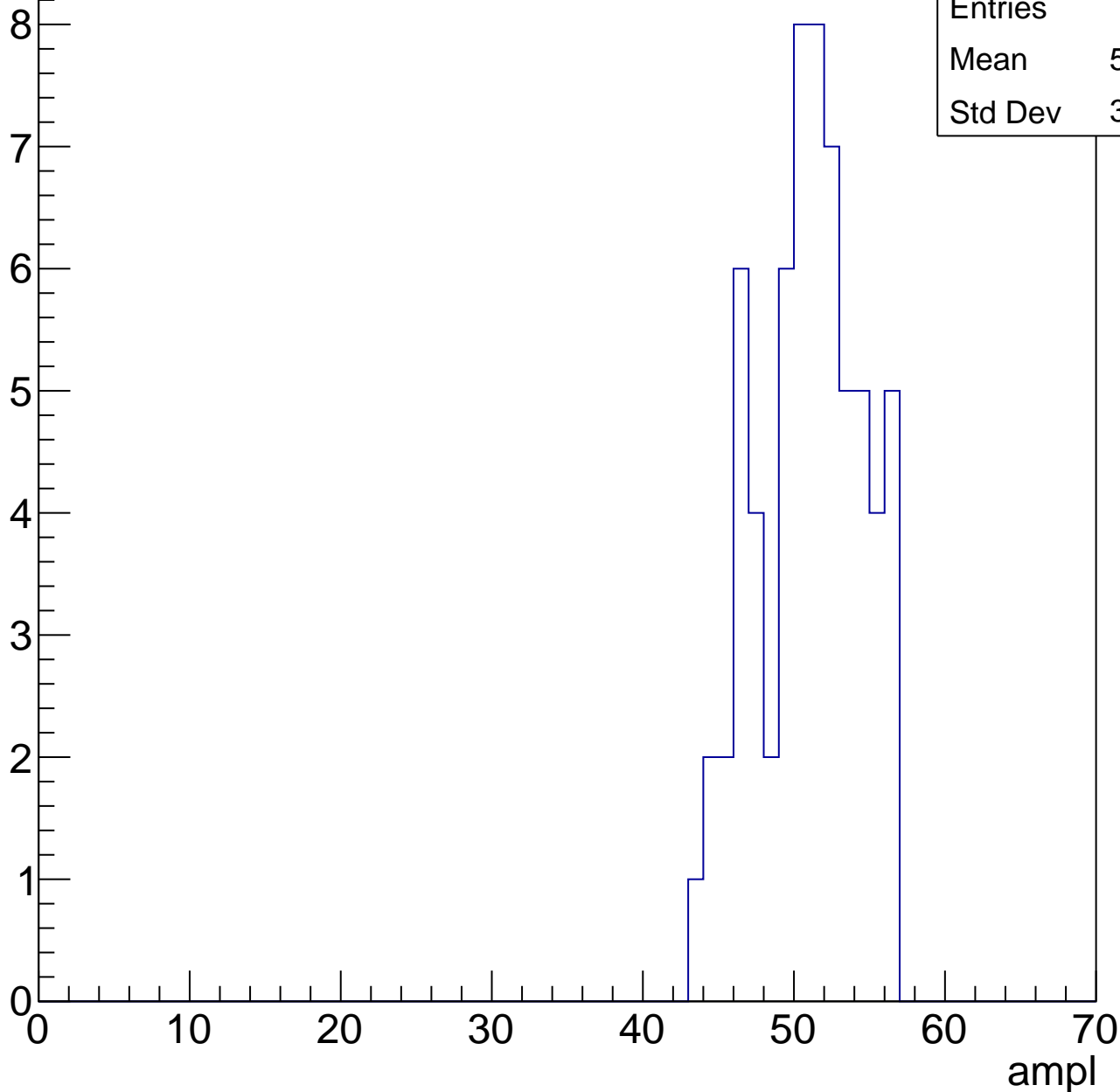


B0L001S, U25-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.49
Std Dev	3.379

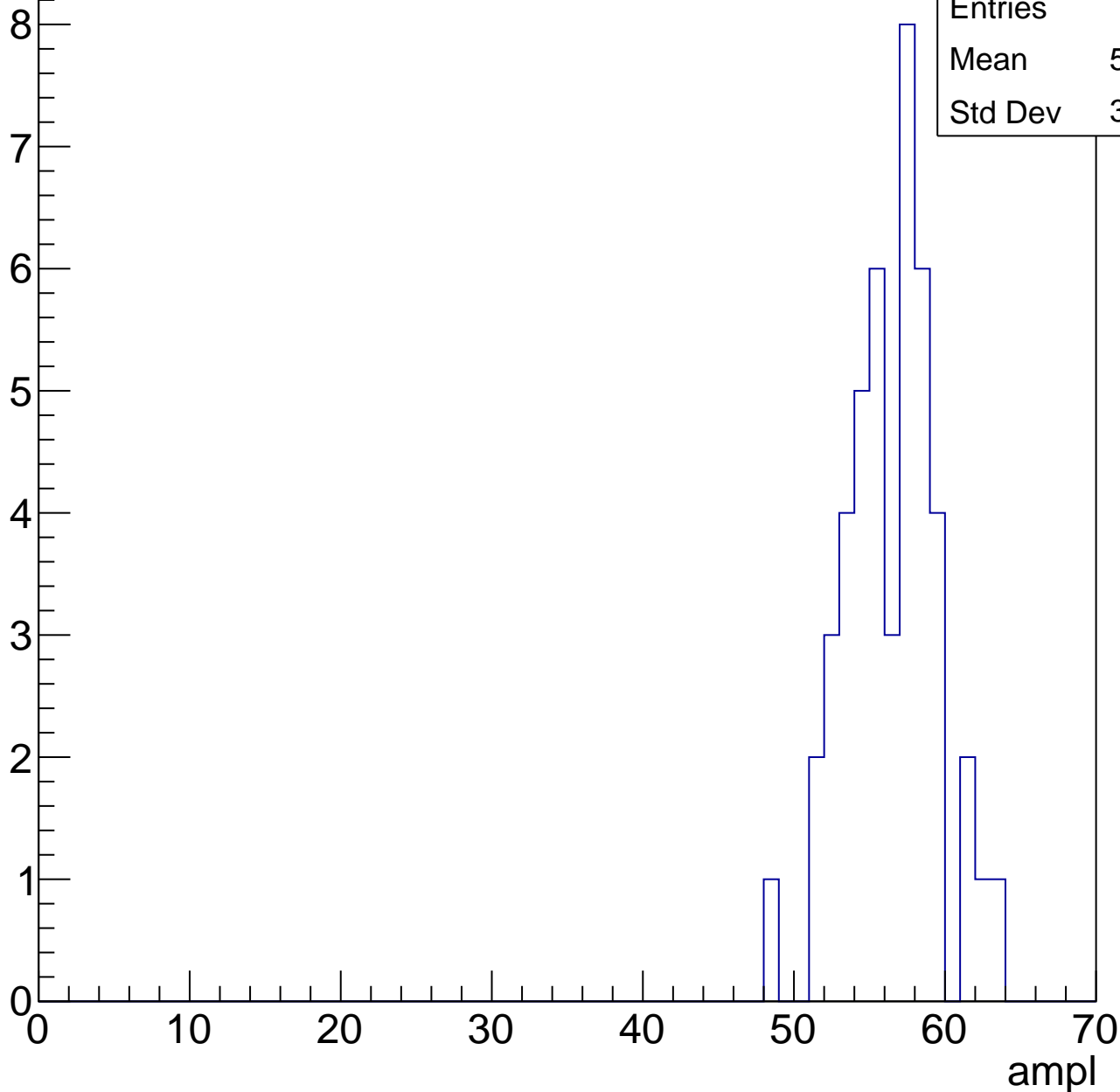


B0L001S, U25-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	55.93
Std Dev	3.046



B0L001S, U25-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

8

6

4

2

0

Entries	47
Mean	59.04
Std Dev	8.906

ampl

0

10

20

30

40

50

60

70

B0L001S, U25-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch84, adc7

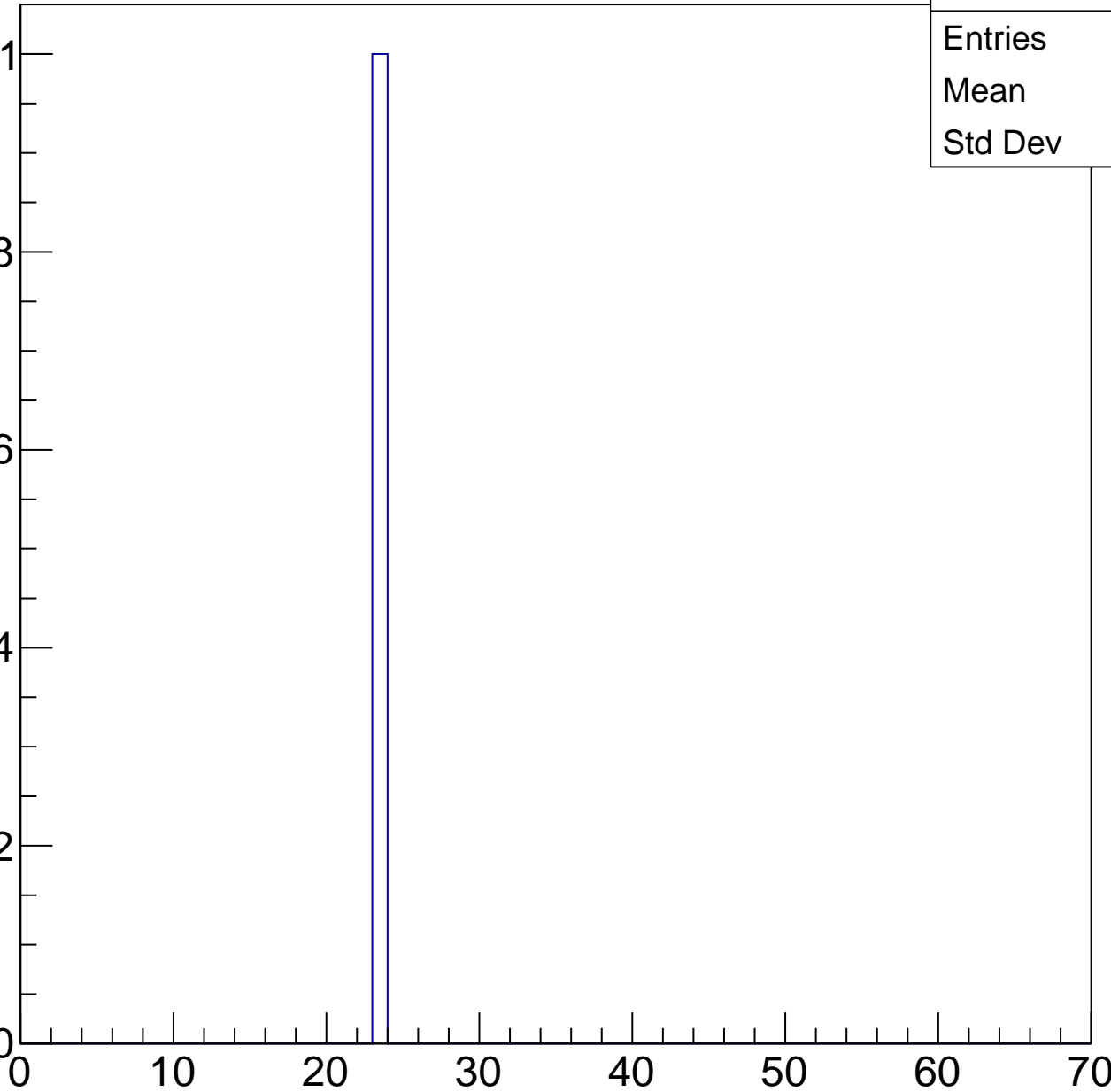
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B0L001S, U25-ch85, adc0

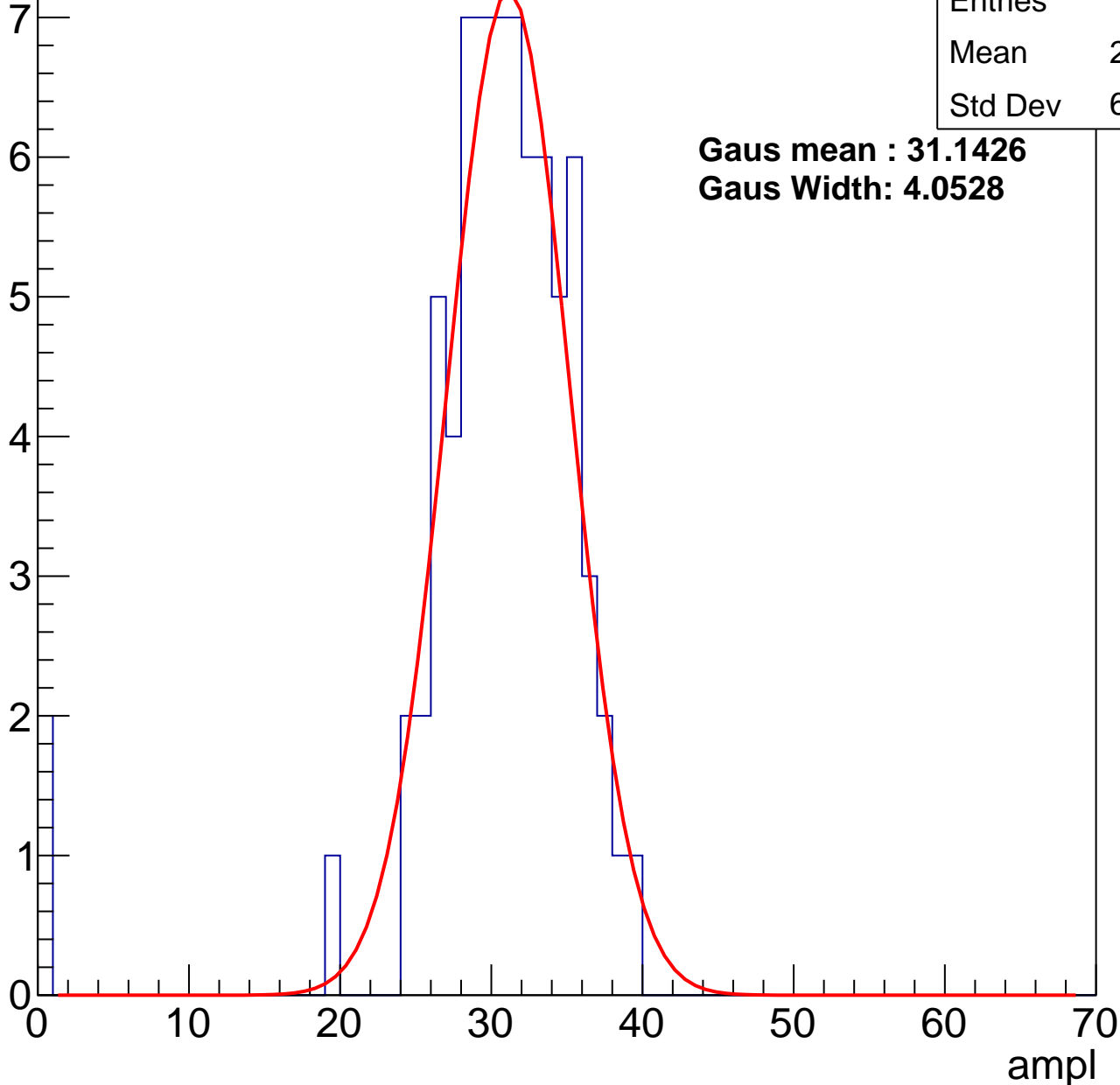
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.86
Std Dev	6.215

Gaus mean : 31.1426

Gaus Width: 4.0528



B0L001S, U25-ch85, adc1

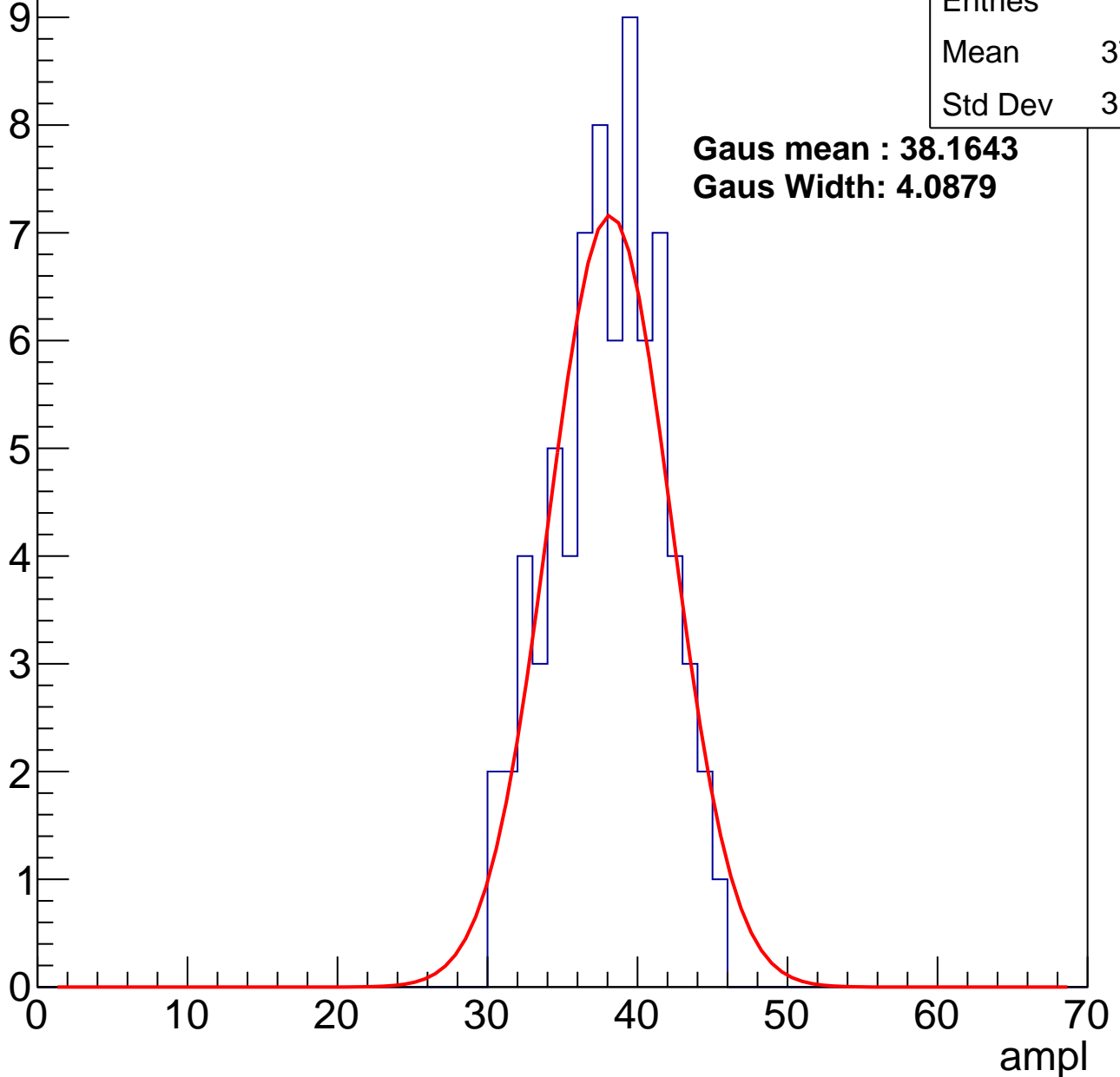
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.58
Std Dev	3.588

Gaus mean : 38.1643

Gaus Width: 4.0879



B0L001S, U25-ch85, adc2

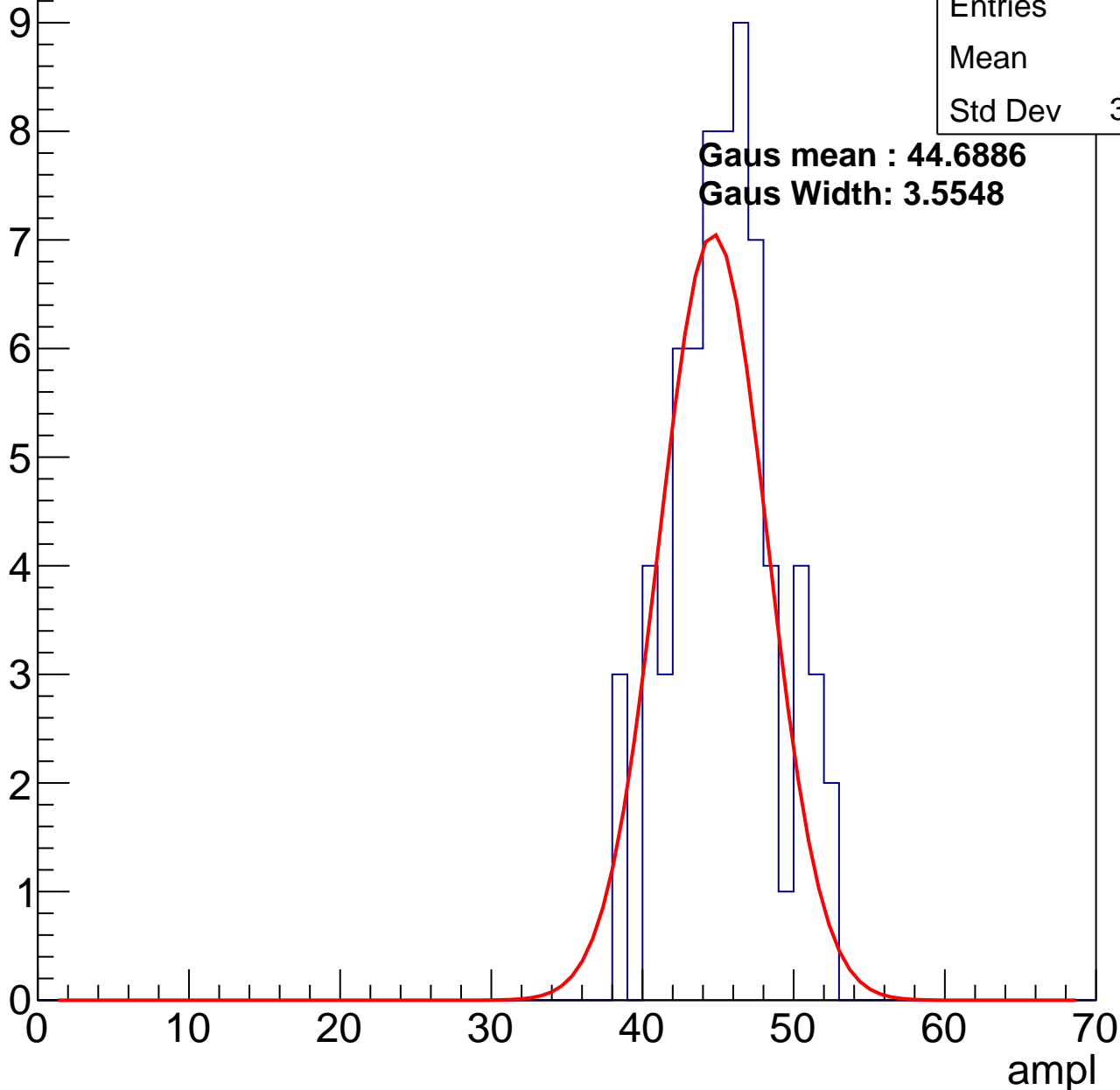
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45
Std Dev	3.378

Gaus mean : 44.6886

Gaus Width: 3.5548

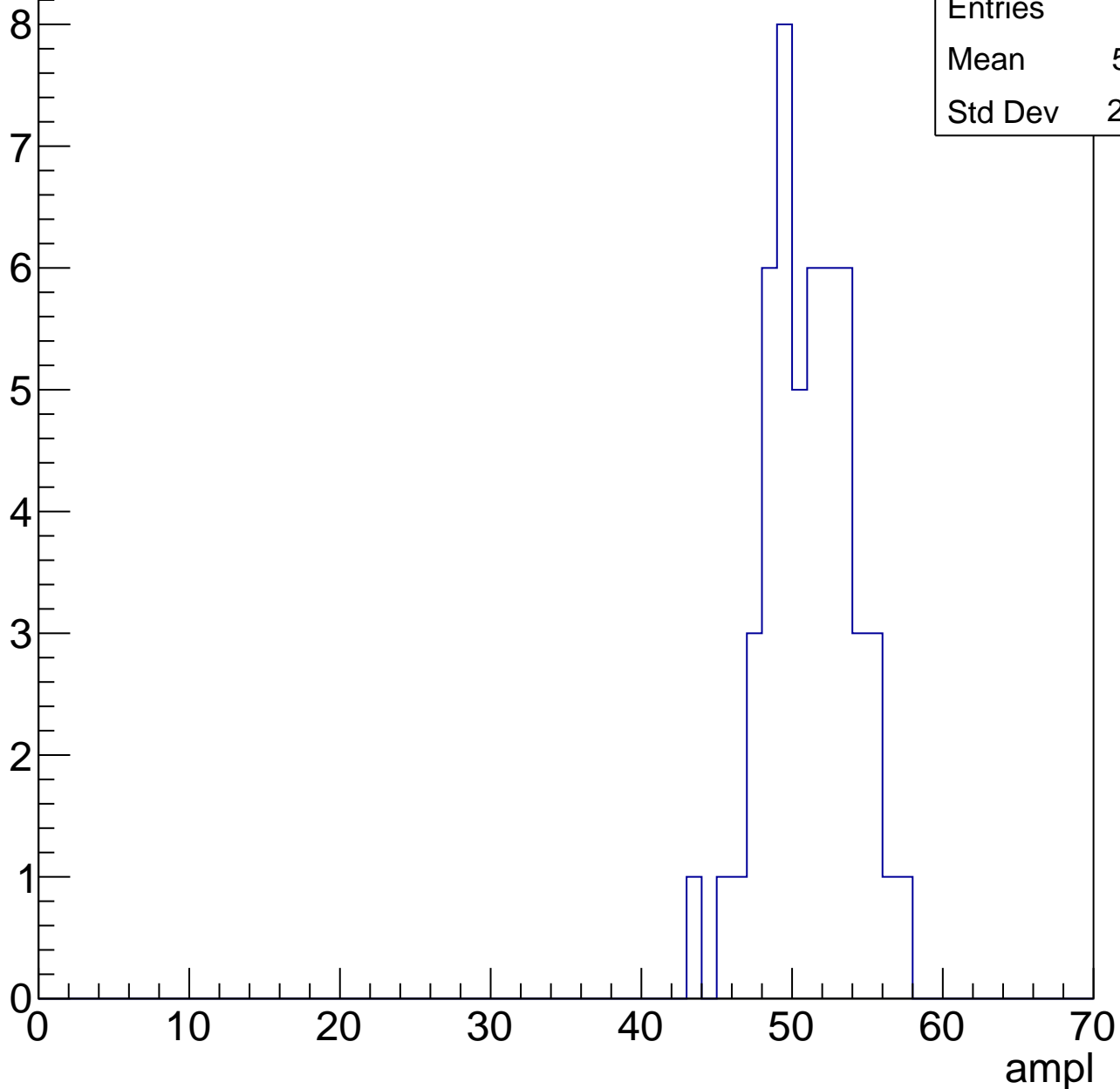


B0L001S, U25-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

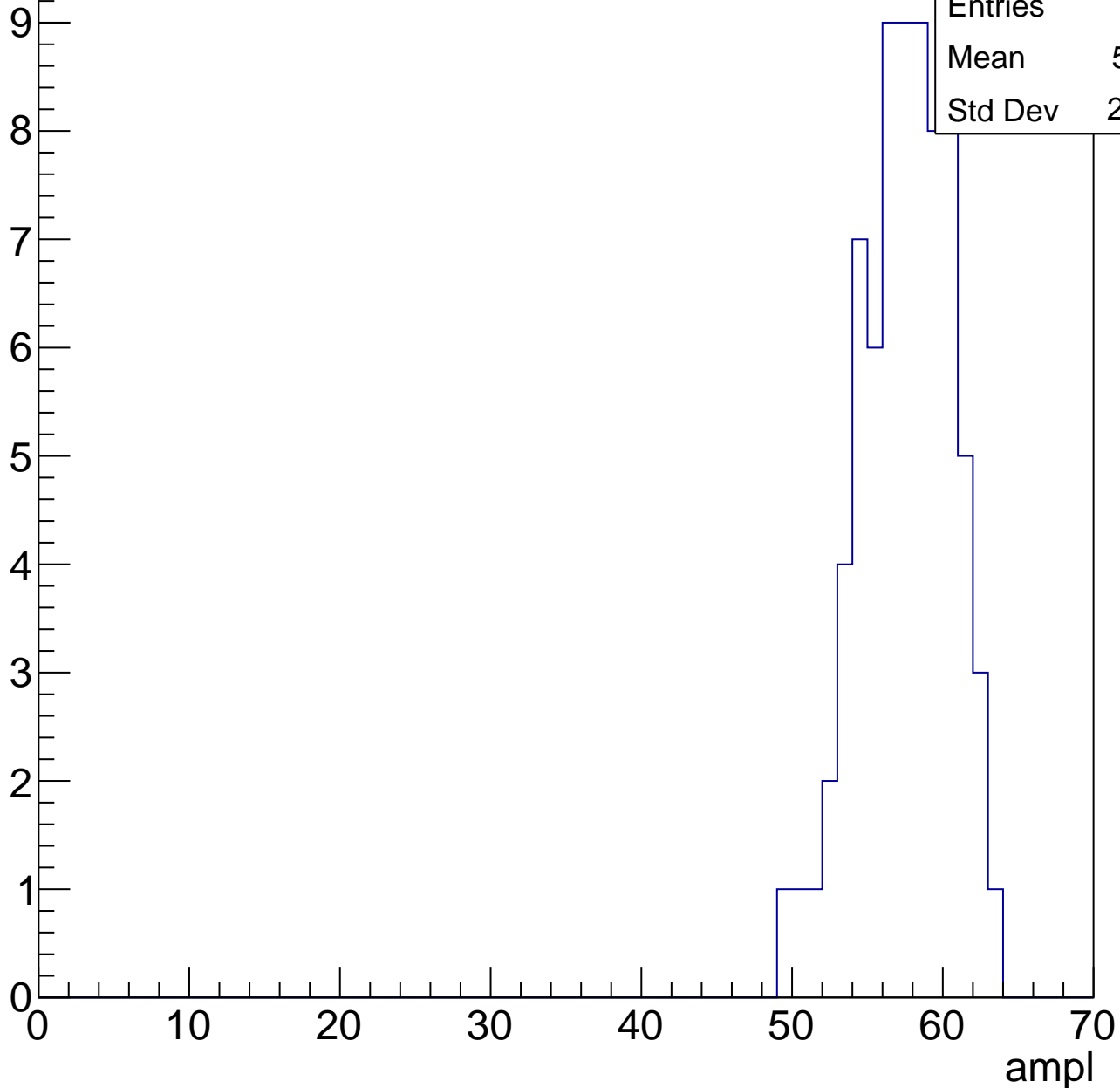
Entries	51
Mean	50.61
Std Dev	2.877



B0L001S, U25-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

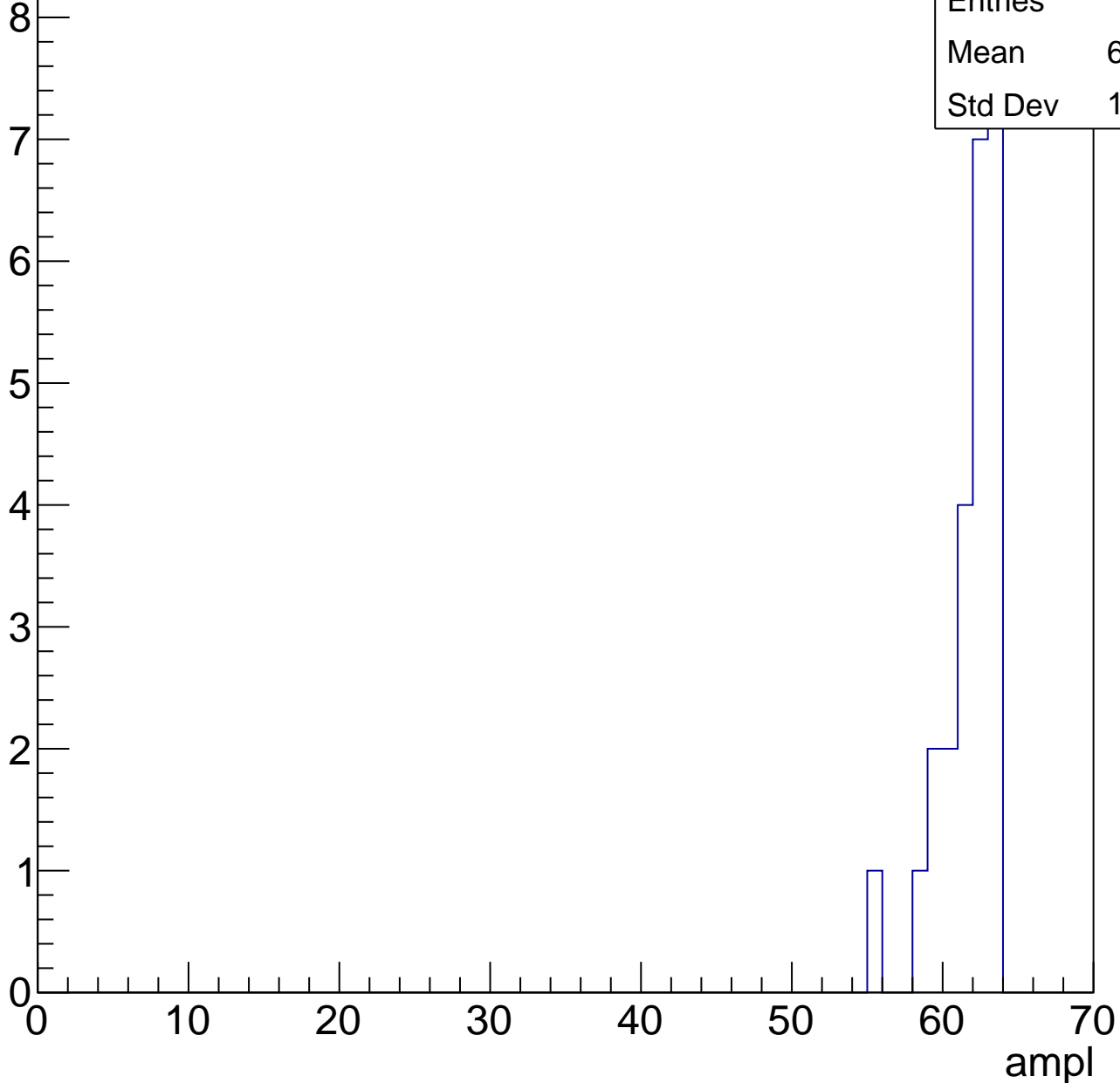


B0L001S, U25-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	61.32
Std Dev	1.912



B0L001S, U25-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch86, adc0

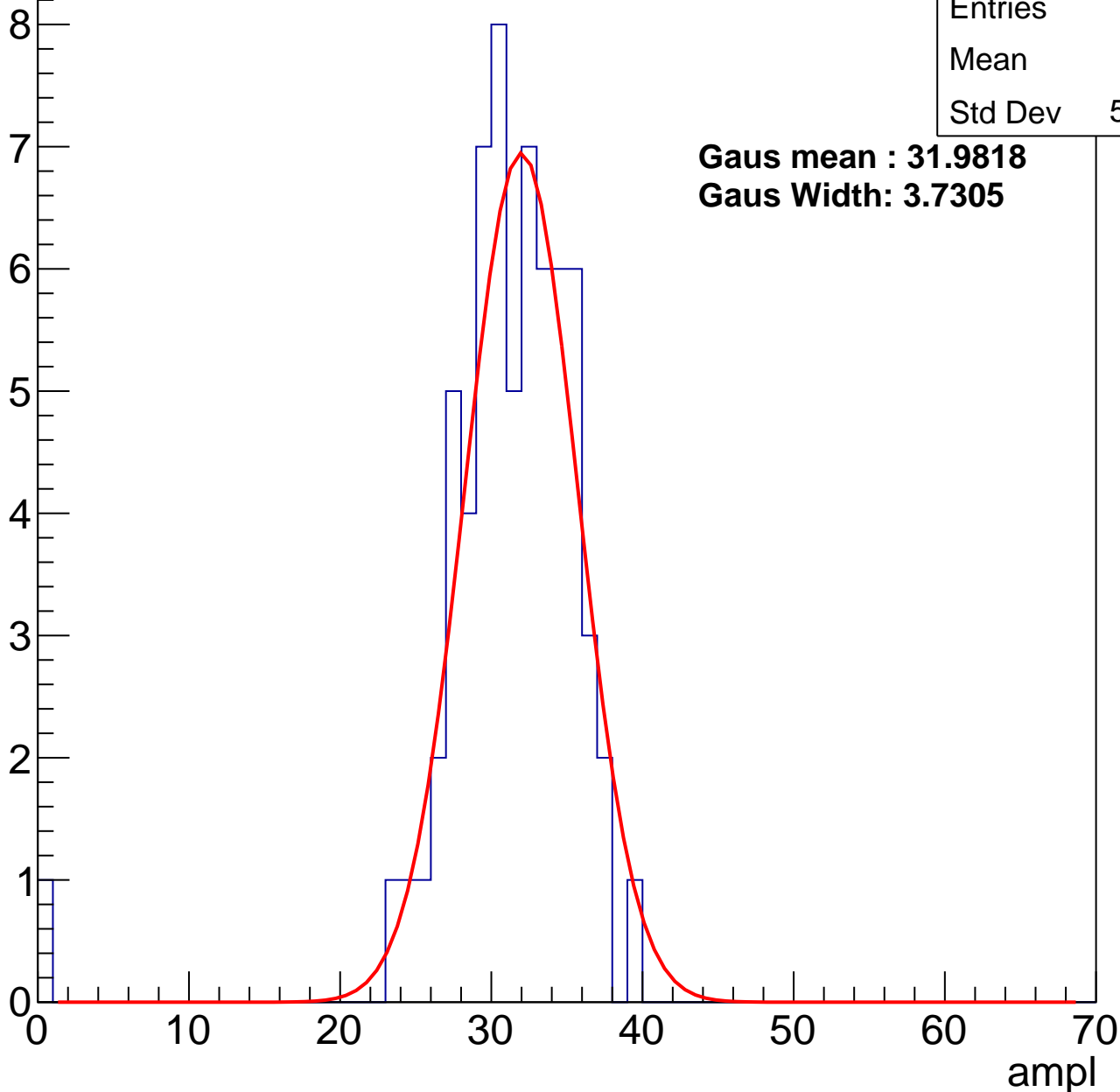
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.7
Std Dev	5.072

Gaus mean : 31.9818

Gaus Width: 3.7305



B0L001S, U25-ch86, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	38.09
Std Dev	3.398

Gaus mean : 38.5734

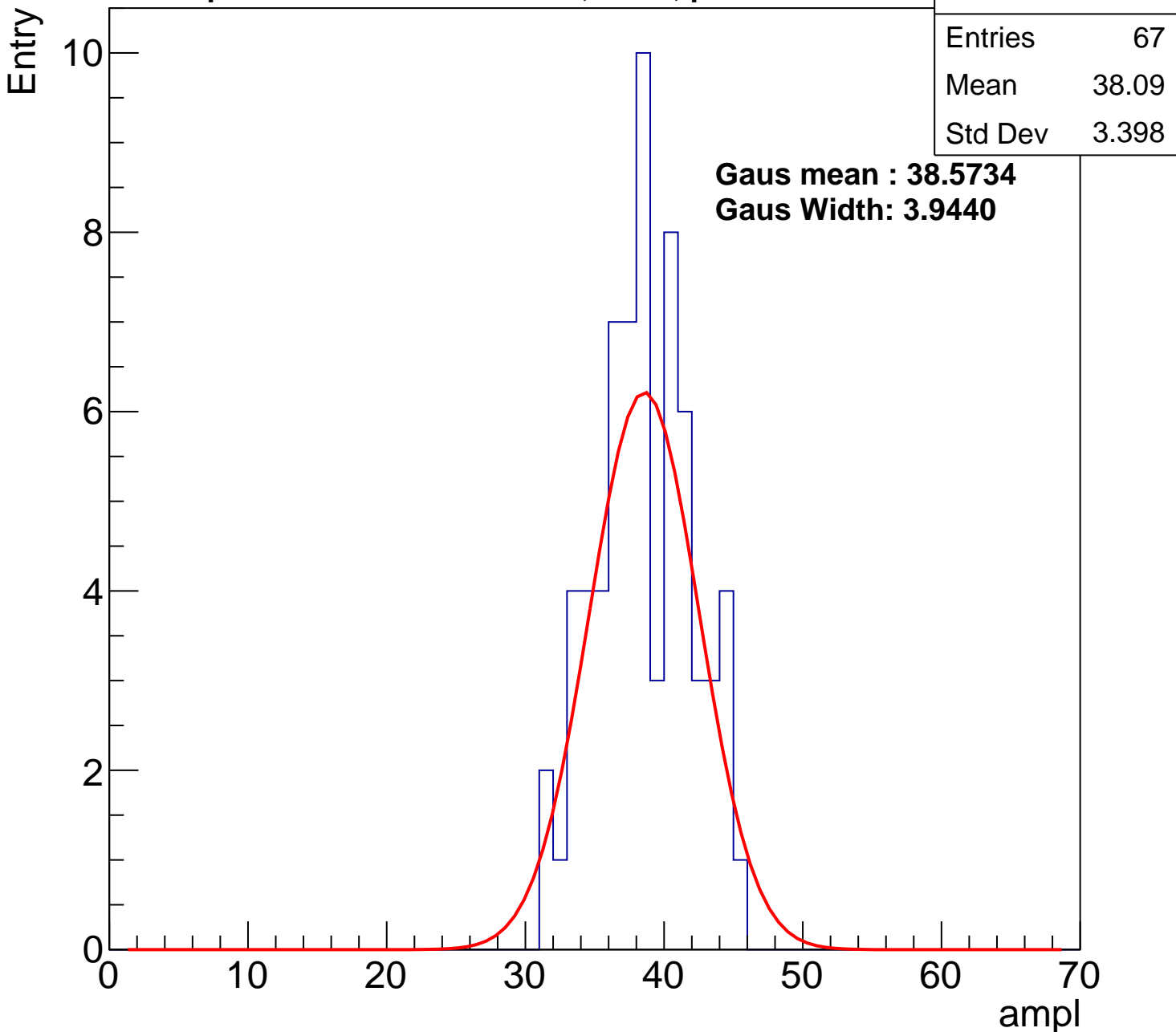
Gaus Width: 3.9440

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

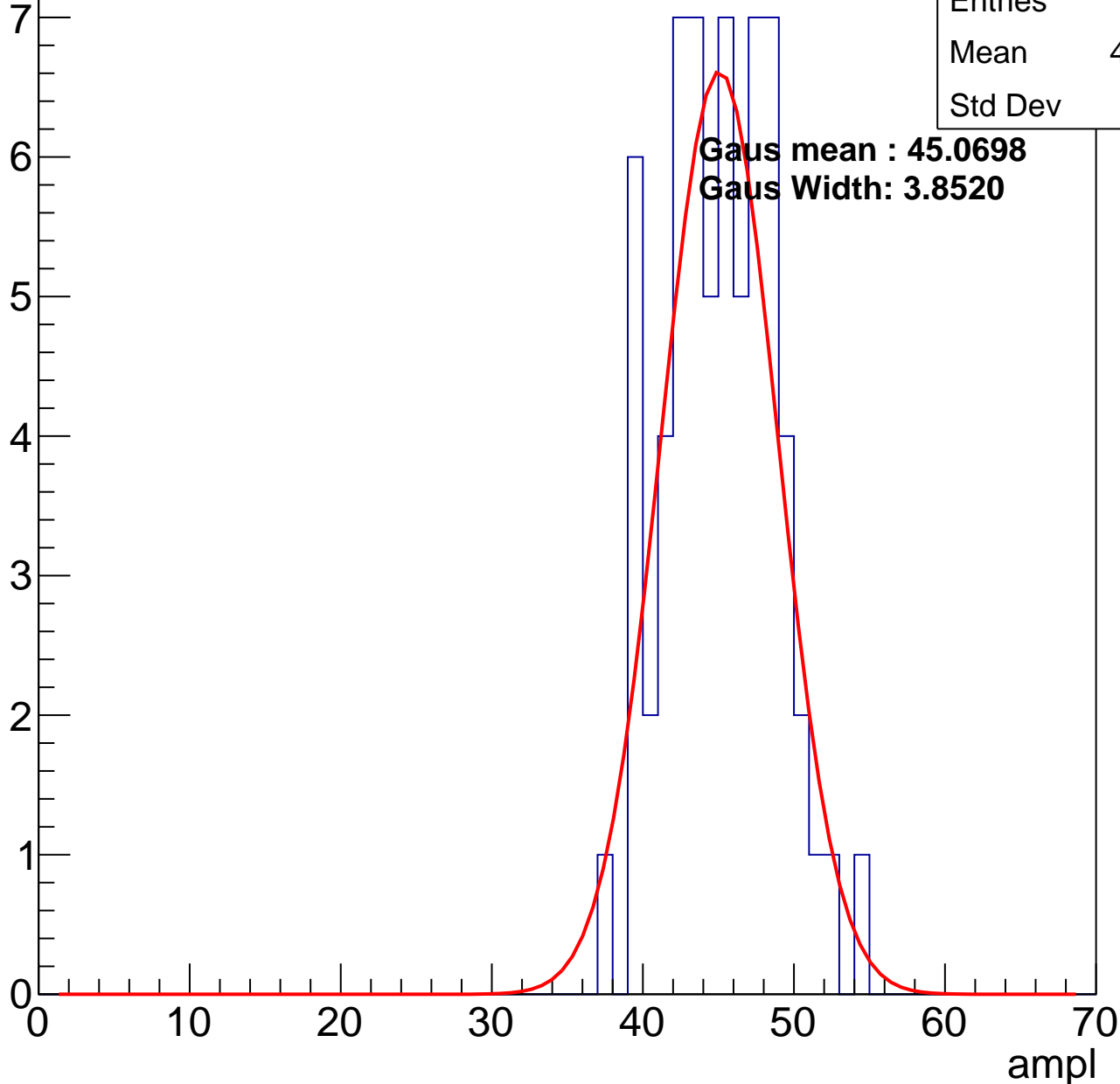


B0L001S, U25-ch86, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.67
Std Dev	3.58

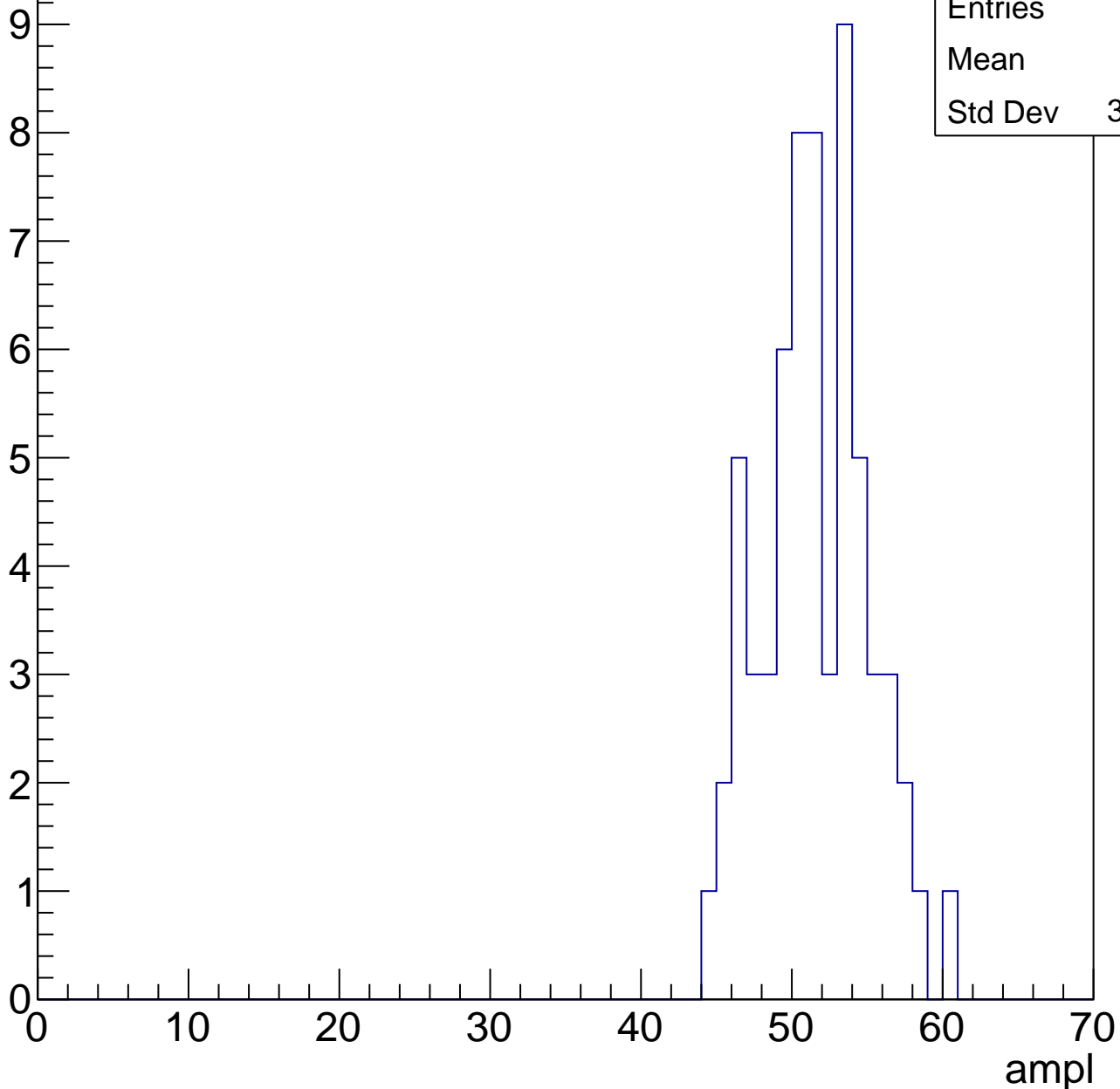


B0L001S, U25-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	51.1
Std Dev	3.477

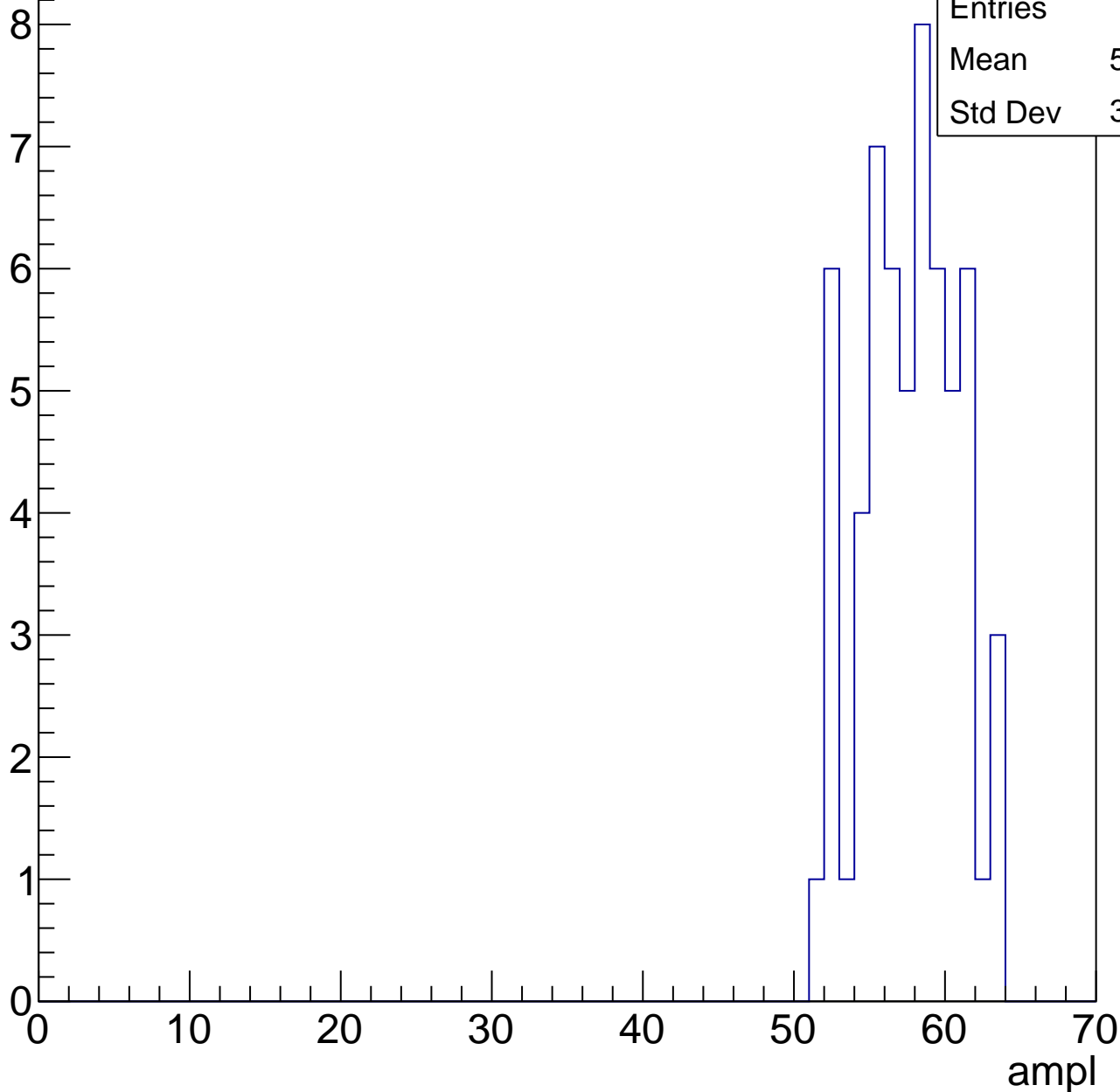


B0L001S, U25-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	57.17
Std Dev	3.125

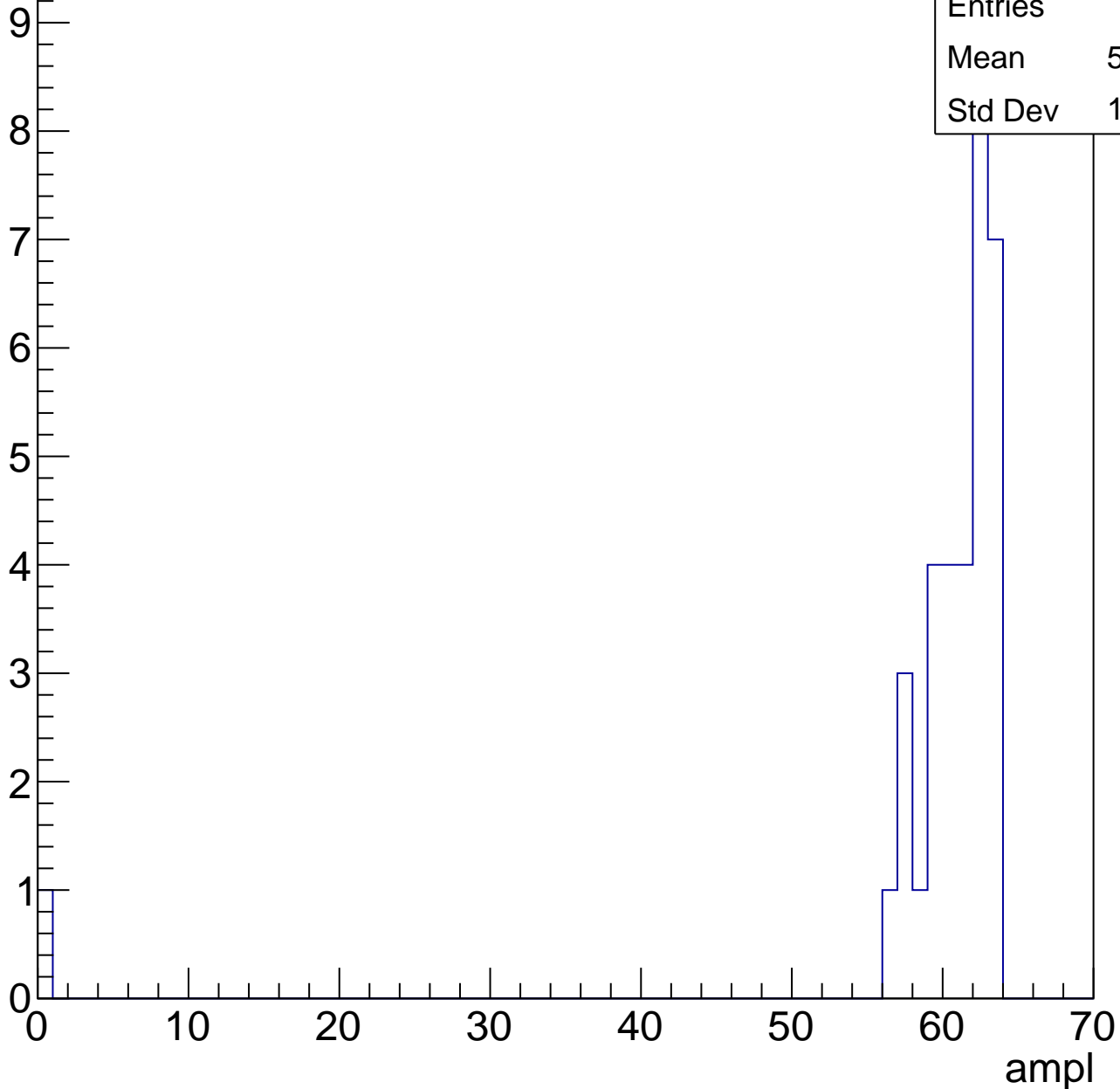


B0L001S, U25-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.94
Std Dev	10.45



B0L001S, U25-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch87, adc0

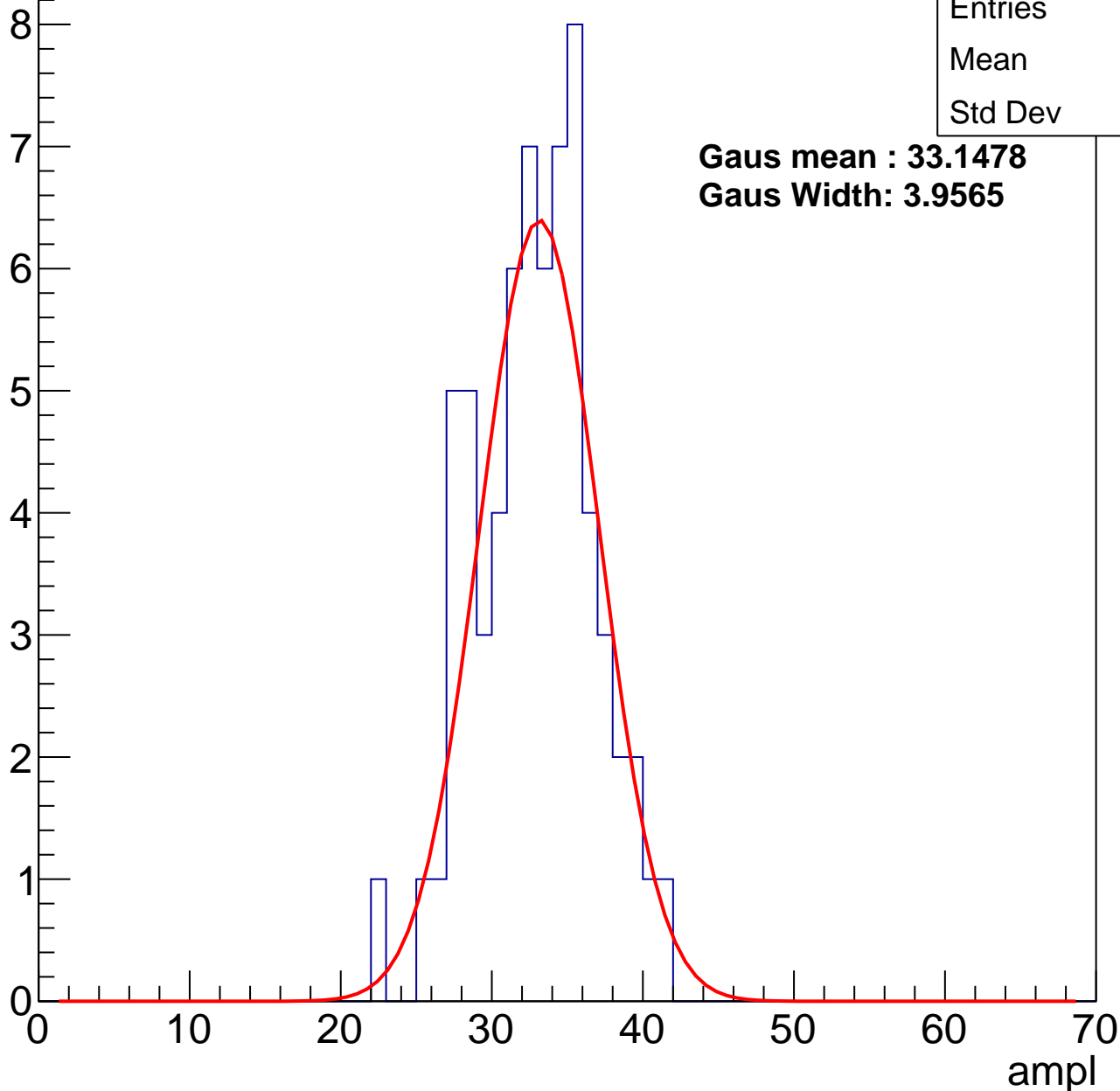
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	32.4
Std Dev	3.84

Gaus mean : 33.1478

Gaus Width: 3.9565



B0L001S, U25-ch87, adc1

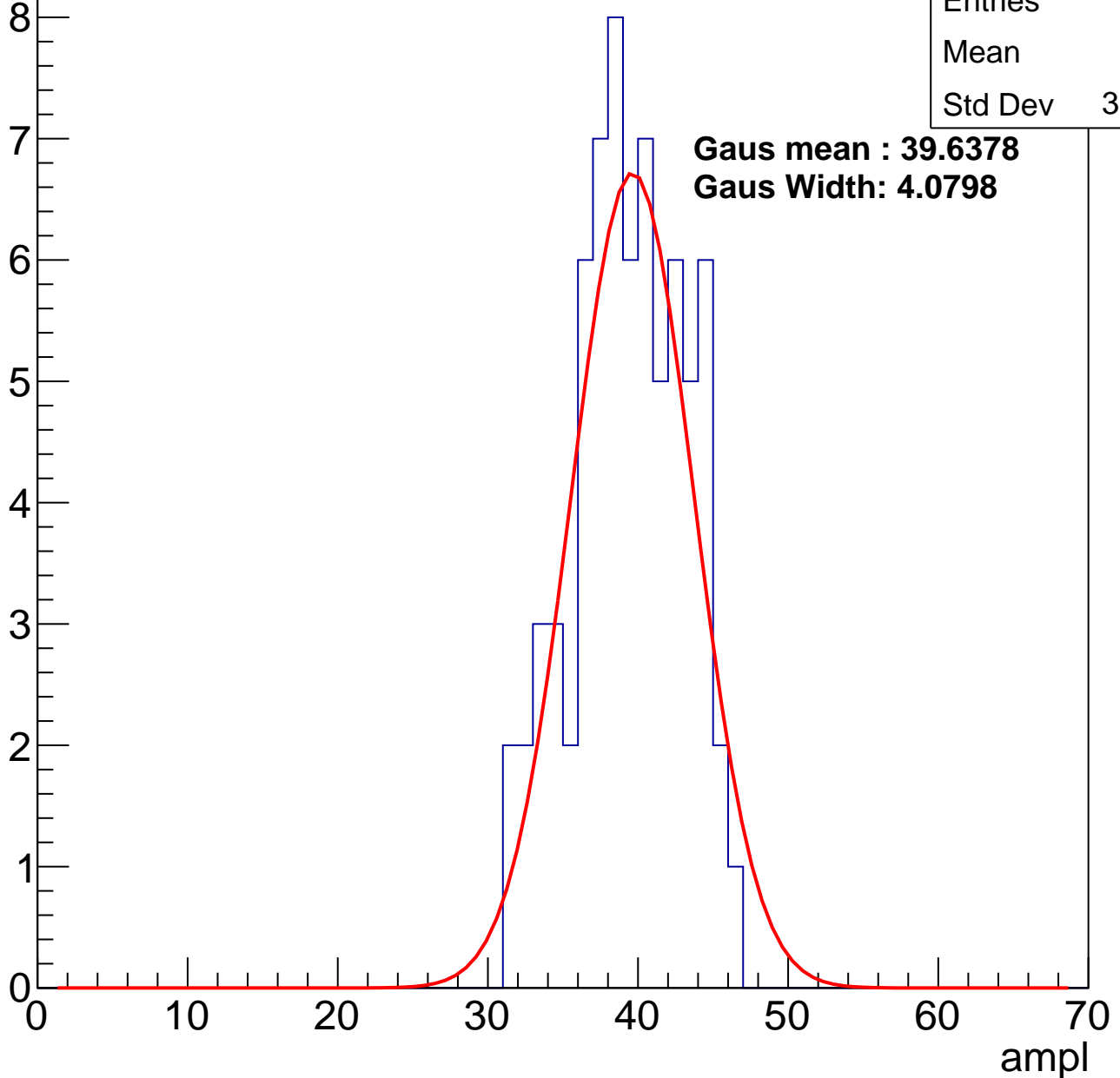
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.9
Std Dev	3.682

Gaus mean : 39.6378

Gaus Width: 4.0798



B0L001S, U25-ch87, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	46.69
Std Dev	3.699

Gaus mean : 47.0294

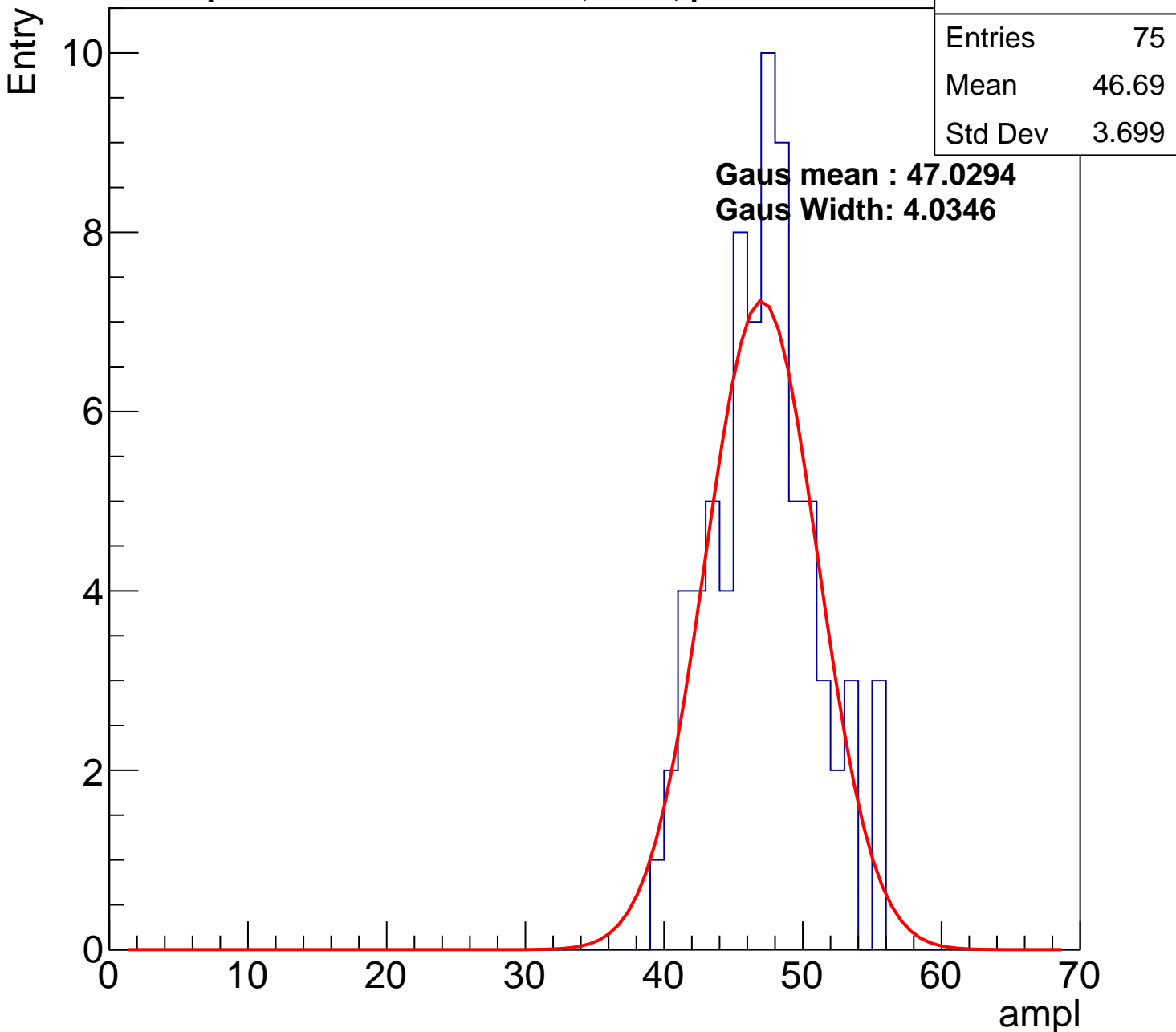
Gaus Width: 4.0346

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

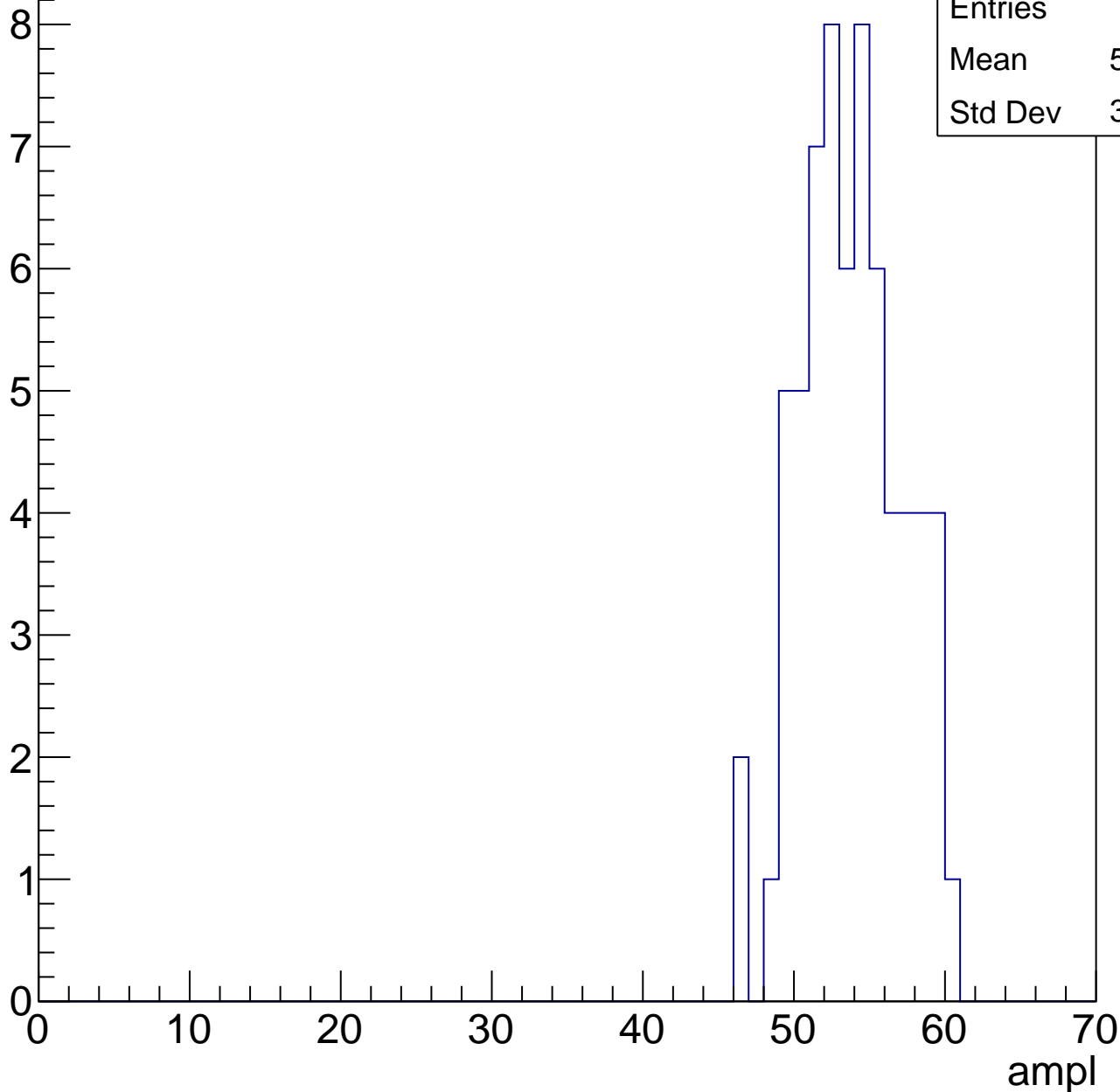


B0L001S, U25-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	53.35
Std Dev	3.284

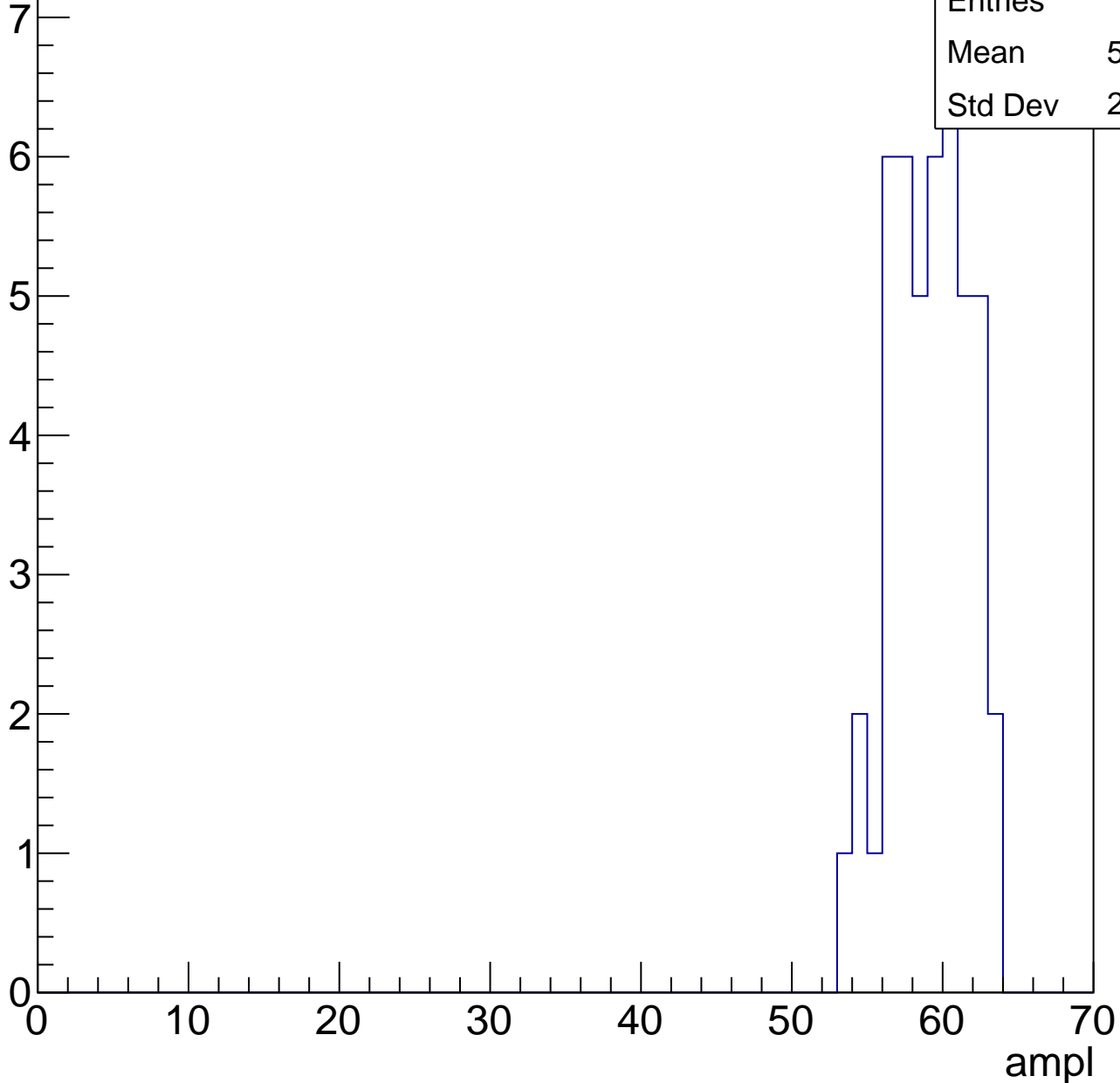


B0L001S, U25-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.67
Std Dev	2.485

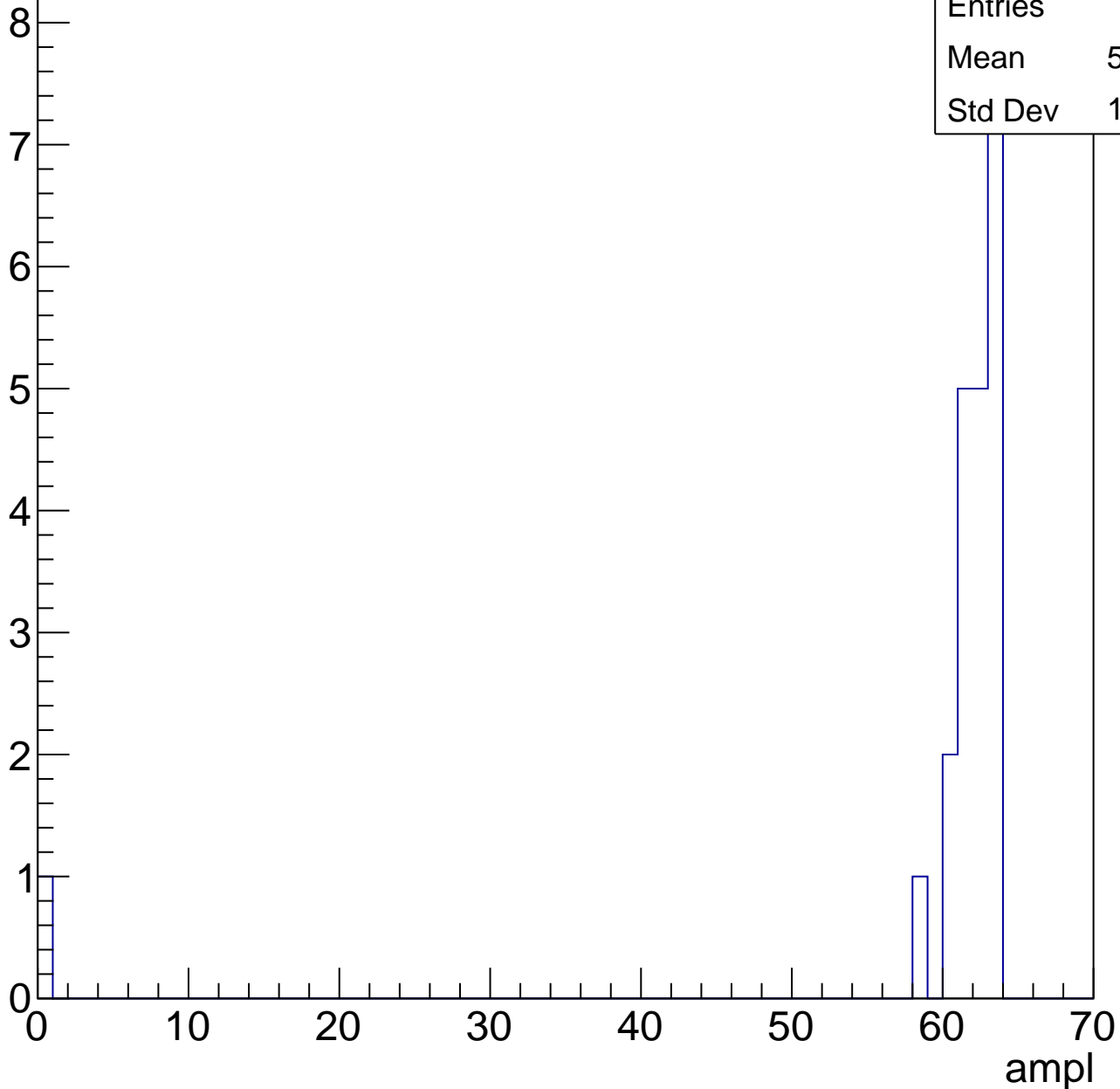


B0L001S, U25-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.95
Std Dev	12.93



B0L001S, U25-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch88, adc0

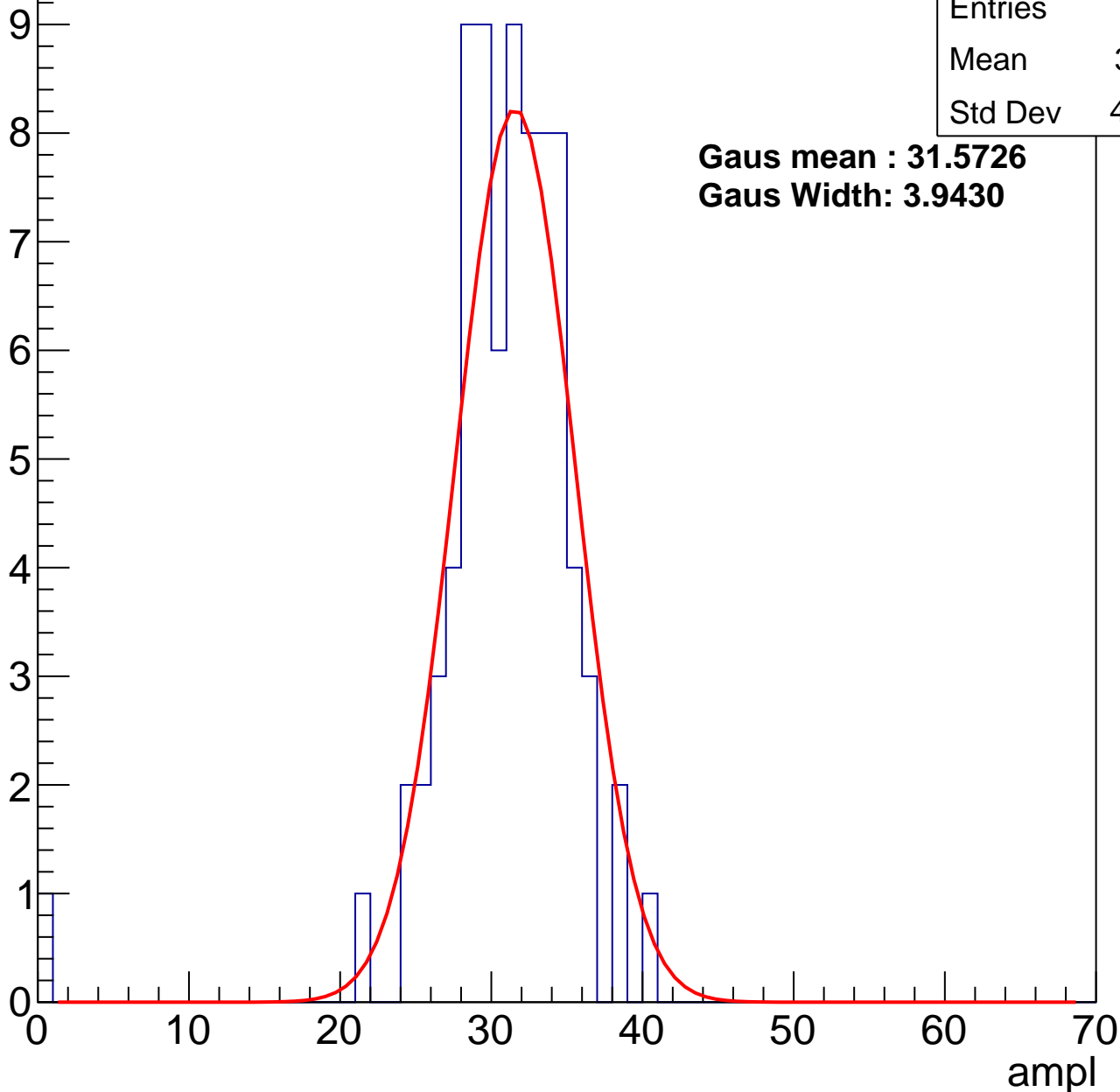
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.41
Std Dev	4.867

Gaus mean : 31.5726

Gaus Width: 3.9430



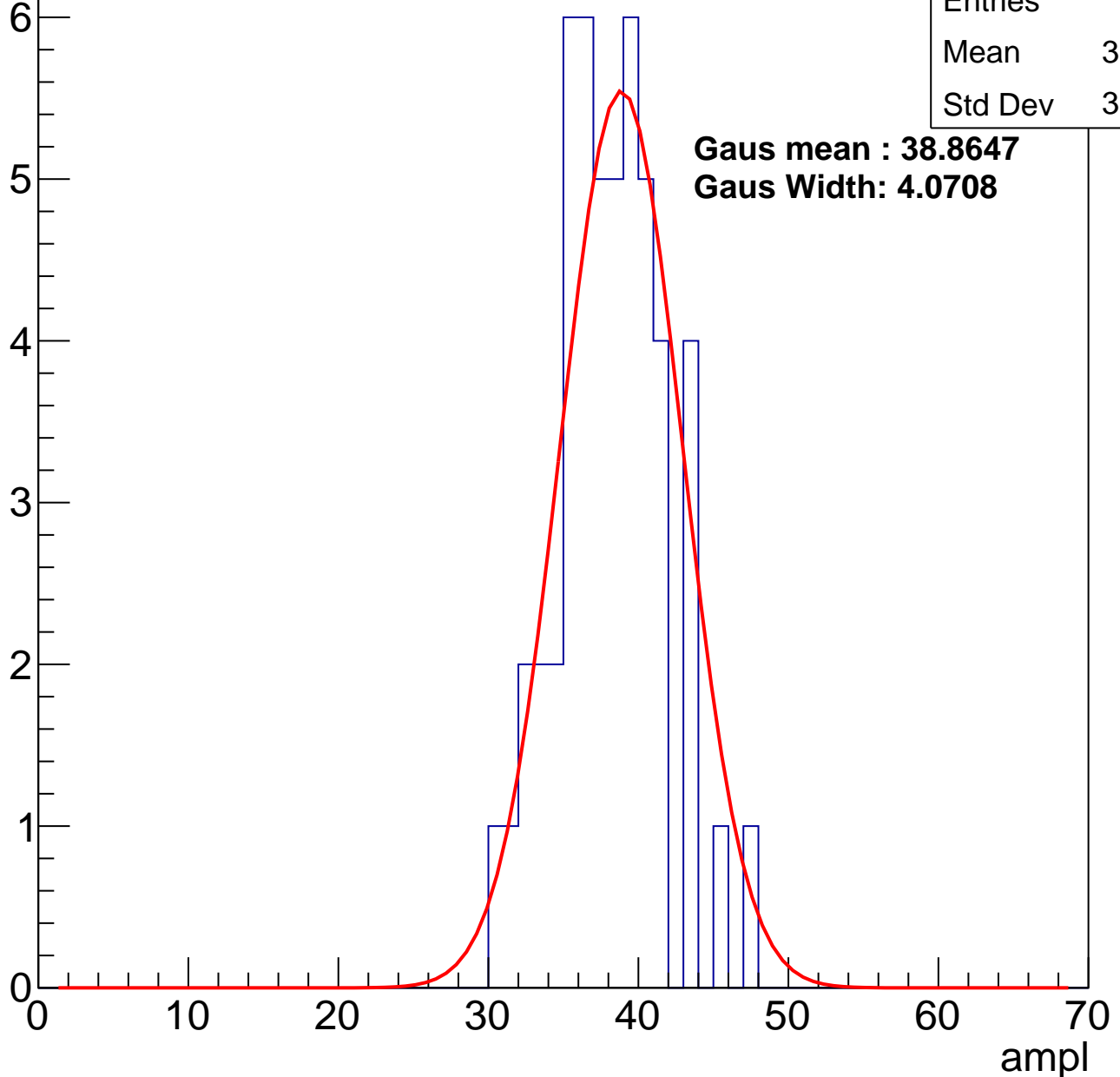
B0L001S, U25-ch88, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	37.69
Std Dev	3.534

Gaus mean : 38.8647
Gaus Width: 4.0708



B0L001S, U25-ch88, adc2

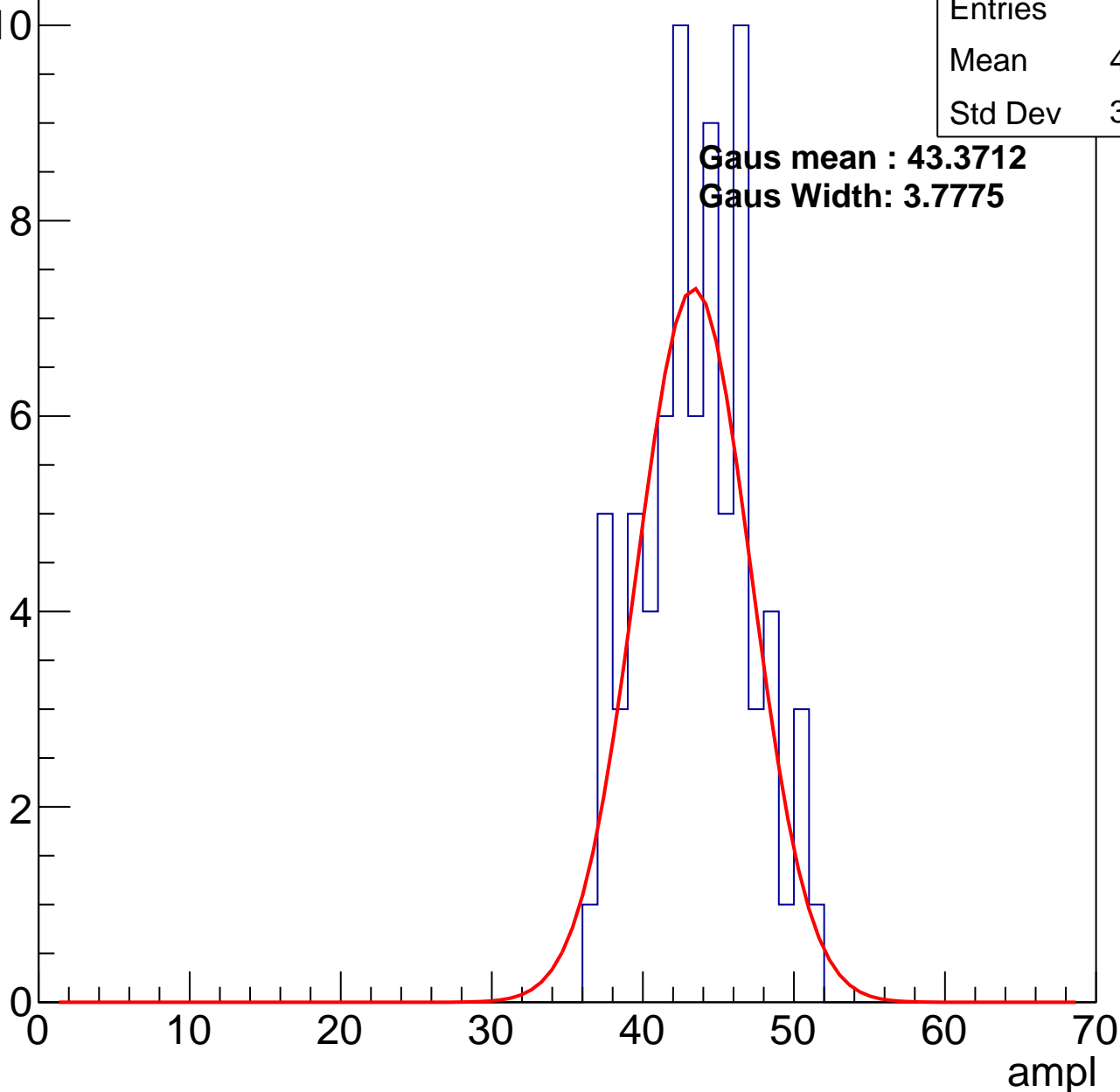
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	43.13
Std Dev	3.566

Gaus mean : 43.3712

Gaus Width: 3.7775

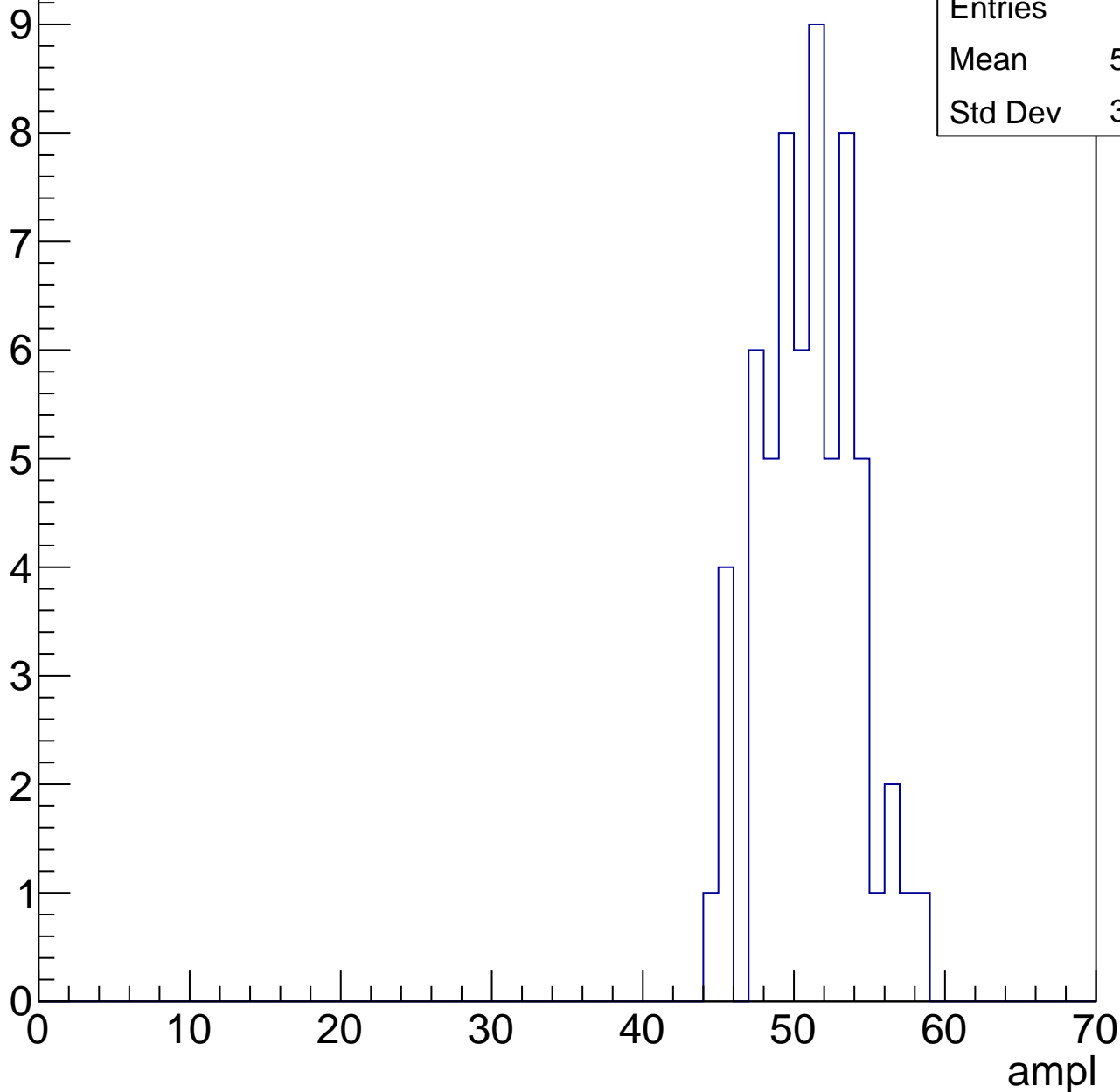


B0L001S, U25-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.53
Std Dev	3.089

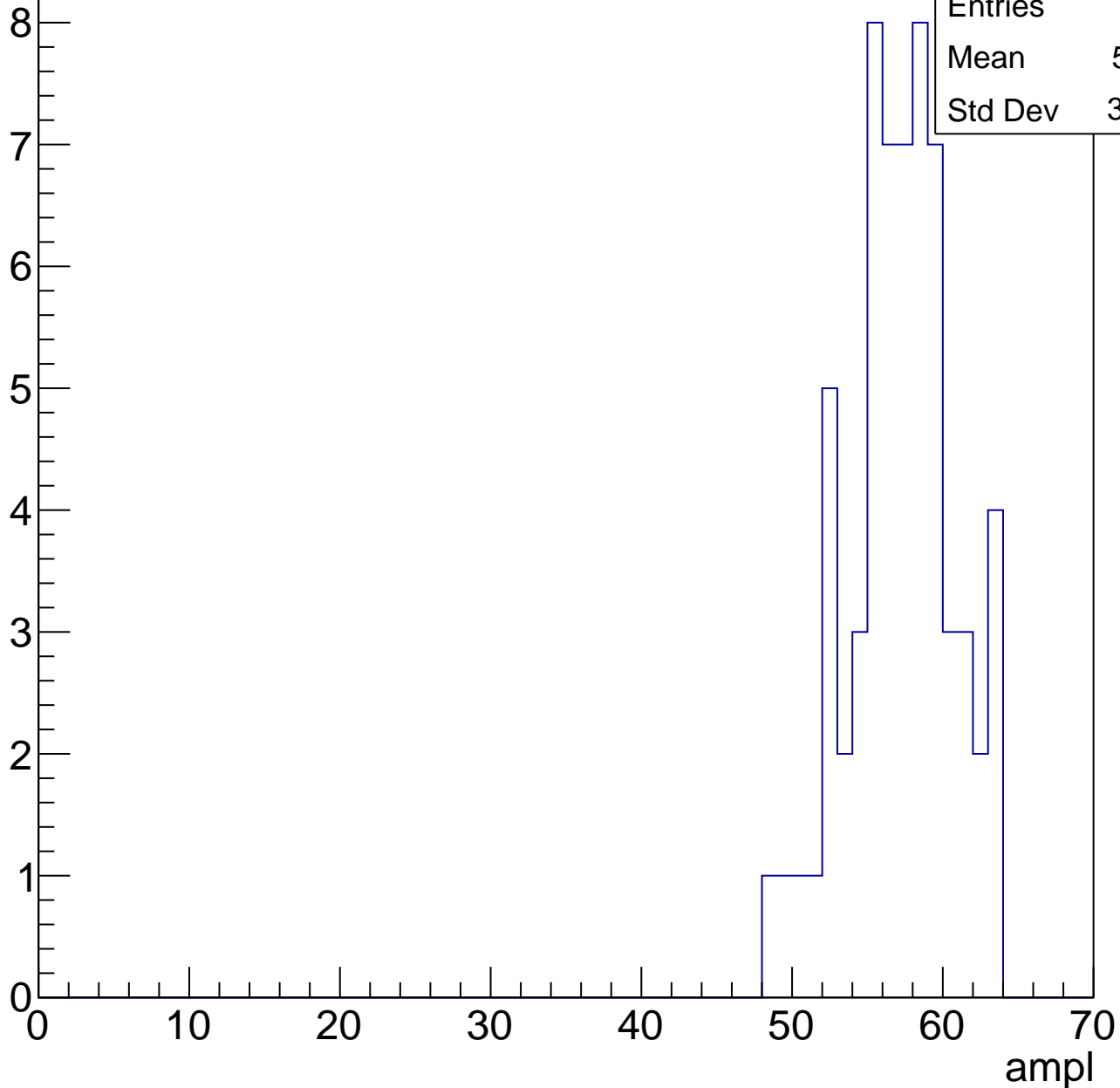


B0L001S, U25-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

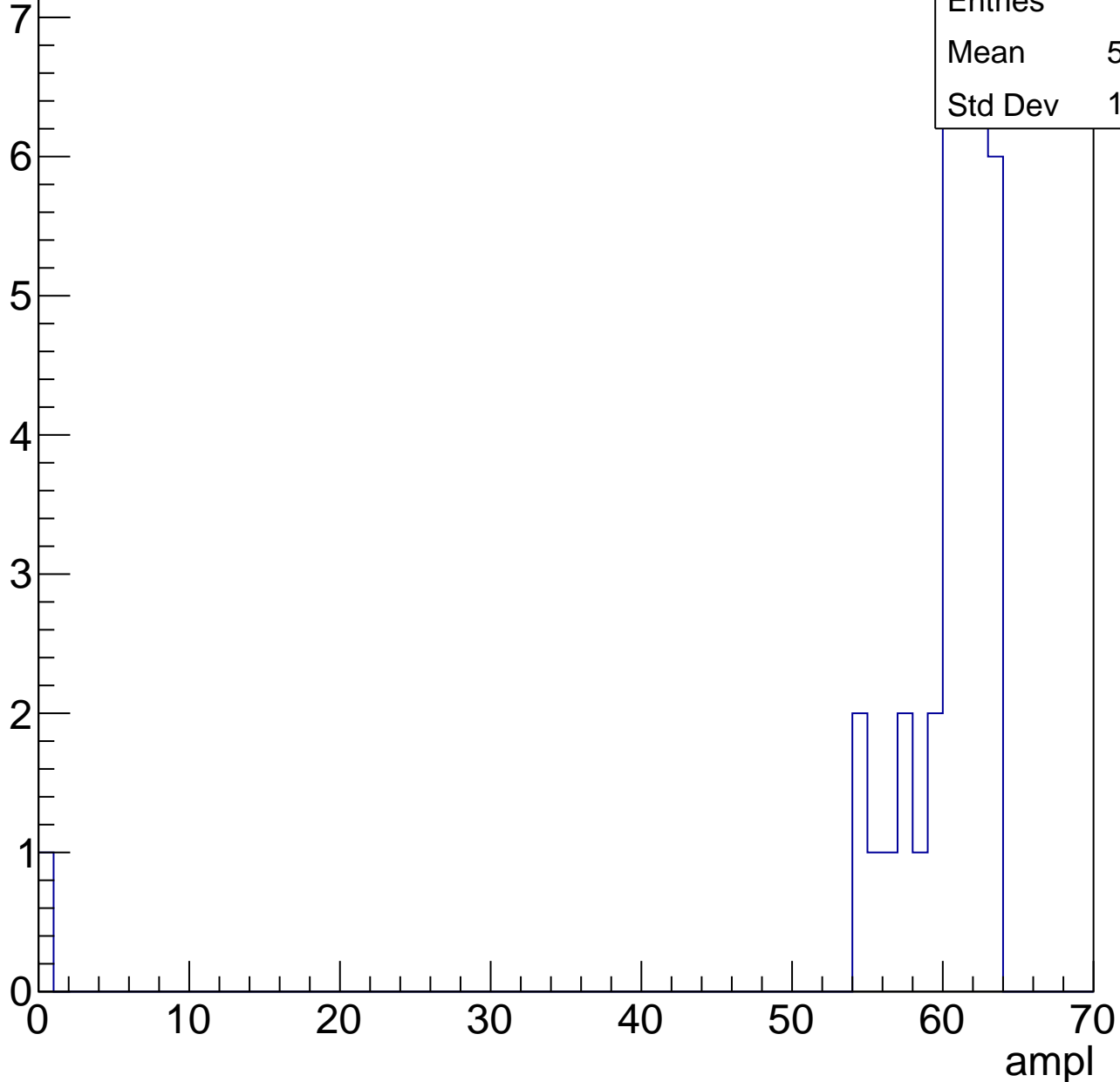
Entries	63
Mean	56.71
Std Dev	3.457



B0L001S, U25-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

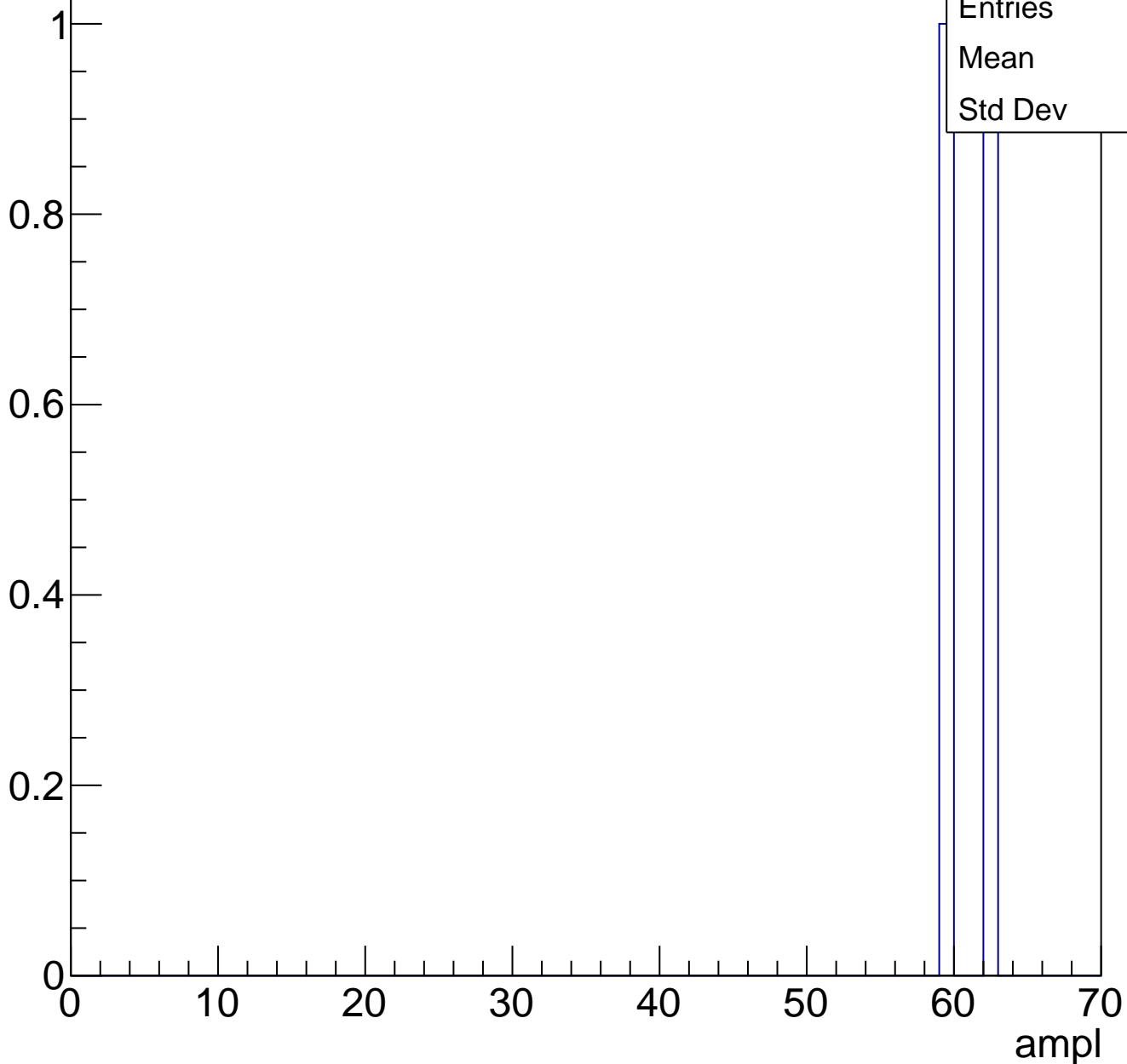


Entries	37
Mean	58.59
Std Dev	10.07

B0L001S, U25-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U25-ch89, adc0

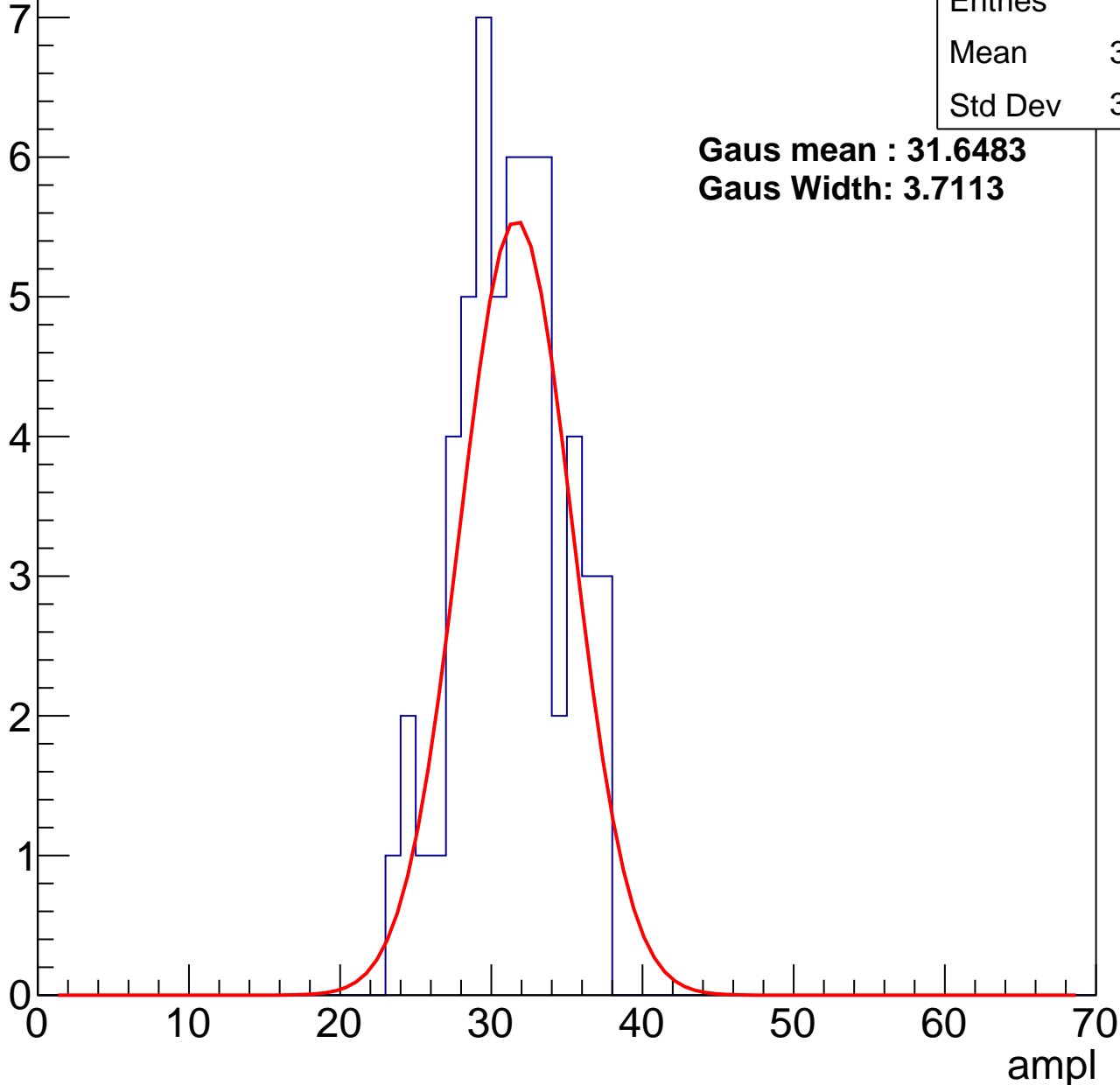
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.82
Std Dev	3.428

Gaus mean : 31.6483

Gaus Width: 3.7113



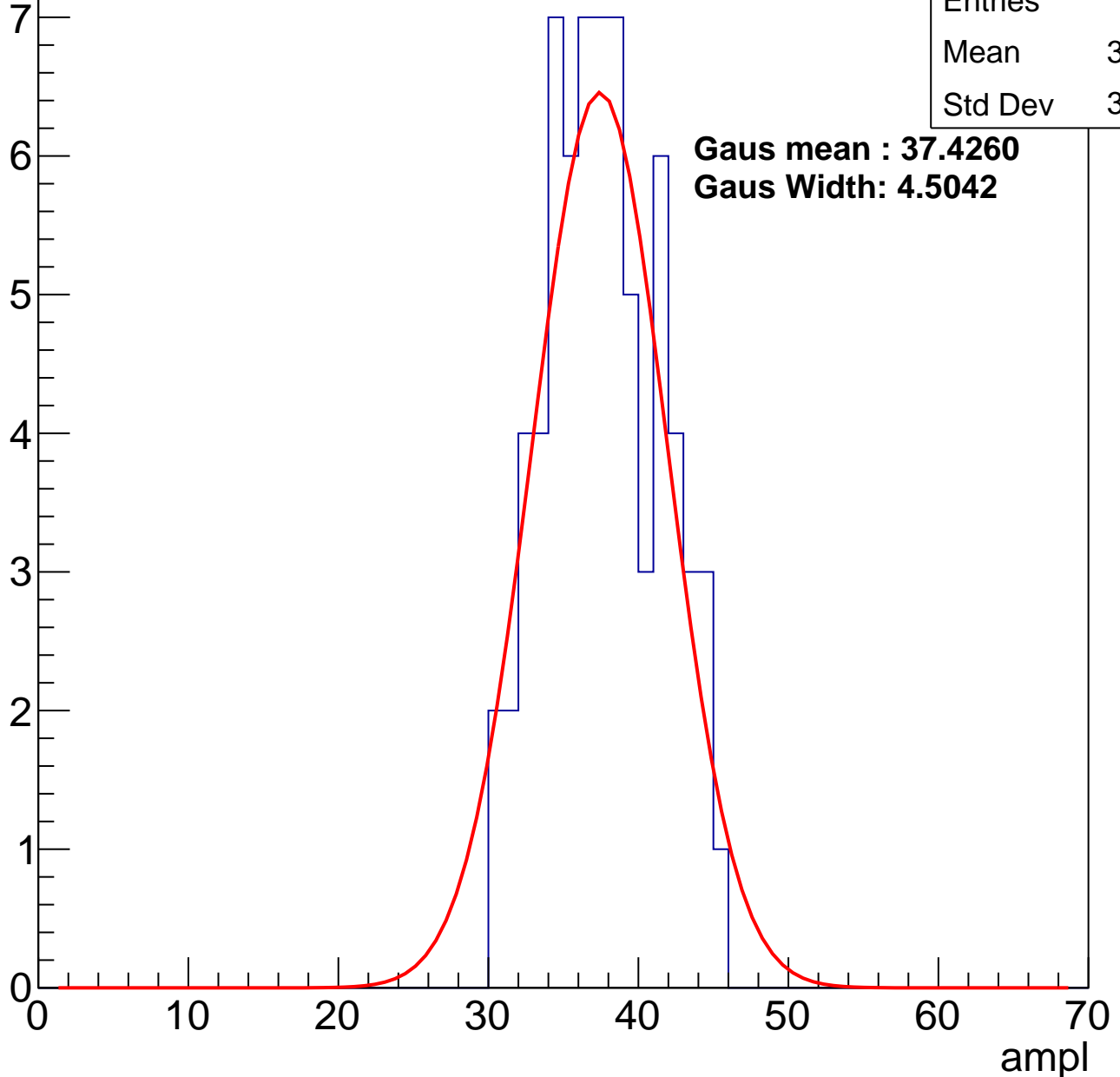
B0L001S, U25-ch89, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.21
Std Dev	3.741

Gaus mean : 37.4260
Gaus Width: 4.5042



B0L001S, U25-ch89, adc2

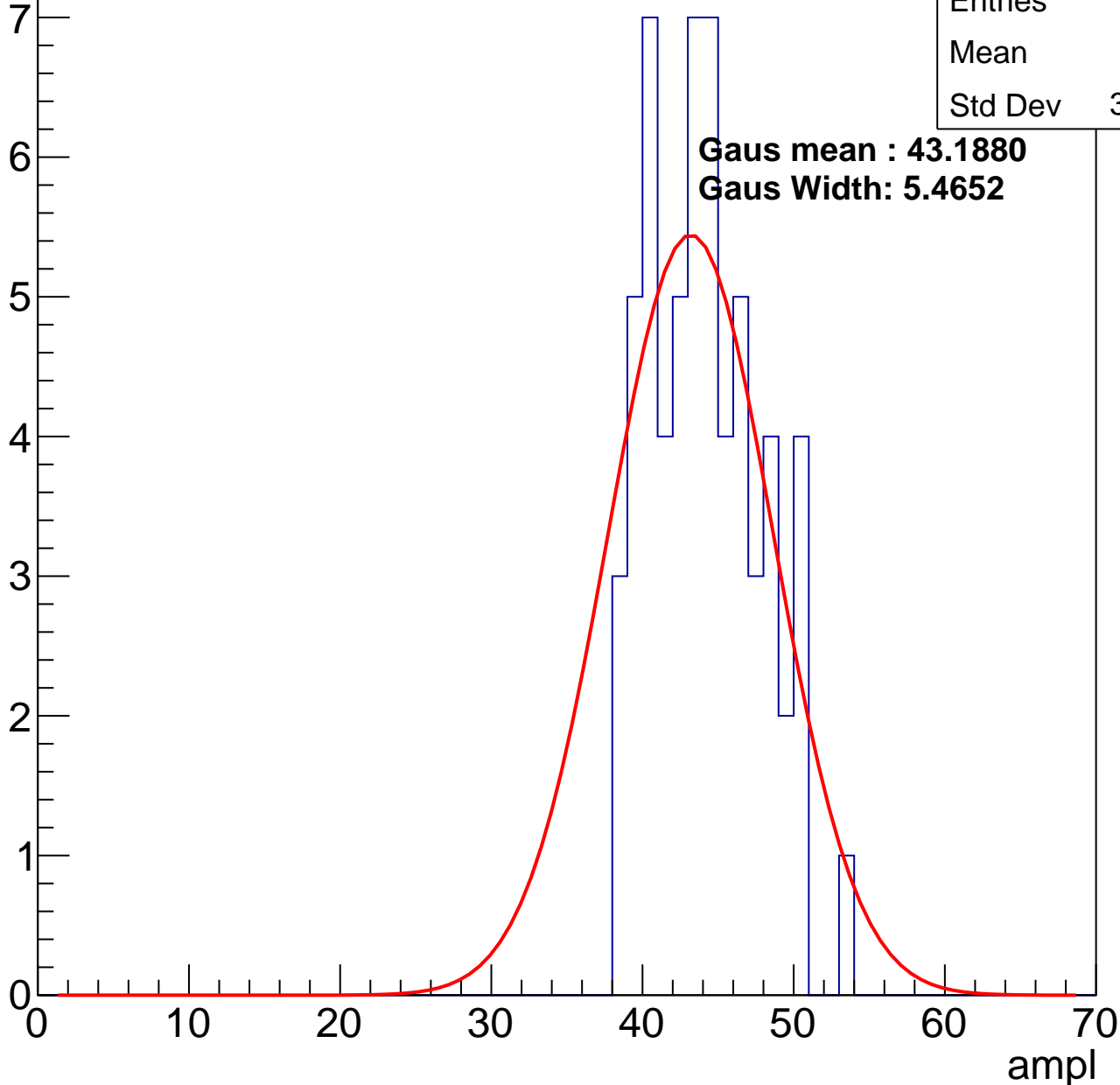
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.7
Std Dev	3.605

Gaus mean : 43.1880

Gaus Width: 5.4652



B0L001S, U25-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	50.36
Std Dev	3.44

Entry

10

8

6

4

2

0

0

10

20

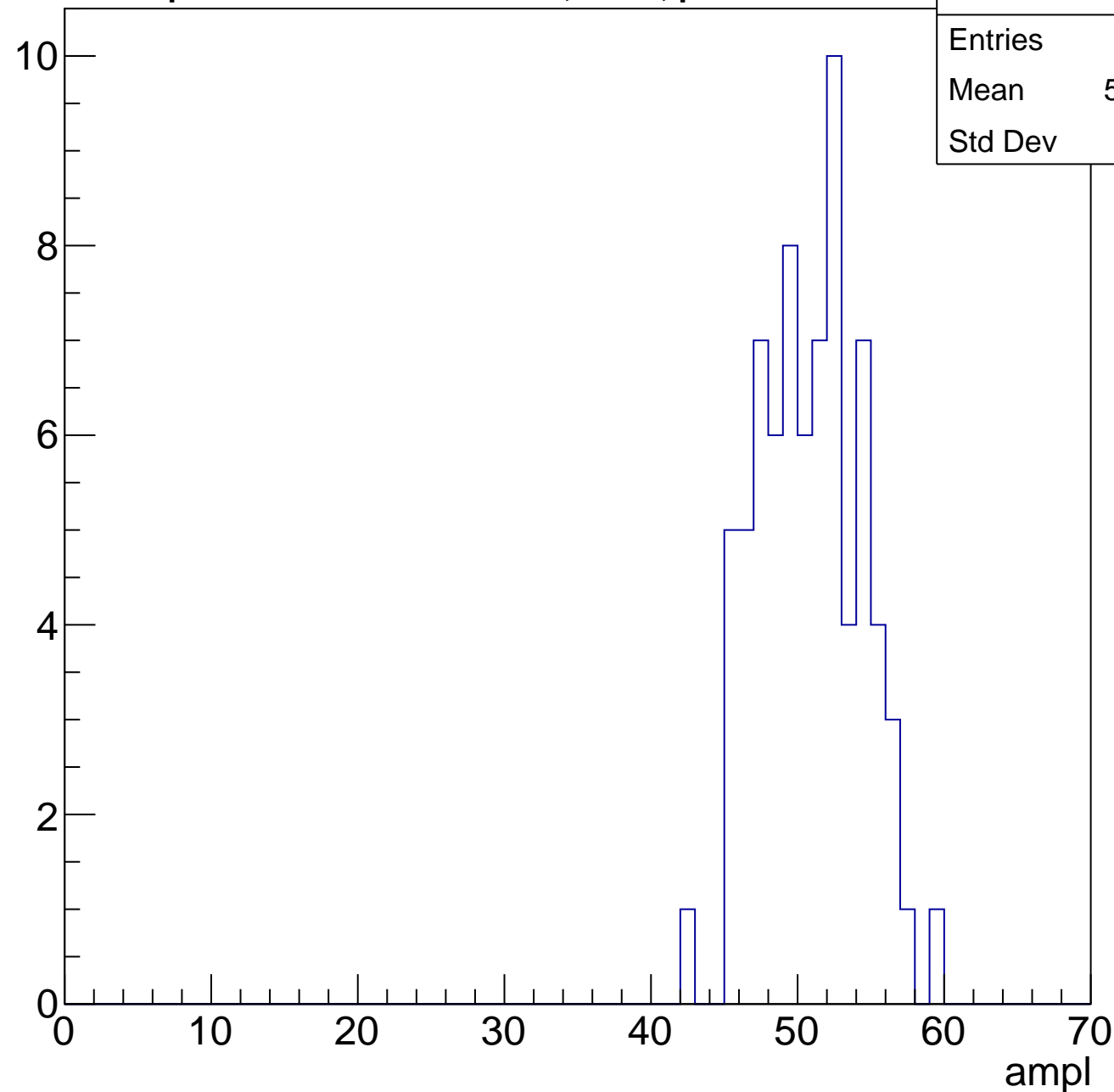
30

40

50

60

ampl

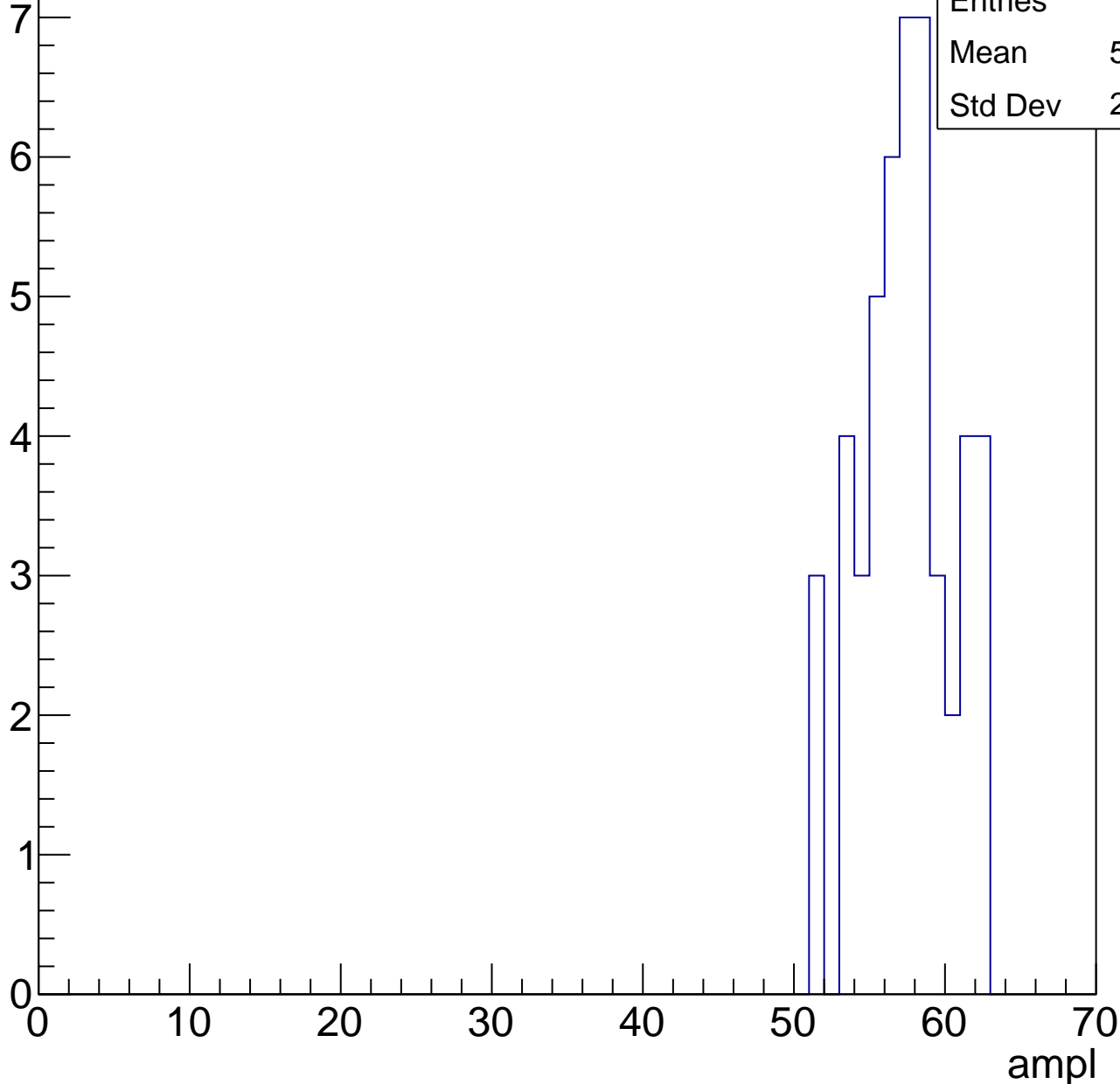


B0L001S, U25-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.92
Std Dev	2.978

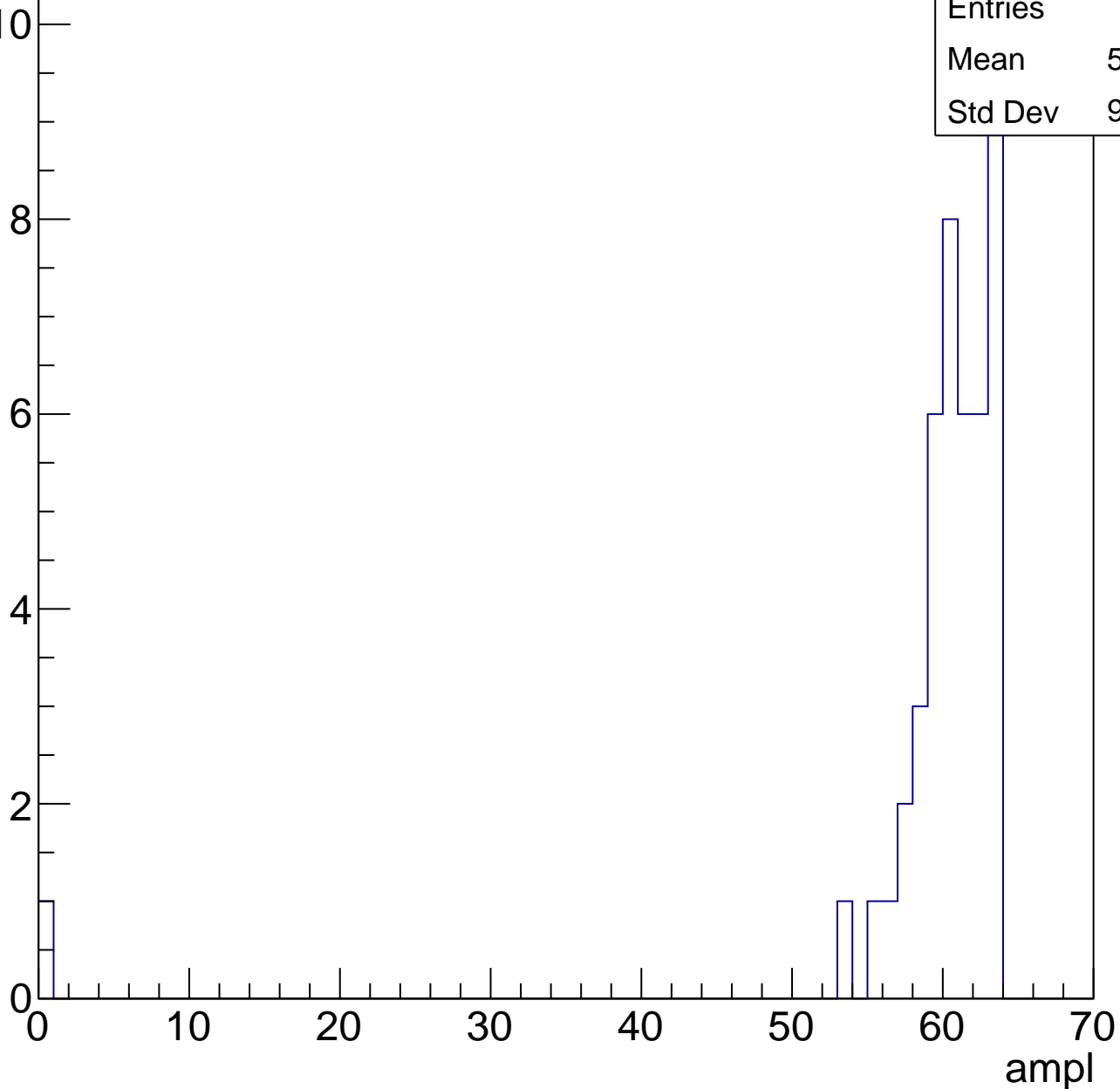


B0L001S, U25-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.98
Std Dev	9.188



B0L001S, U25-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

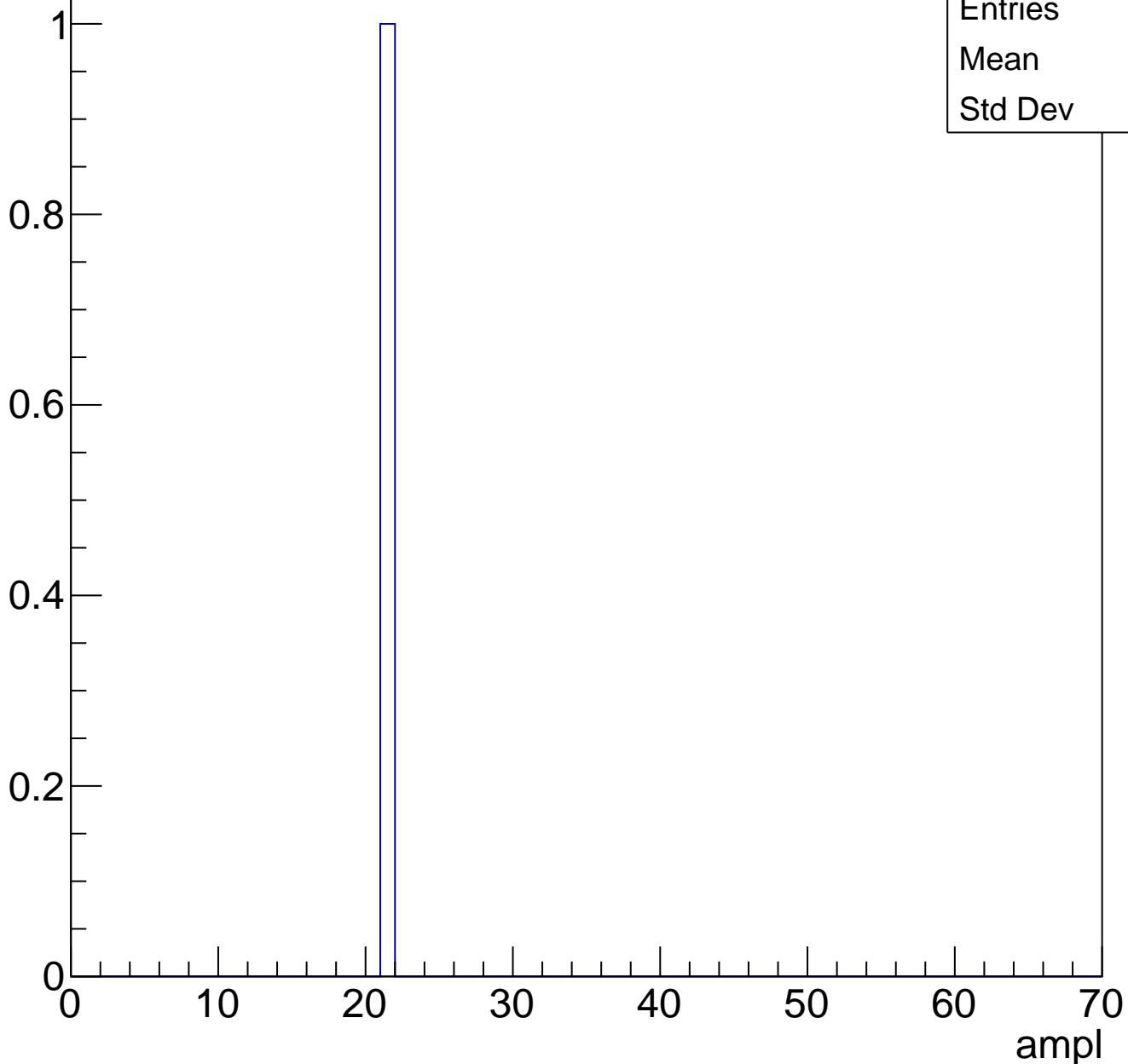
Std Dev

0

B0L001S, U25-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	21
Std Dev	0

B0L001S, U25-ch90, adc0

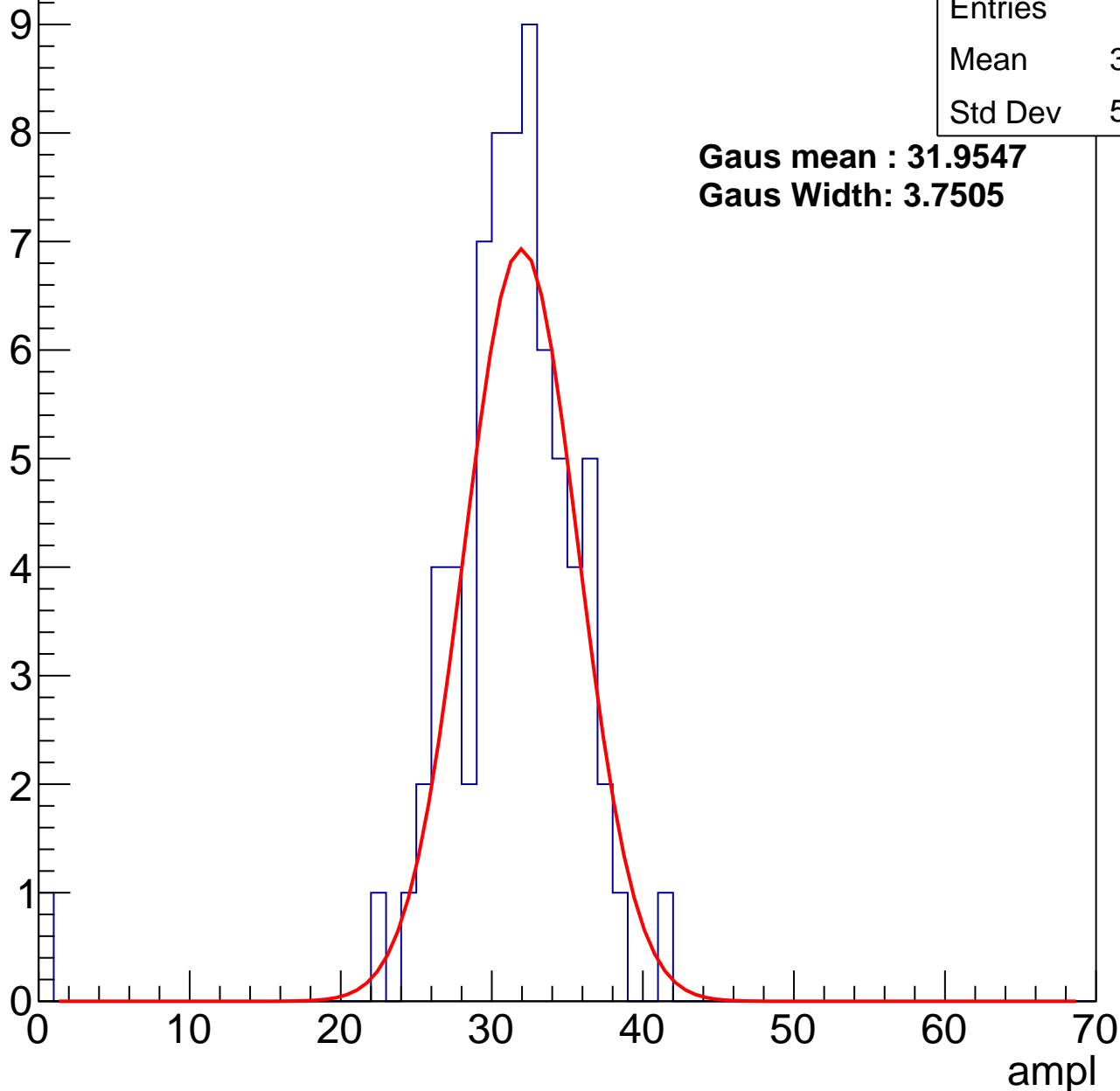
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	30.76
Std Dev	5.125

Gaus mean : 31.9547

Gaus Width: 3.7505



B0L001S, U25-ch90, adc1

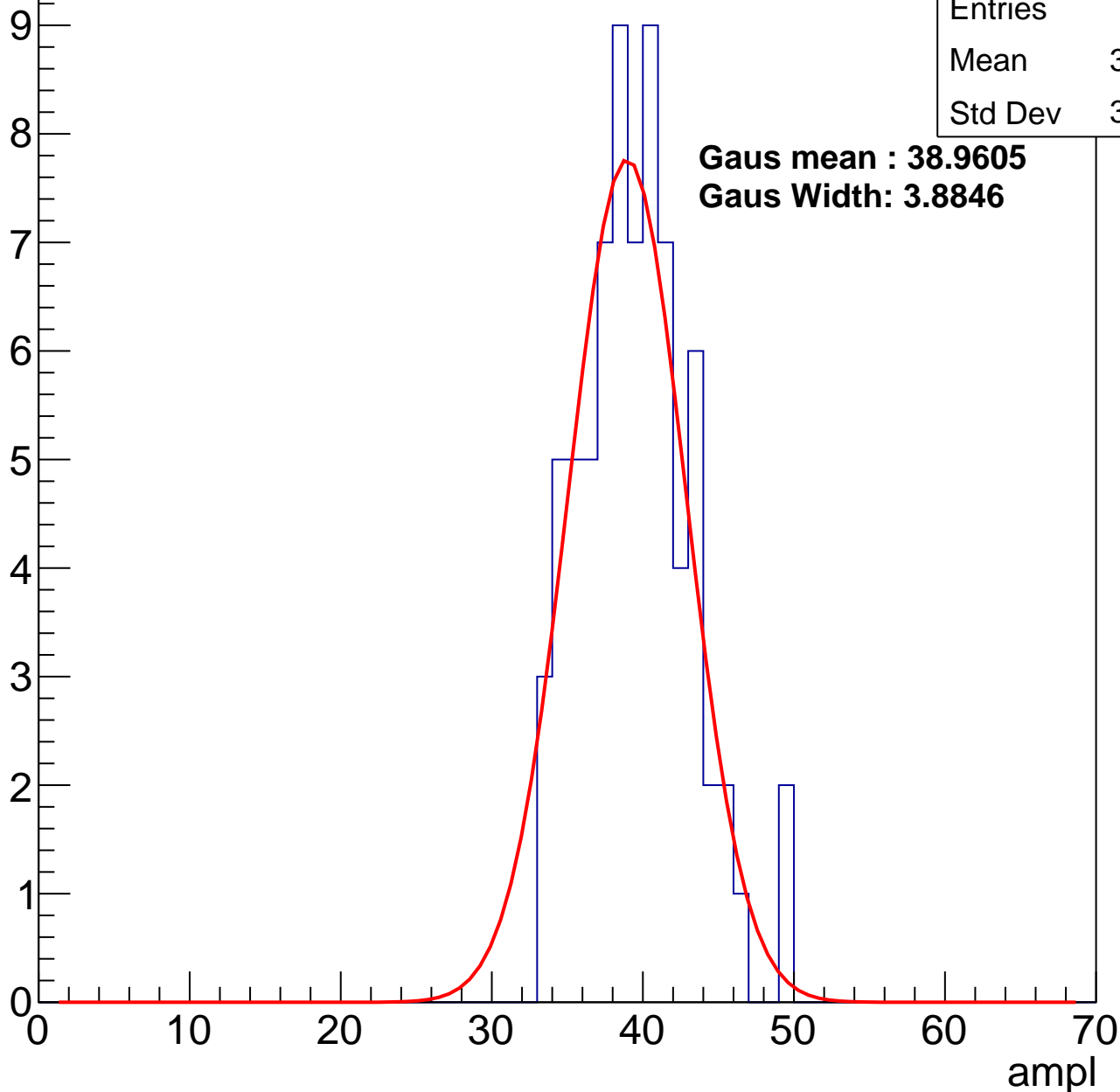
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	39.09
Std Dev	3.553

Gaus mean : 38.9605

Gaus Width: 3.8846



B0L001S, U25-ch90, adc2

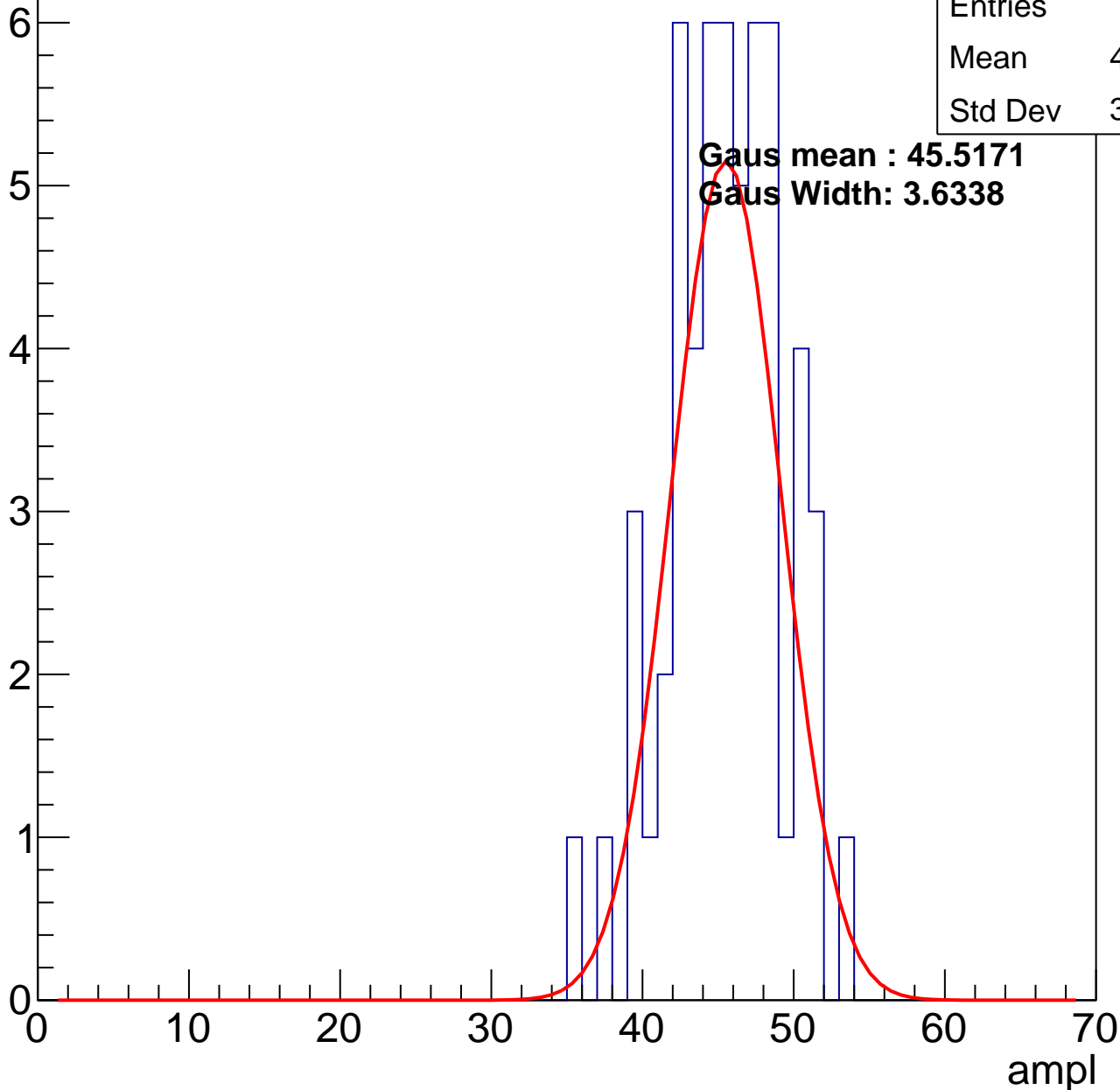
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	45.07
Std Dev	3.722

Gaus mean : 45.5171

Gaus Width: 3.6338

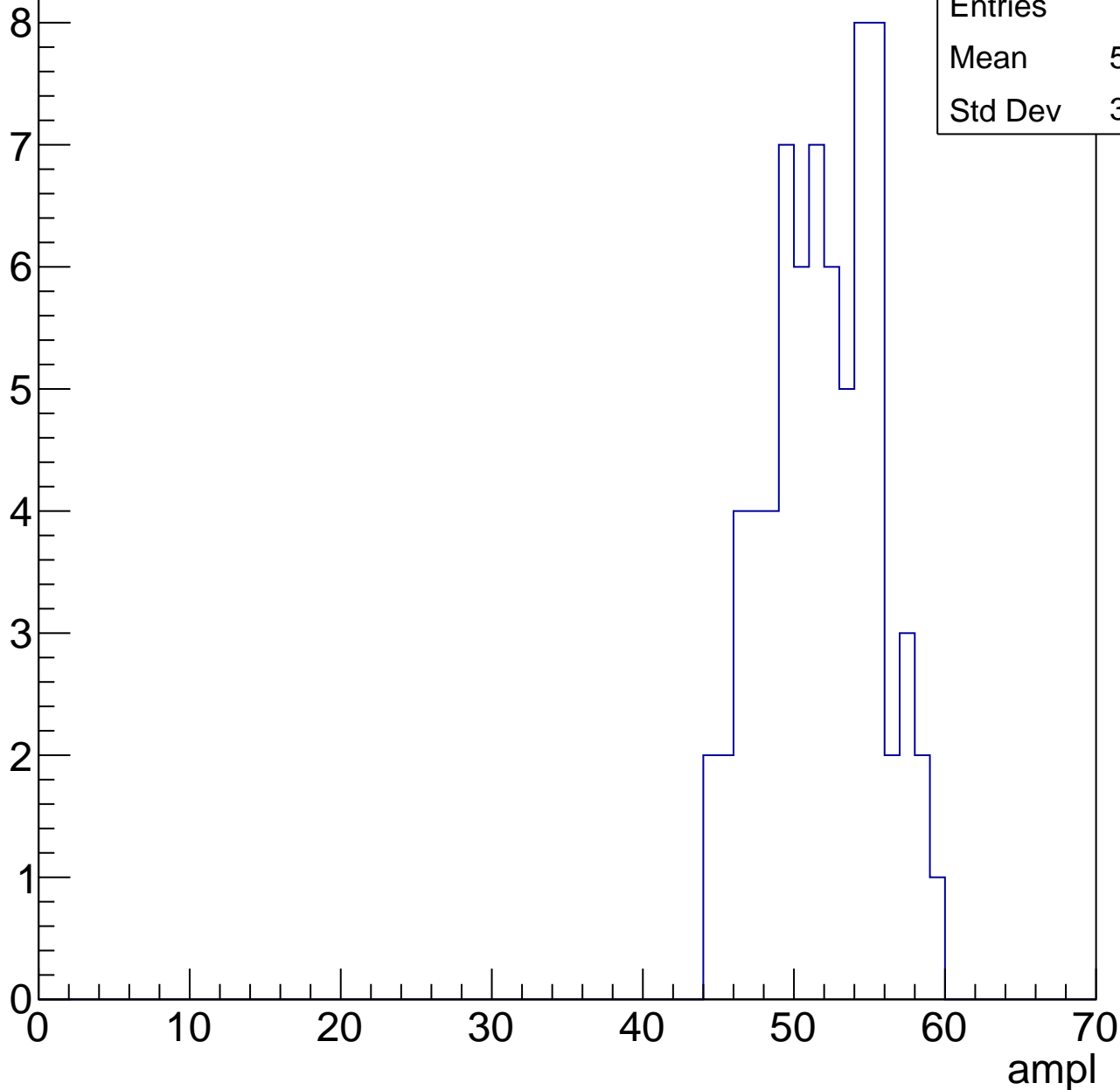


B0L001S, U25-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

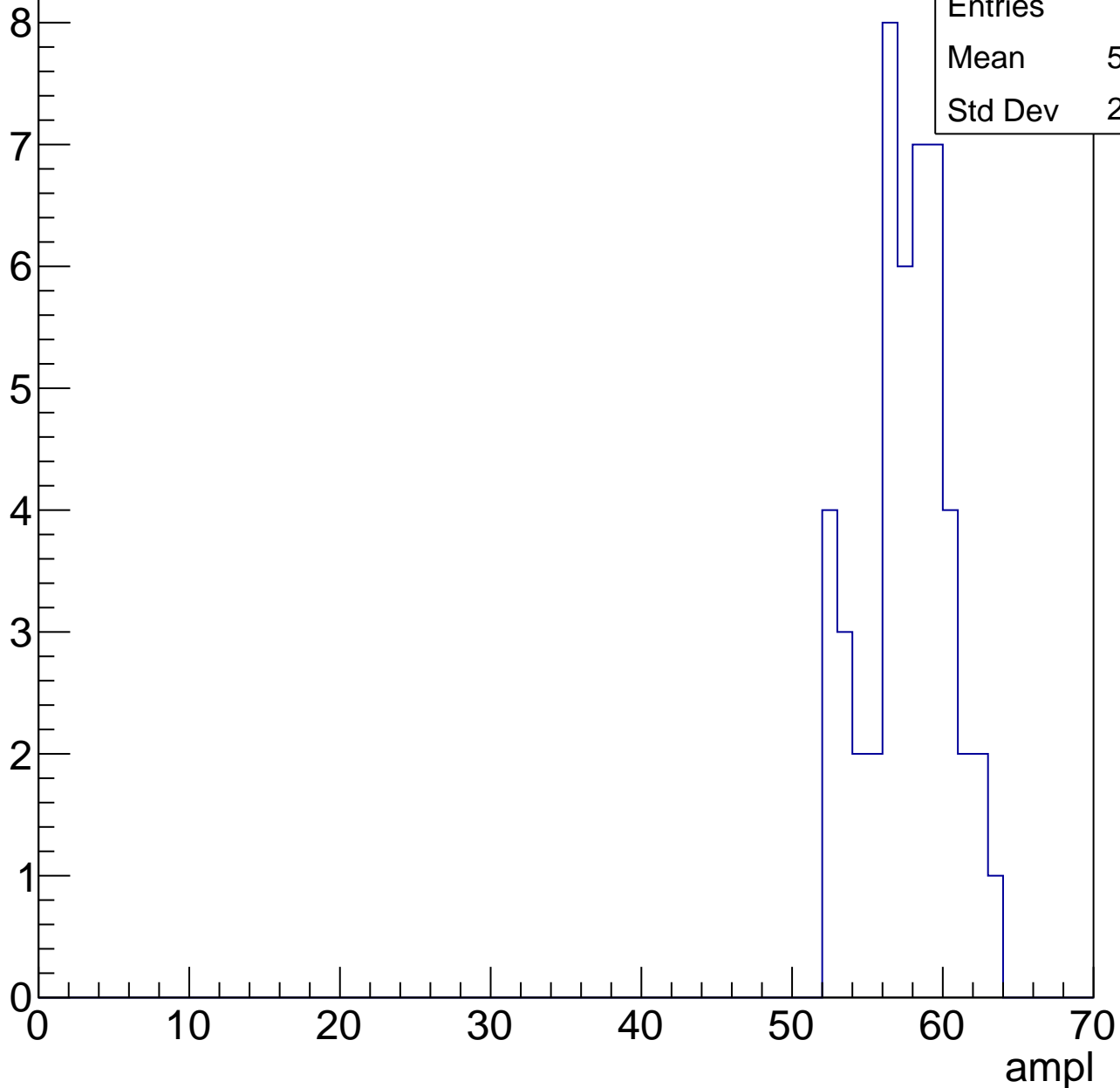
Entries	71
Mean	51.39
Std Dev	3.637



B0L001S, U25-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

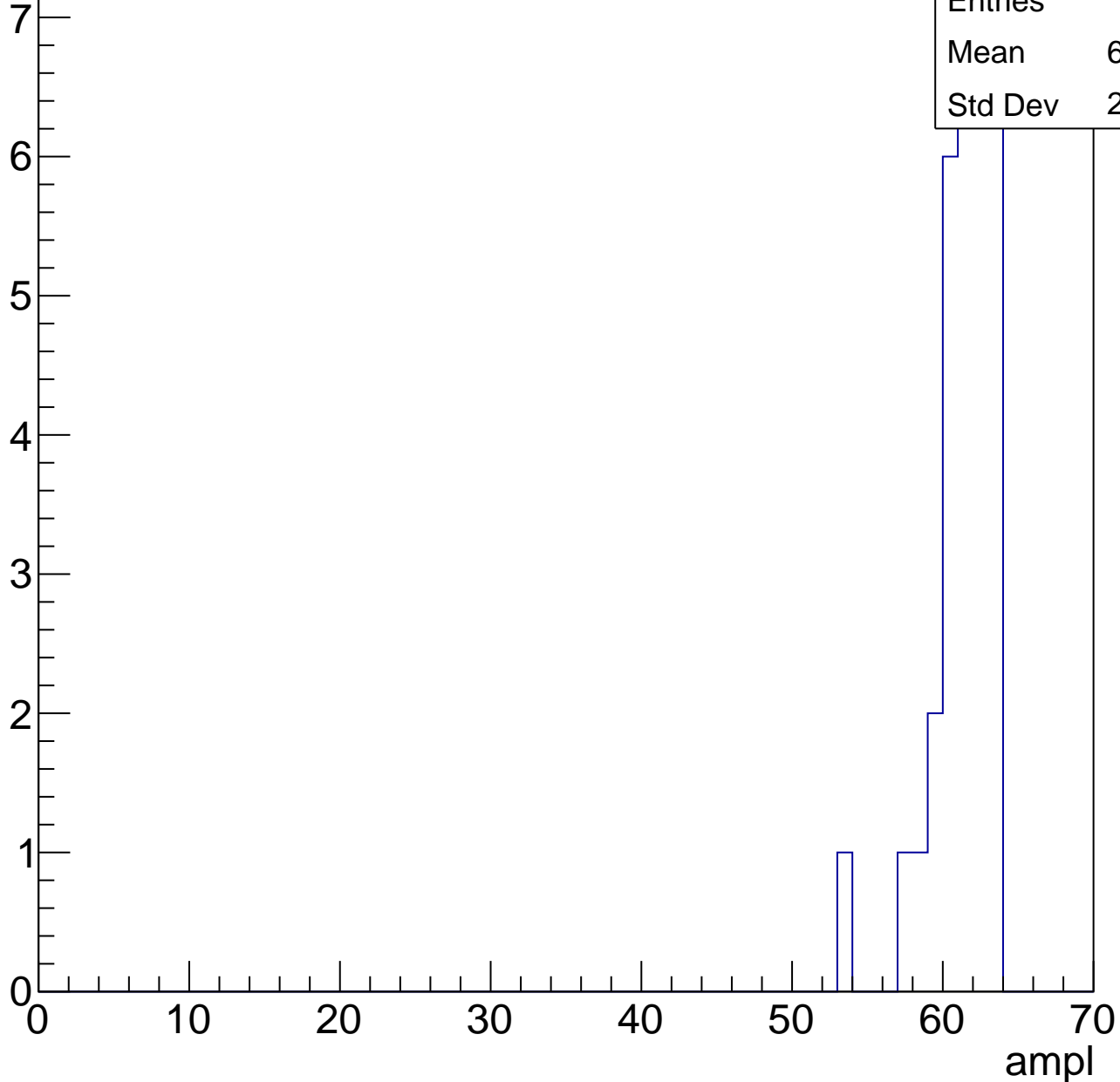


Entries	48
Mean	57.15
Std Dev	2.776

B0L001S, U25-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch91, adc0

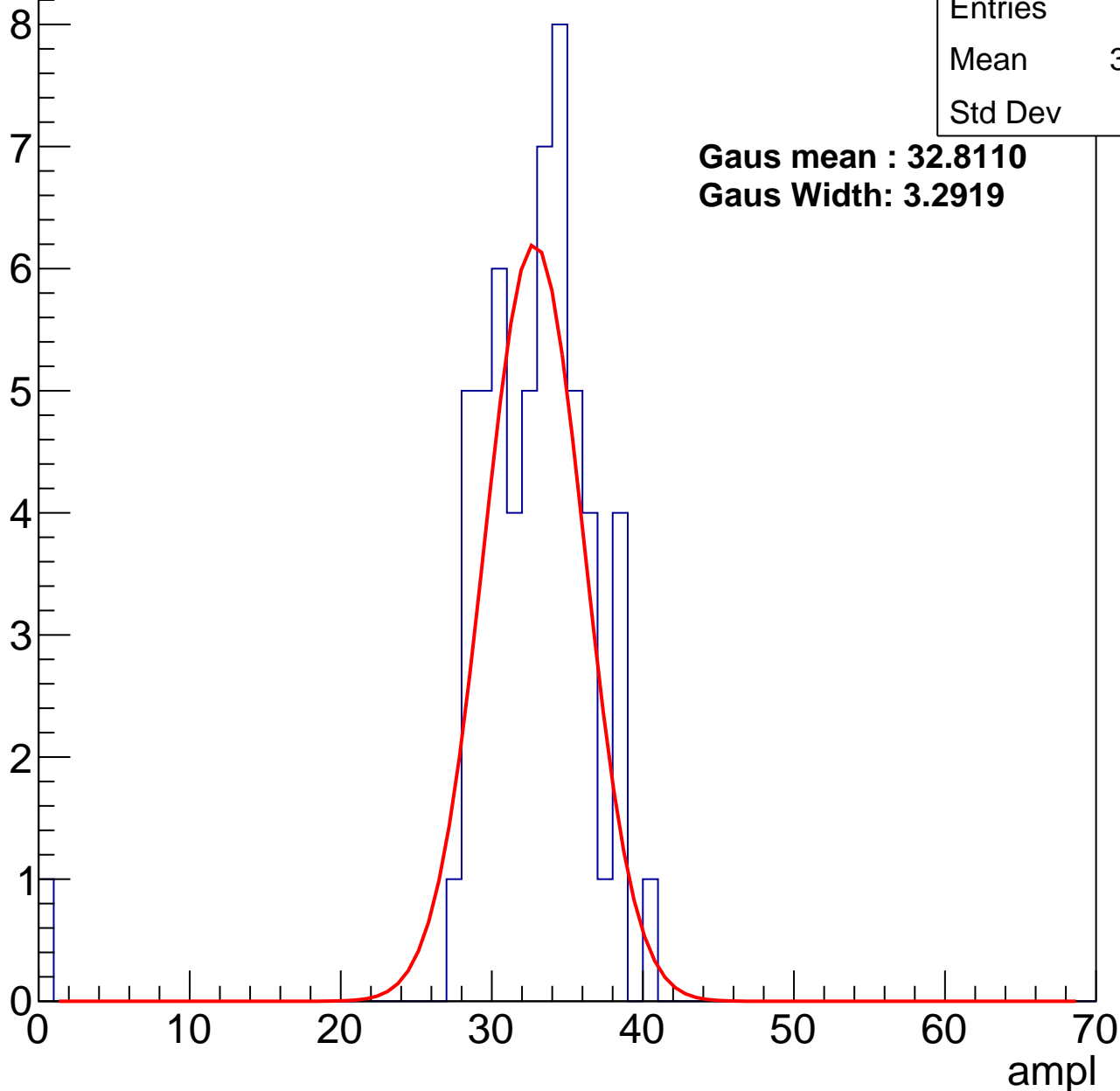
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	32.05
Std Dev	5.27

Gaus mean : 32.8110

Gaus Width: 3.2919



B0L001S, U25-ch91, adc1

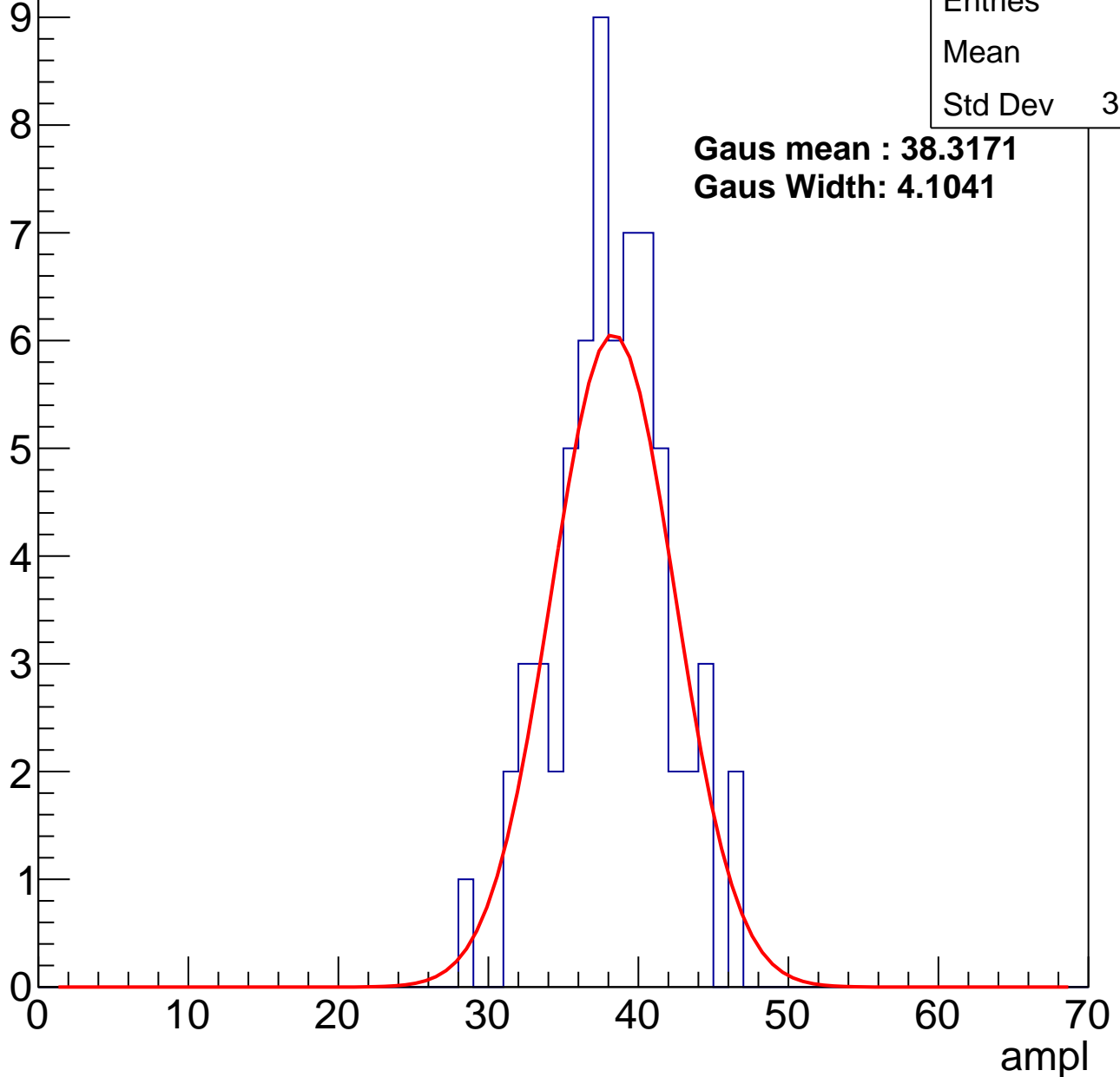
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.8
Std Dev	3.693

Gaus mean : 38.3171

Gaus Width: 4.1041



B0L001S, U25-ch91, adc2

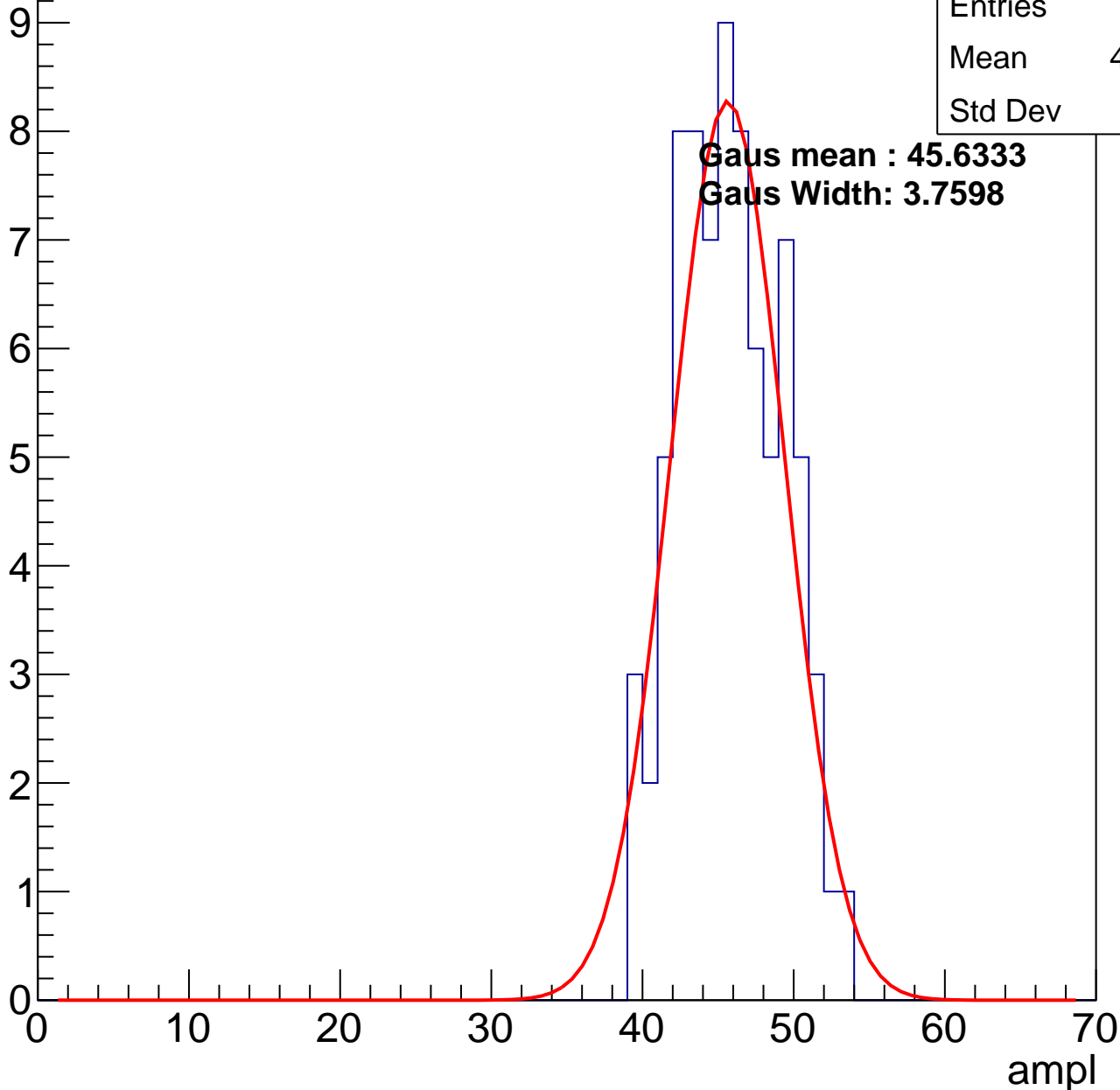
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	45.33
Std Dev	3.35

Gaus mean : 45.6333

Gaus Width: 3.7598

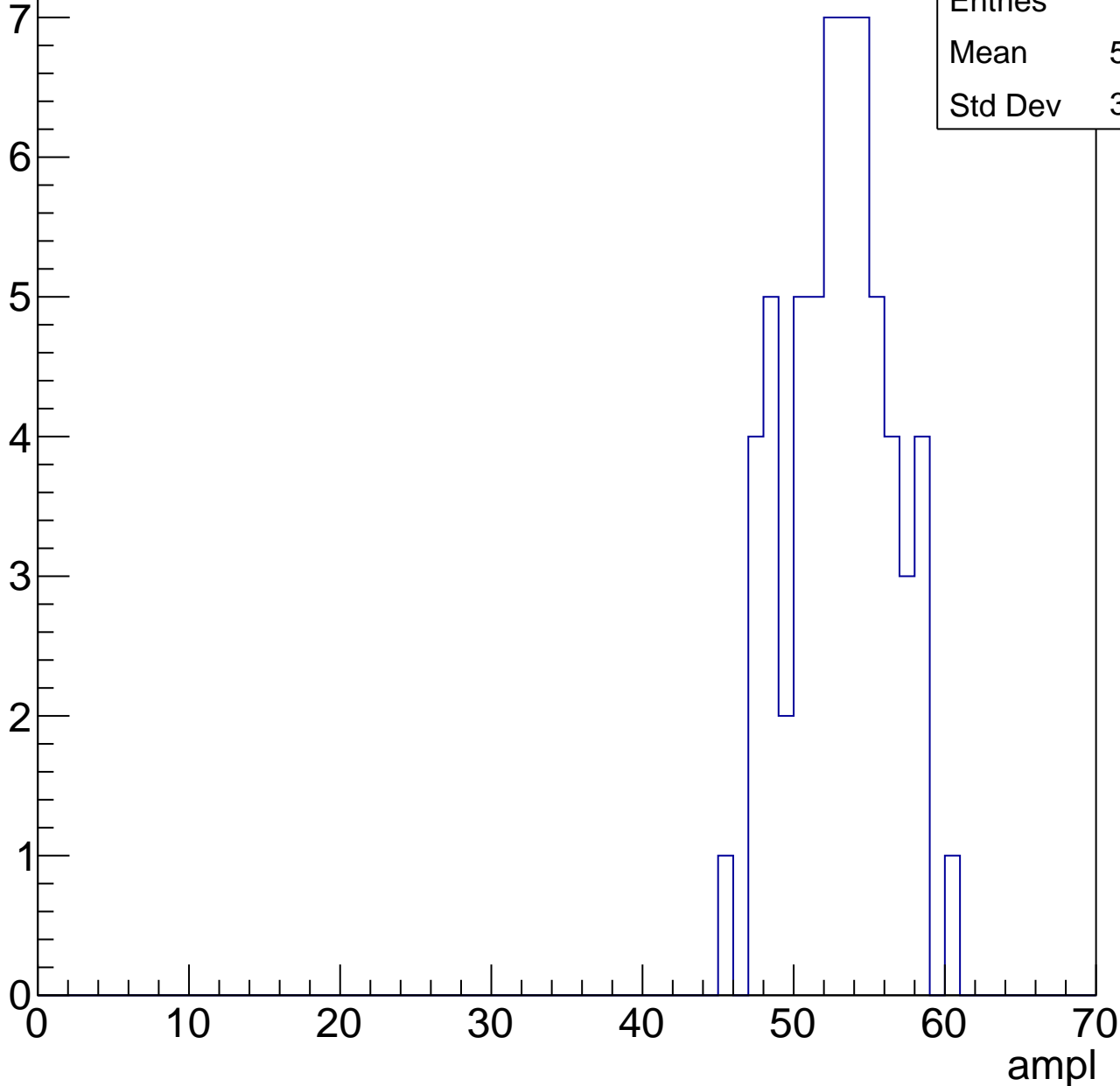


B0L001S, U25-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	52.52
Std Dev	3.374

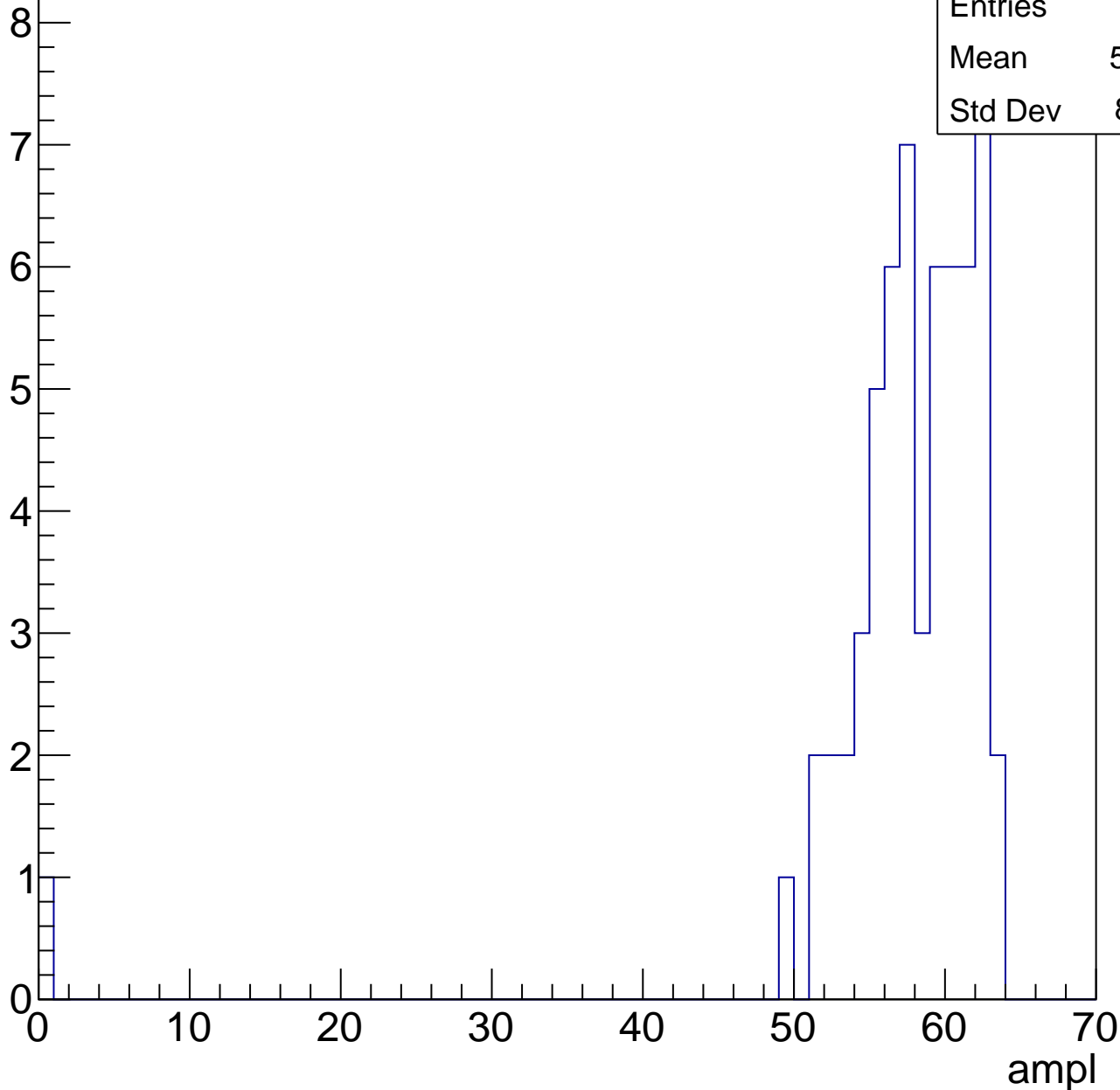


B0L001S, U25-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.82
Std Dev	8.131

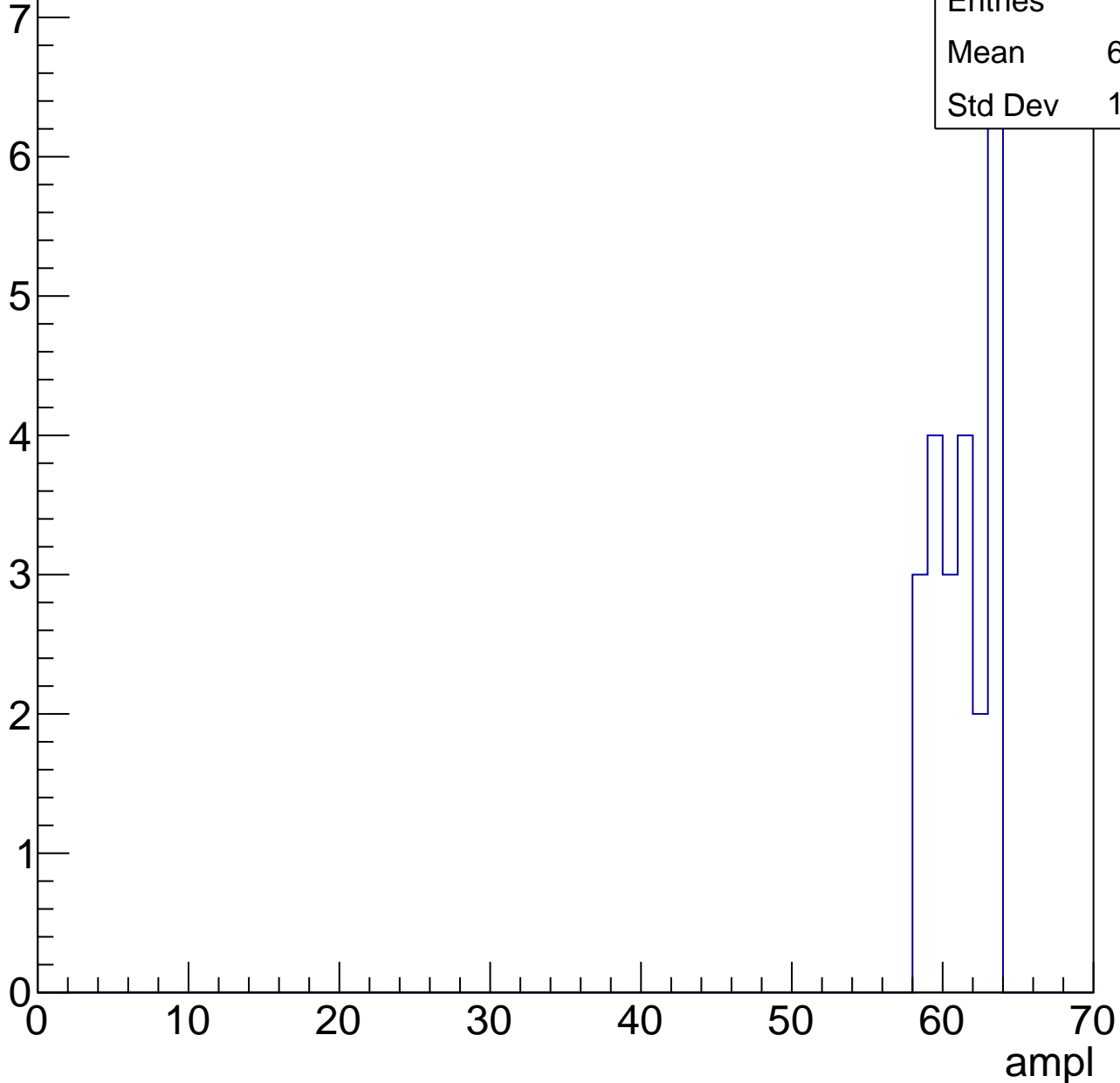


B0L001S, U25-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	60.83
Std Dev	1.809



B0L001S, U25-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch92, adc0

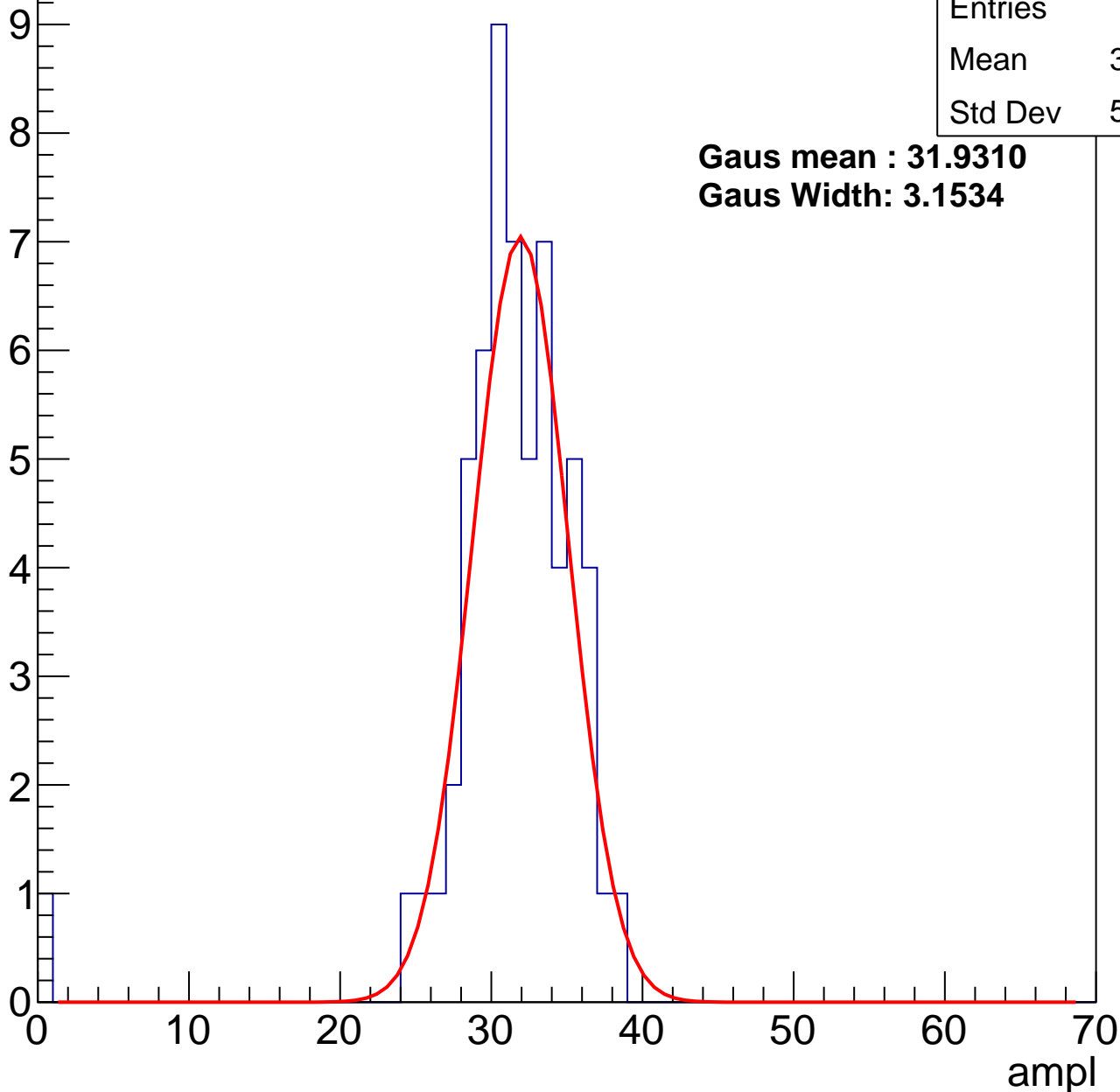
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.85
Std Dev	5.023

Gaus mean : 31.9310

Gaus Width: 3.1534



B0L001S, U25-ch92, adc1

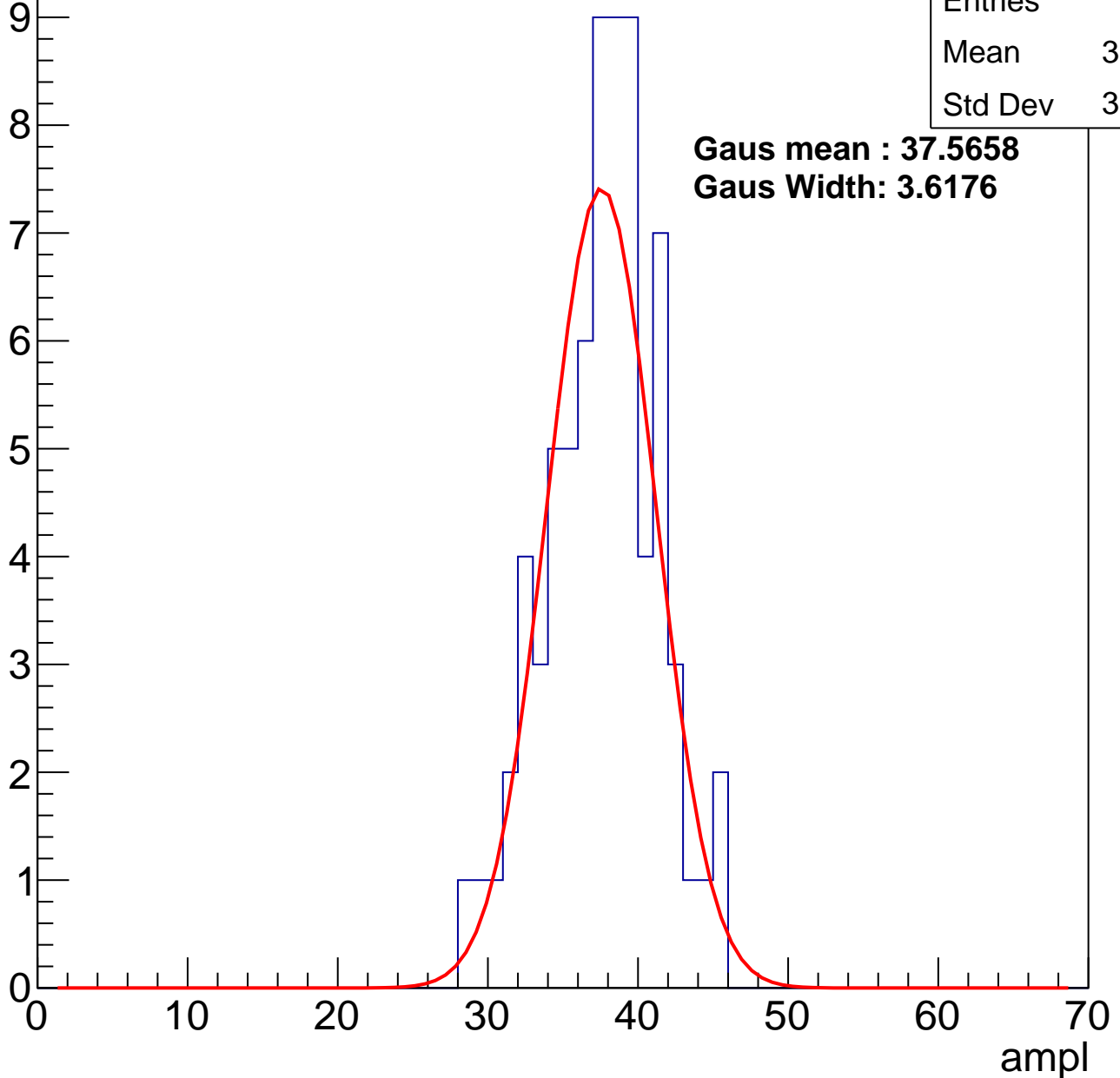
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.16
Std Dev	3.634

Gaus mean : 37.5658

Gaus Width: 3.6176



B0L001S, U25-ch92, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	44.71
Std Dev	3.436

Gaus mean : 45.1447

Gaus Width: 3.4574

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

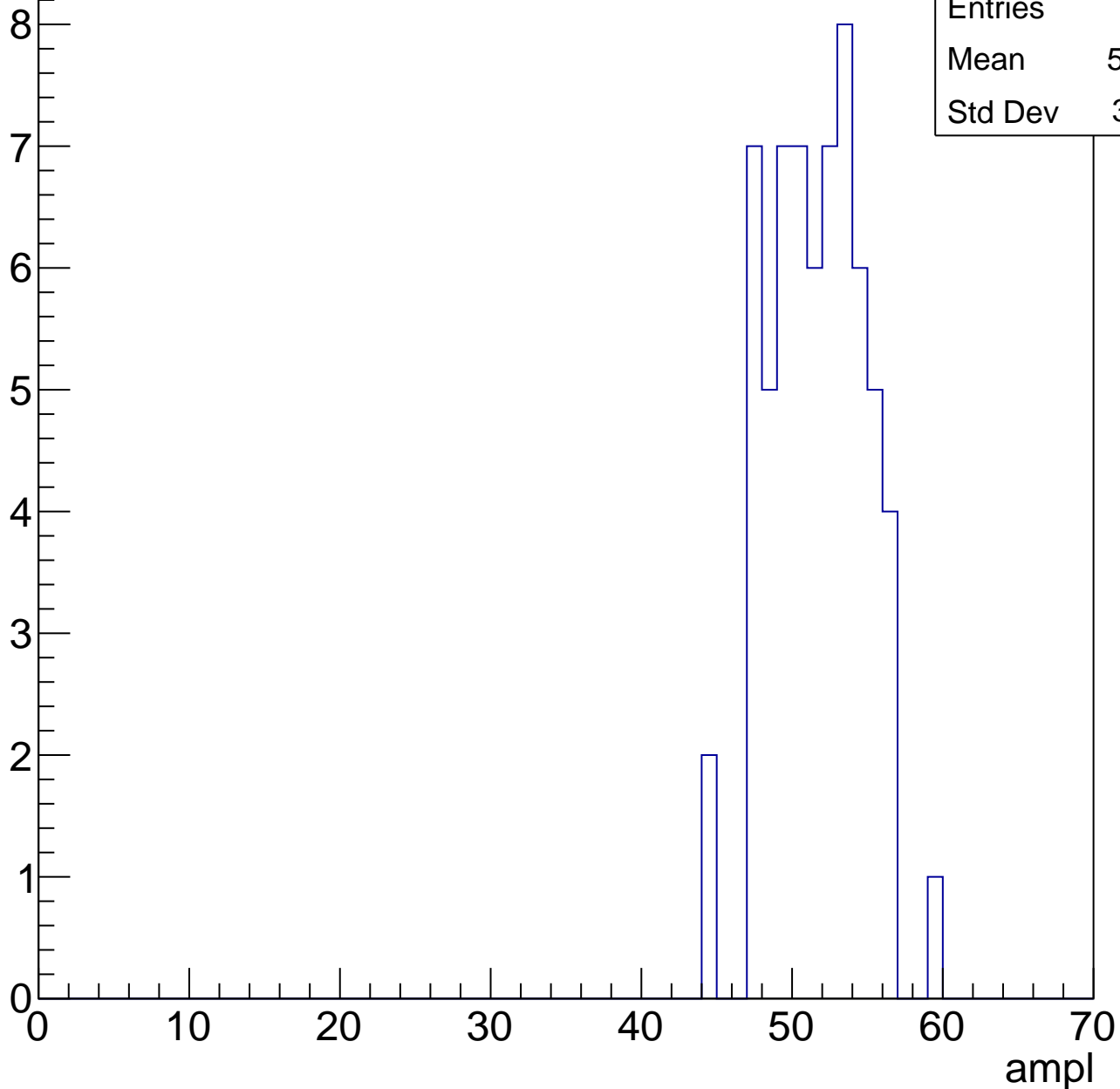
70

B0L001S, U25-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.17
Std Dev	3.101

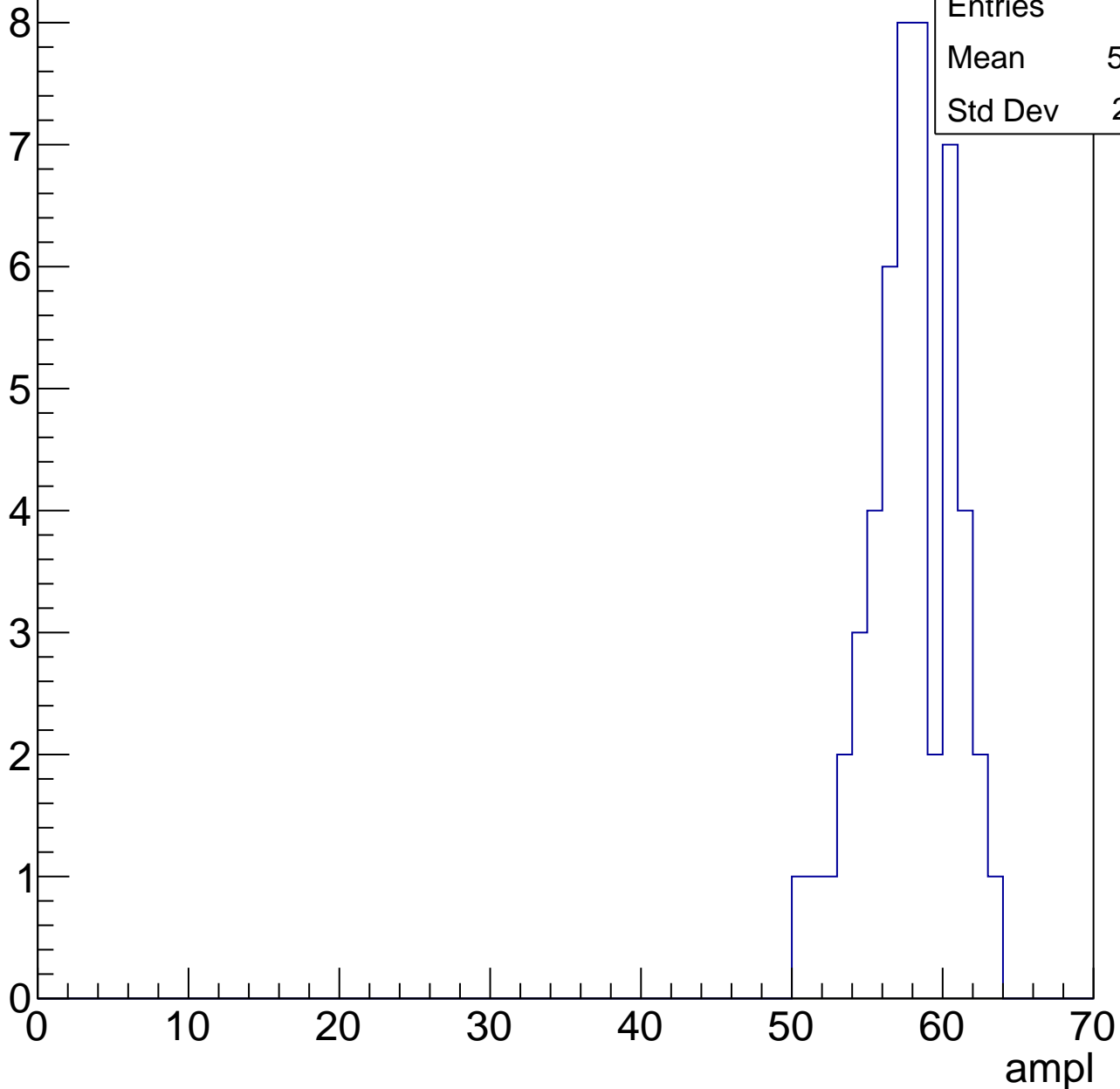


B0L001S, U25-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.32
Std Dev	2.881

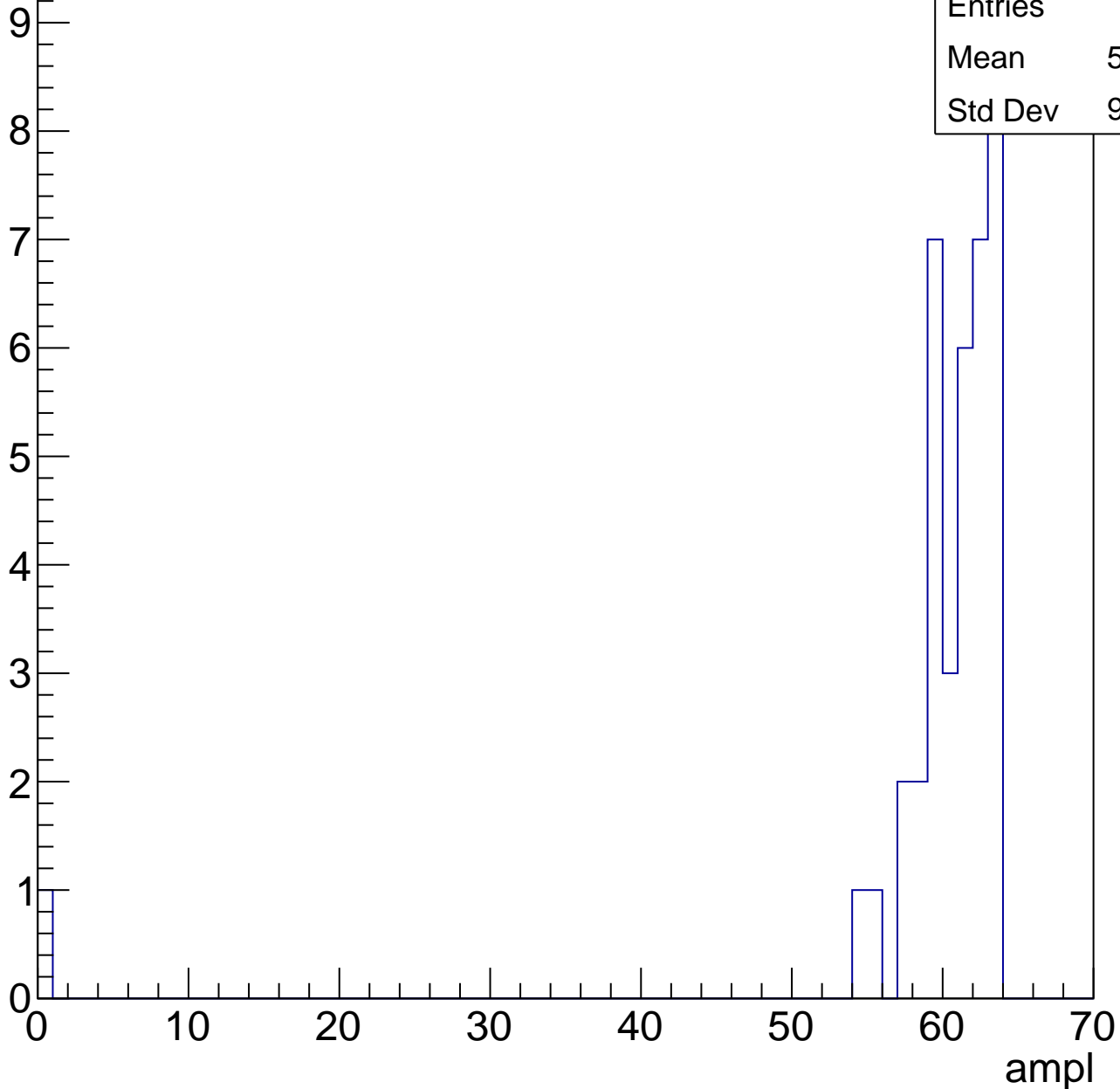


B0L001S, U25-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.95
Std Dev	9.827



B0L001S, U25-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch93, adc0

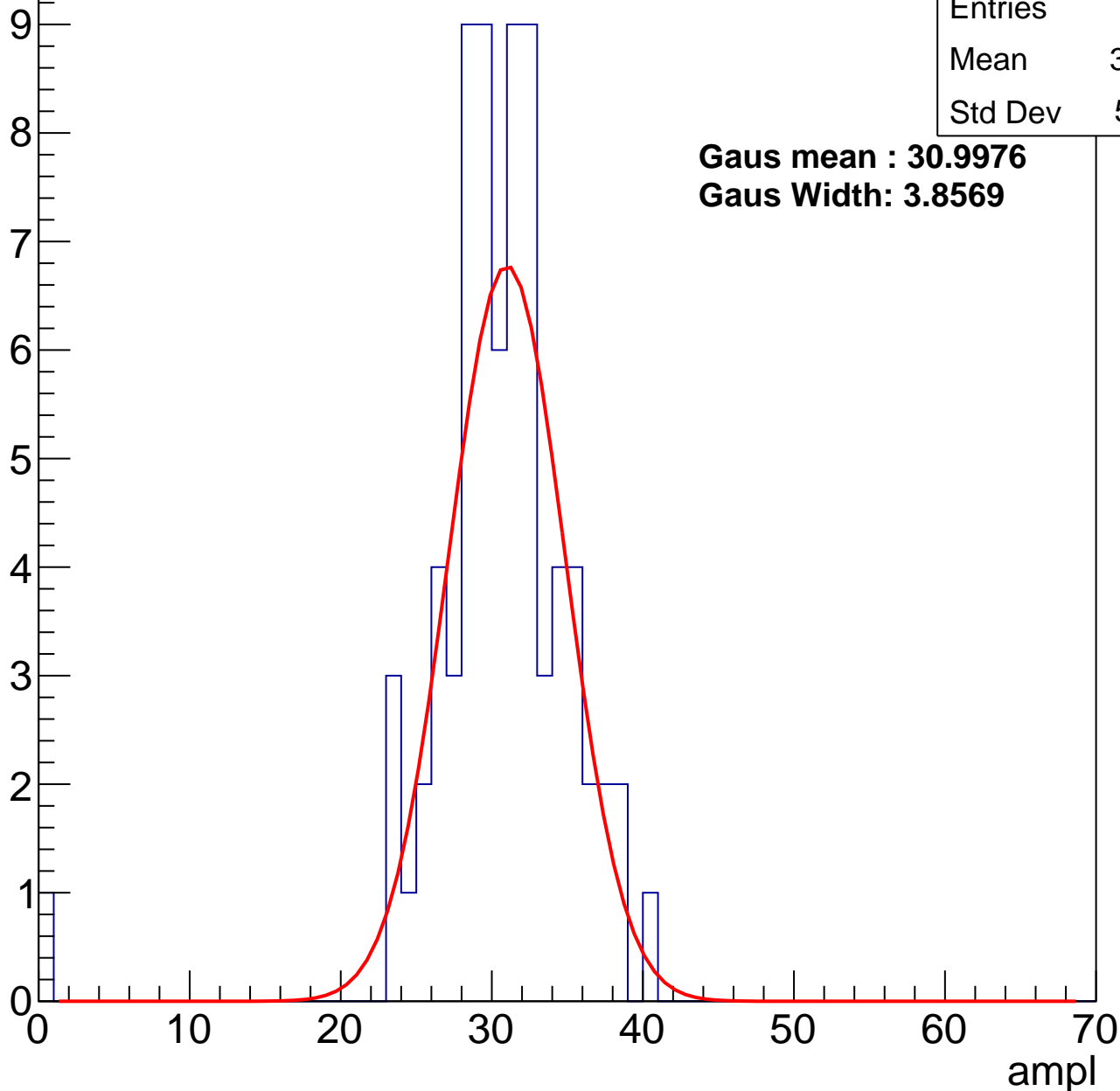
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.07
Std Dev	5.071

Gaus mean : 30.9976

Gaus Width: 3.8569



B0L001S, U25-ch93, adc1

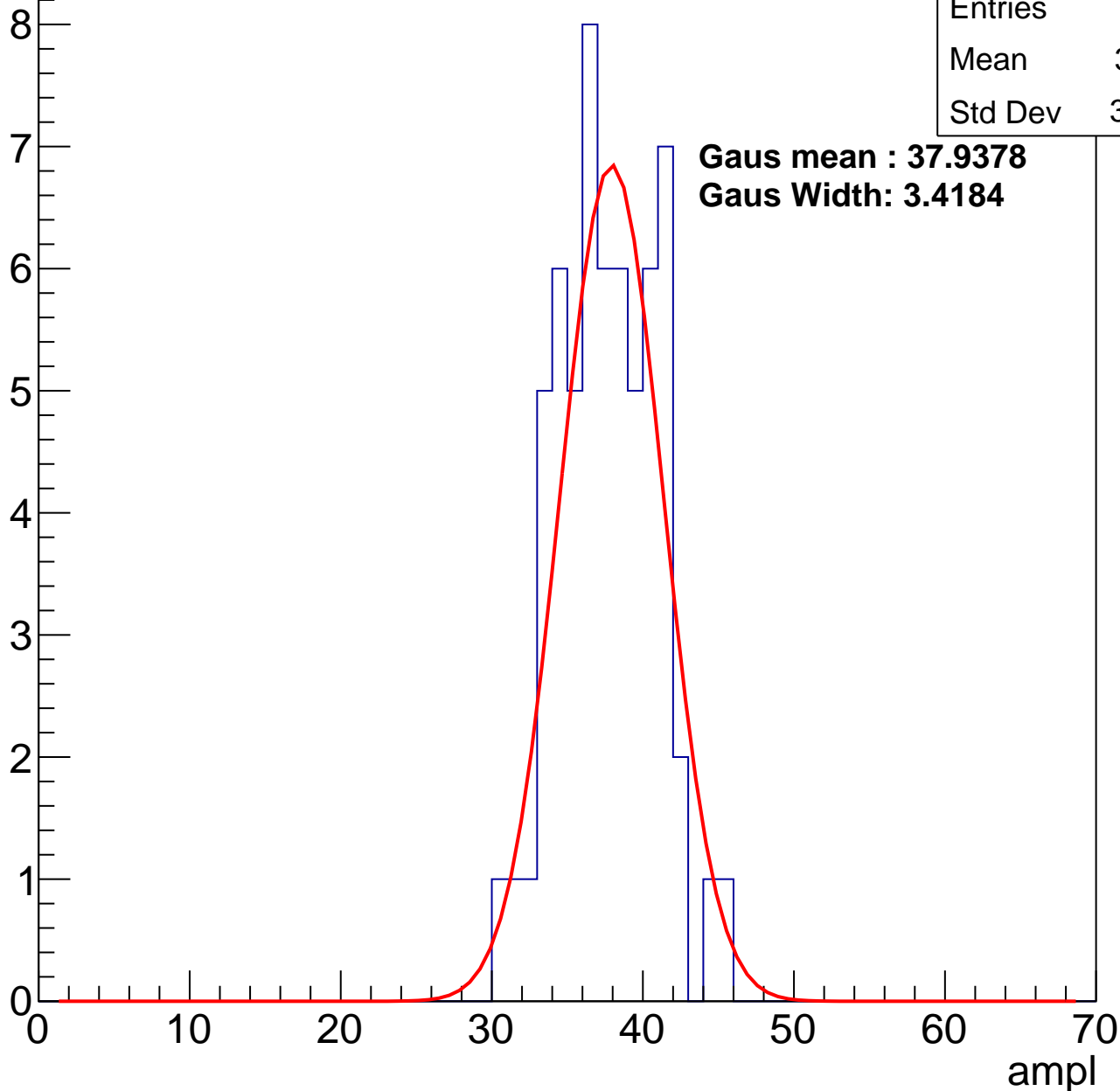
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.21
Std Dev	3.199

Gaus mean : 37.9378

Gaus Width: 3.4184



B0L001S, U25-ch93, adc2

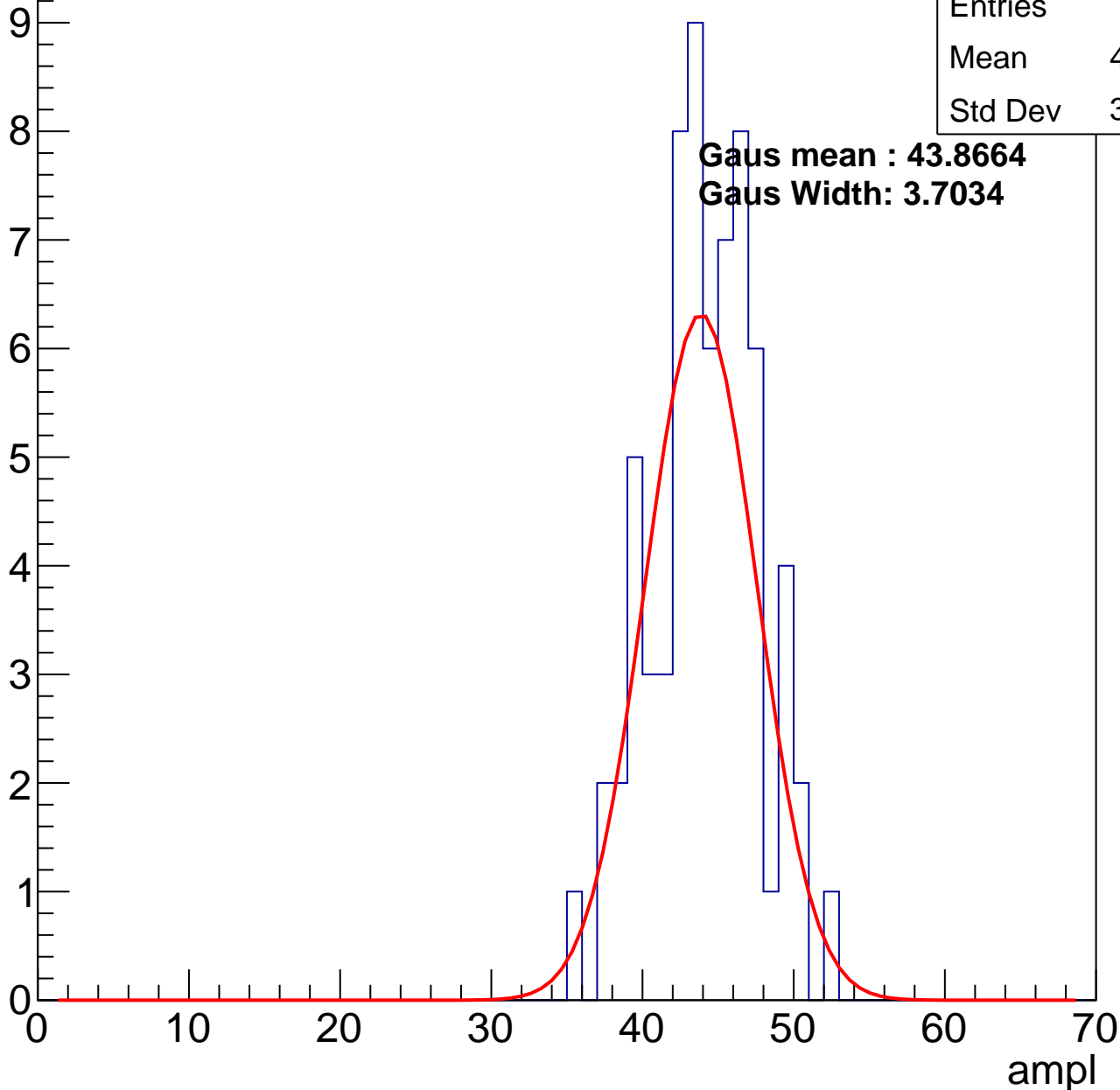
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.69
Std Dev	3.495

Gaus mean : 43.8664

Gaus Width: 3.7034

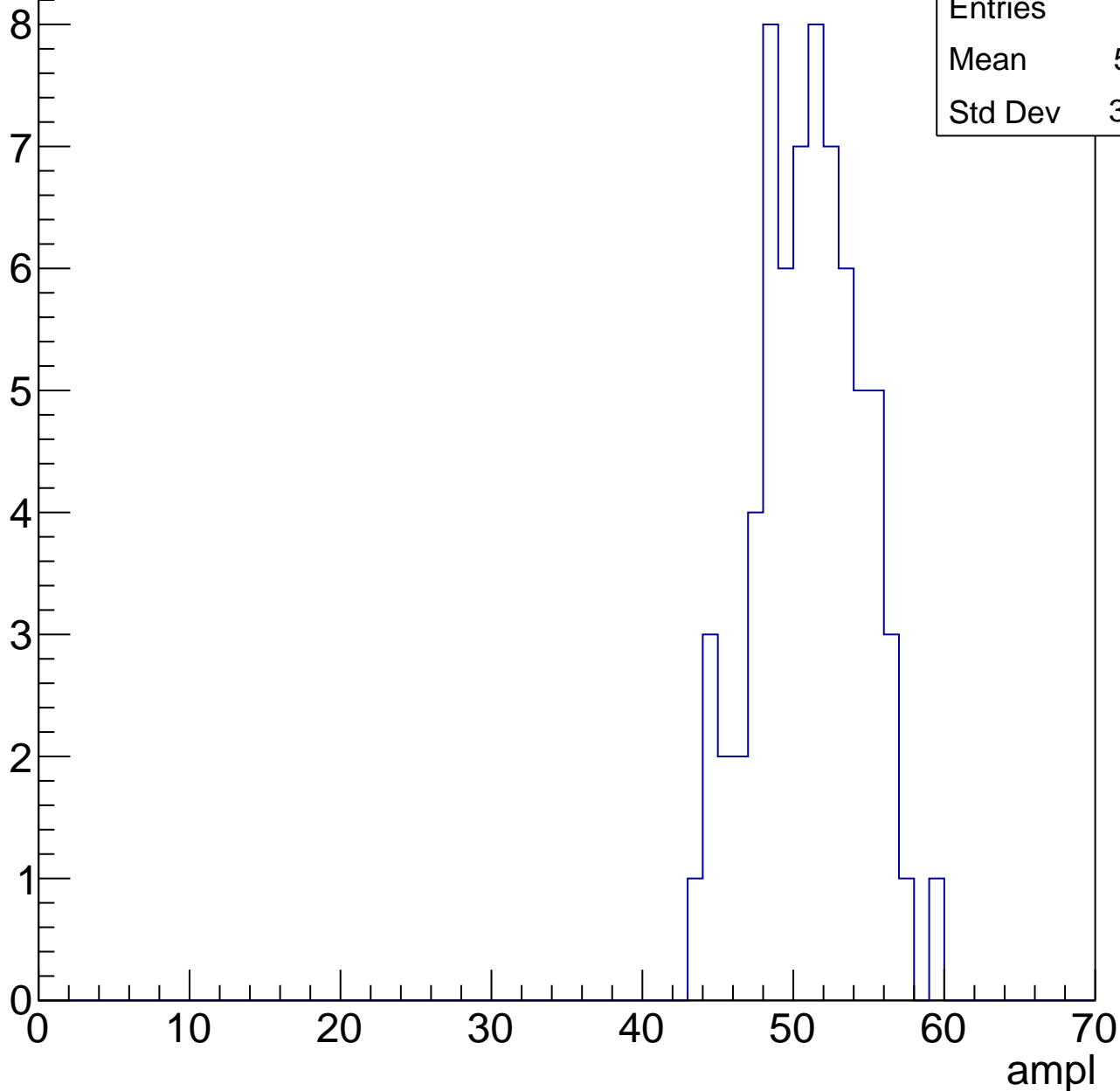


B0L001S, U25-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

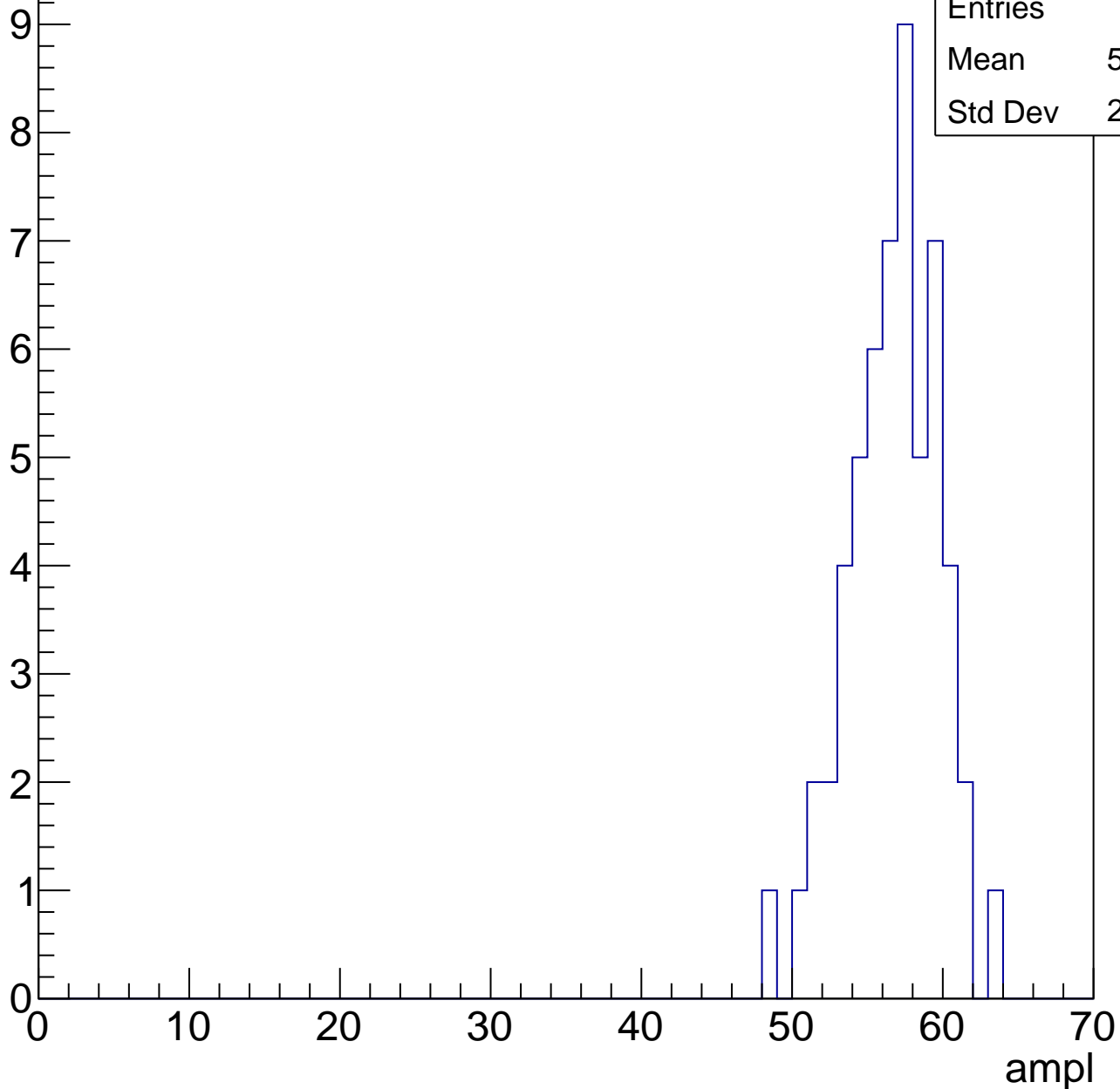
Entries	69
Mean	50.61
Std Dev	3.448



B0L001S, U25-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

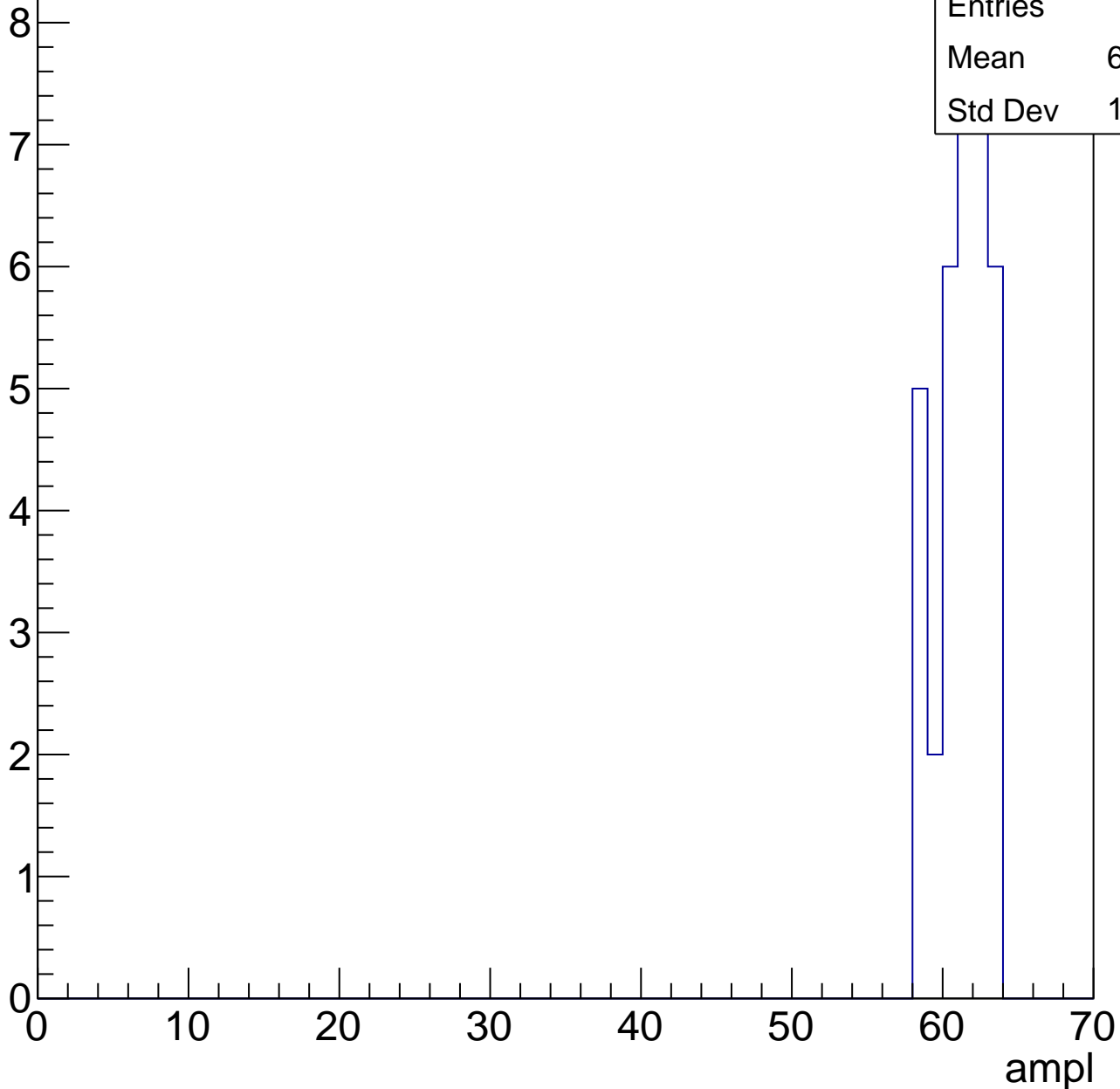


B0L001S, U25-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.86
Std Dev	1.606



B0L001S, U25-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch94, adc0

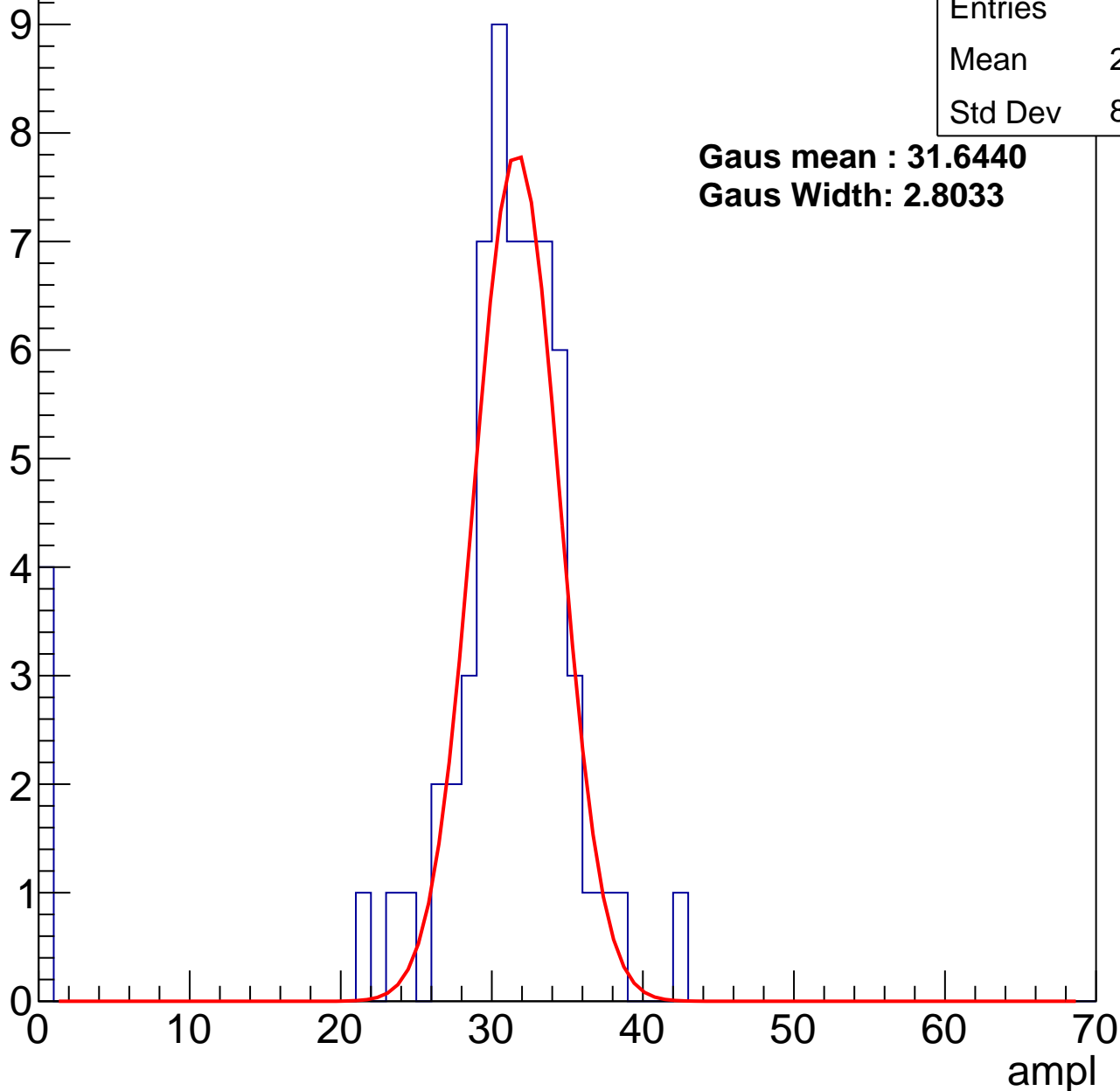
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	29.14
Std Dev	8.244

Gaus mean : 31.6440

Gaus Width: 2.8033



B0L001S, U25-ch94, adc1

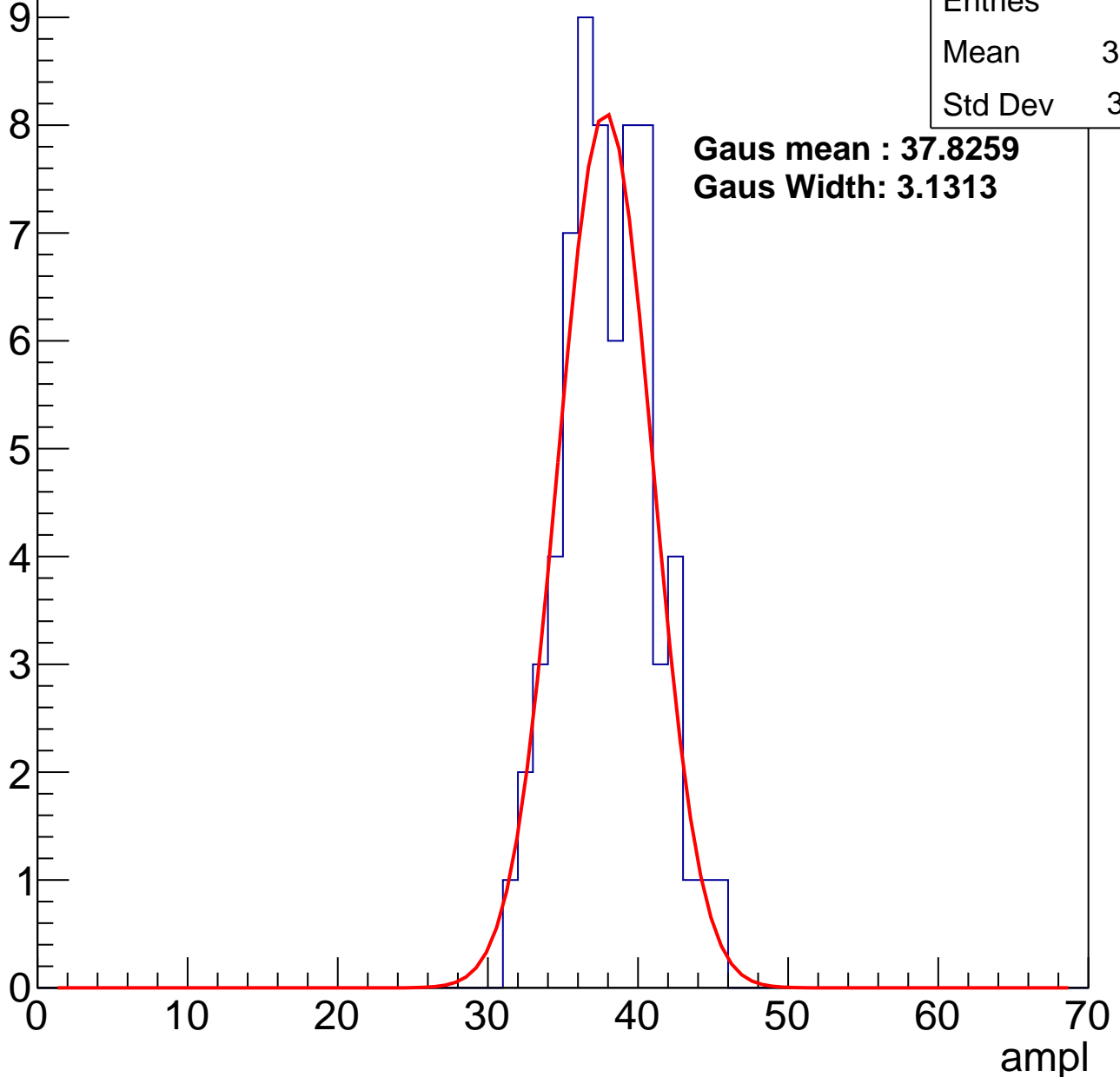
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.55
Std Dev	3.001

Gaus mean : 37.8259

Gaus Width: 3.1313



B0L001S, U25-ch94, adc2

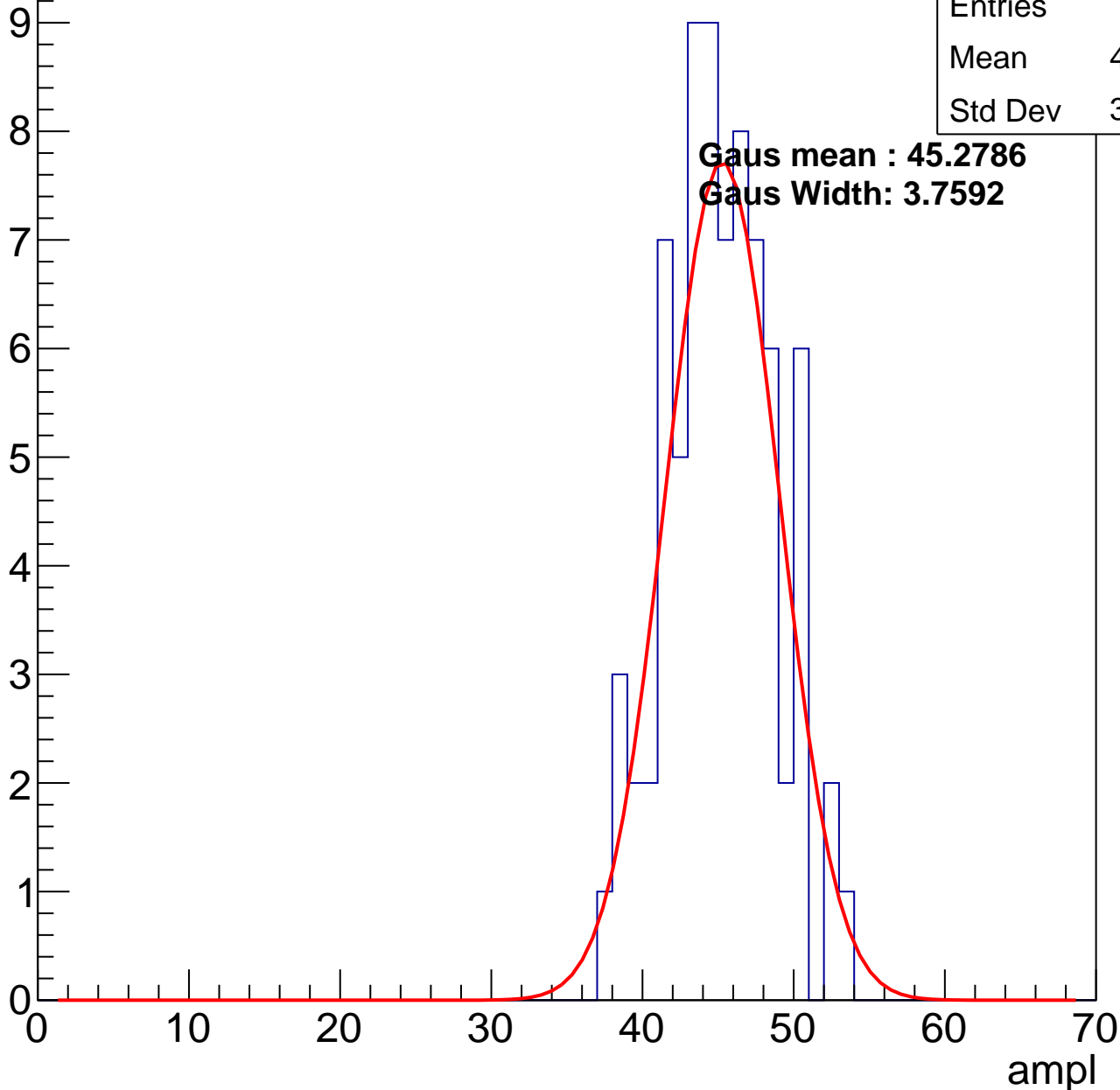
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.73
Std Dev	3.537

Gaus mean : 45.2786

Gaus Width: 3.7592

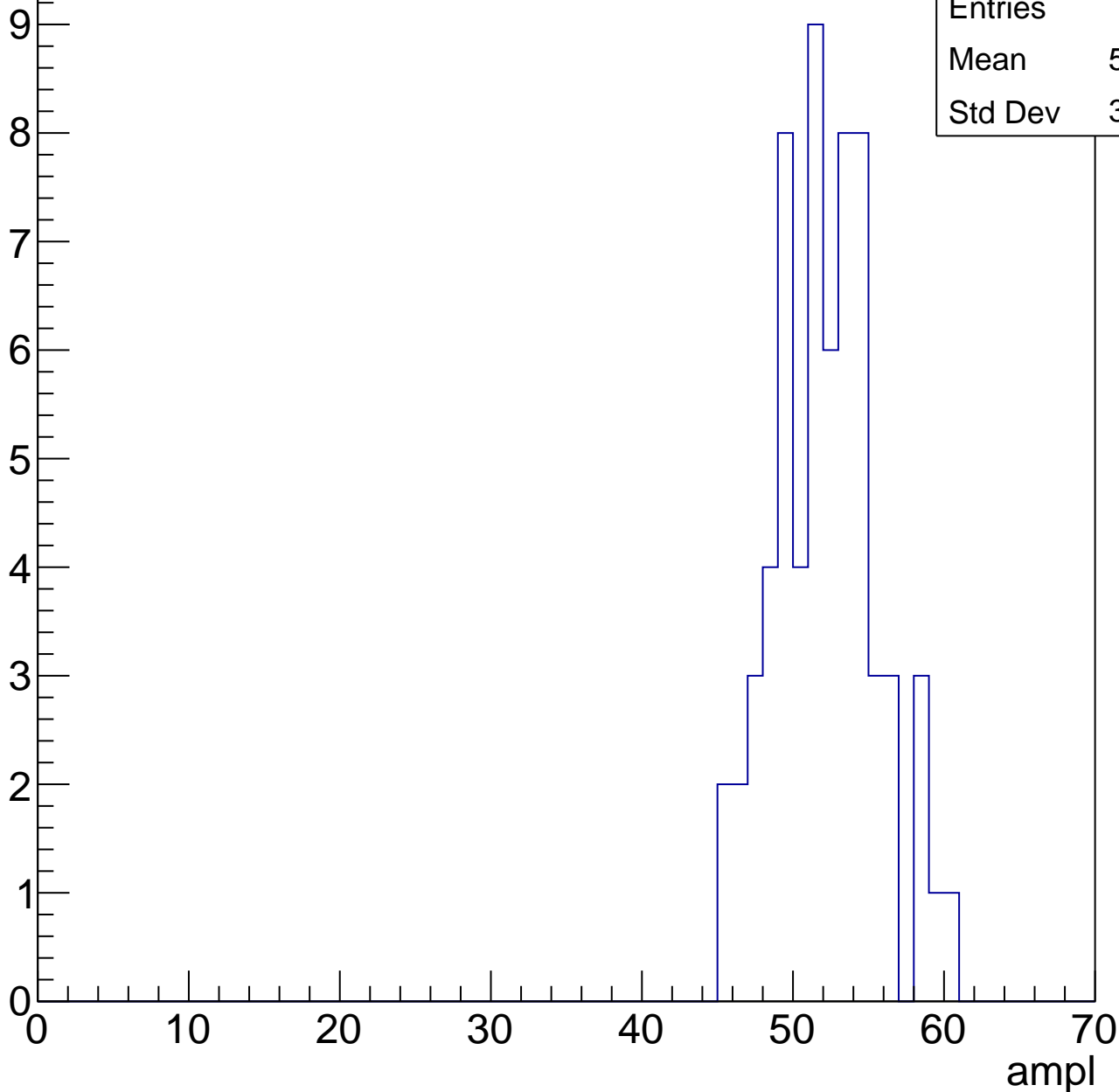


B0L001S, U25-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

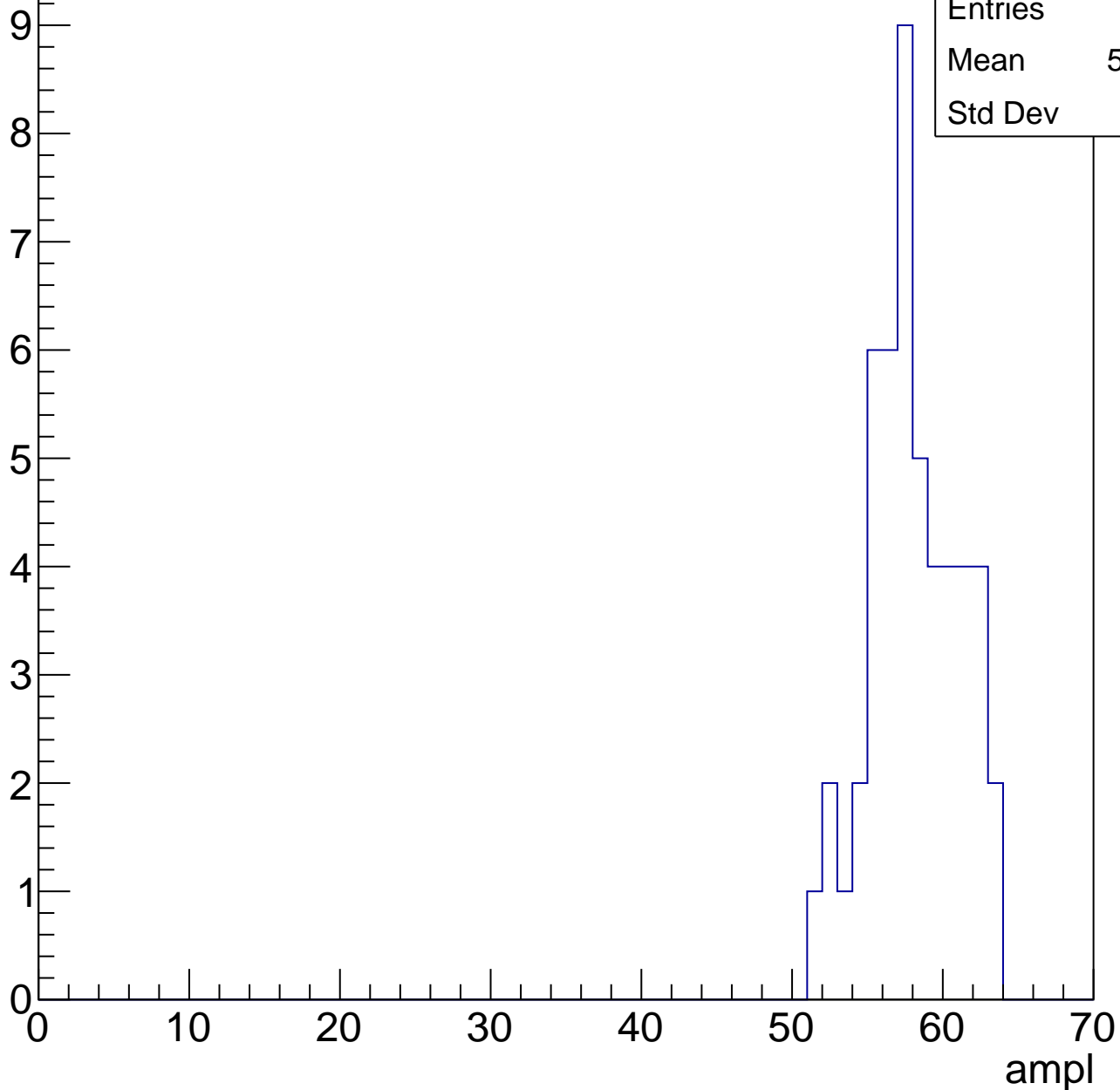
Entries	65
Mean	51.69
Std Dev	3.365



B0L001S, U25-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



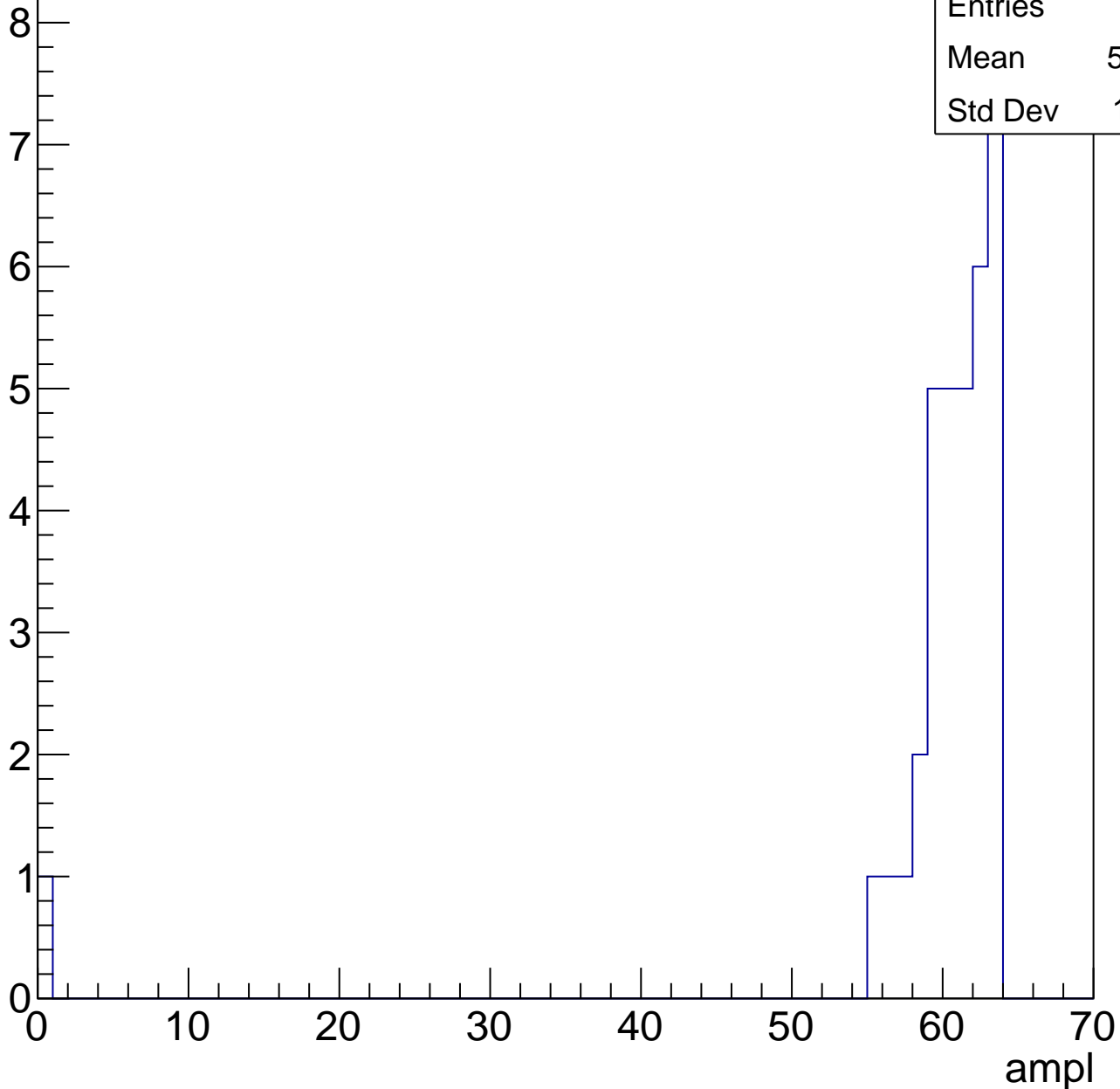
Entries	50
Mean	57.58
Std Dev	2.92

B0L001S, U25-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.86
Std Dev	10.31



B0L001S, U25-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch95, adc0

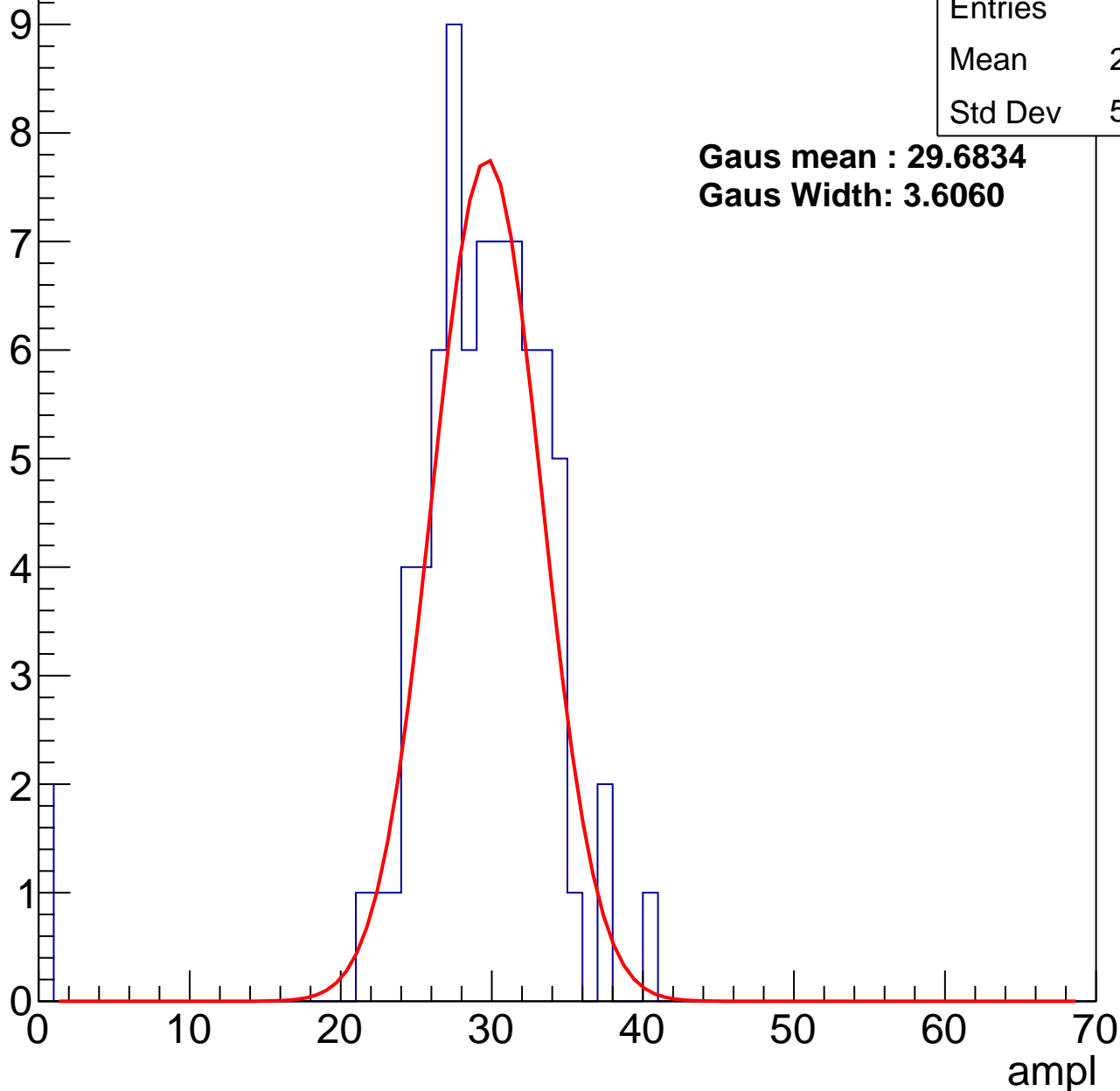
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	28.53
Std Dev	5.926

Gaus mean : 29.6834

Gaus Width: 3.6060



B0L001S, U25-ch95, adc1

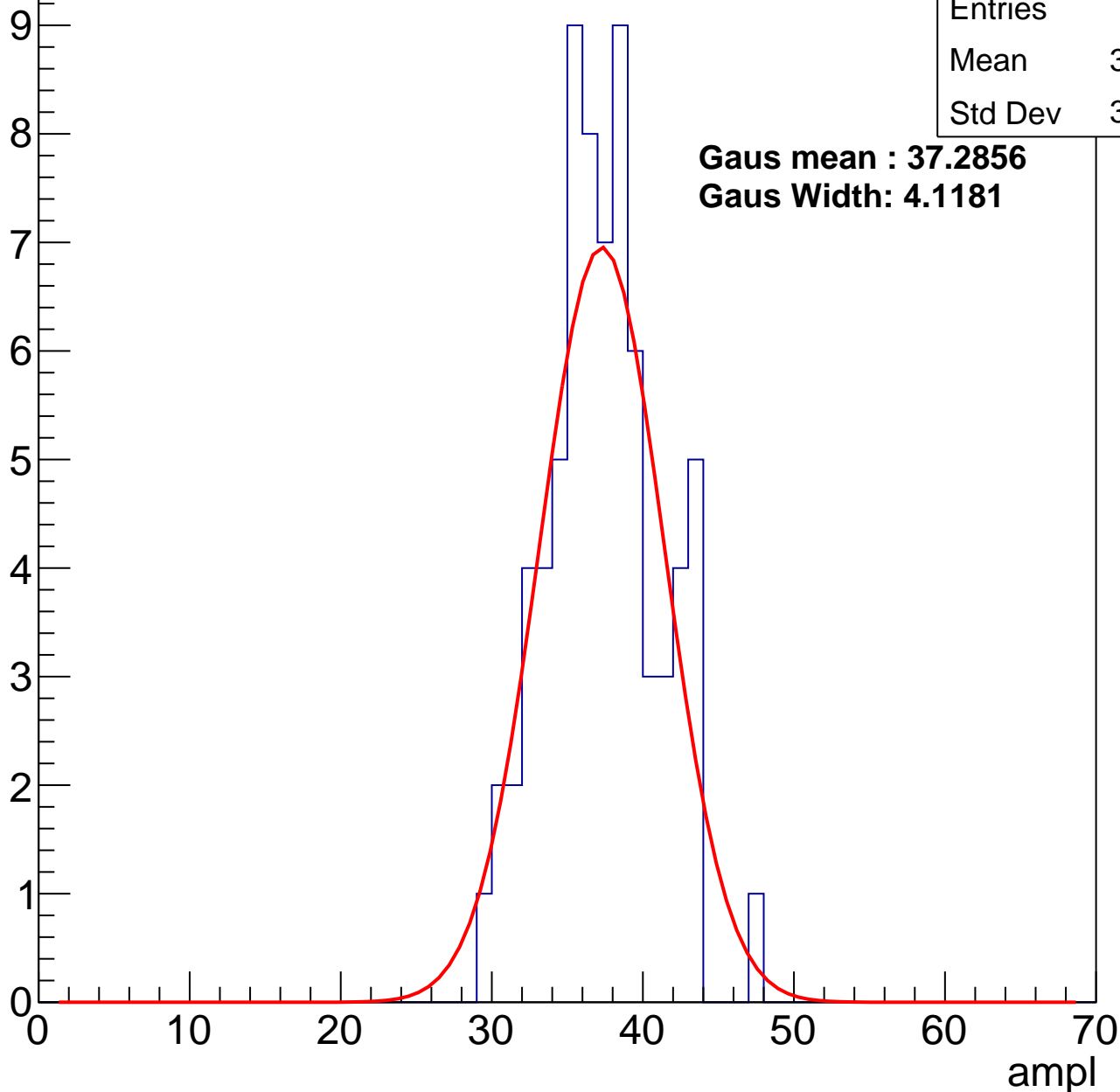
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	36.88
Std Dev	3.653

Gaus mean : 37.2856

Gaus Width: 4.1181



B0L001S, U25-ch95, adc2

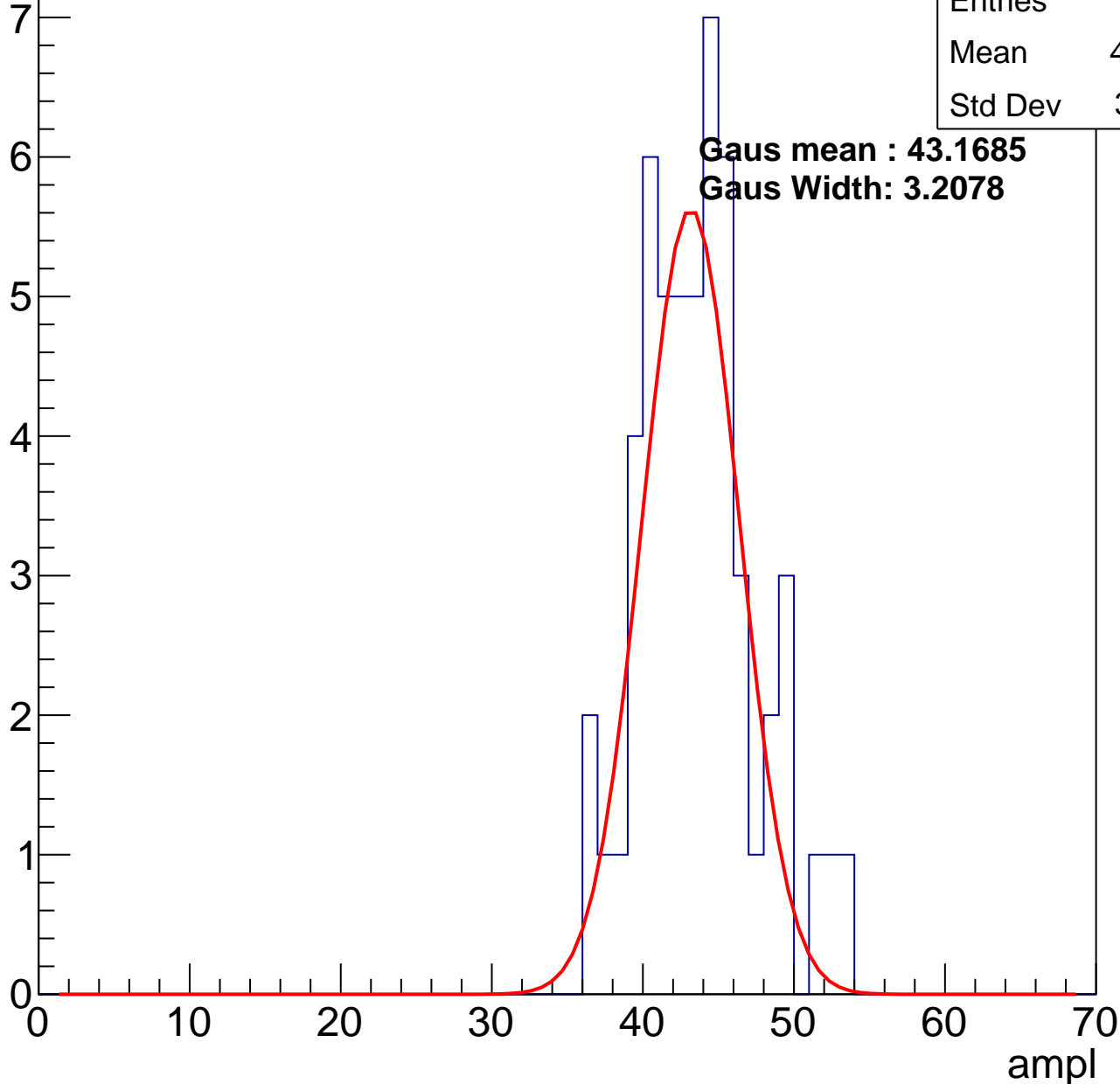
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.24
Std Dev	3.791

Gaus mean : 43.1685

Gaus Width: 3.2078

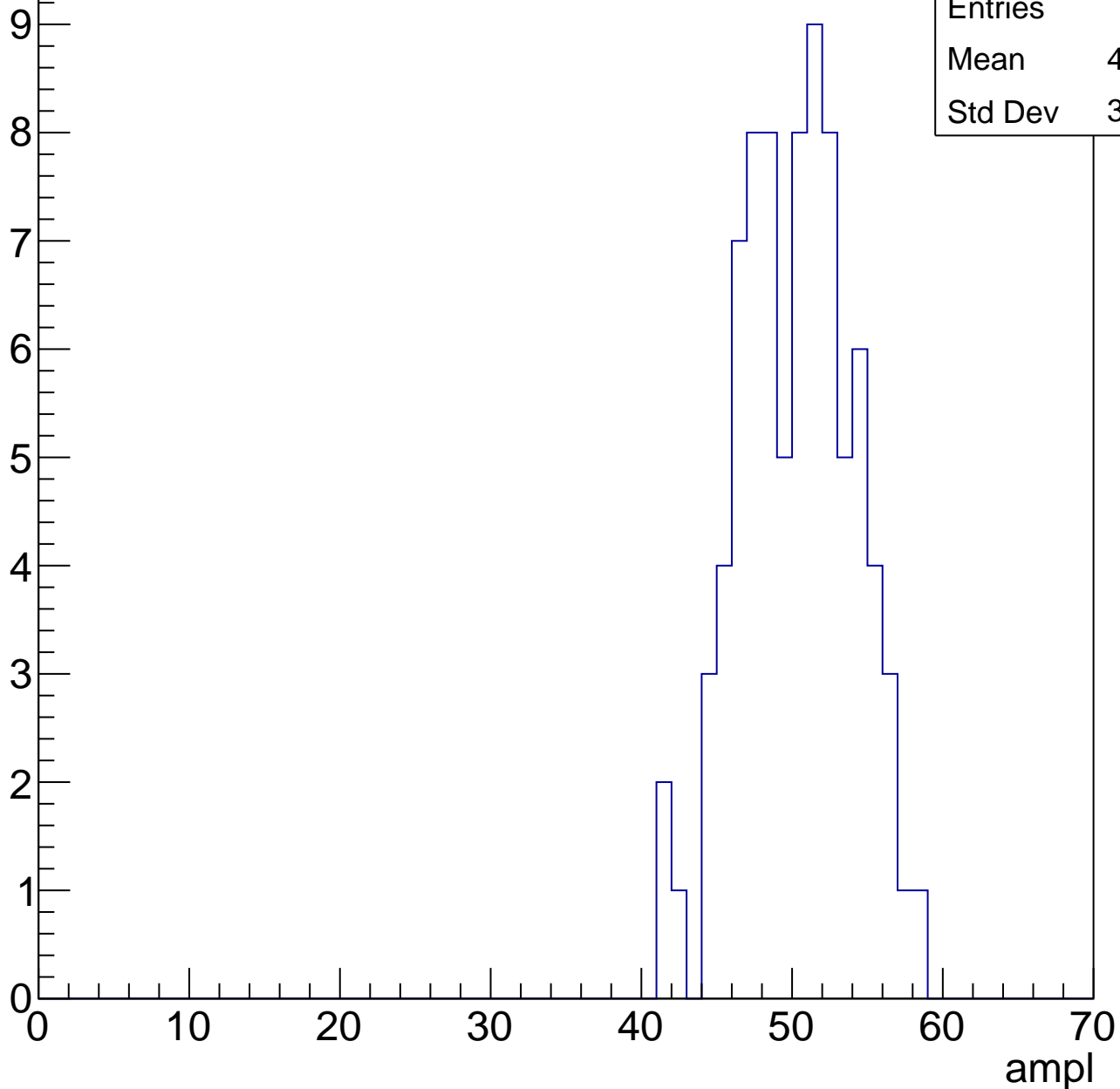


B0L001S, U25-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	49.76
Std Dev	3.727

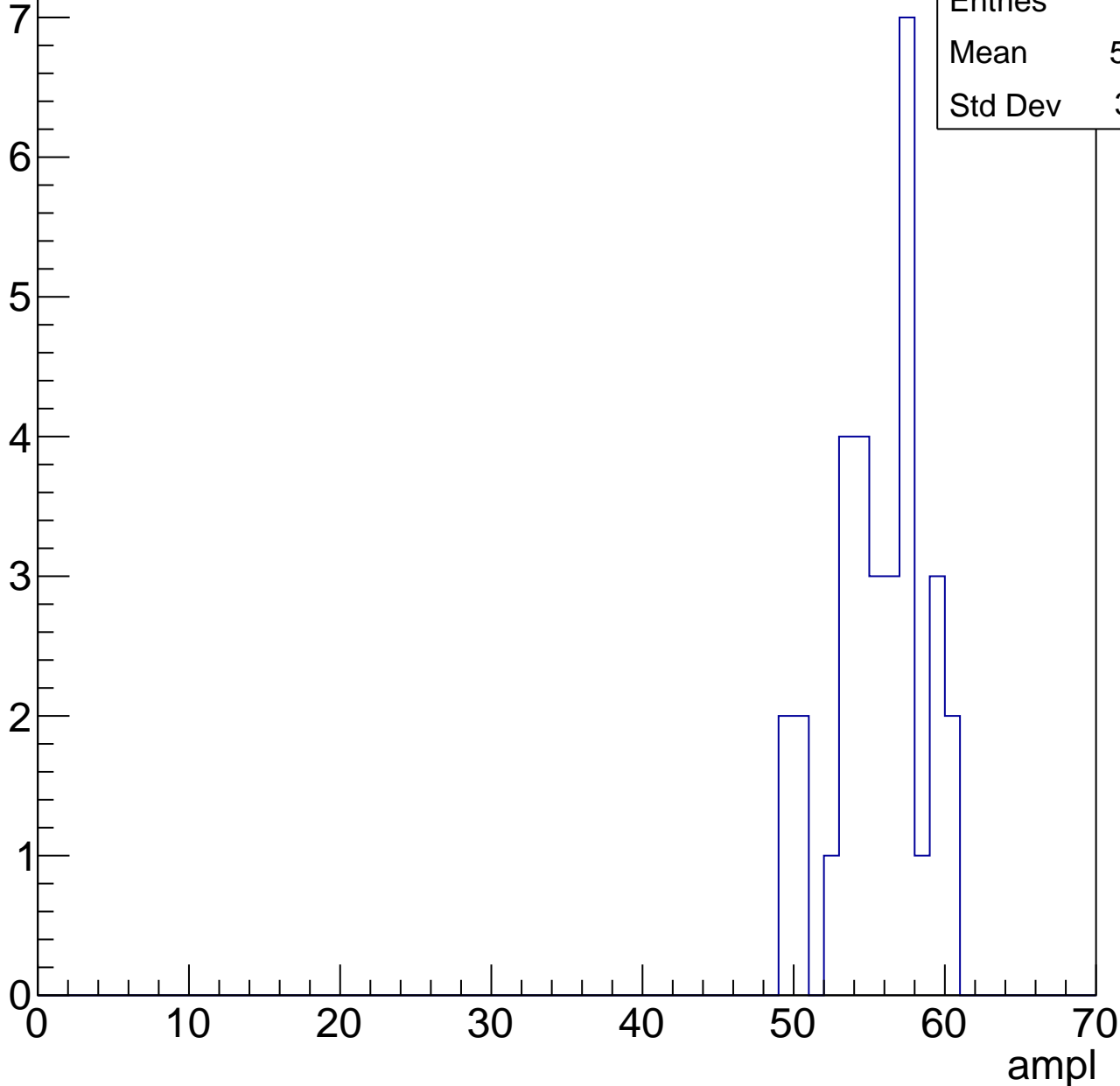


B0L001S, U25-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

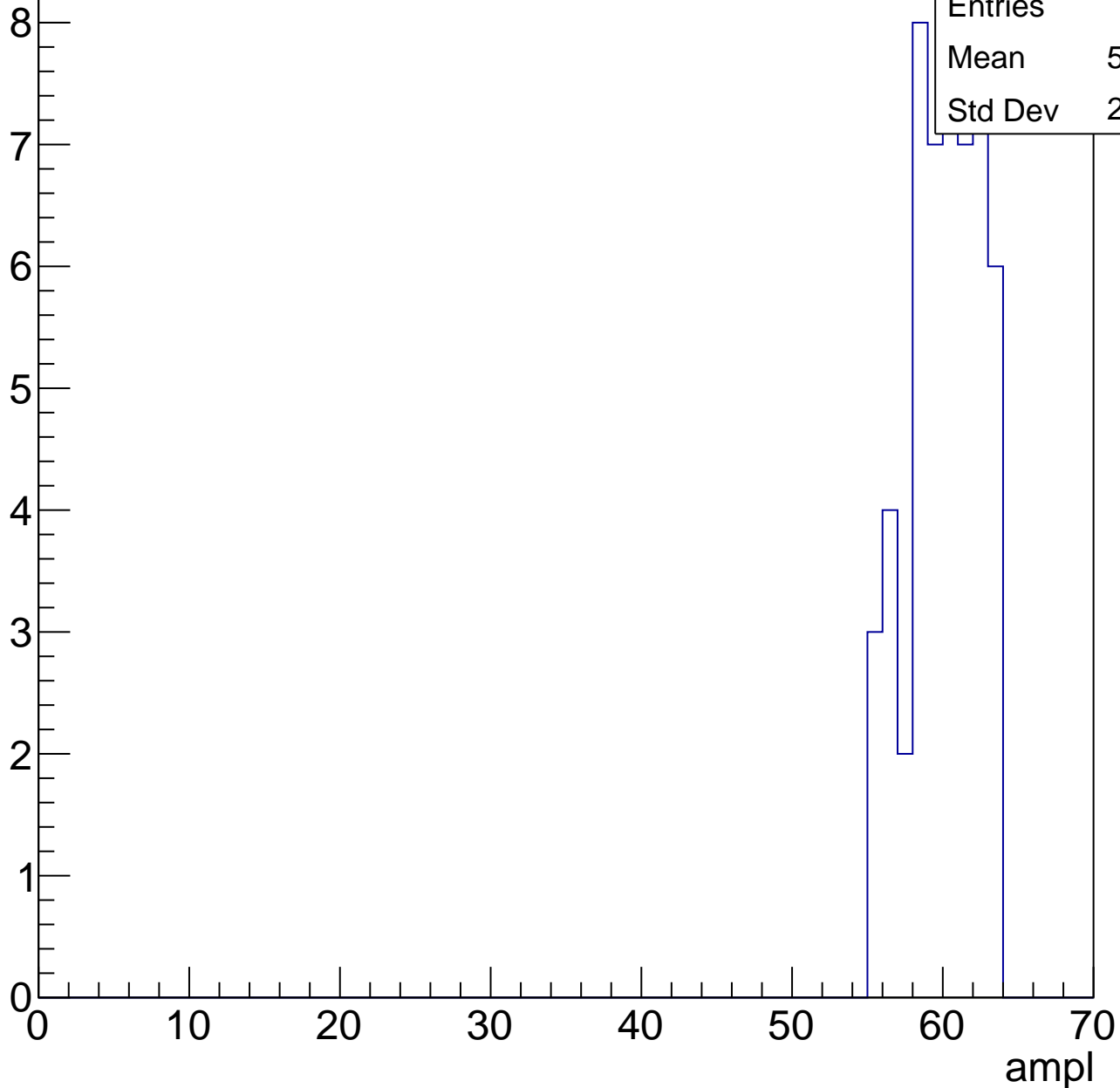
Entries	32
Mean	55.16
Std Dev	3.001



B0L001S, U25-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

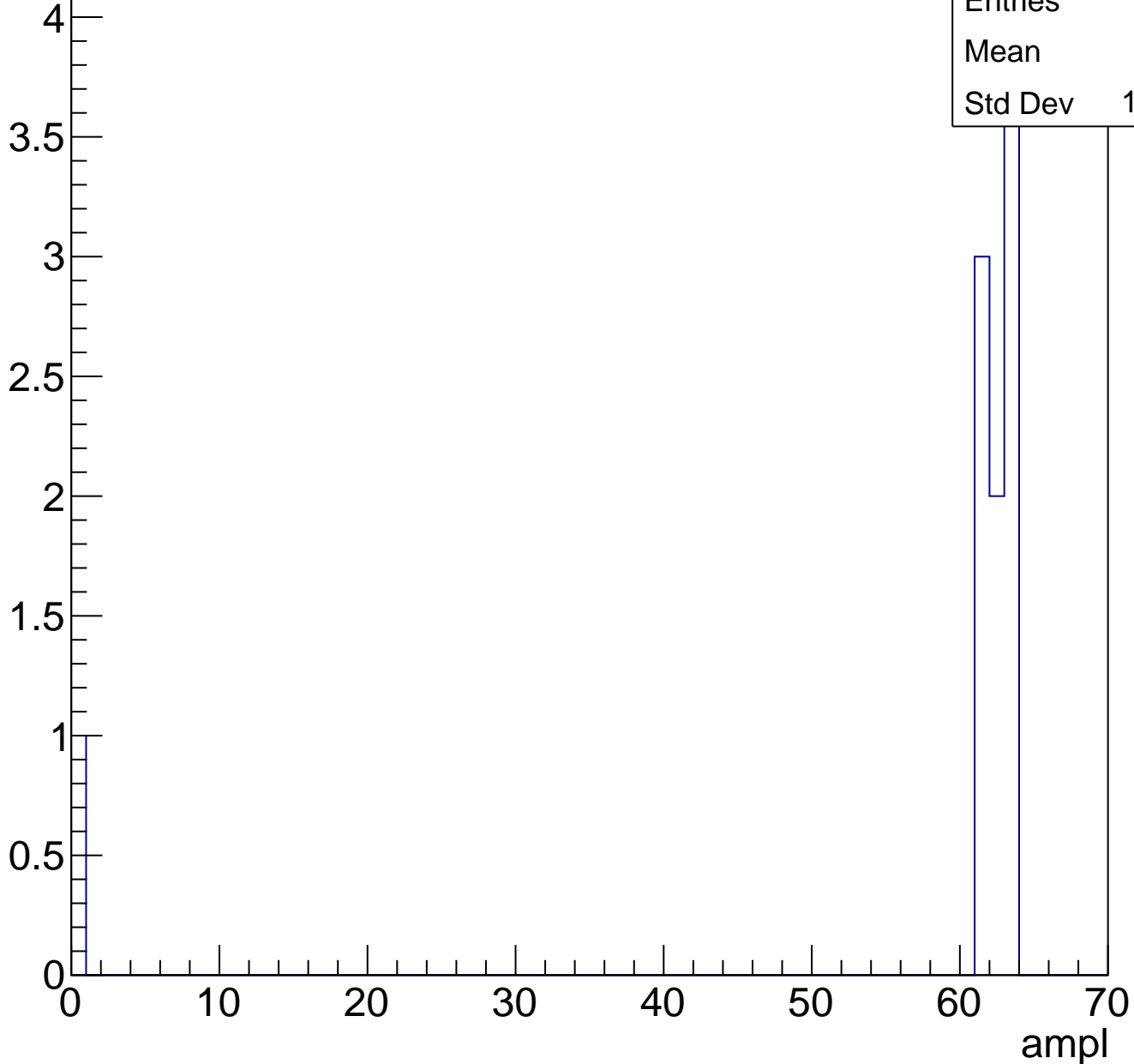


Entries	53
Mean	59.64
Std Dev	2.307

B0L001S, U25-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch96, adc0

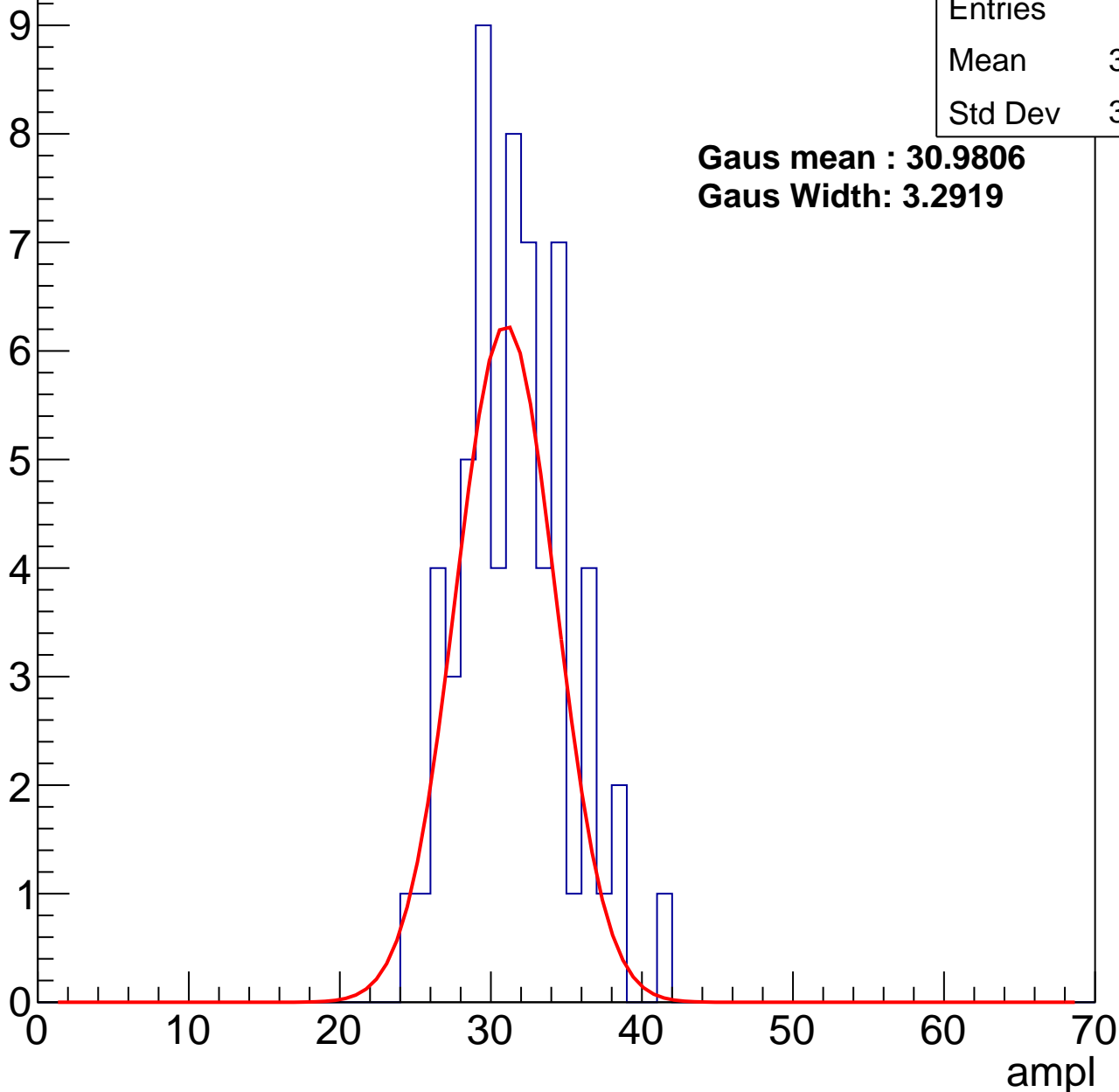
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.13
Std Dev	3.485

Gaus mean : 30.9806

Gaus Width: 3.2919



B0L001S, U25-ch96, adc1

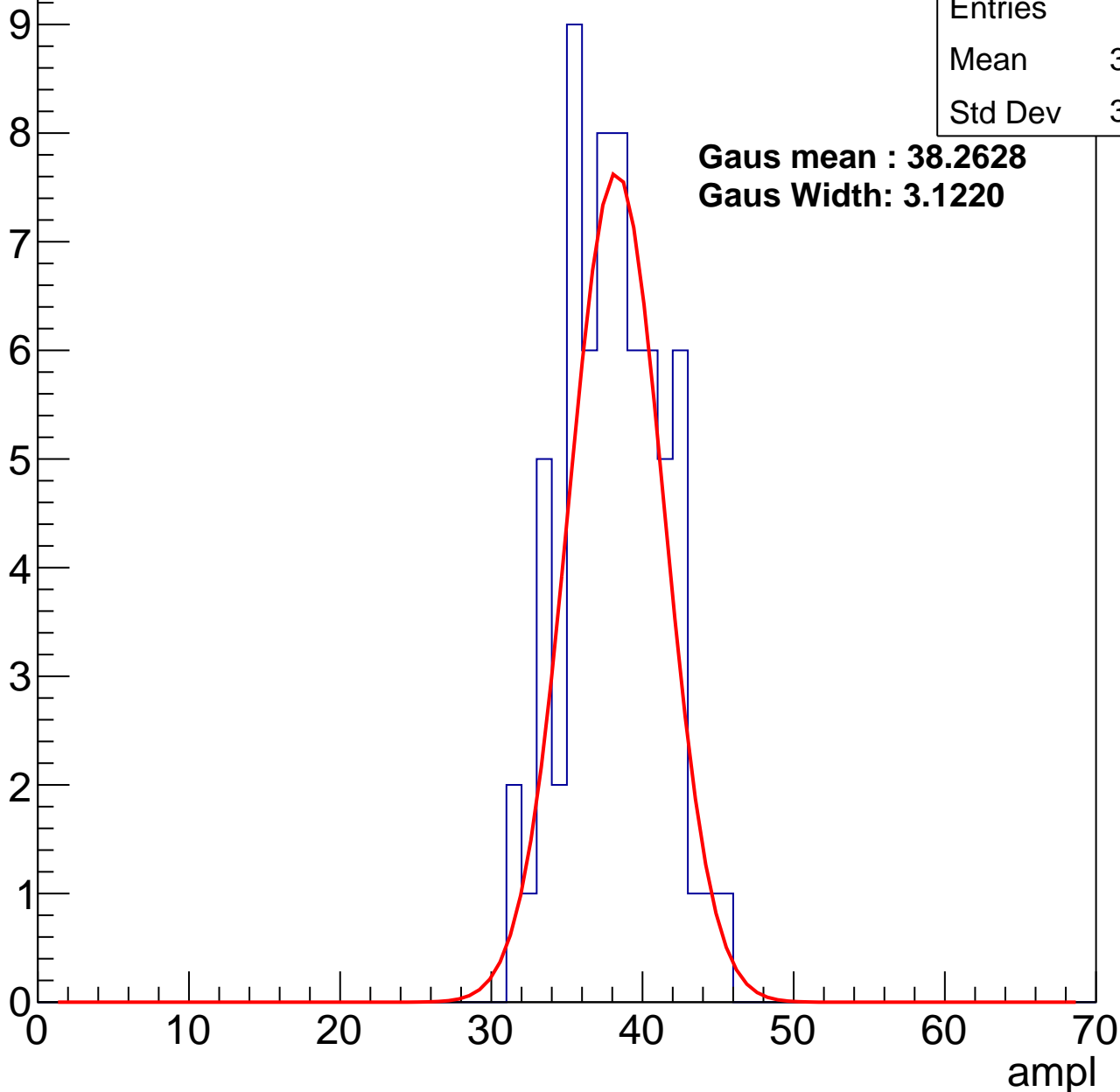
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.63
Std Dev	3.176

Gaus mean : 38.2628

Gaus Width: 3.1220



B0L001S, U25-ch96, adc2

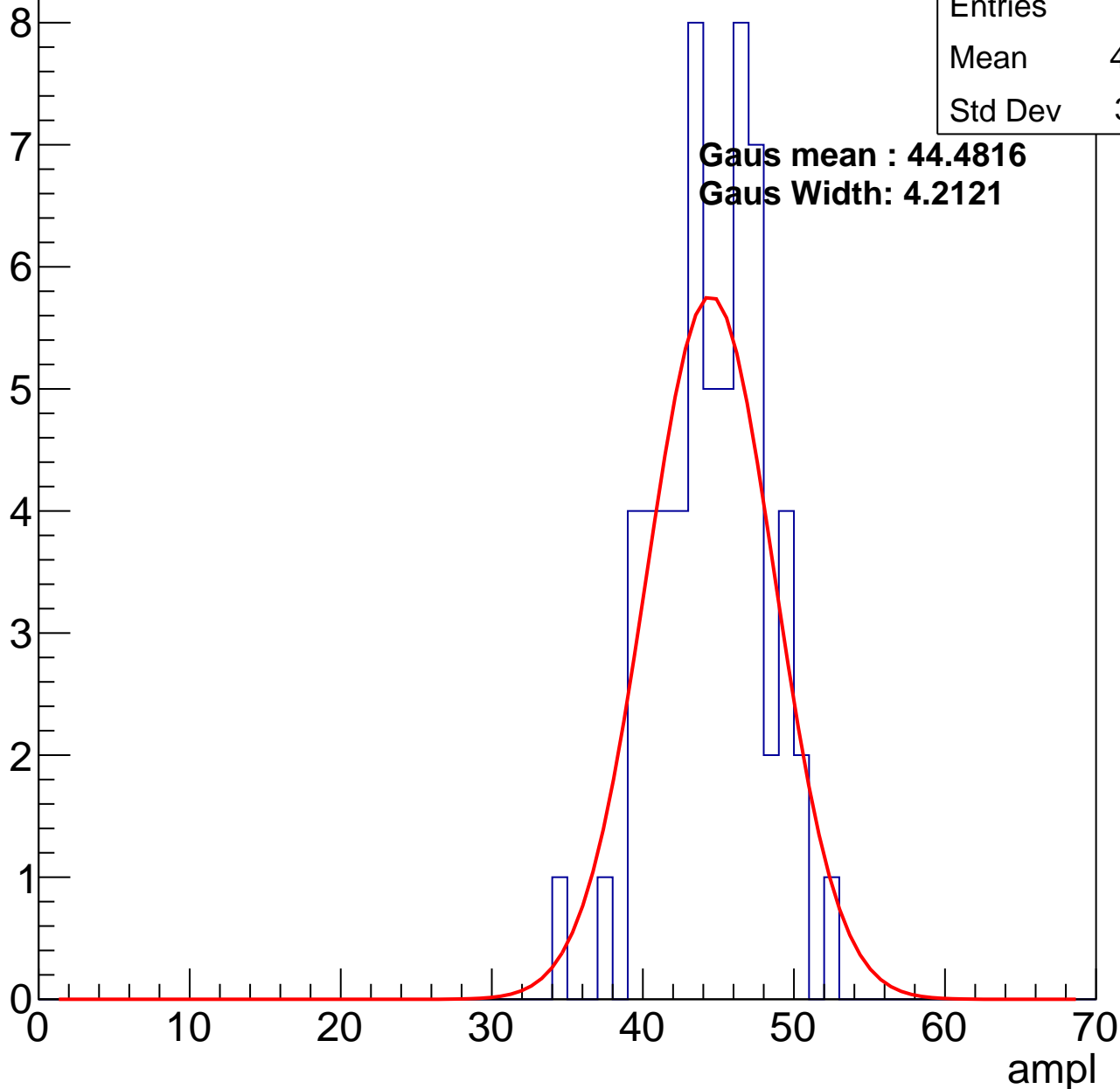
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.15
Std Dev	3.511

Gaus mean : 44.4816

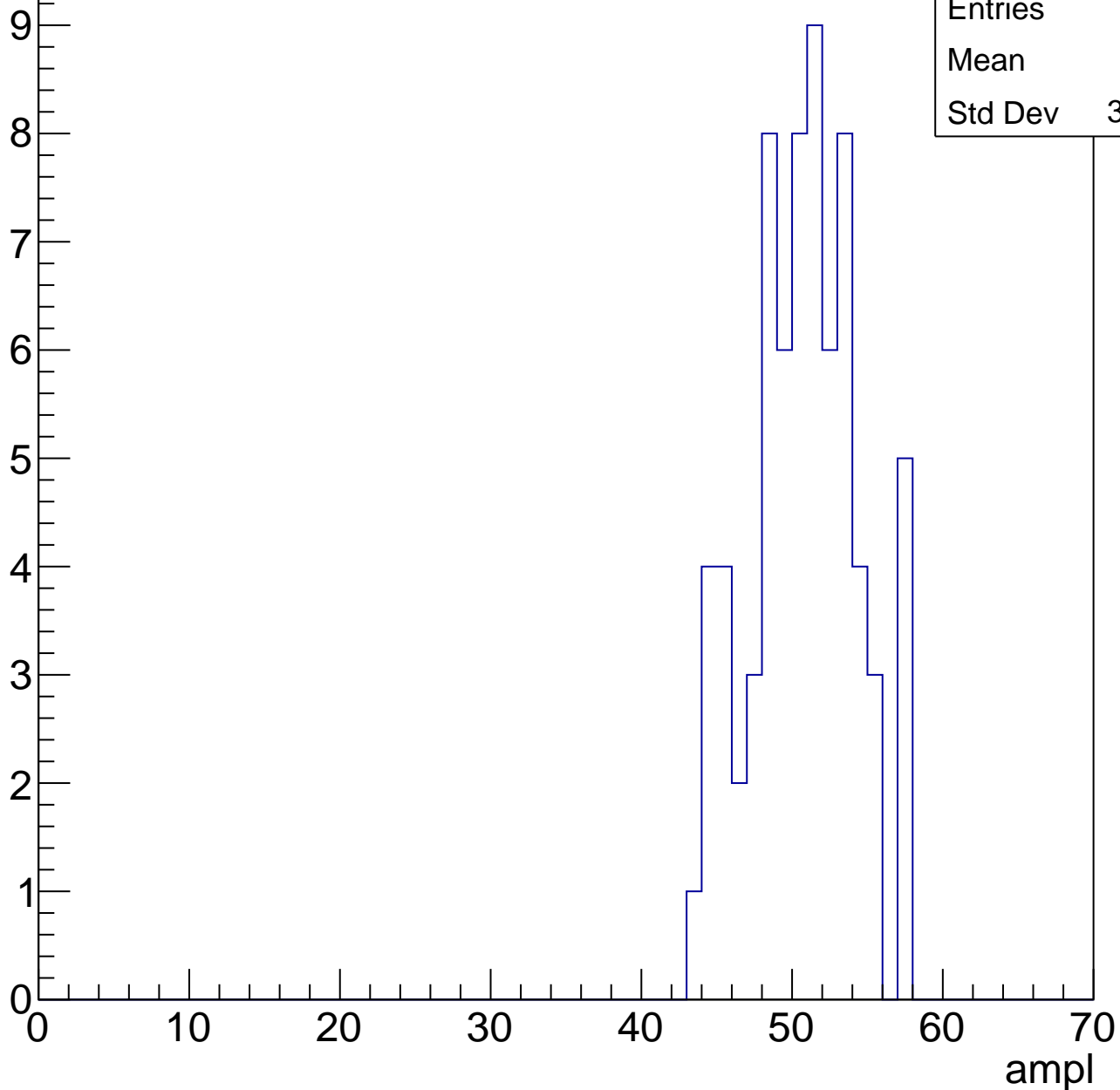
Gaus Width: 4.2121



B0L001S, U25-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

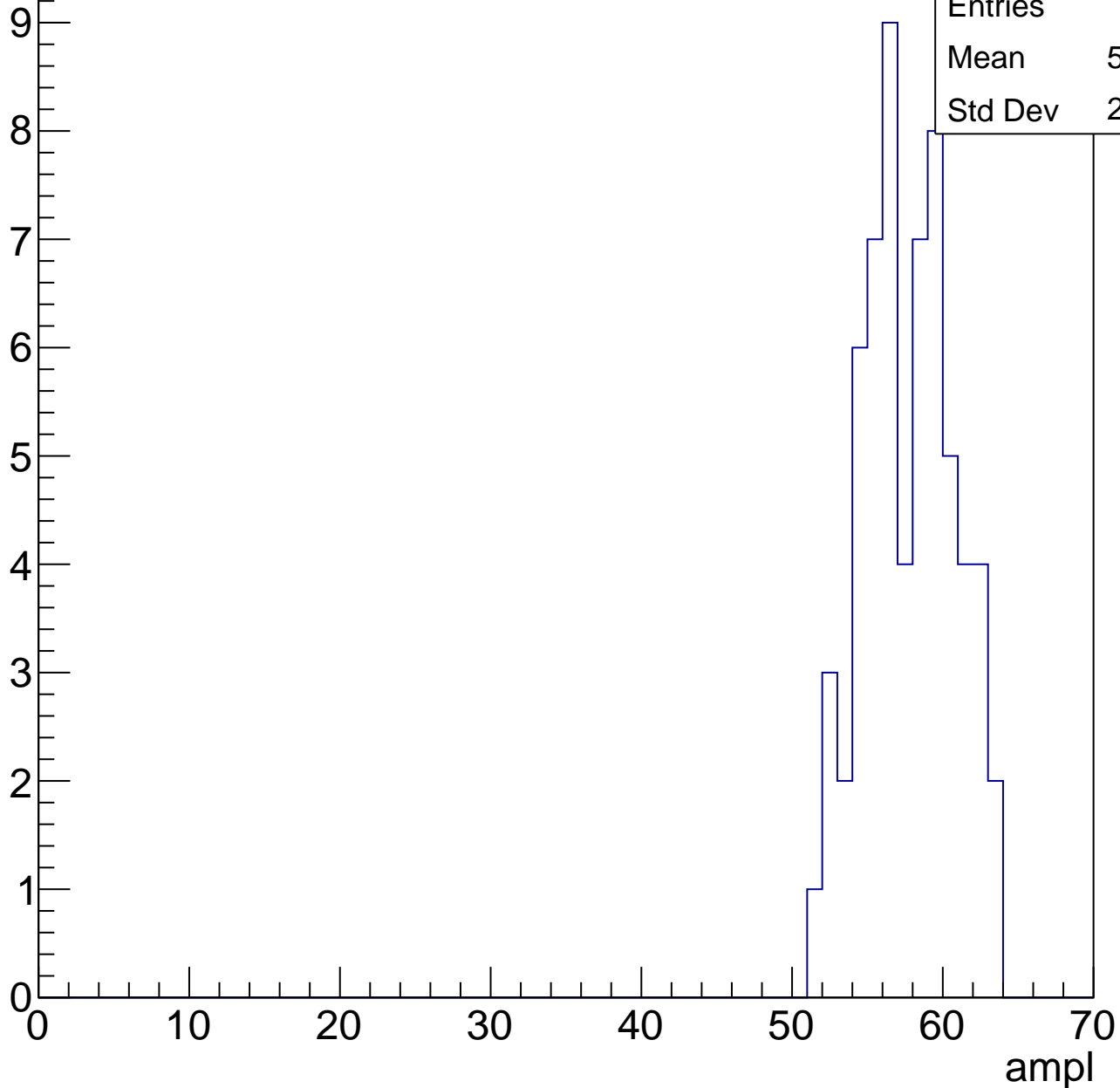
Entry



B0L001S, U25-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



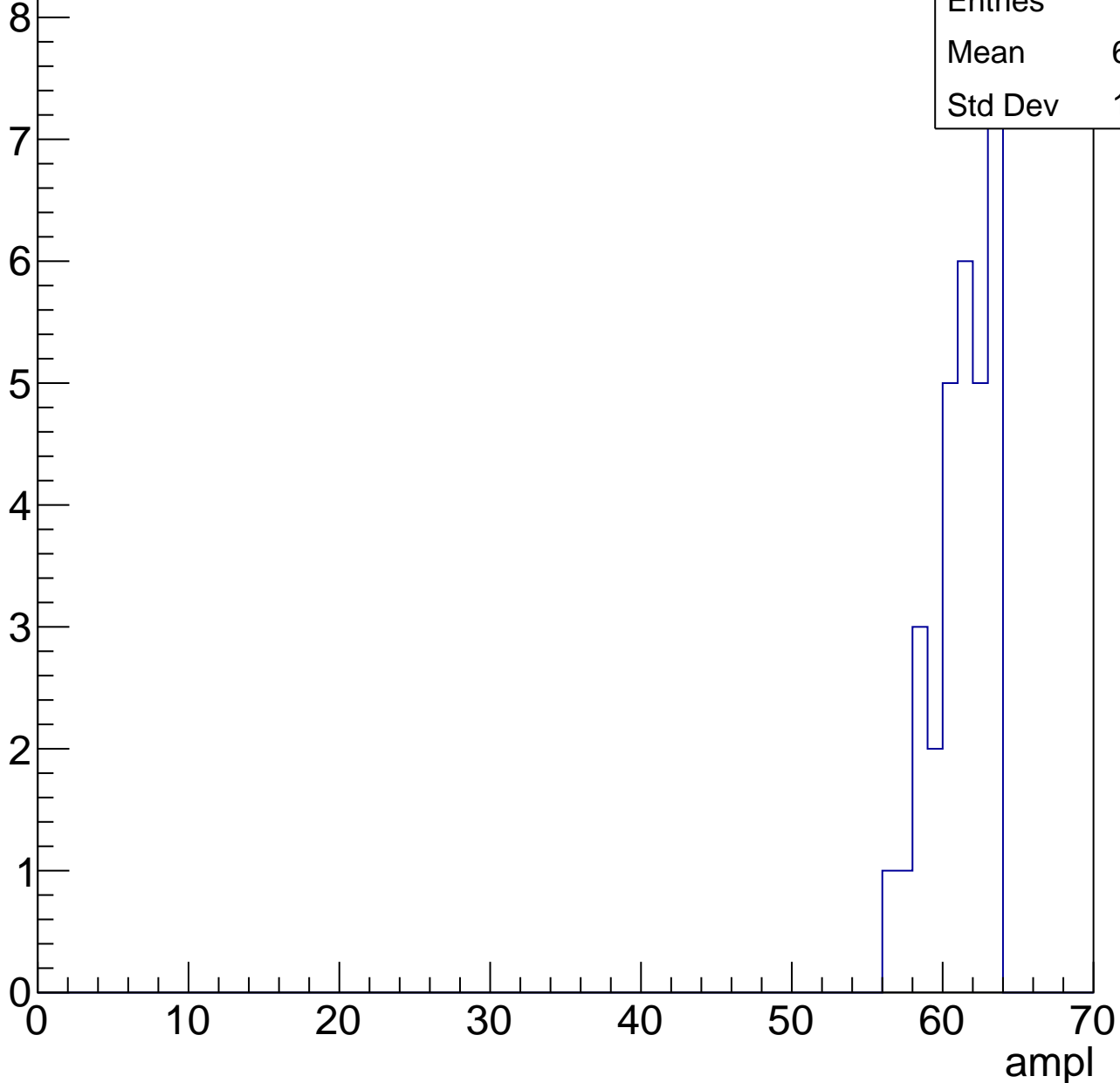
Entries	62
Mean	57.26
Std Dev	2.978

B0L001S, U25-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.81
Std Dev	1.941



B0L001S, U25-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch97, adc0

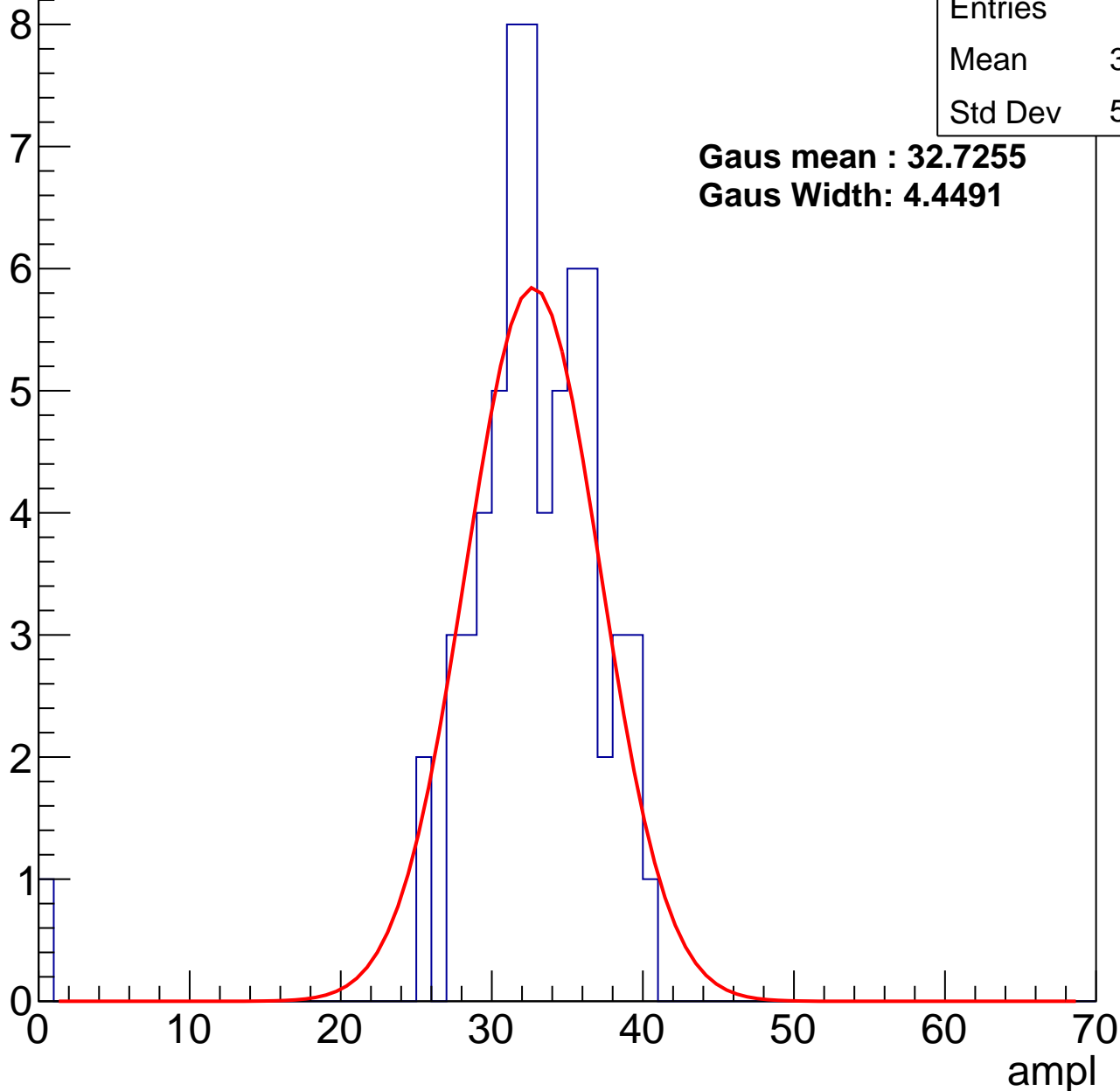
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	32.16
Std Dev	5.377

Gaus mean : 32.7255

Gaus Width: 4.4491



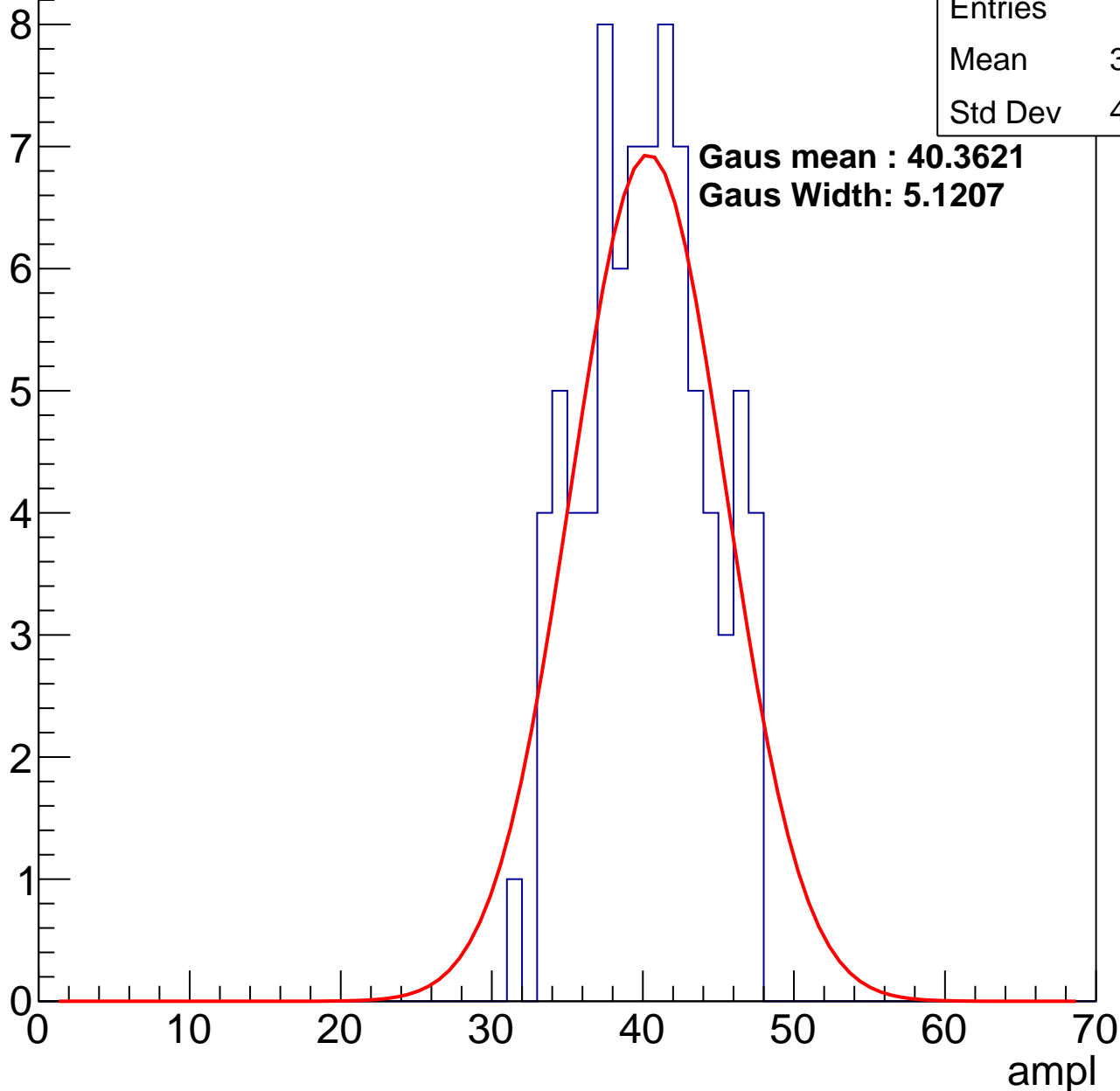
B0L001S, U25-ch97, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	39.76
Std Dev	4.005

Gaus mean : 40.3621
Gaus Width: 5.1207



B0L001S, U25-ch97, adc2

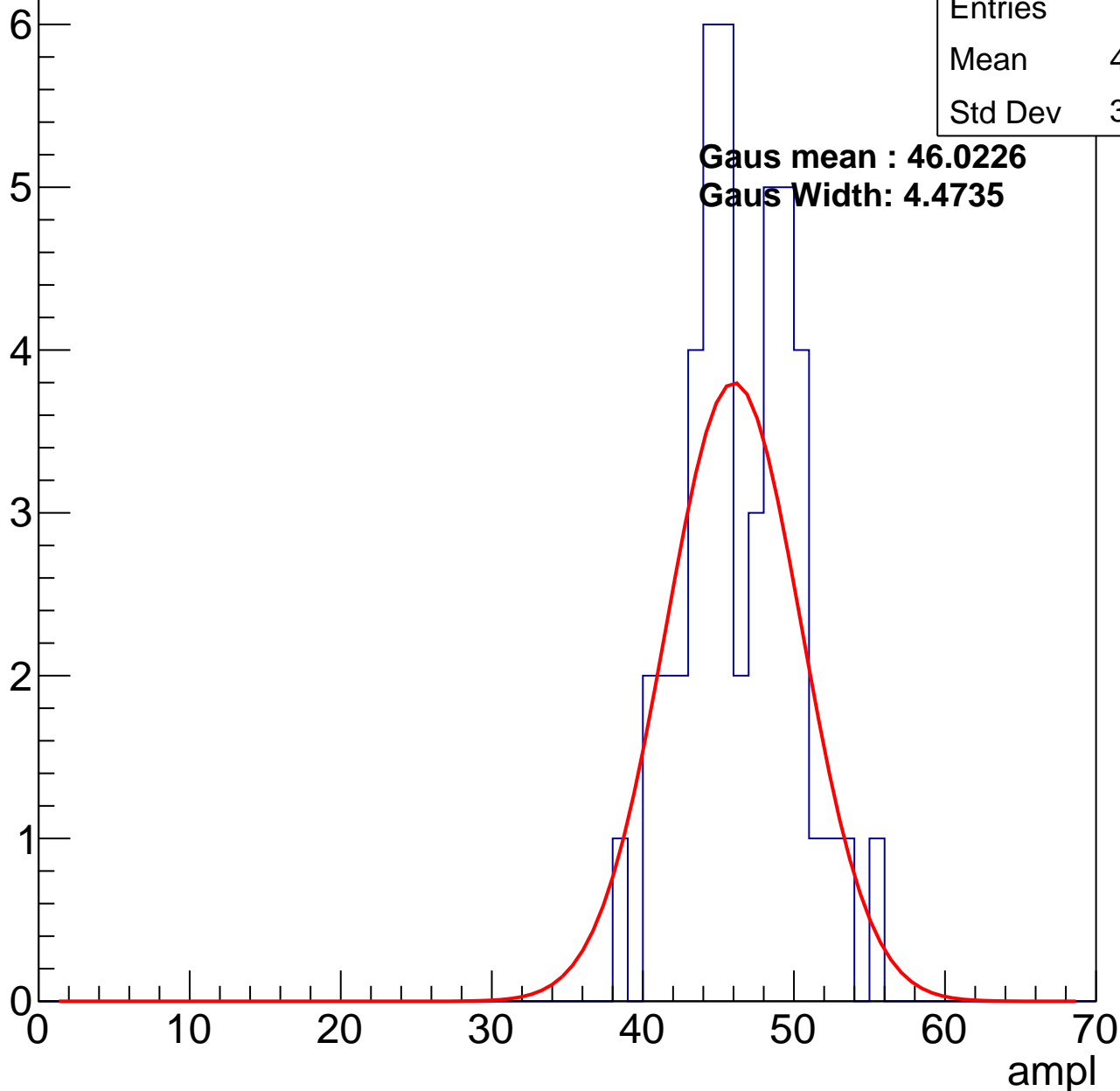
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	46.07
Std Dev	3.632

Gaus mean : 46.0226

Gaus Width: 4.4735



B0L001S, U25-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

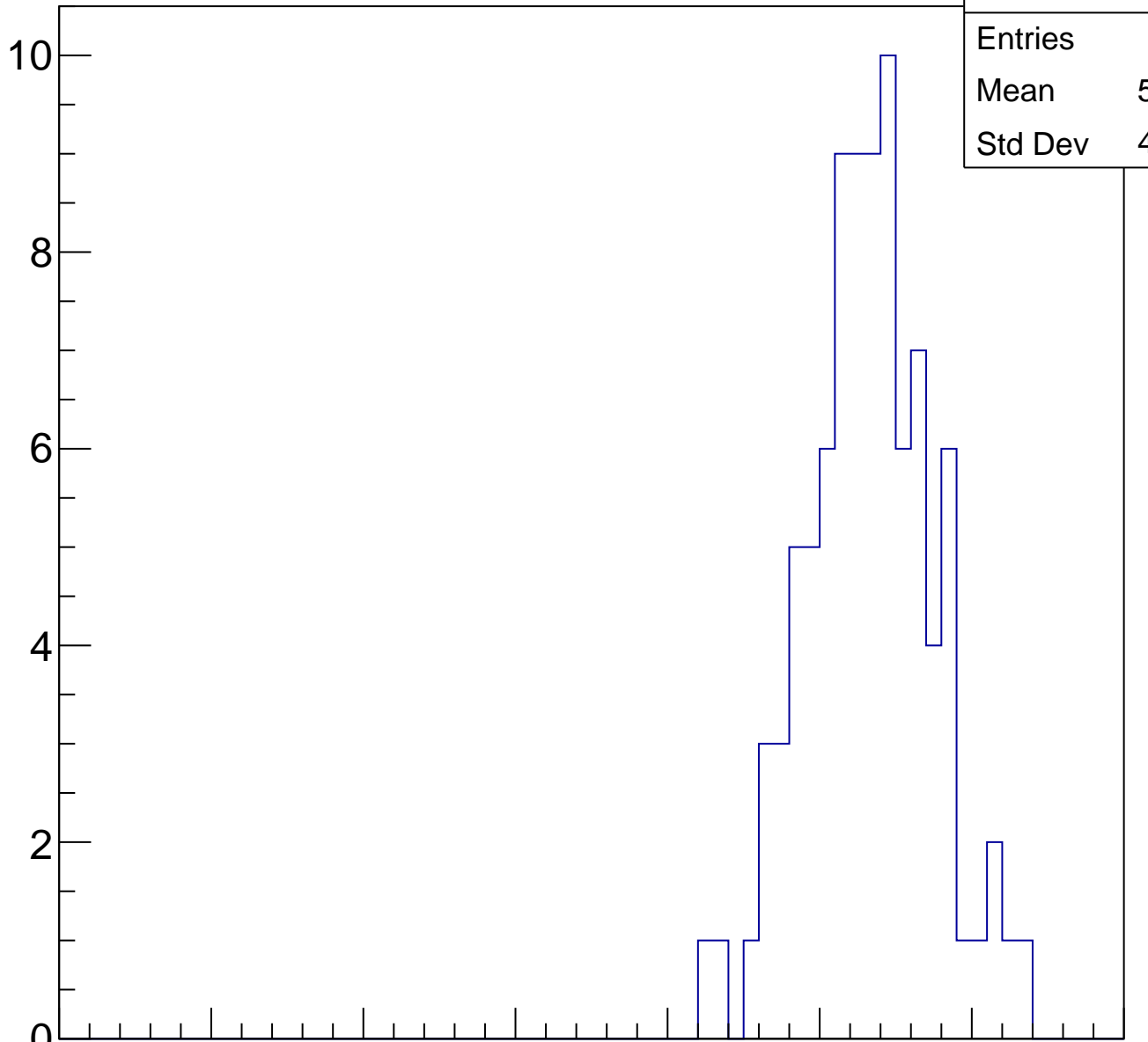
Entries	91
Mean	52.77
Std Dev	4.119

Entry

10
8
6
4
2
0

ampl

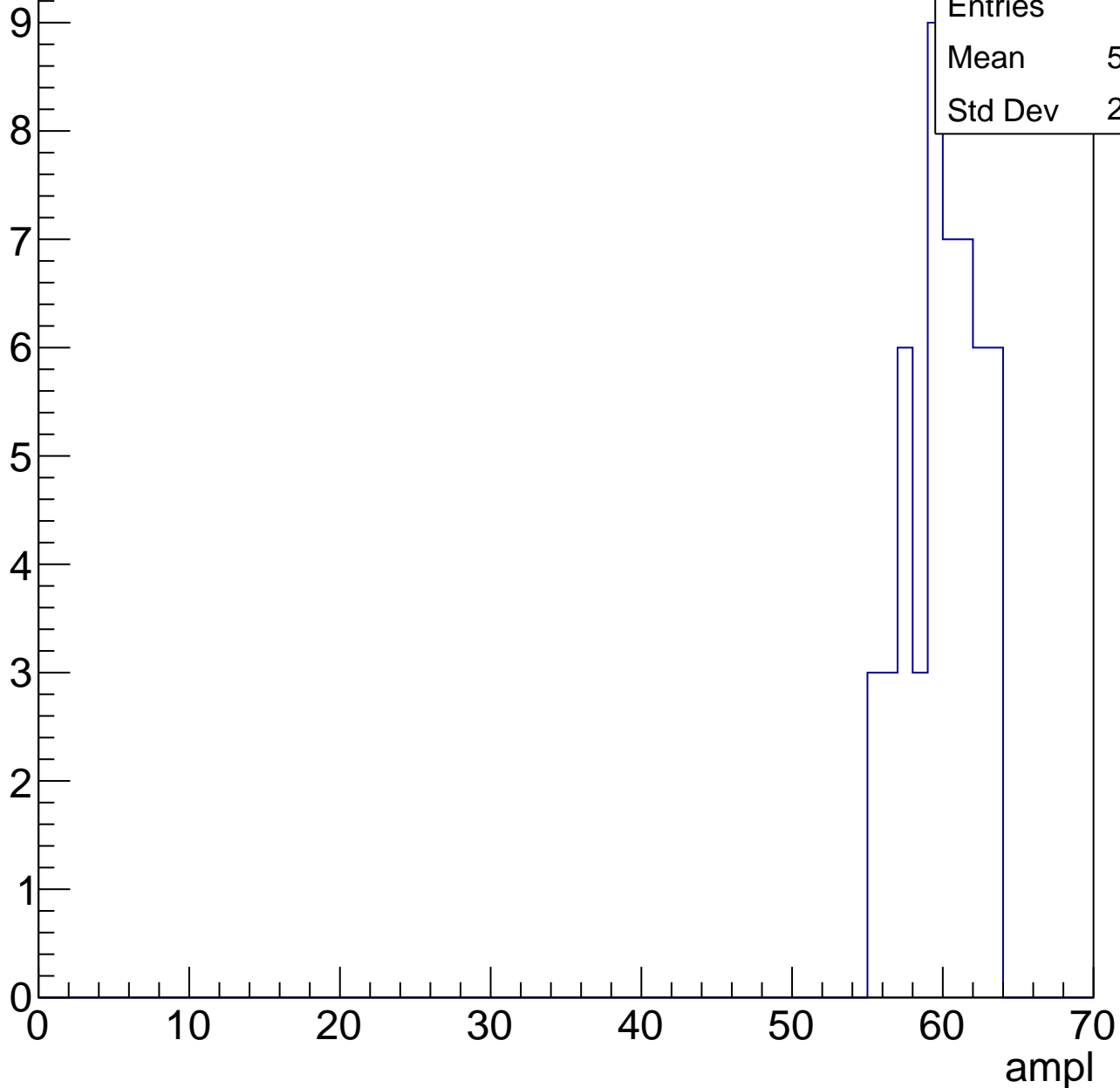
0 10 20 30 40 50 60 70



B0L001S, U25-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

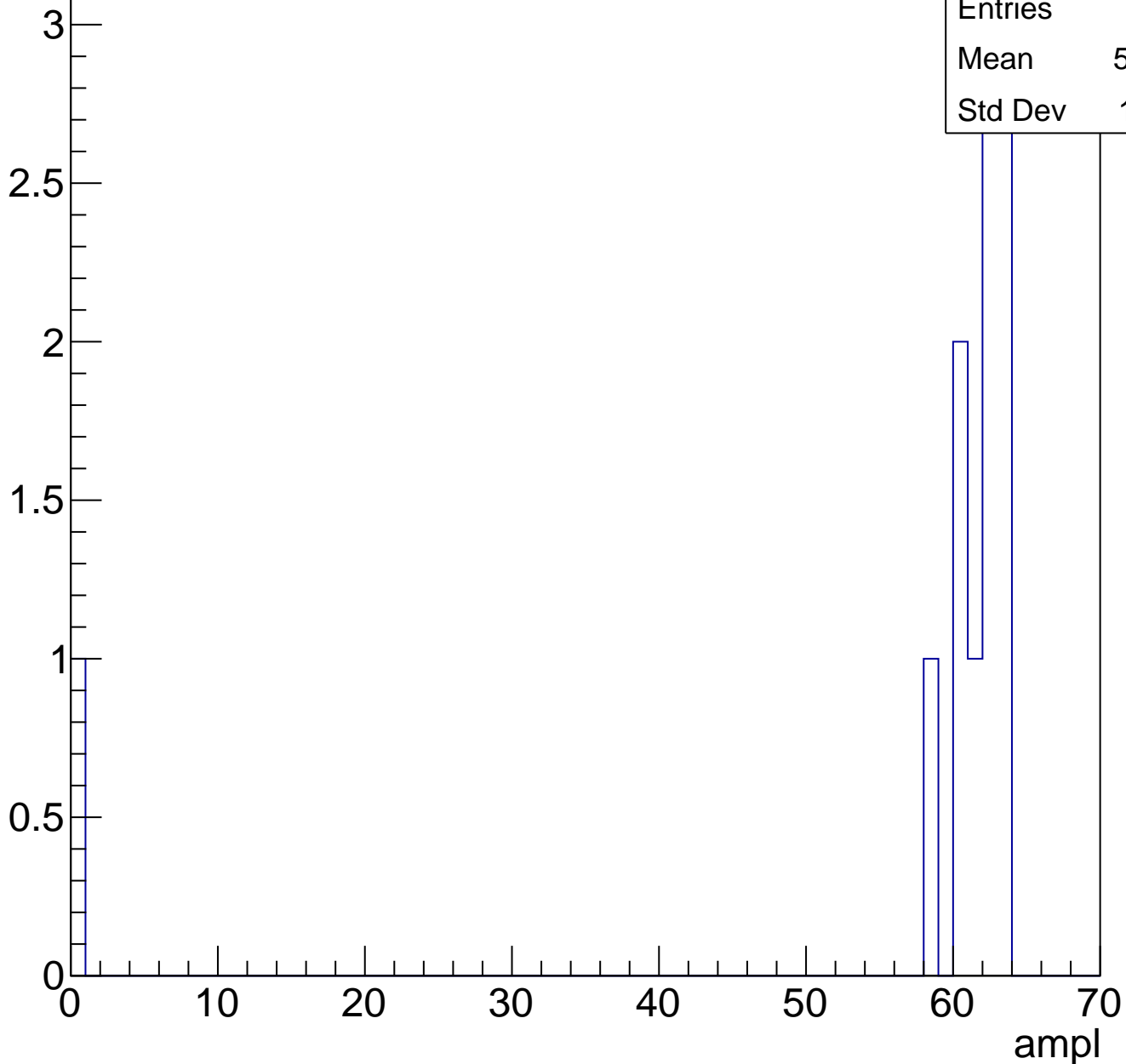
Entry



B0L001S, U25-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch98, adc0

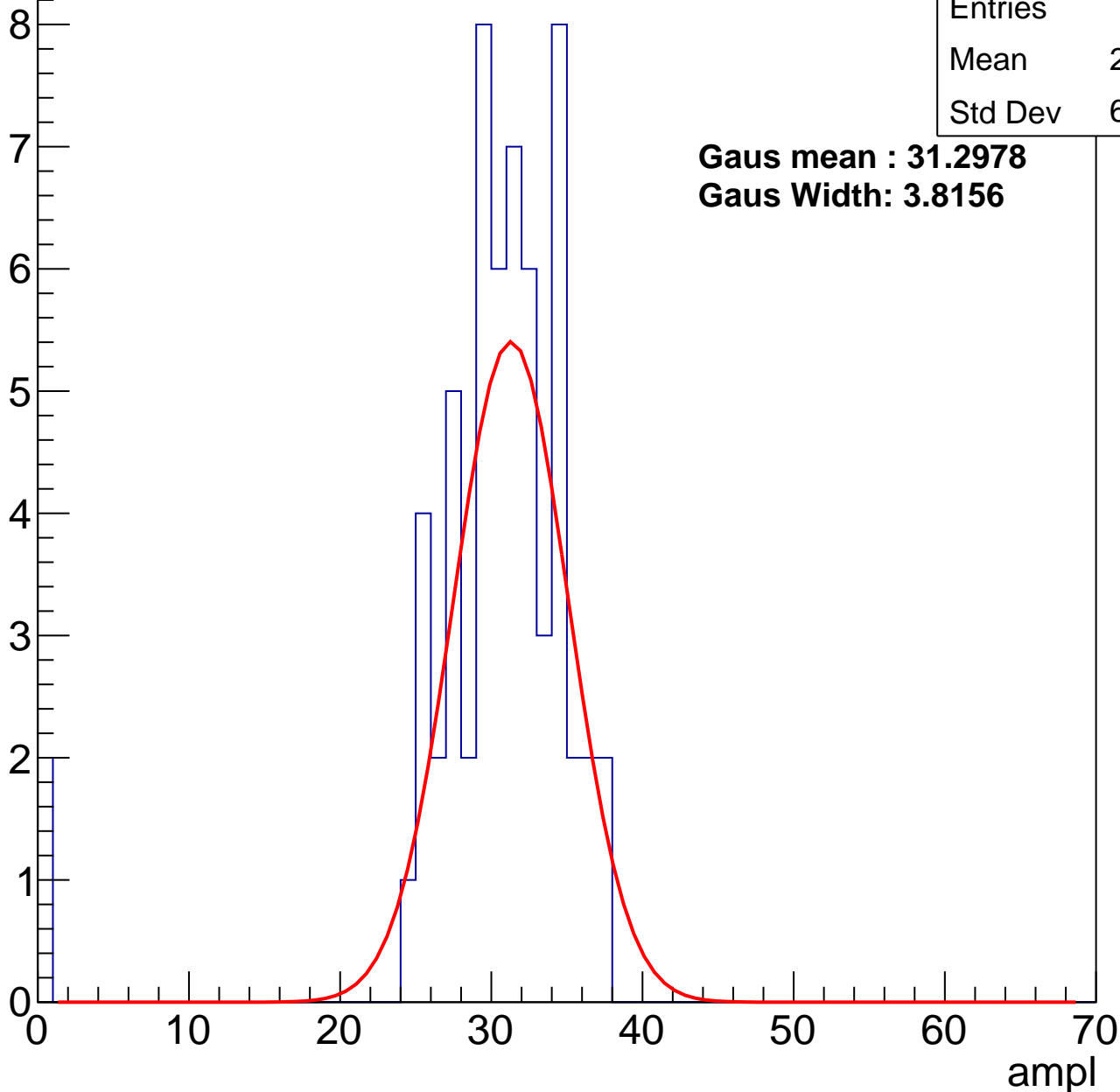
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.58
Std Dev	6.357

Gaus mean : 31.2978

Gaus Width: 3.8156



B0L001S, U25-ch98, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	37.85
Std Dev	3.722

Gaus mean : 38.5289

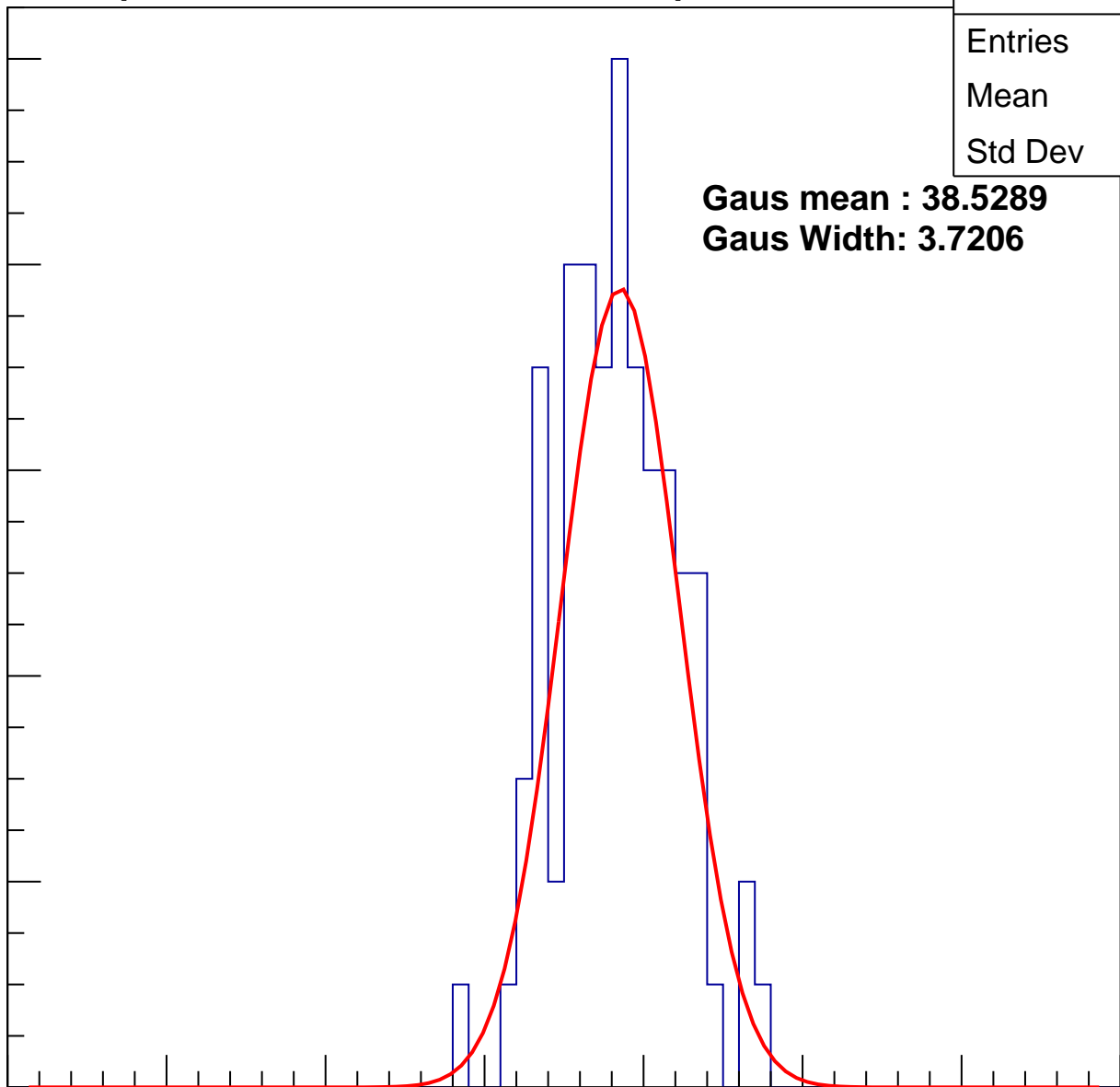
Gaus Width: 3.7206

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch98, adc2

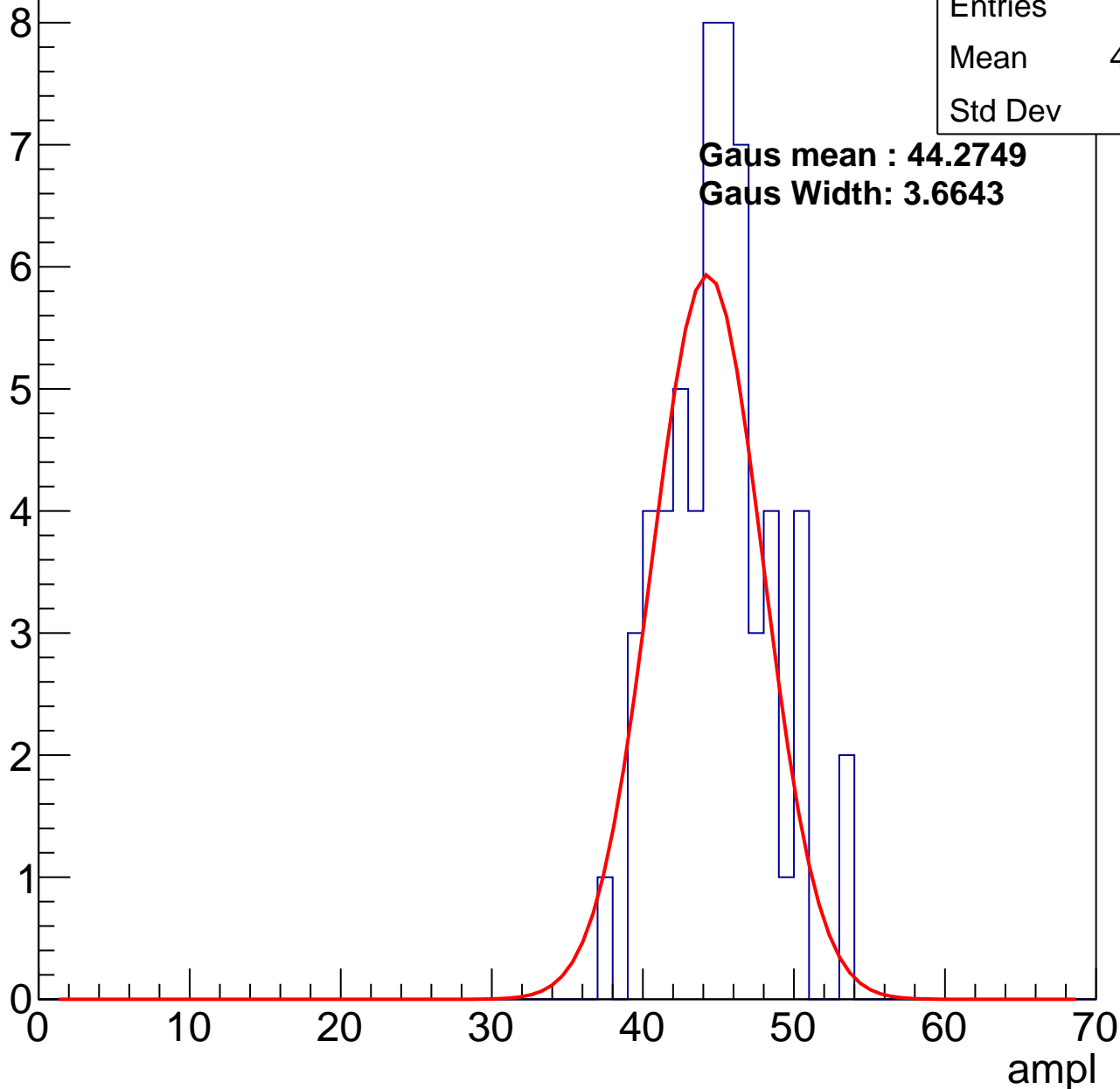
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.52
Std Dev	3.45

Gaus mean : 44.2749

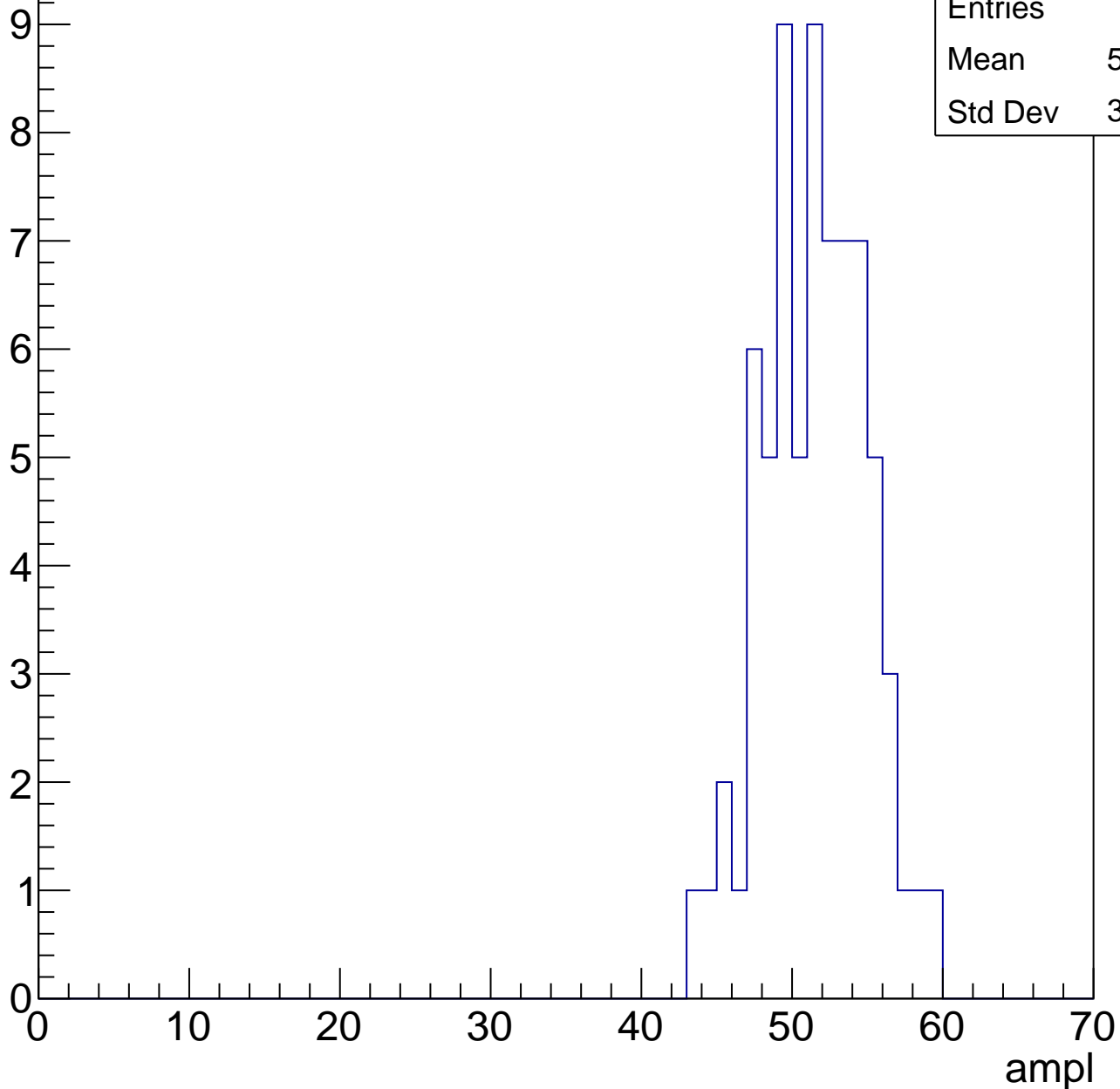
Gaus Width: 3.6643



B0L001S, U25-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



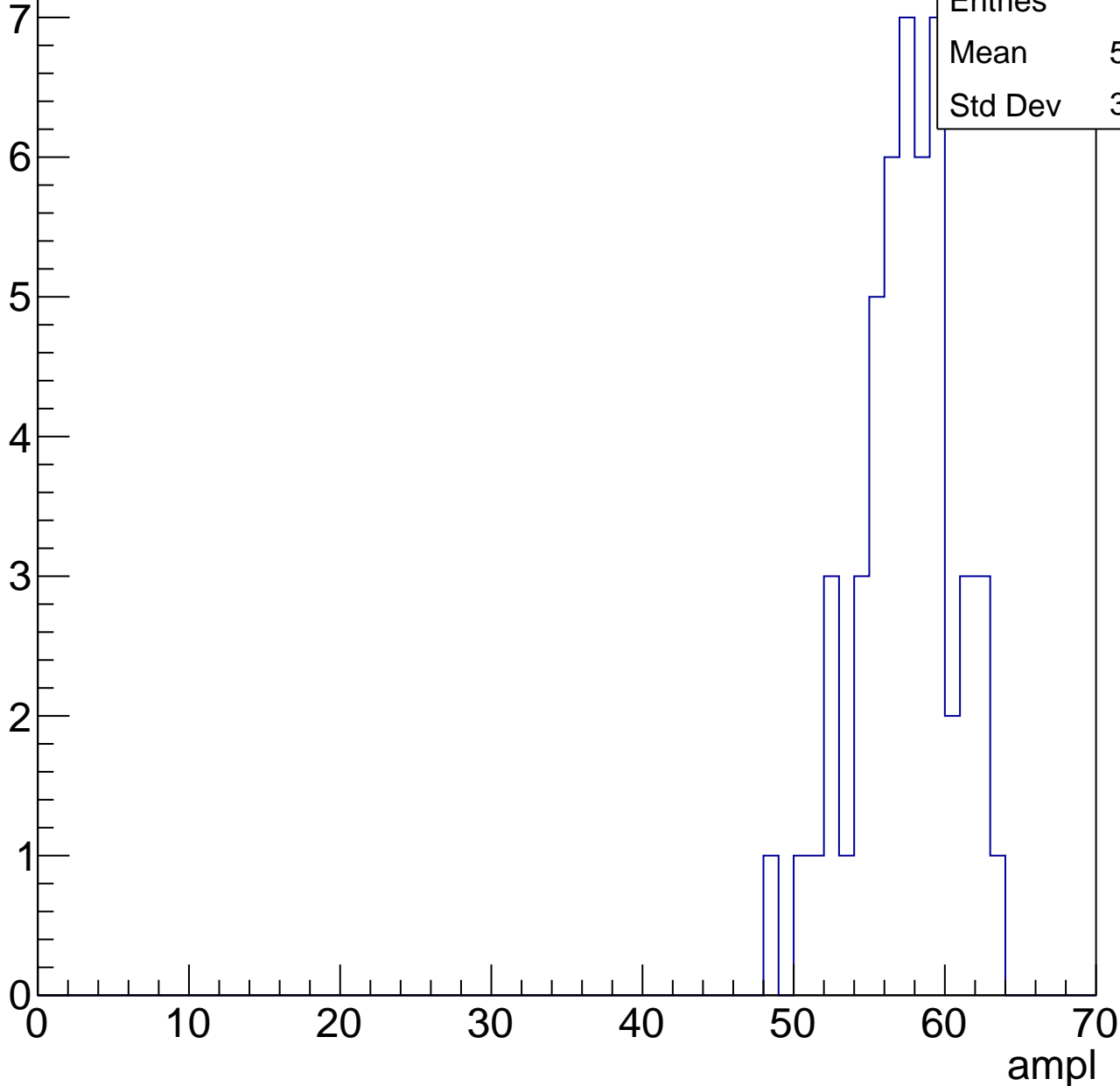
Entries	71
Mean	51.06
Std Dev	3.344

B0L001S, U25-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.86
Std Dev	3.225

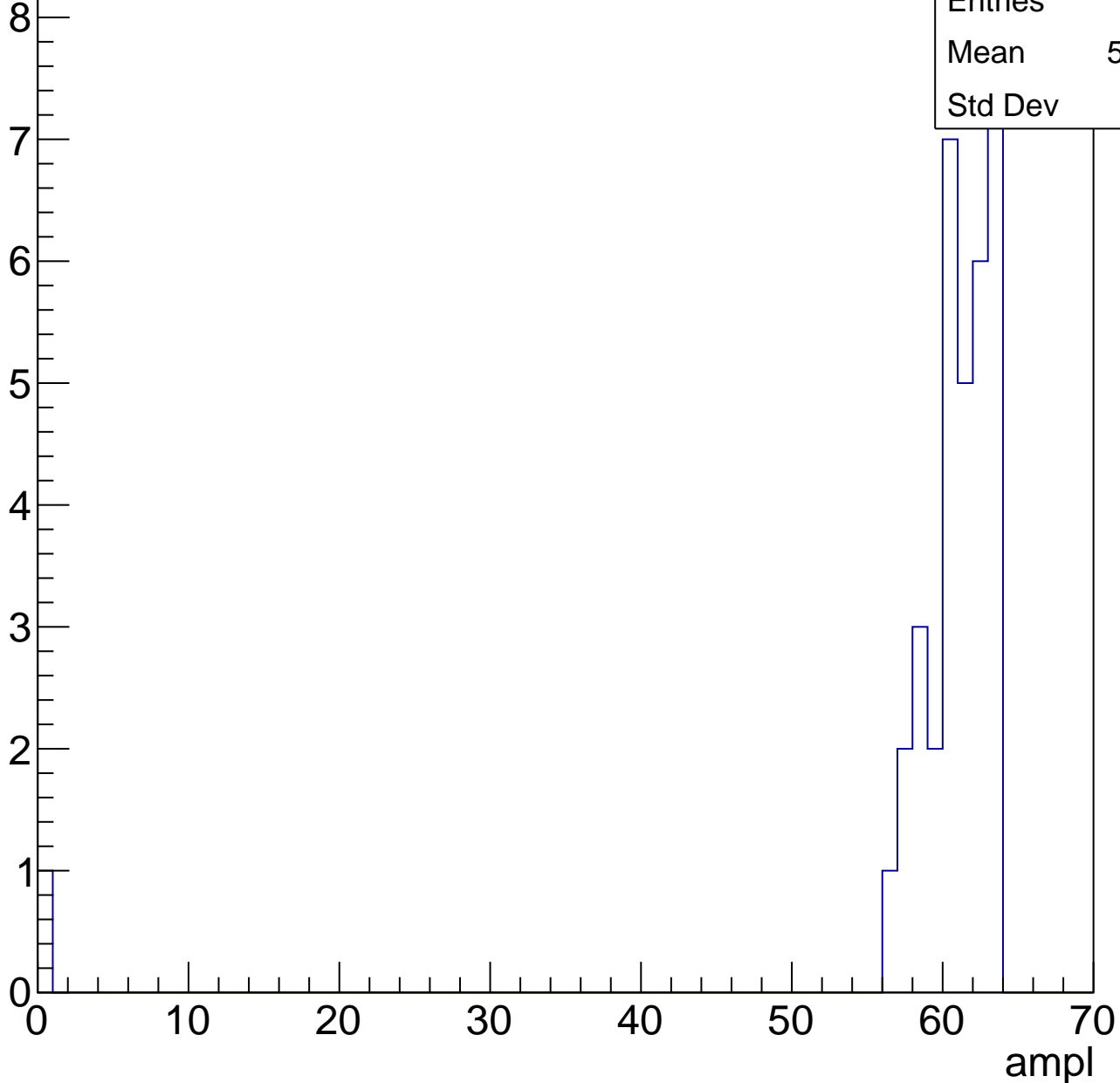


B0L001S, U25-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.94
Std Dev	10.3



B0L001S, U25-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B0L001S, U25-ch99, adc0

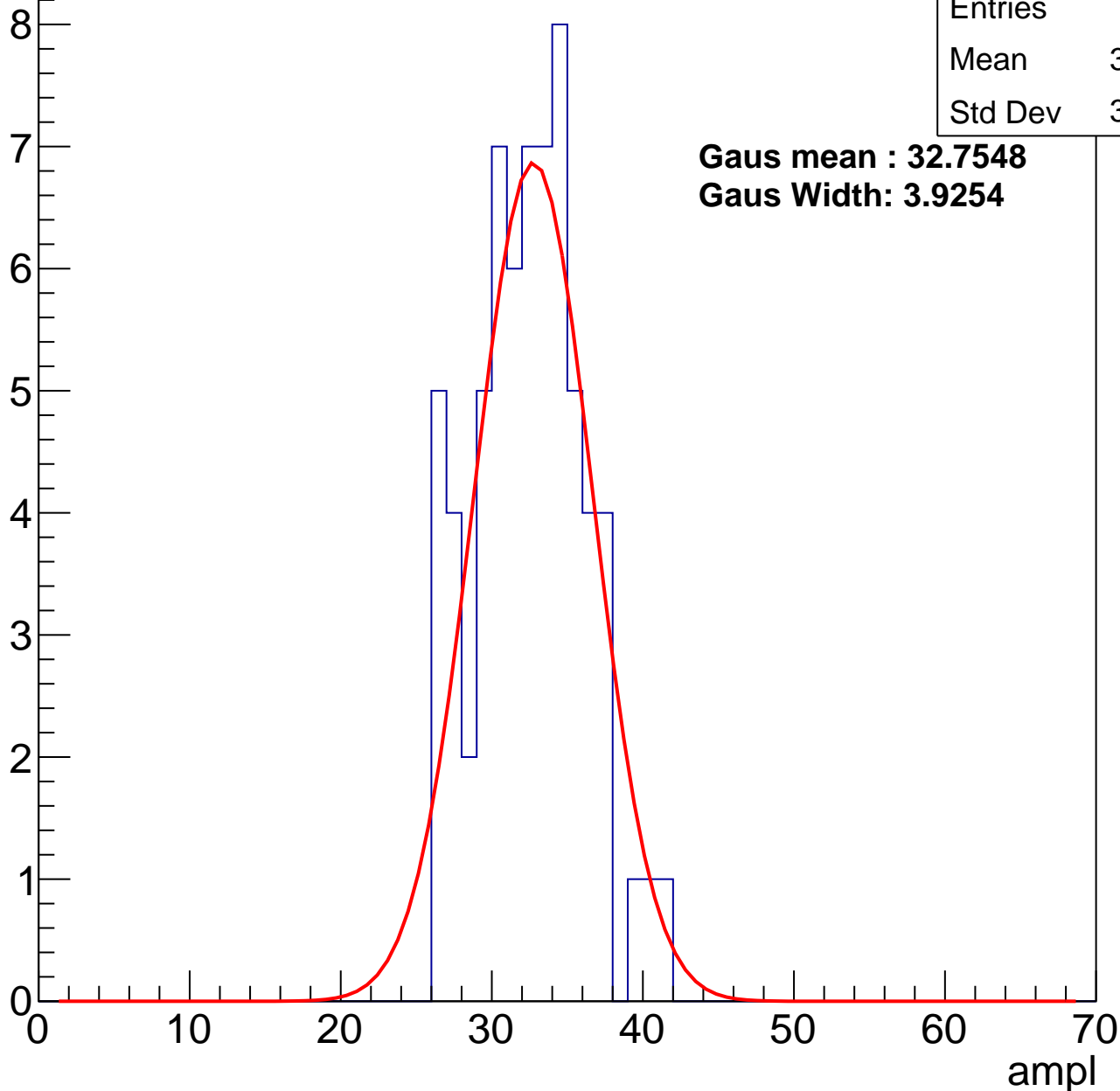
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	32.07
Std Dev	3.525

Gaus mean : 32.7548

Gaus Width: 3.9254



B0L001S, U25-ch99, adc1

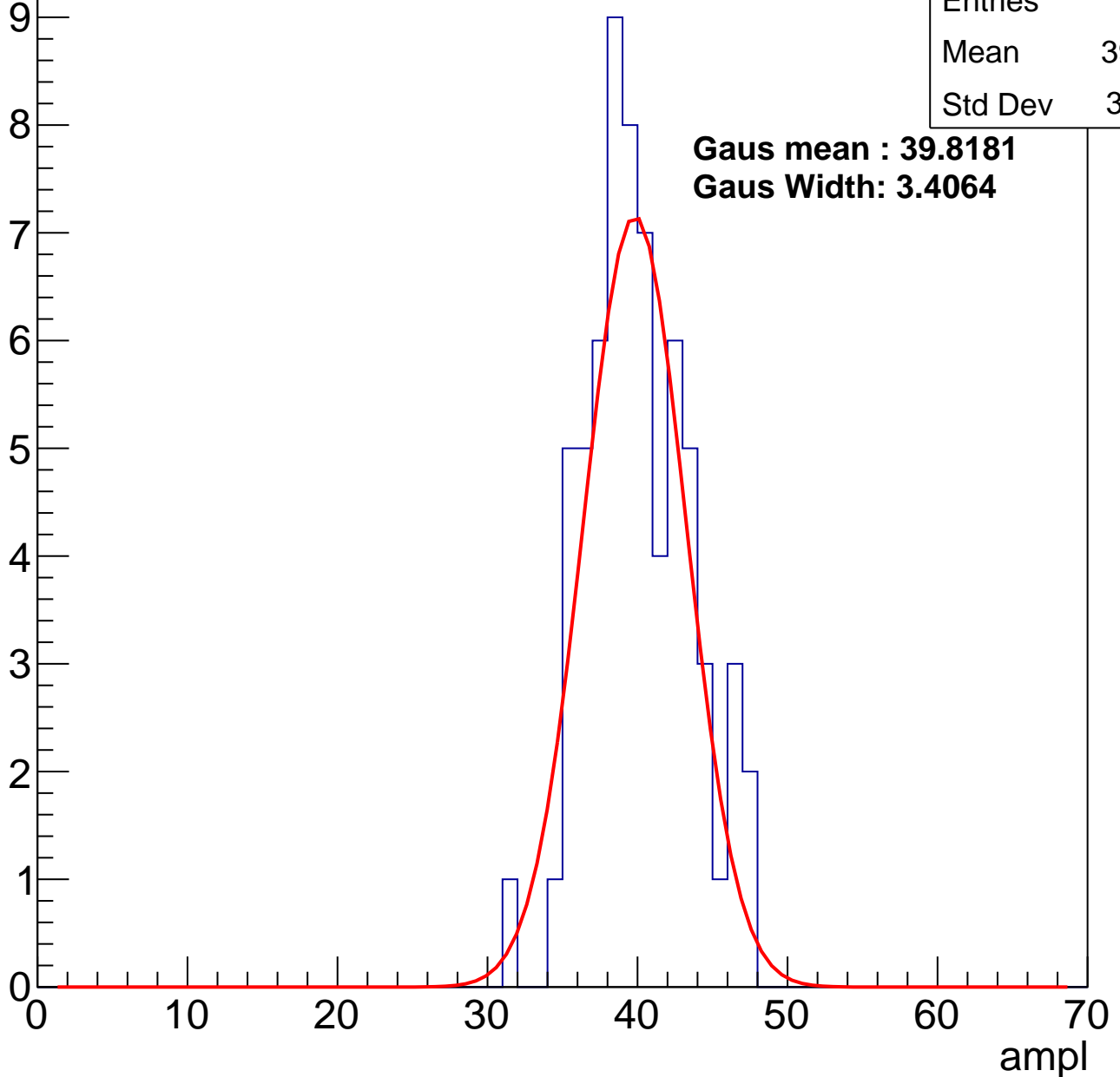
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	39.64
Std Dev	3.401

Gaus mean : 39.8181

Gaus Width: 3.4064



B0L001S, U25-ch99, adc2

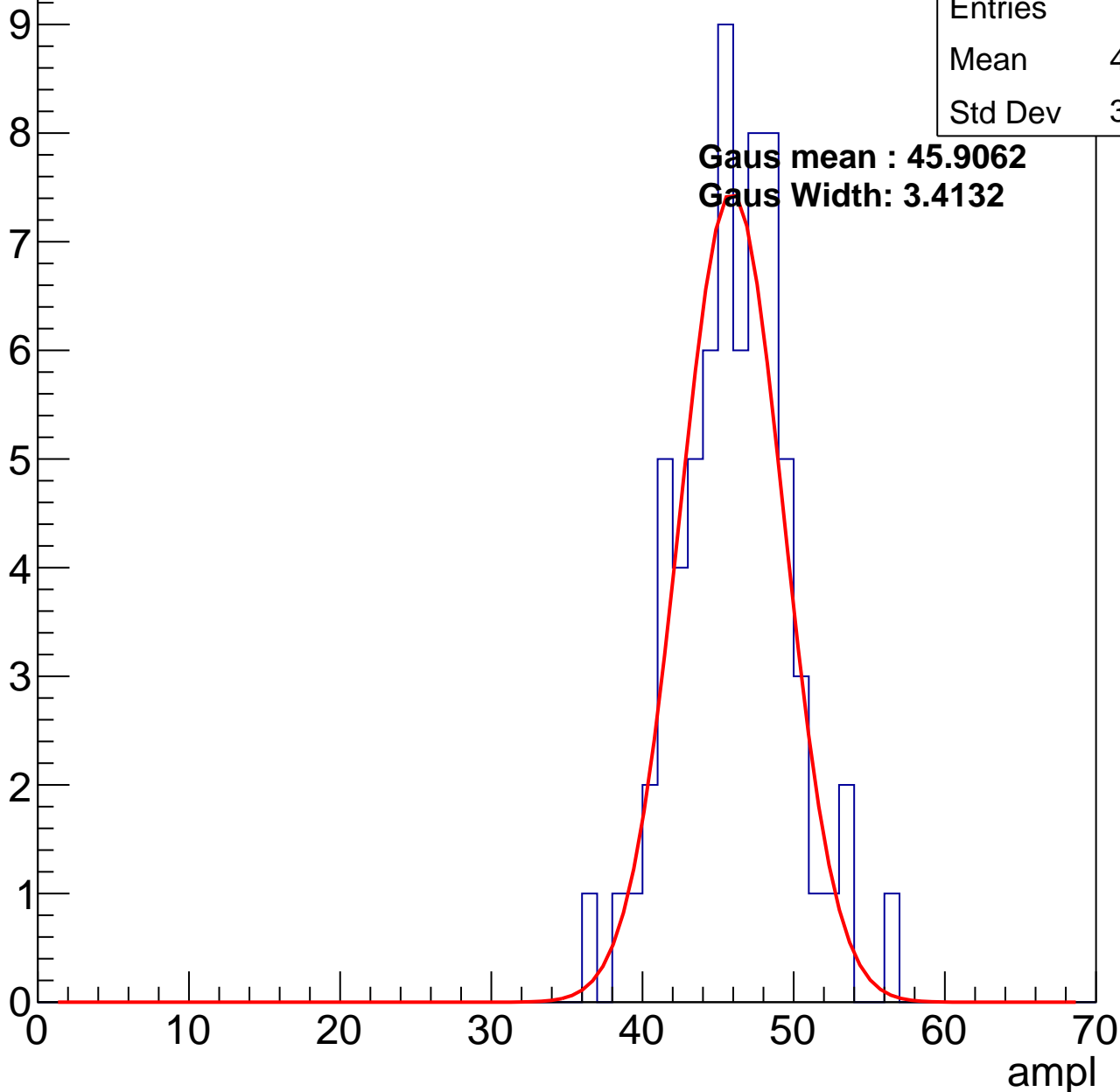
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	45.59
Std Dev	3.676

Gaus mean : 45.9062

Gaus Width: 3.4132

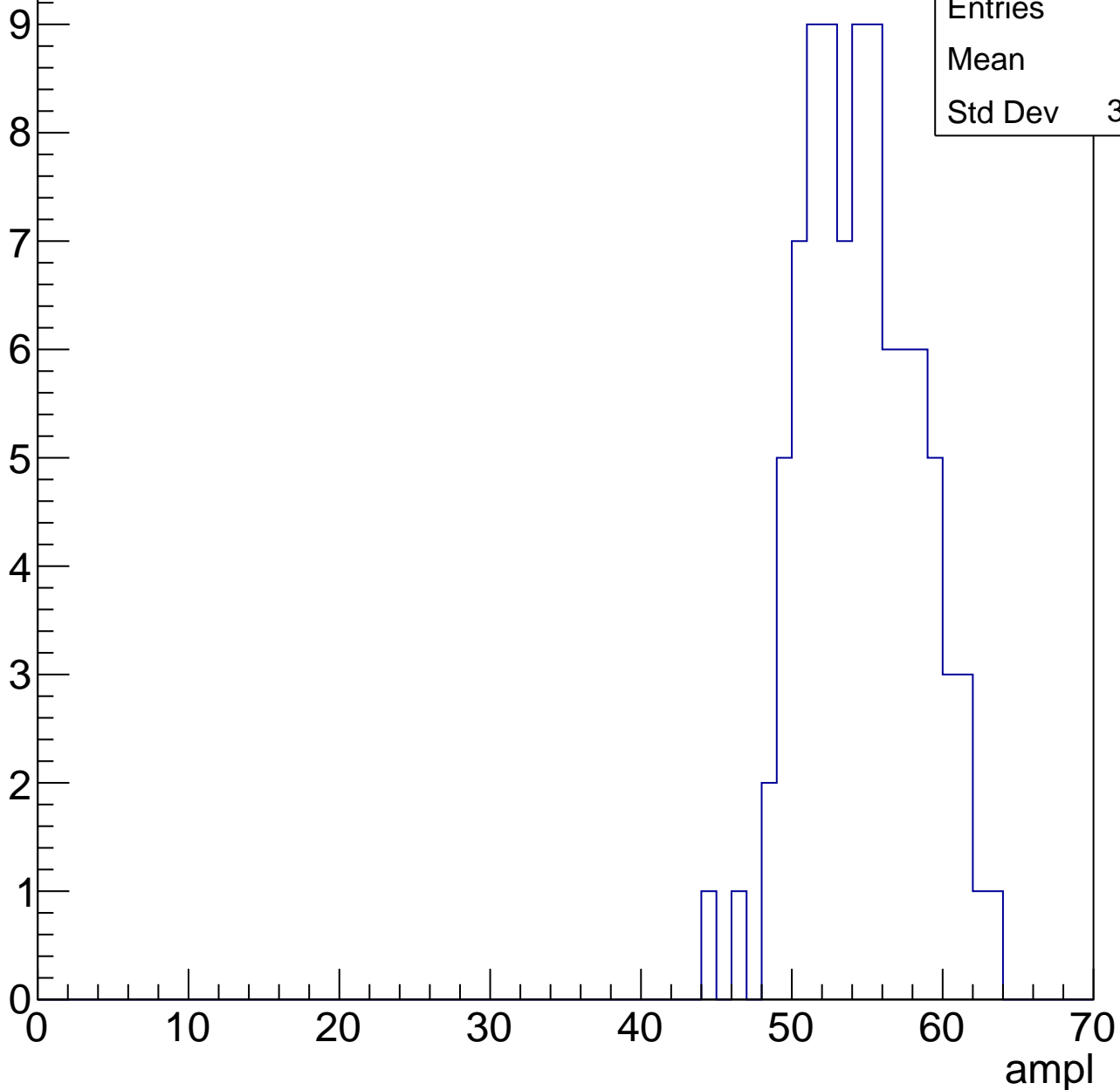


B0L001S, U25-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	90
Mean	54.1
Std Dev	3.807

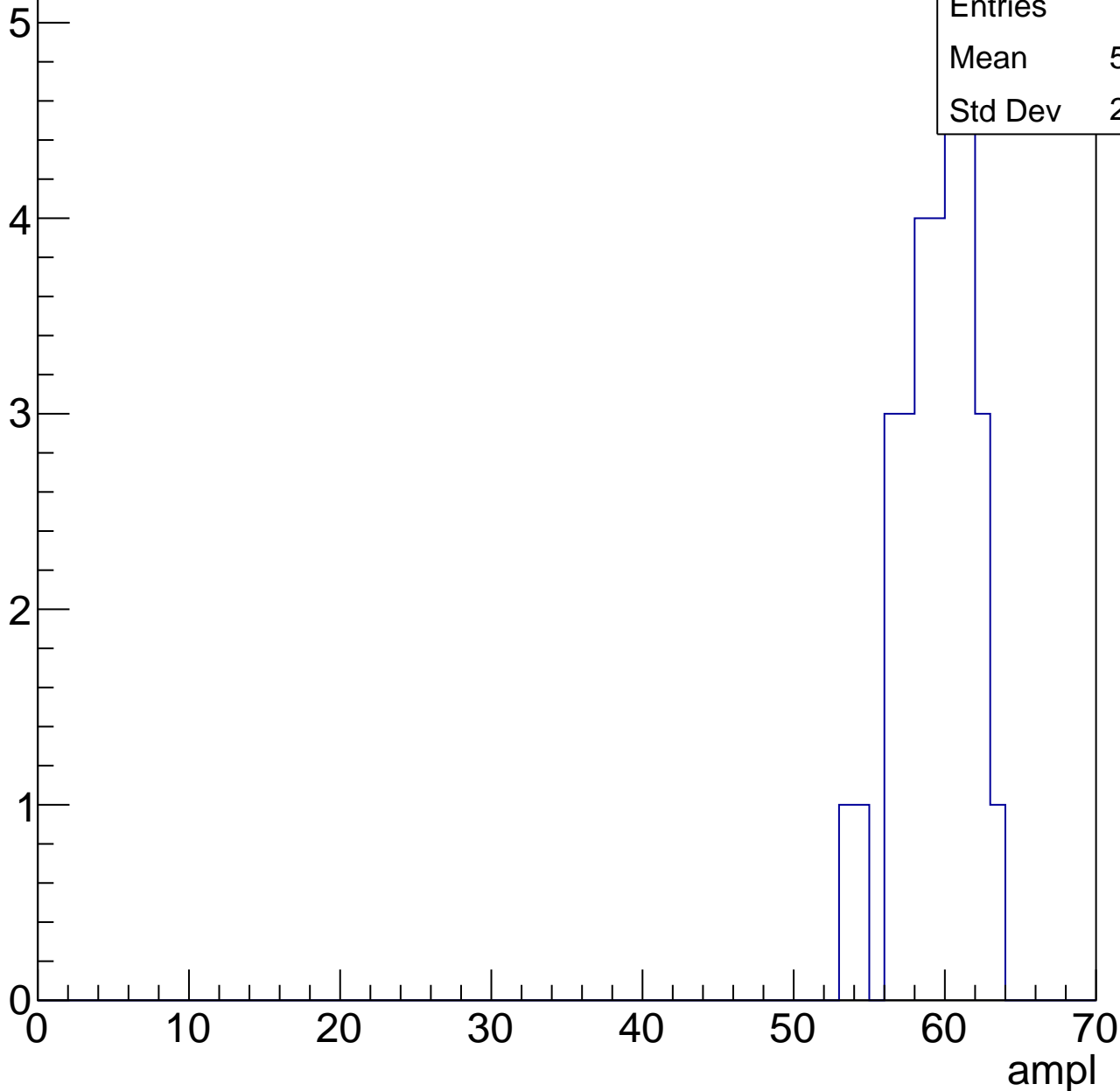


B0L001S, U25-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.93
Std Dev	2.394

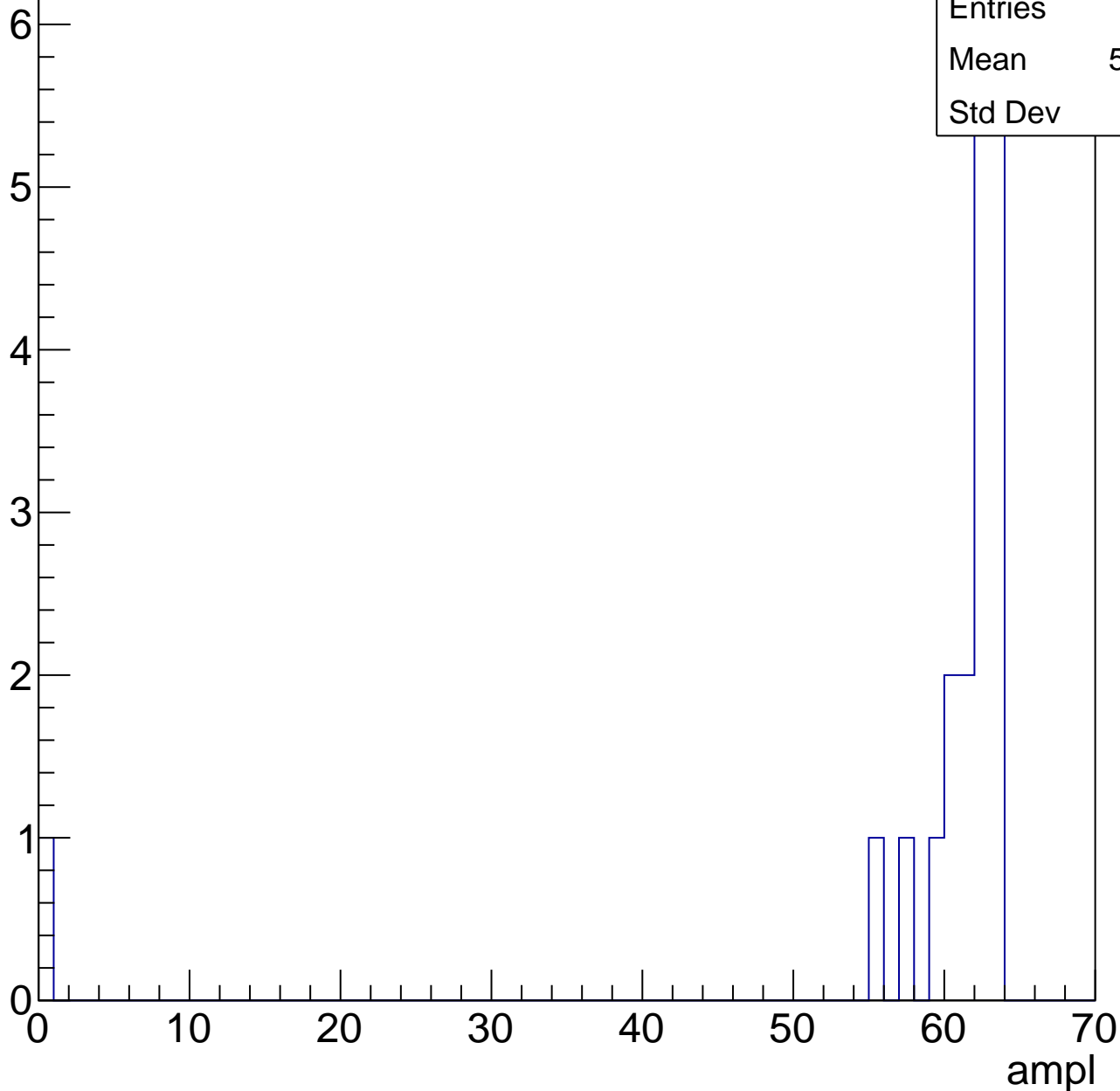


B0L001S, U25-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	20
Mean	58.15
Std Dev	13.5



B0L001S, U25-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch100, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	56
Mean	30.11
Std Dev	3.244

Gaus mean : 30.5725

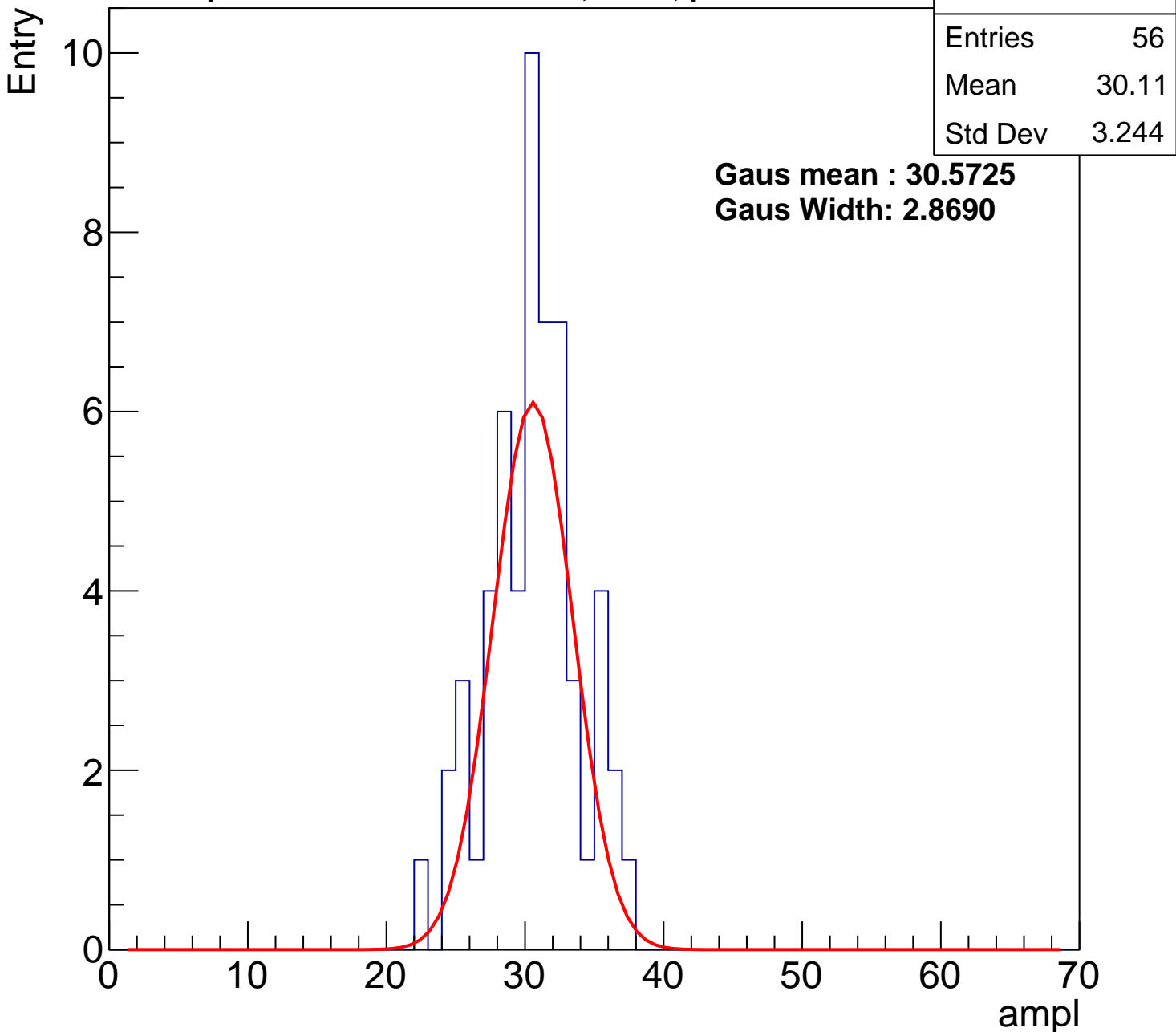
Gaus Width: 2.8690

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch100, adc1

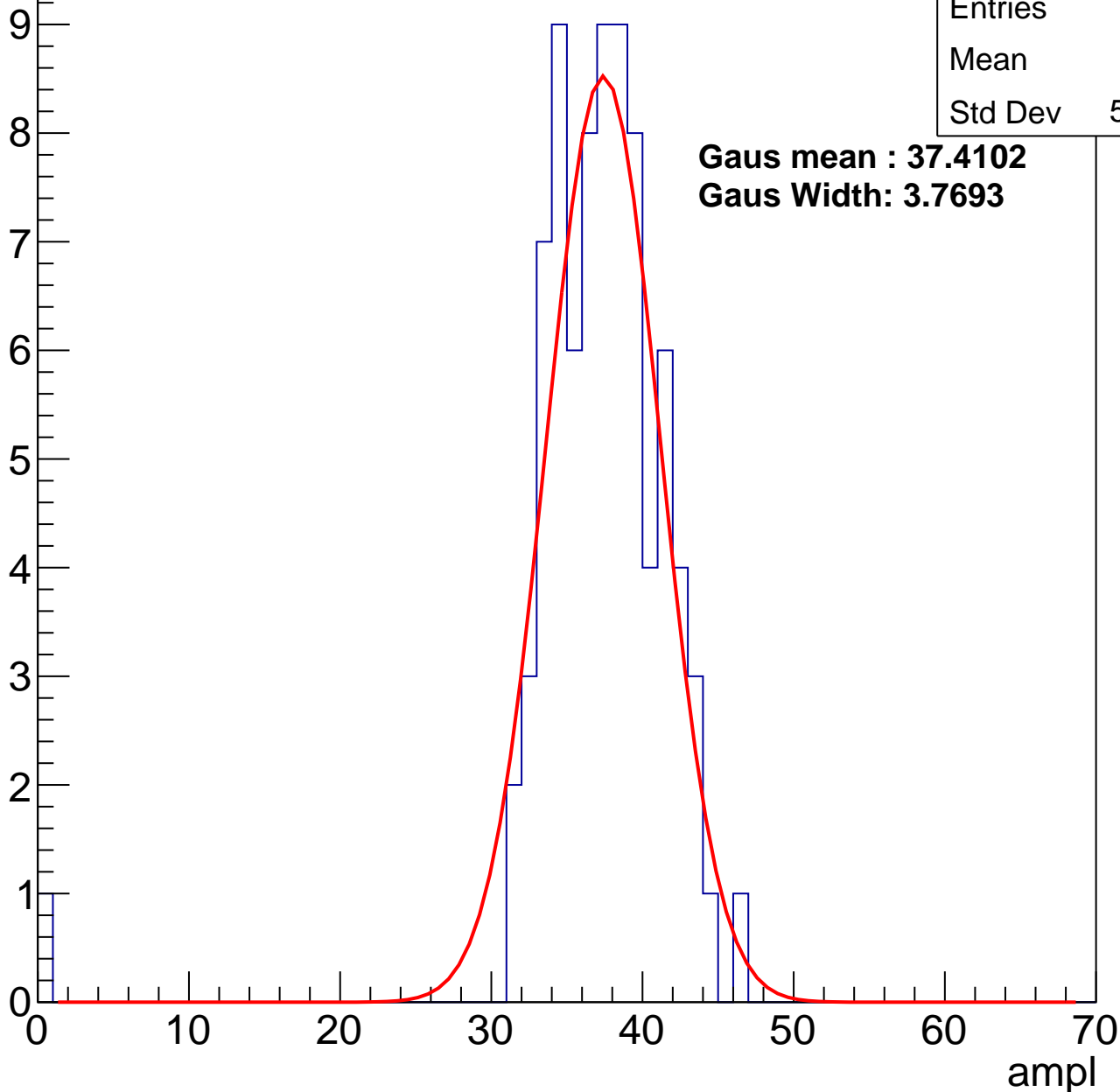
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	36.7
Std Dev	5.264

Gaus mean : 37.4102

Gaus Width: 3.7693



B0L001S, U25-ch100, adc2

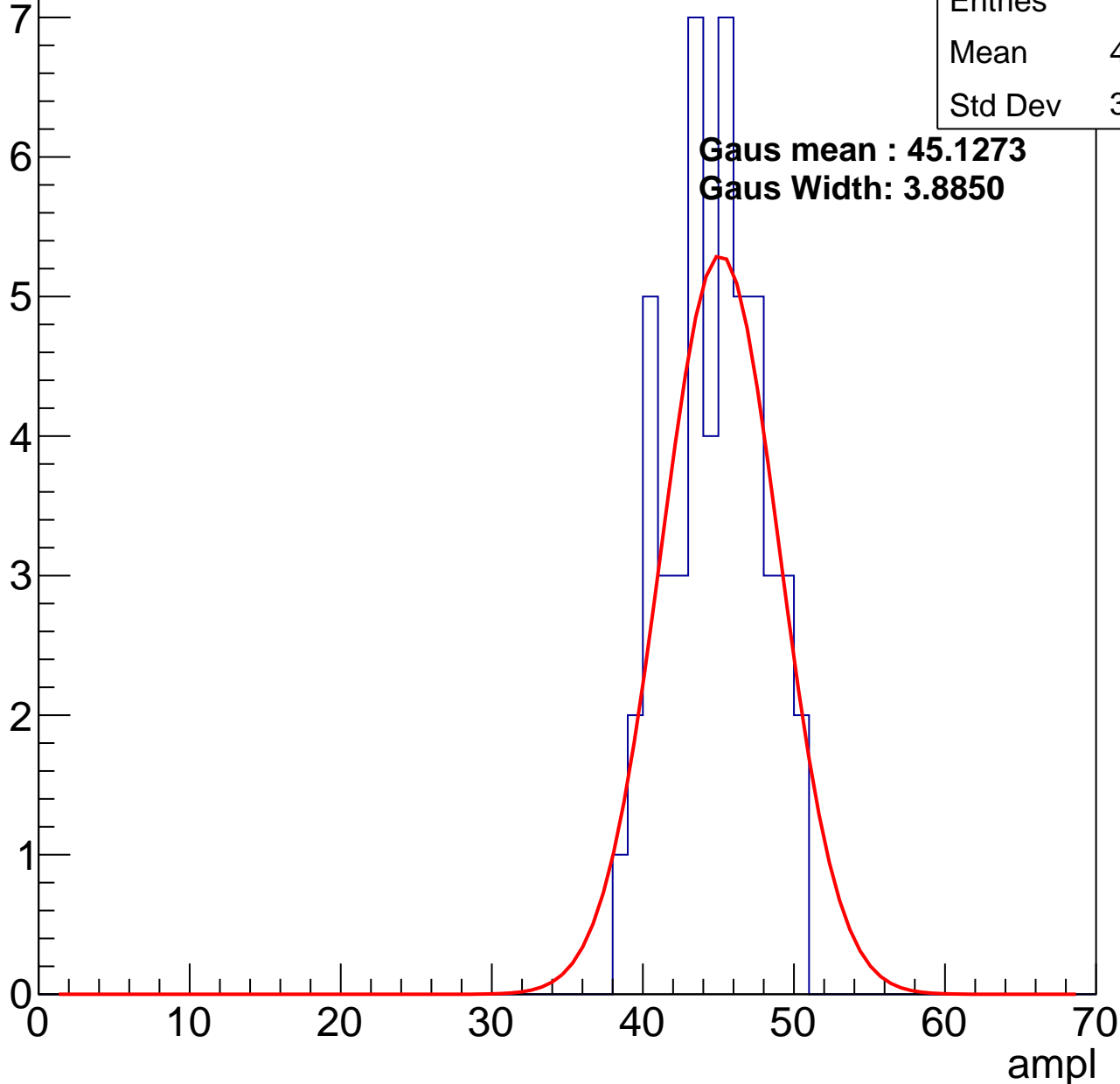
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	44.26
Std Dev	3.084

Gaus mean : 45.1273

Gaus Width: 3.8850

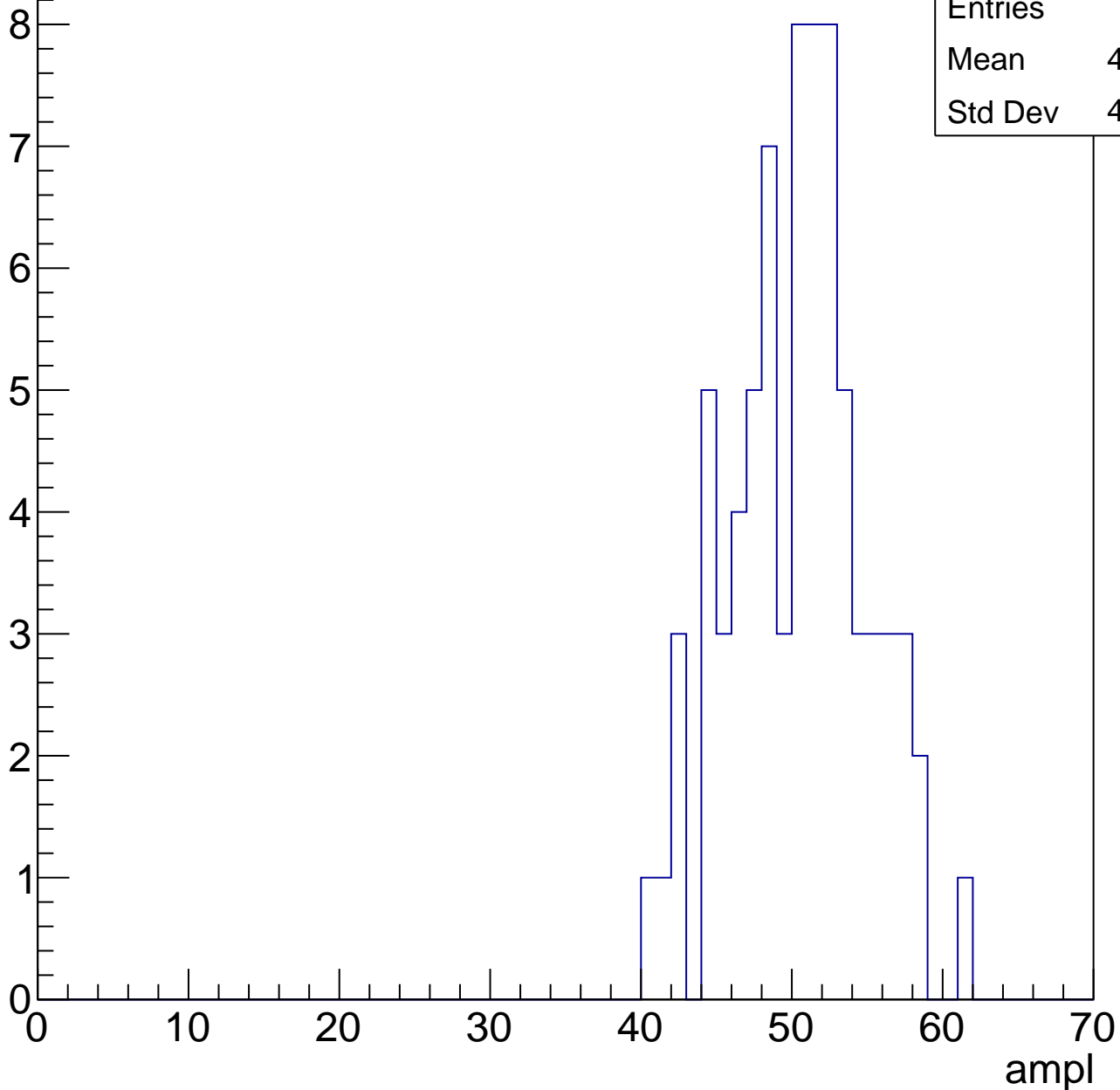


B0L001S, U25-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	49.95
Std Dev	4.413

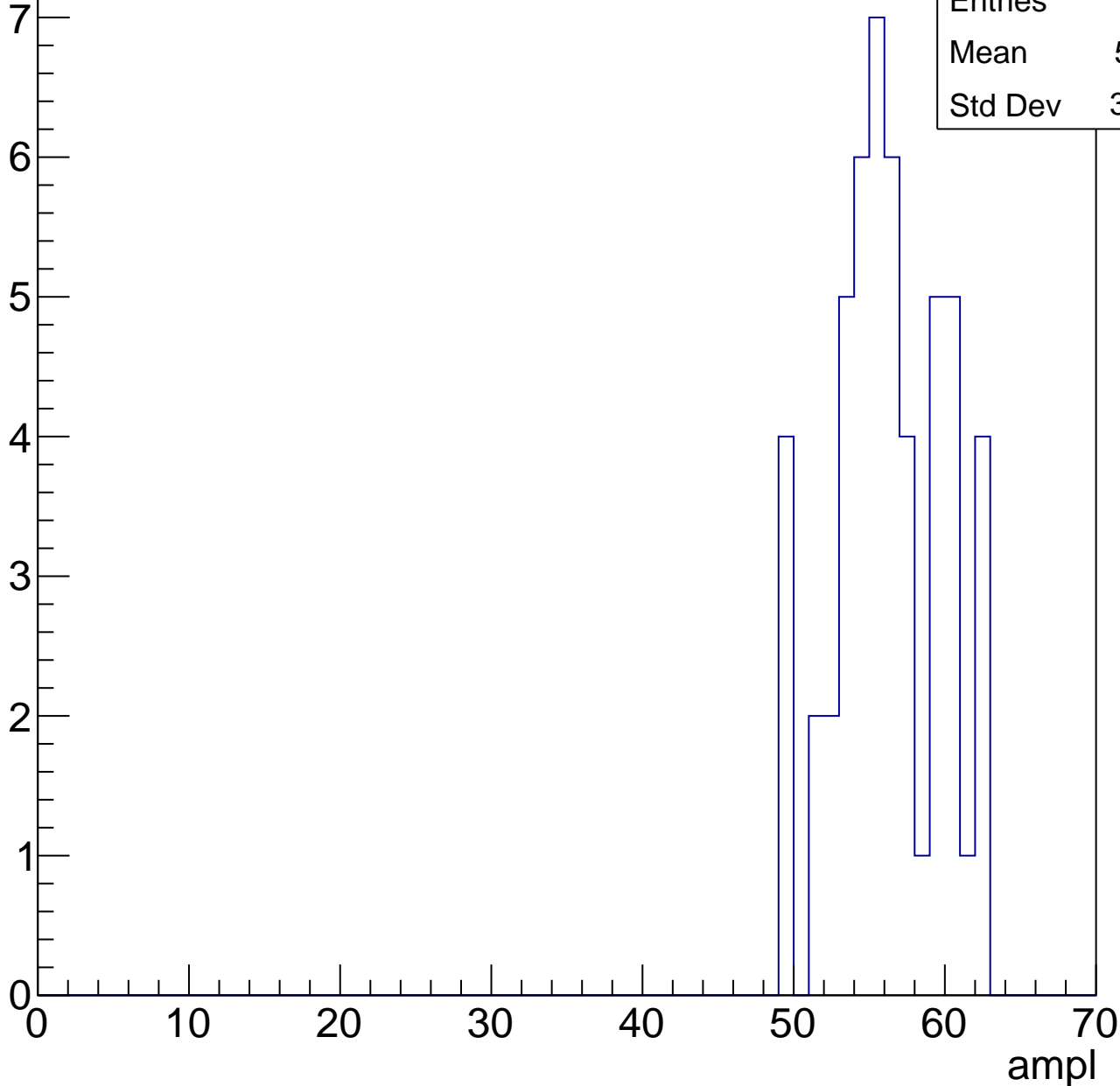


B0L001S, U25-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	55.81
Std Dev	3.547

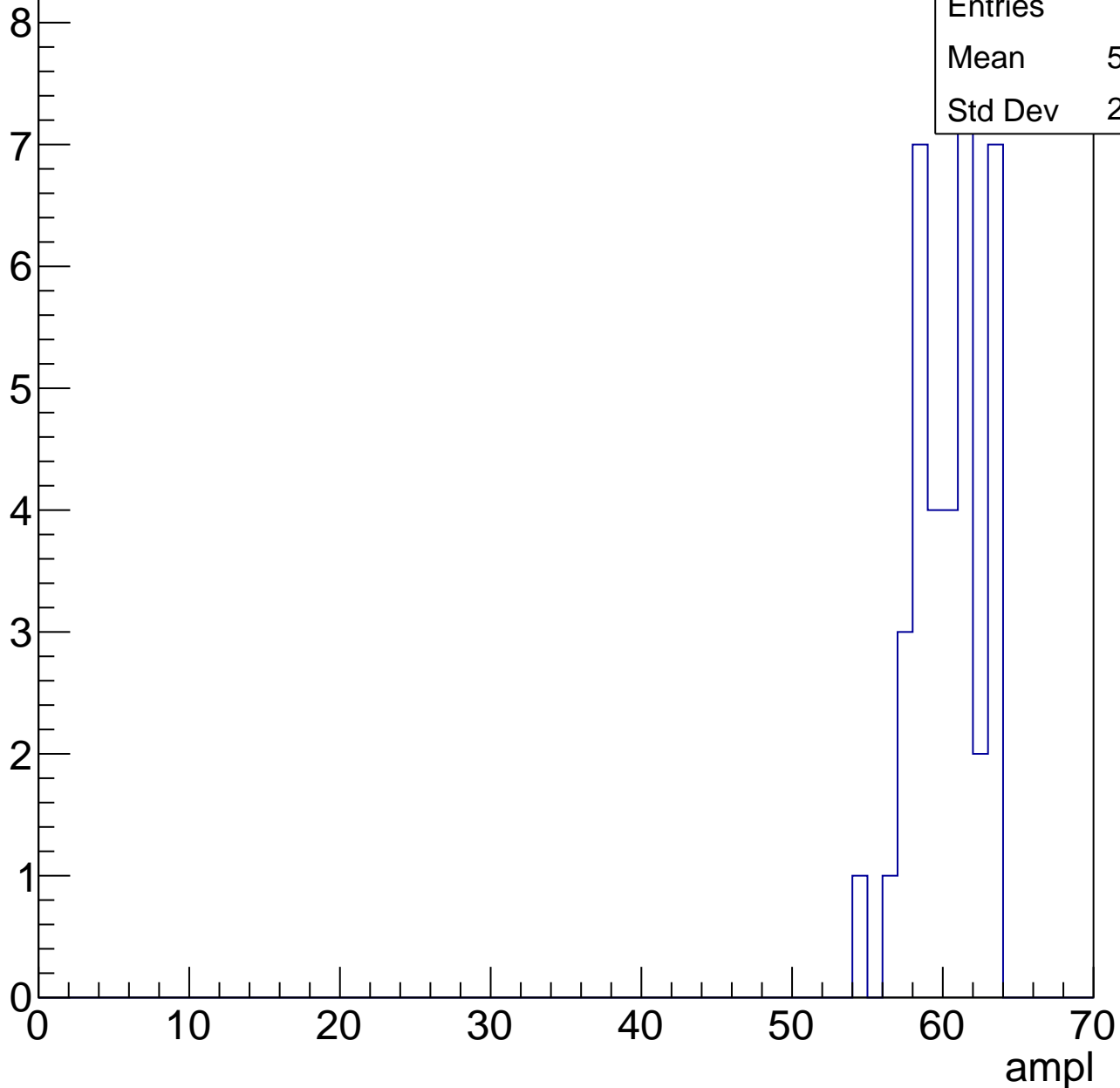


B0L001S, U25-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

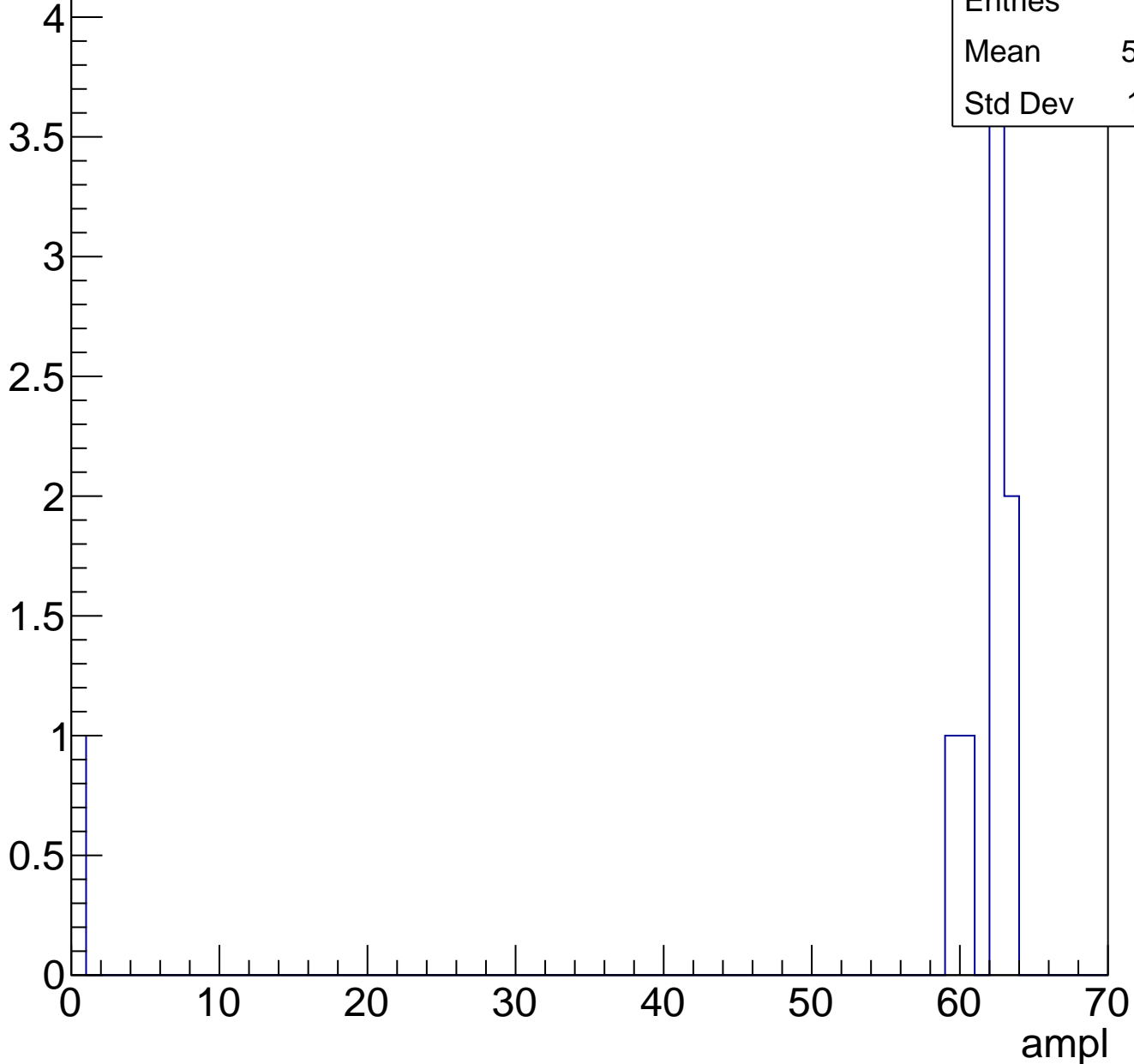
Entries	37
Mean	59.89
Std Dev	2.264



B0L001S, U25-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch101, adc0

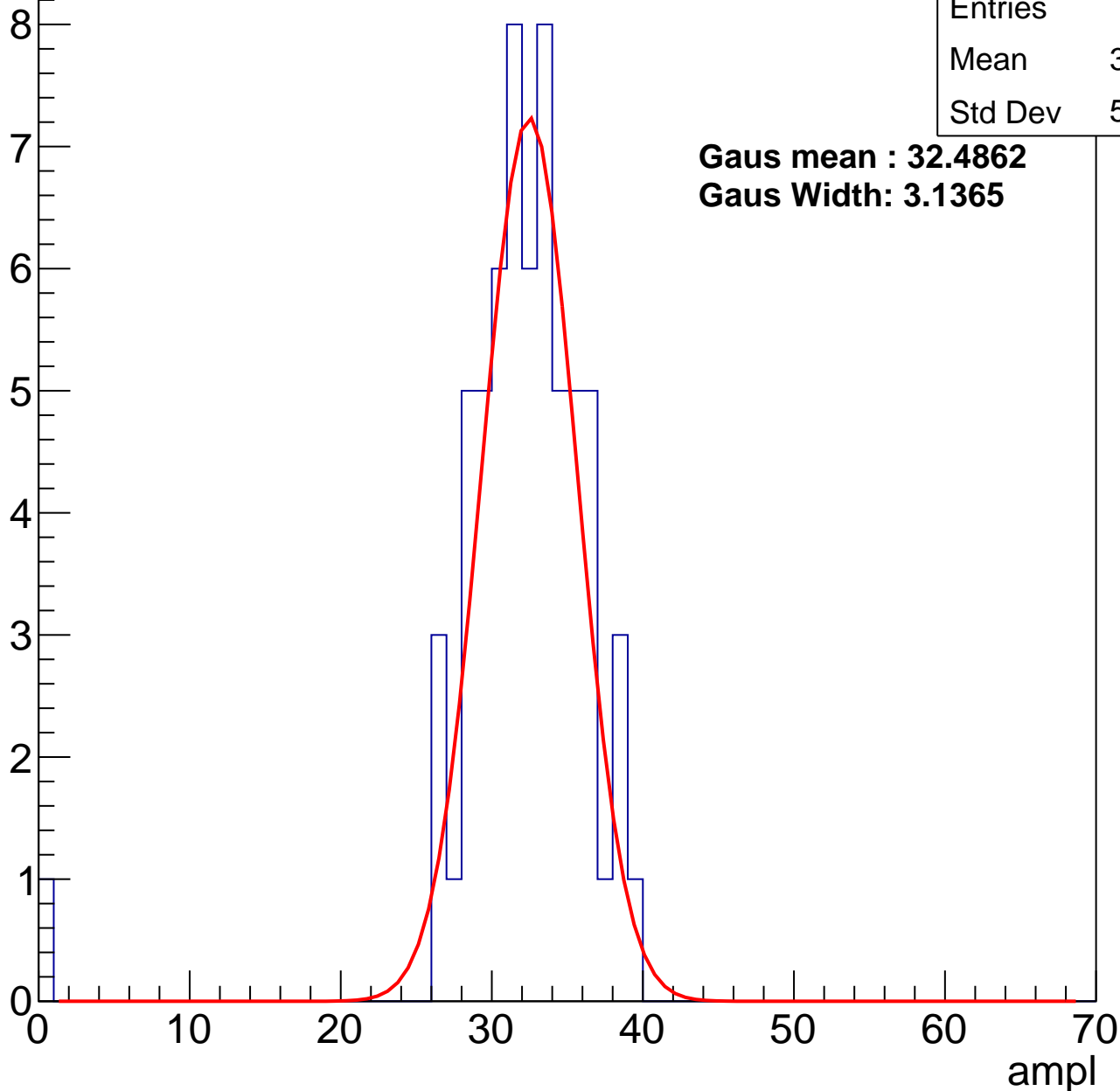
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	31.57
Std Dev	5.098

Gaus mean : 32.4862

Gaus Width: 3.1365



B0L001S, U25-ch101, adc1

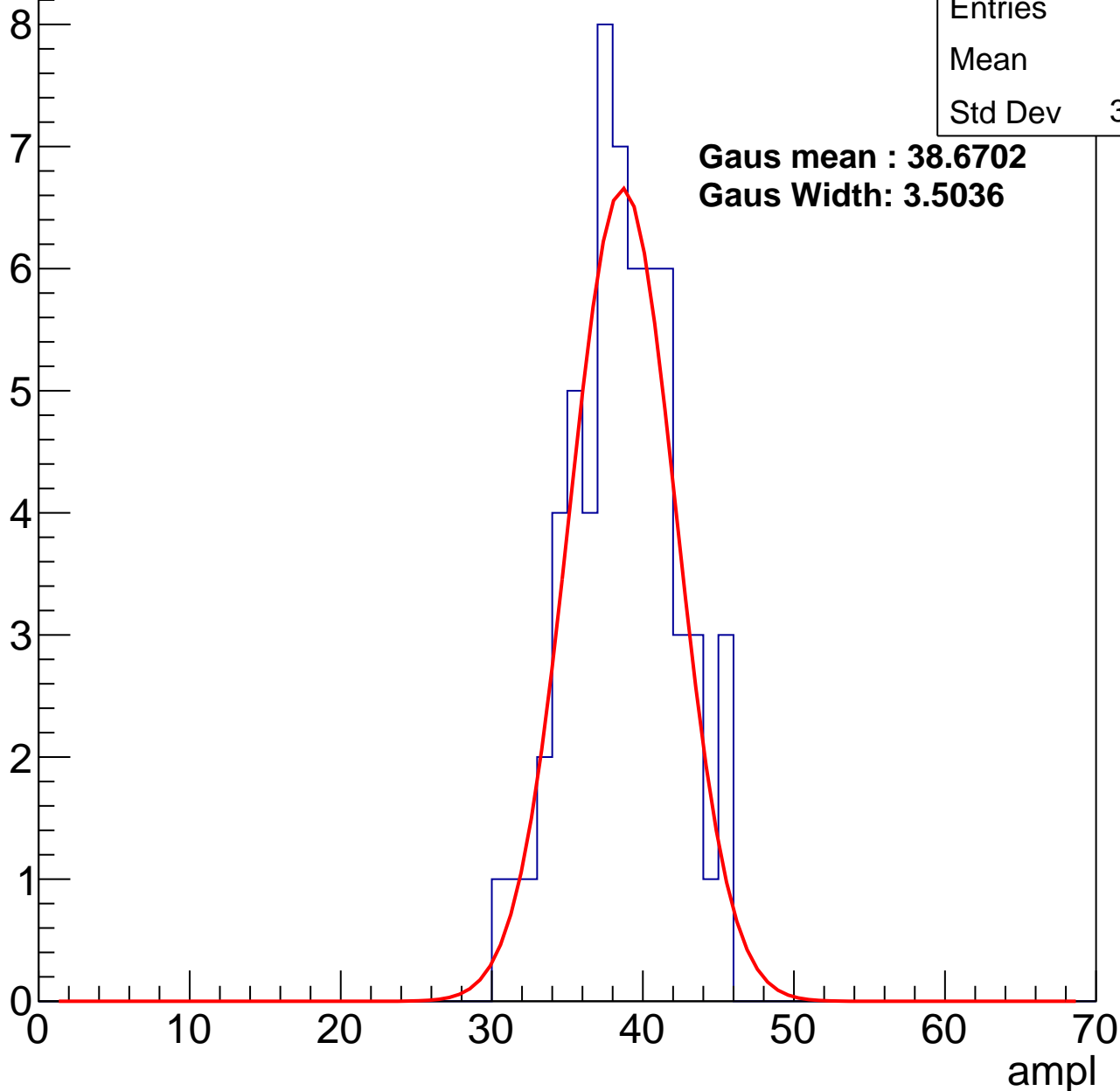
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.2
Std Dev	3.435

Gaus mean : 38.6702

Gaus Width: 3.5036



B0L001S, U25-ch101, adc2

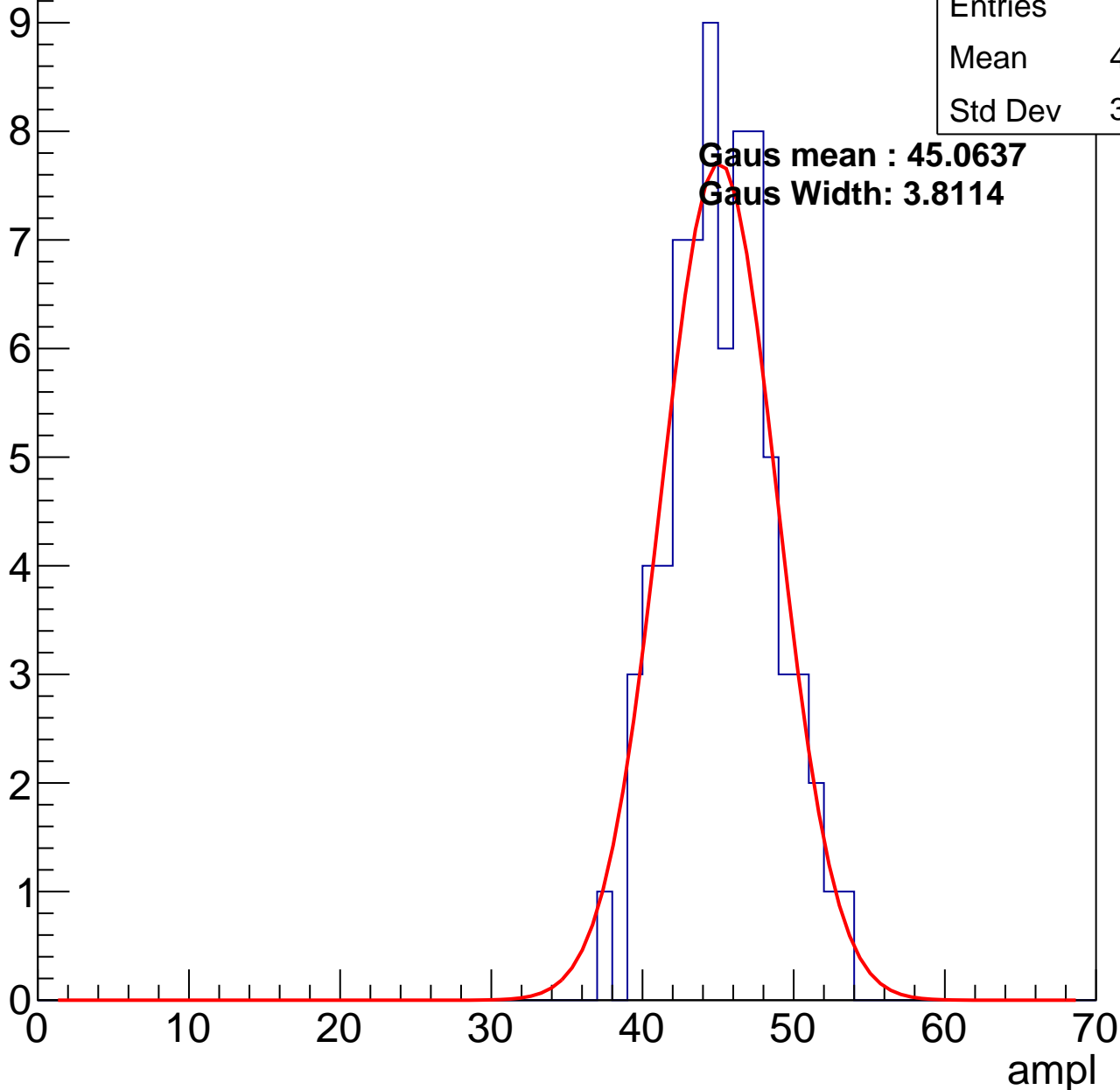
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	44.82
Std Dev	3.388

Gaus mean : 45.0637

Gaus Width: 3.8114

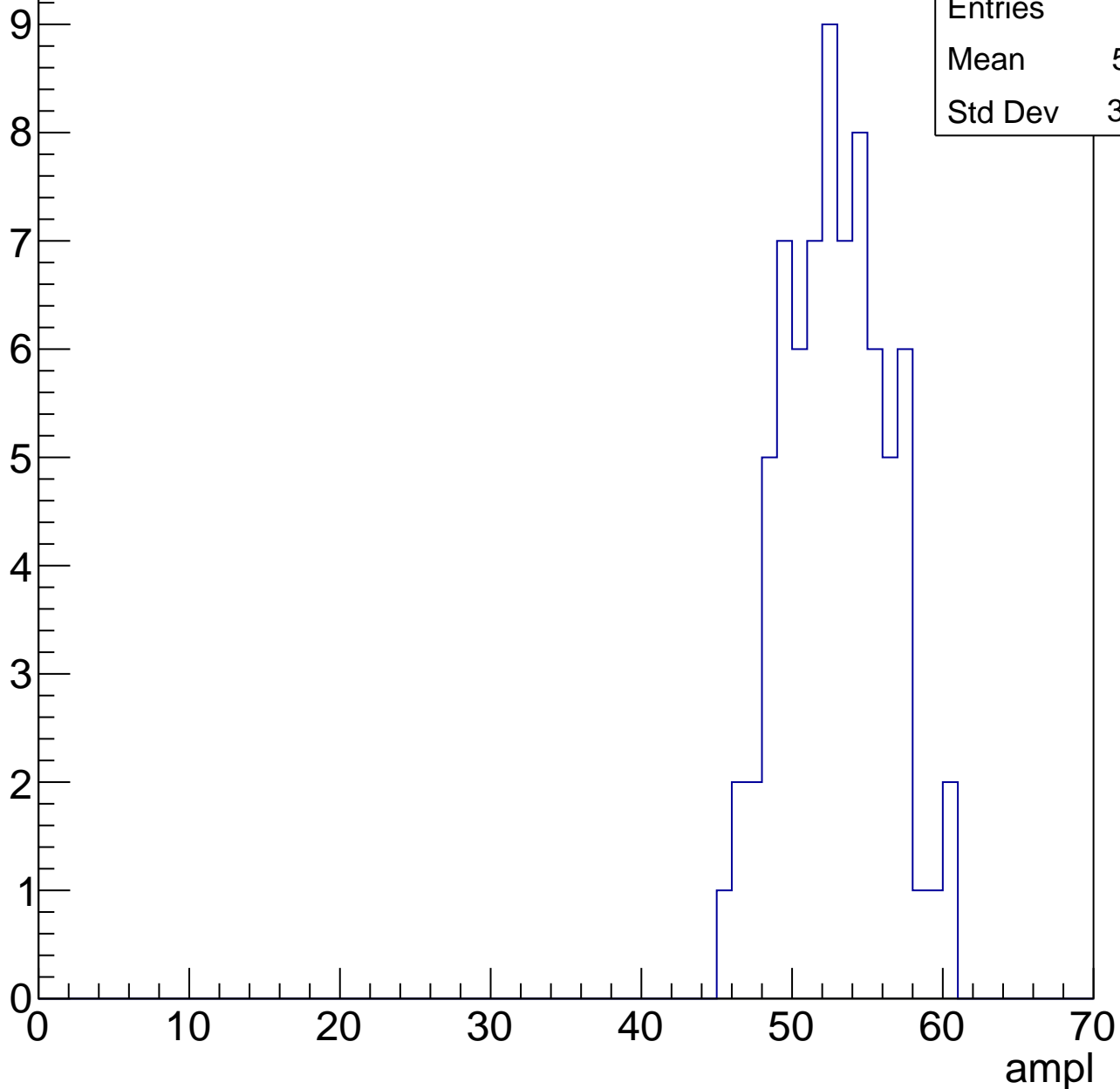


B0L001S, U25-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	52.41
Std Dev	3.402

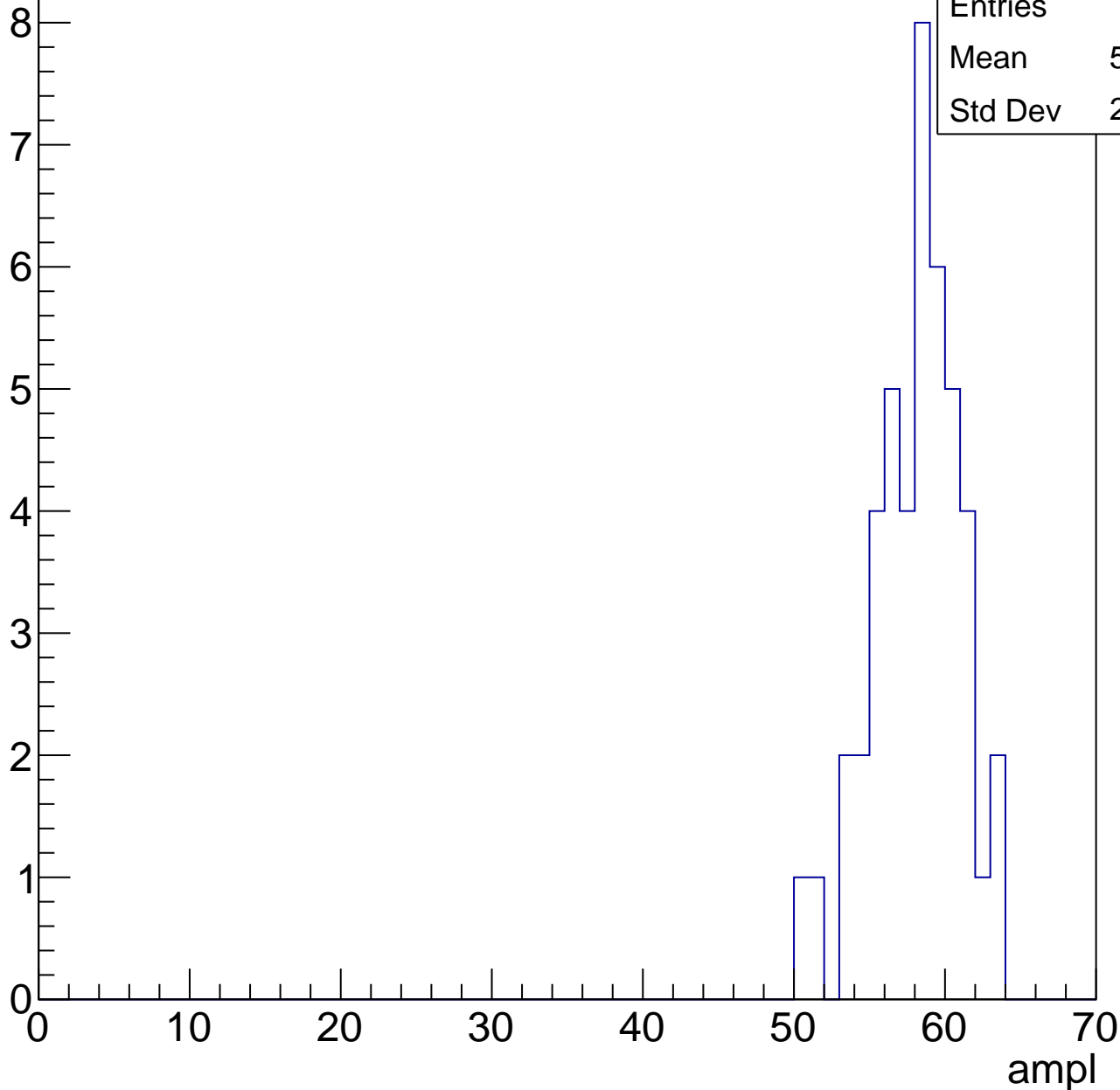


B0L001S, U25-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.62
Std Dev	2.893

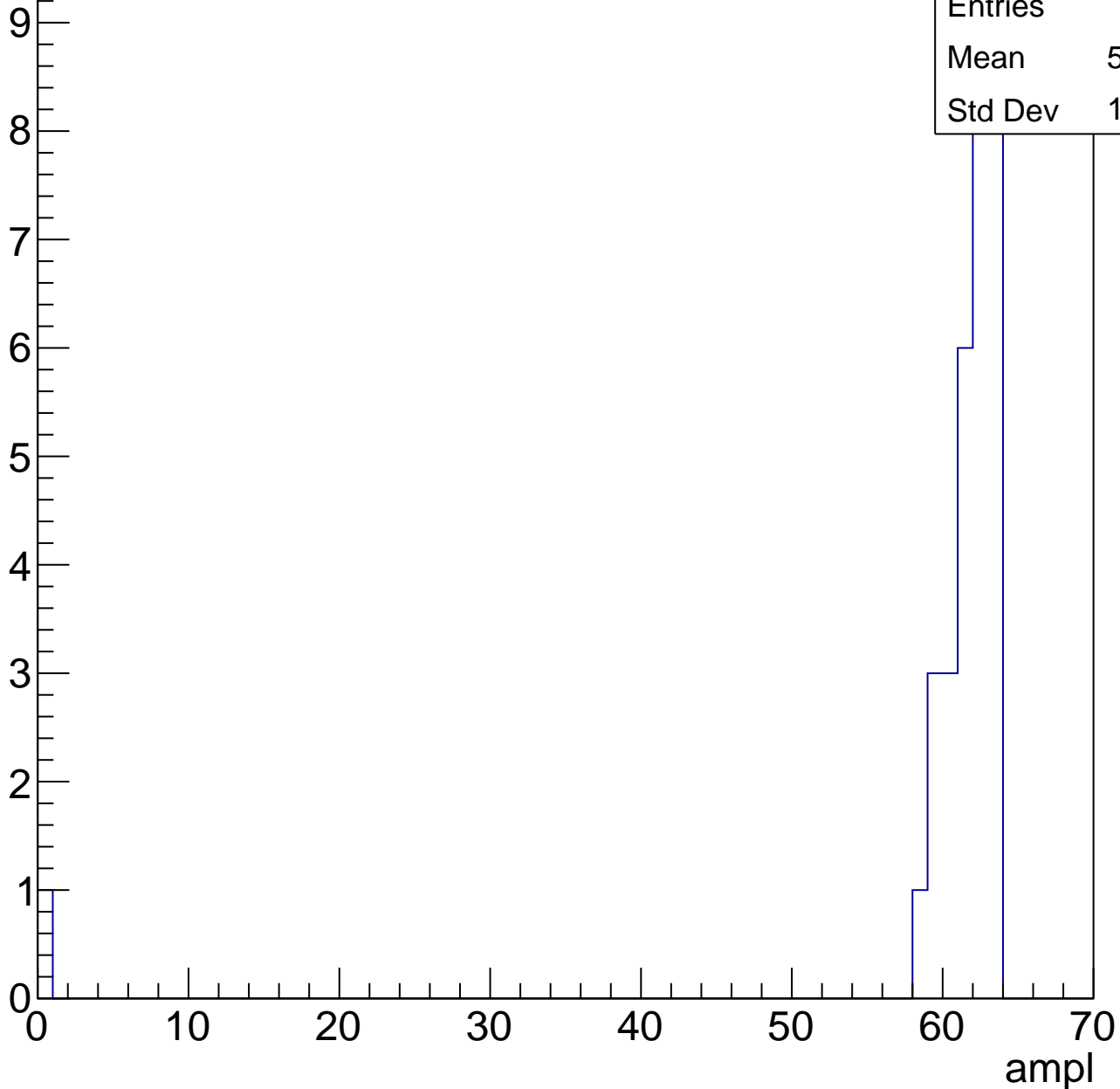


B0L001S, U25-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.45
Std Dev	10.94



B0L001S, U25-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

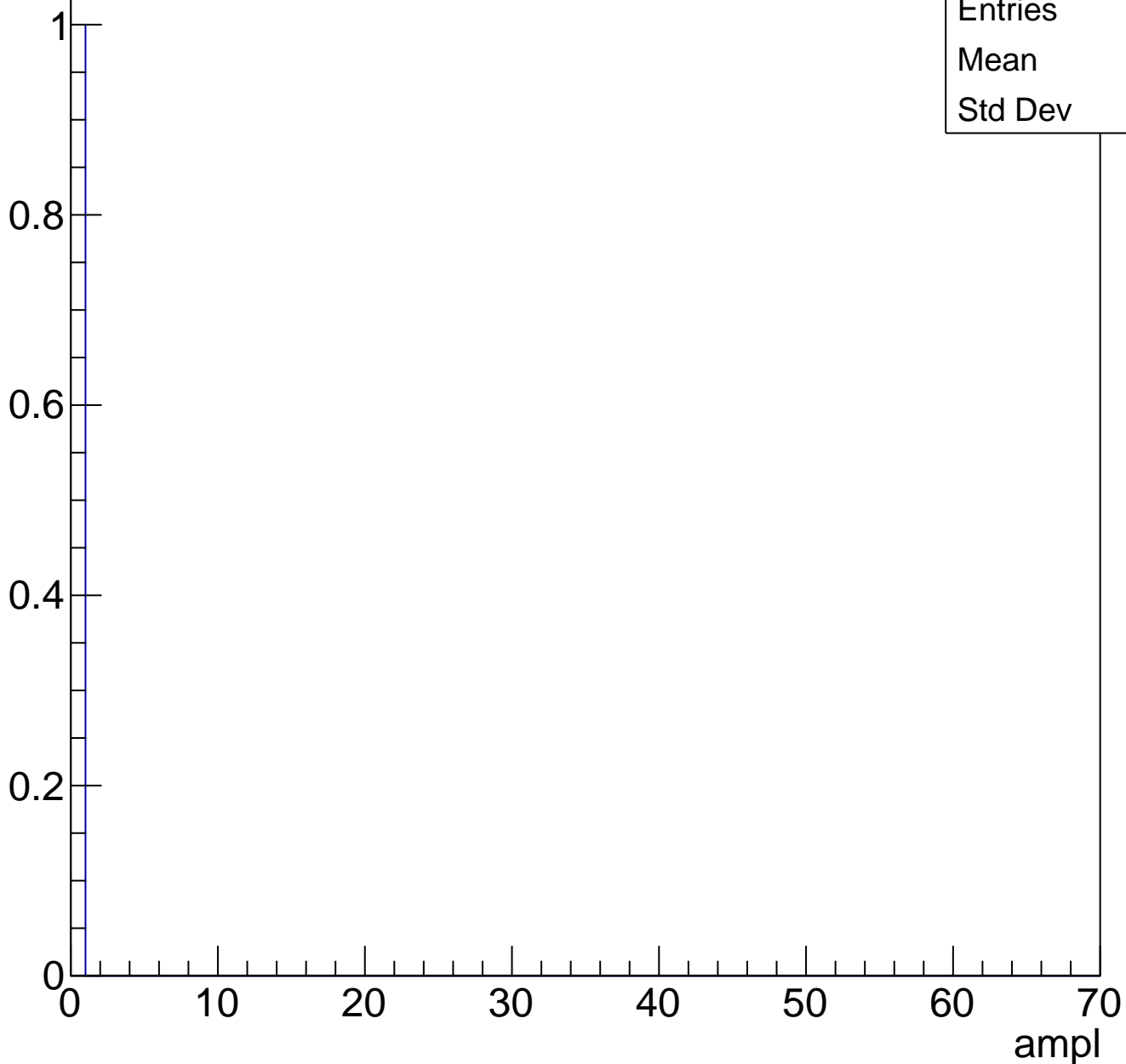


Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch102, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	28.92
Std Dev	5.855

Gaus mean : 30.3207

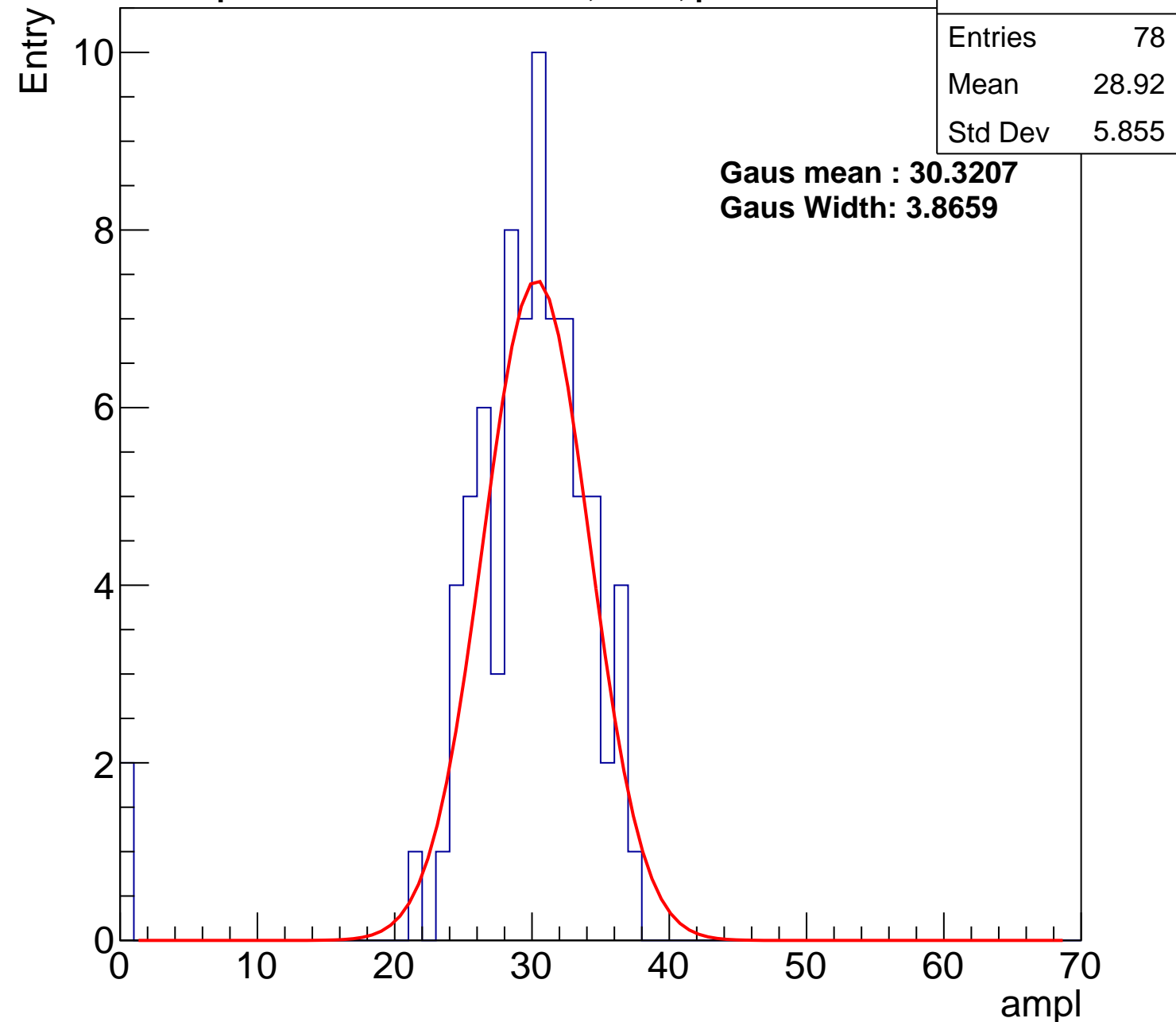
Gaus Width: 3.8659

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch102, adc1

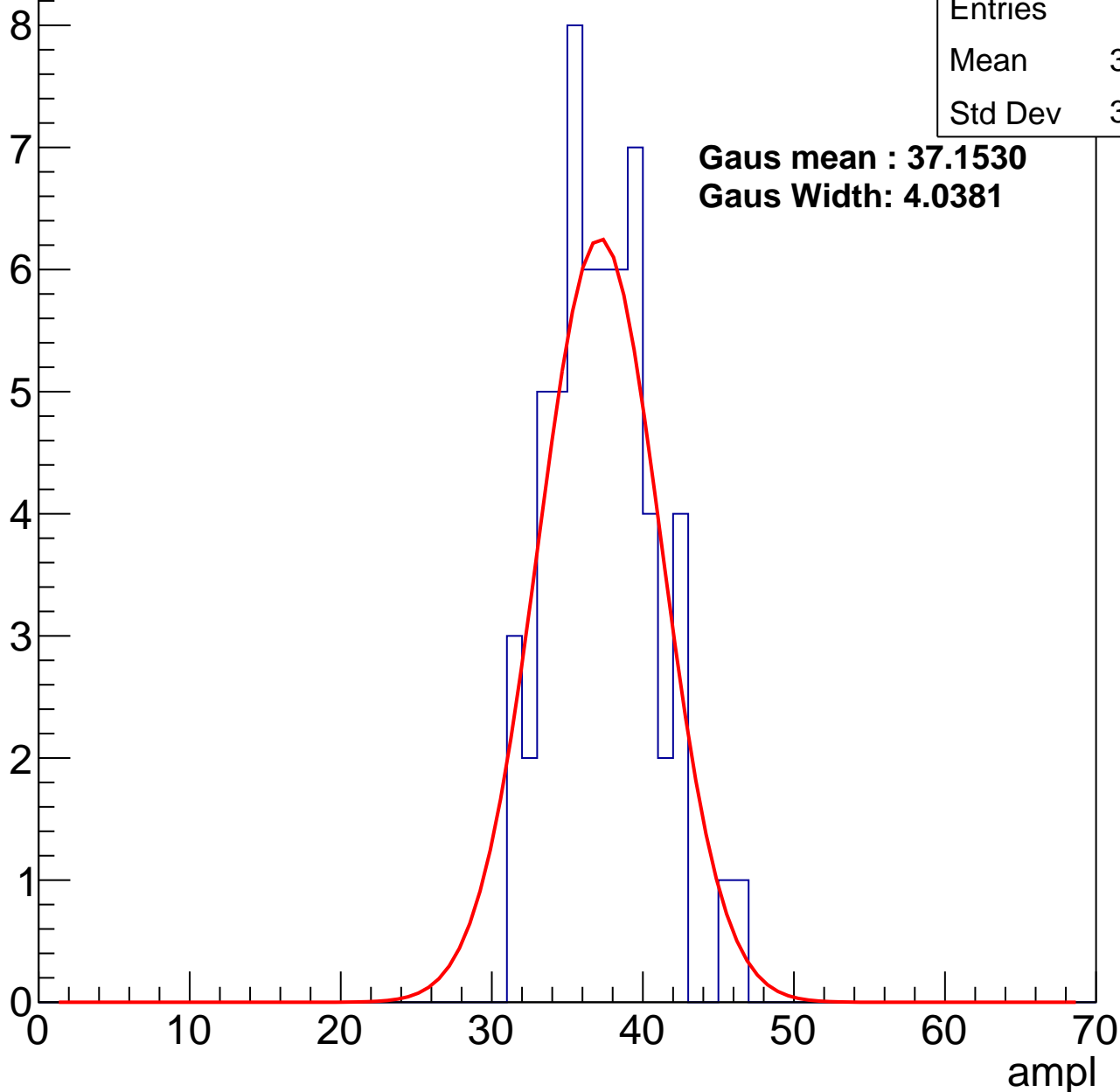
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.87
Std Dev	3.334

Gaus mean : 37.1530

Gaus Width: 4.0381



B0L001S, U25-ch102, adc2

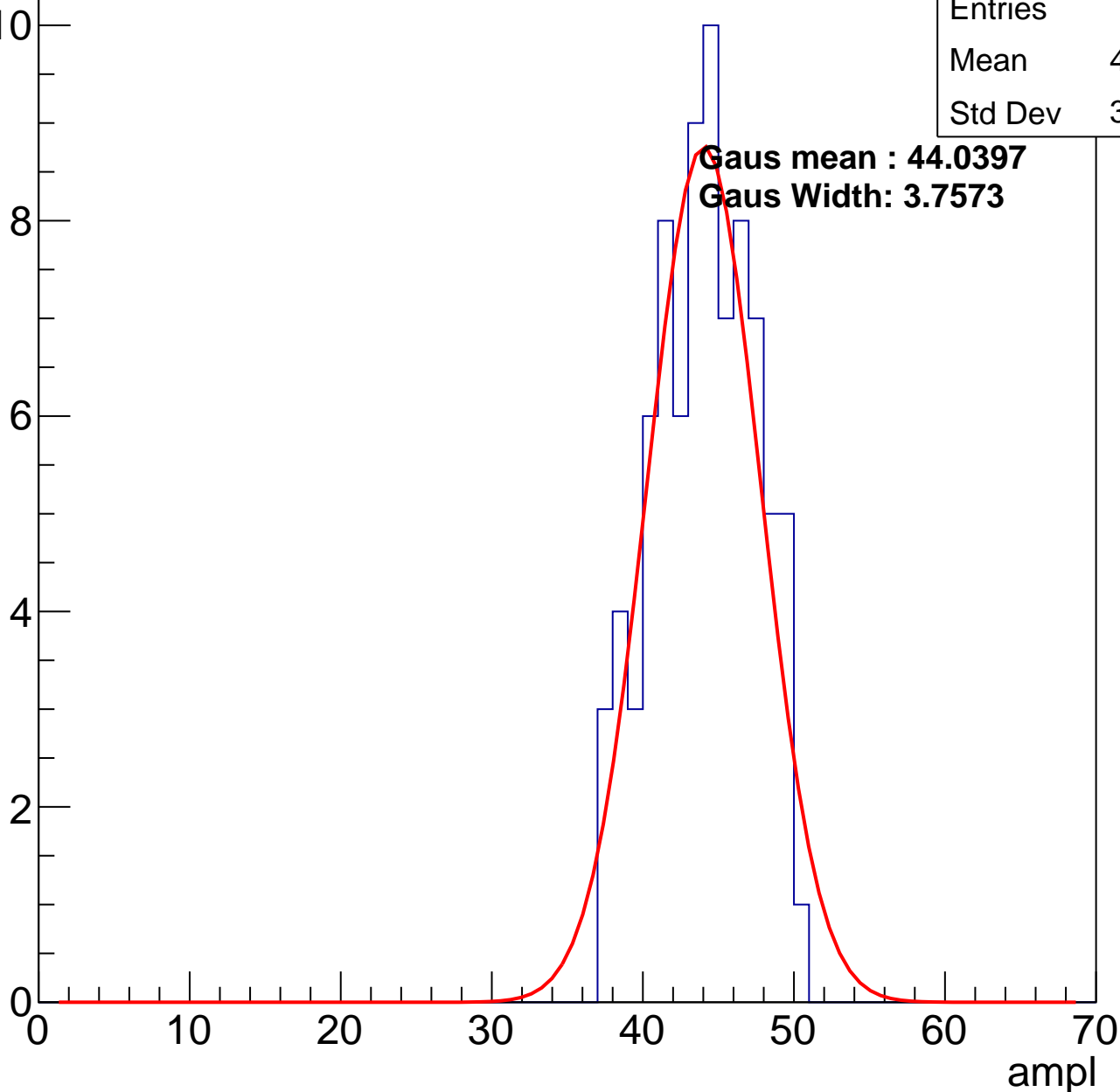
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	43.59
Std Dev	3.305

Gaus mean : 44.0397

Gaus Width: 3.7573

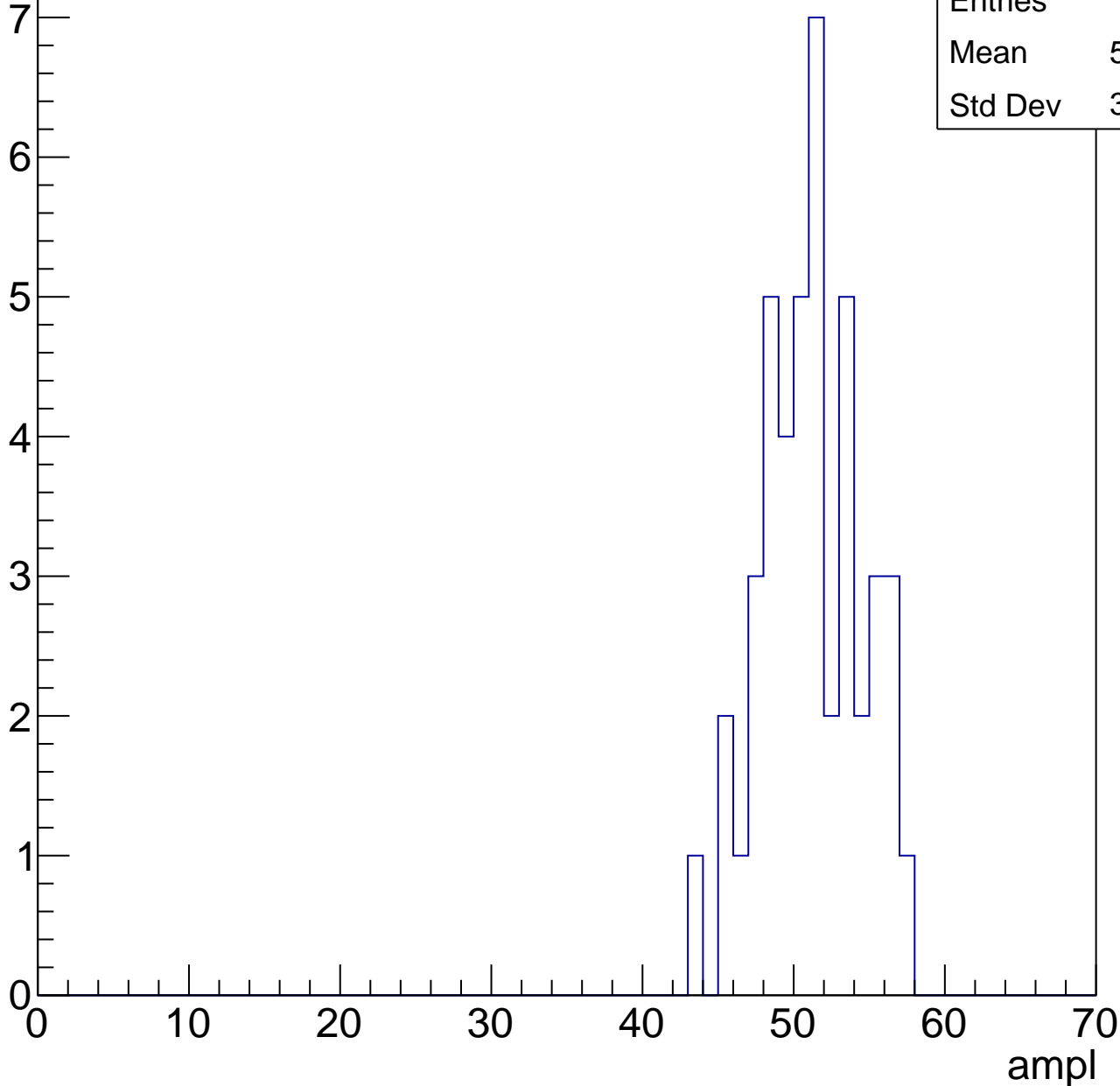


B0L001S, U25-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	50.68
Std Dev	3.267

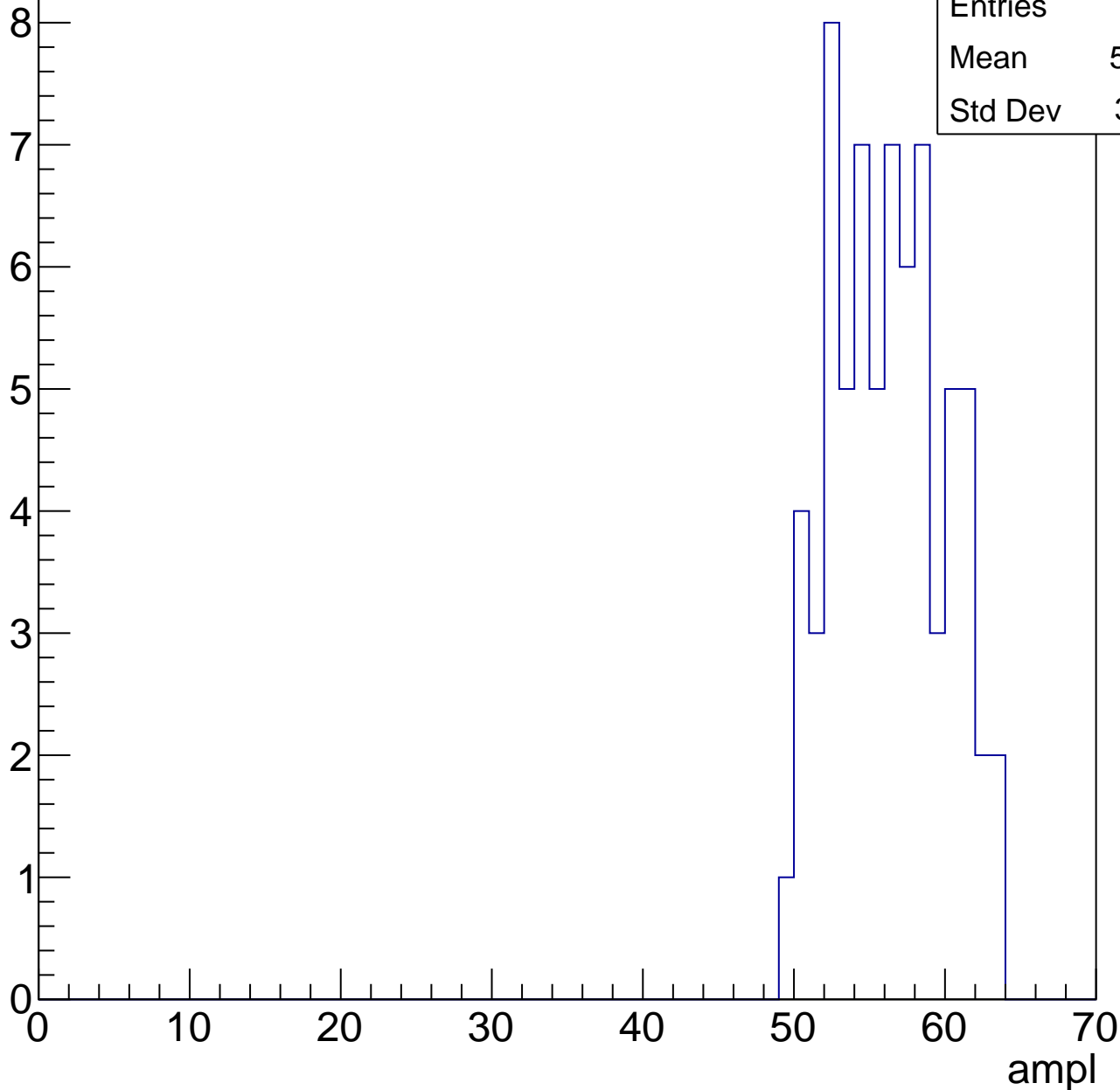


B0L001S, U25-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	55.83
Std Dev	3.601

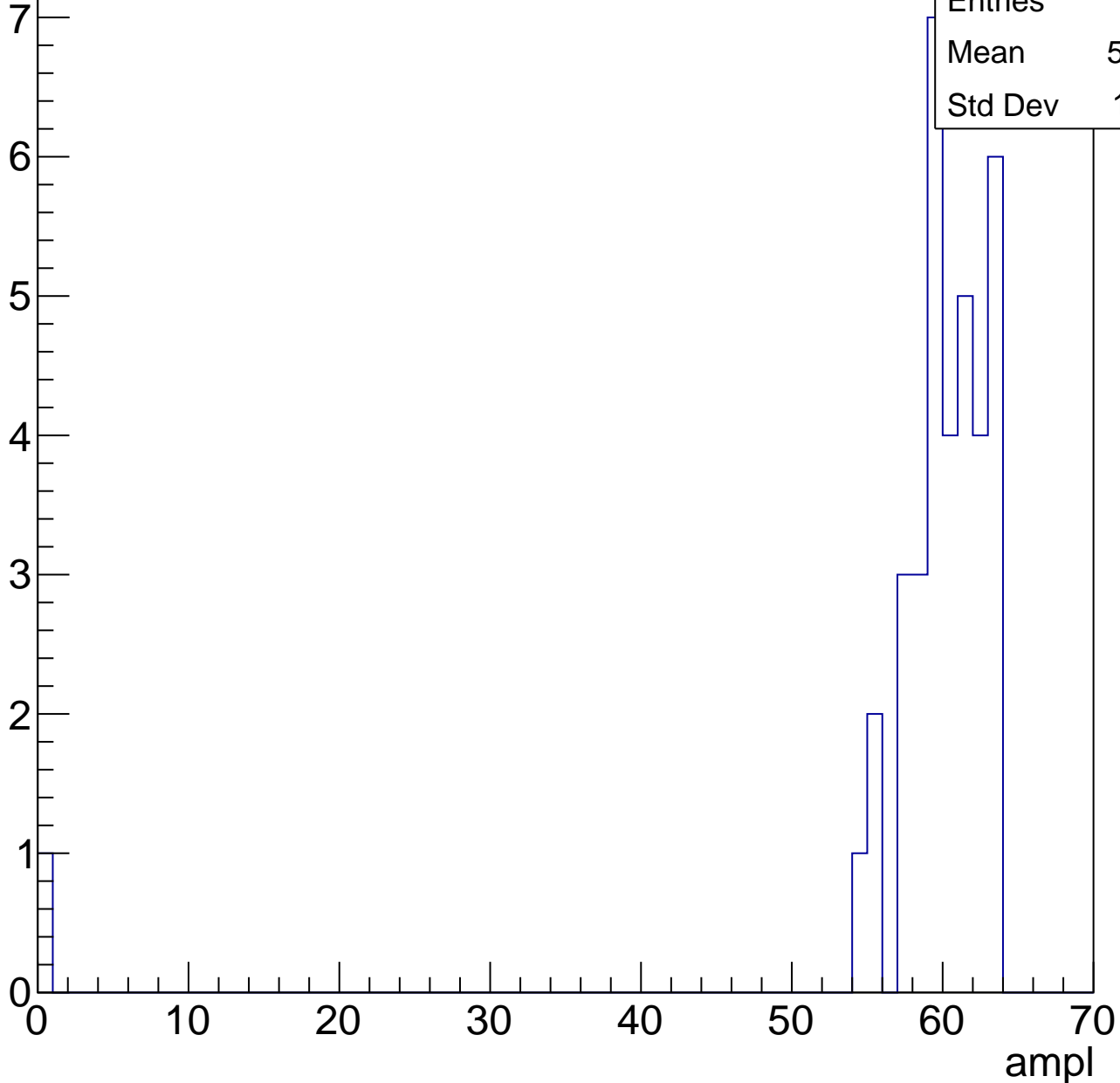


B0L001S, U25-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

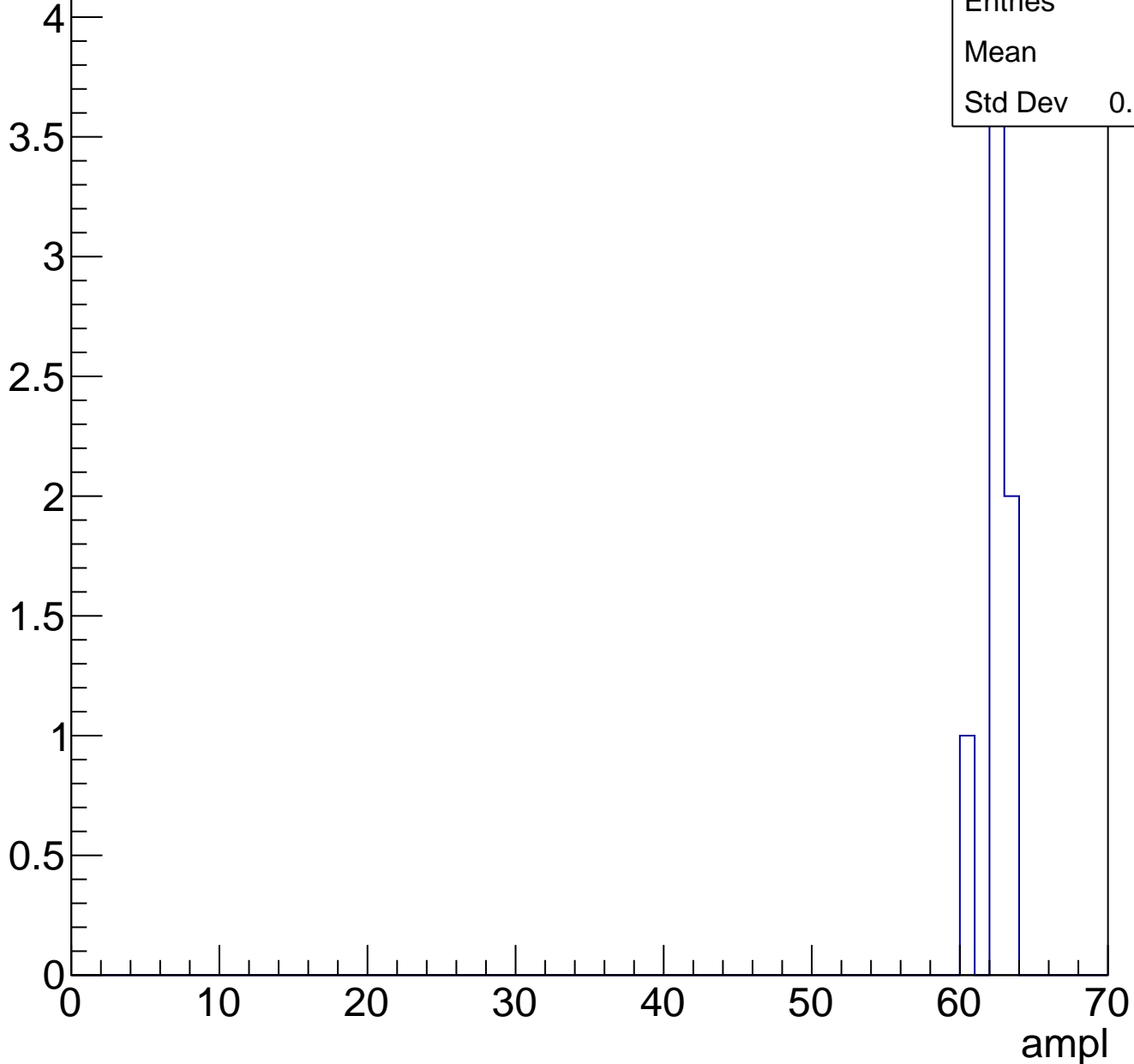
Entries	36
Mean	58.14
Std Dev	10.11



B0L001S, U25-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L001S, U25-ch103, adc0

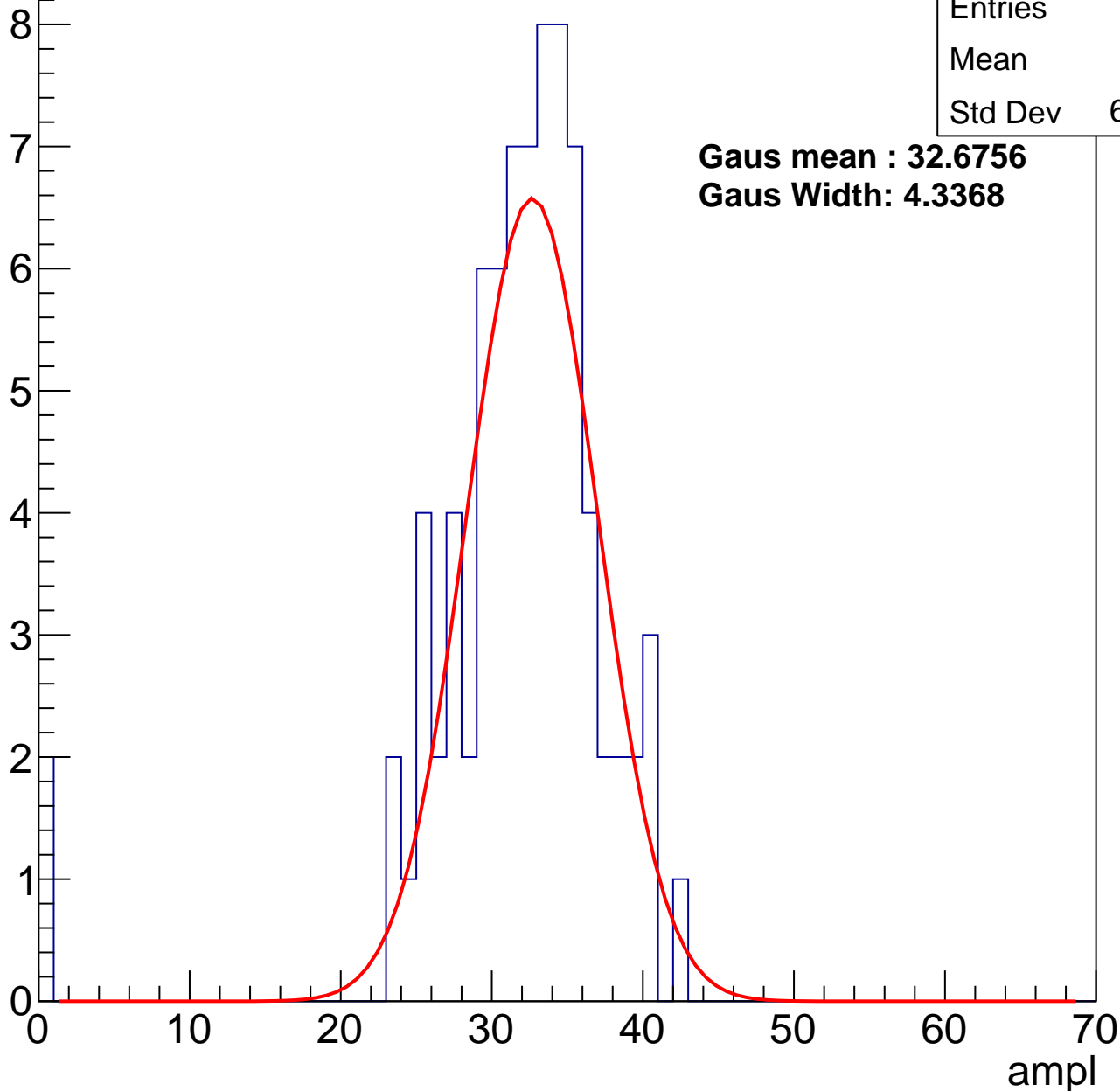
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	31.2
Std Dev	6.507

Gaus mean : 32.6756

Gaus Width: 4.3368



B0L001S, U25-ch103, adc1

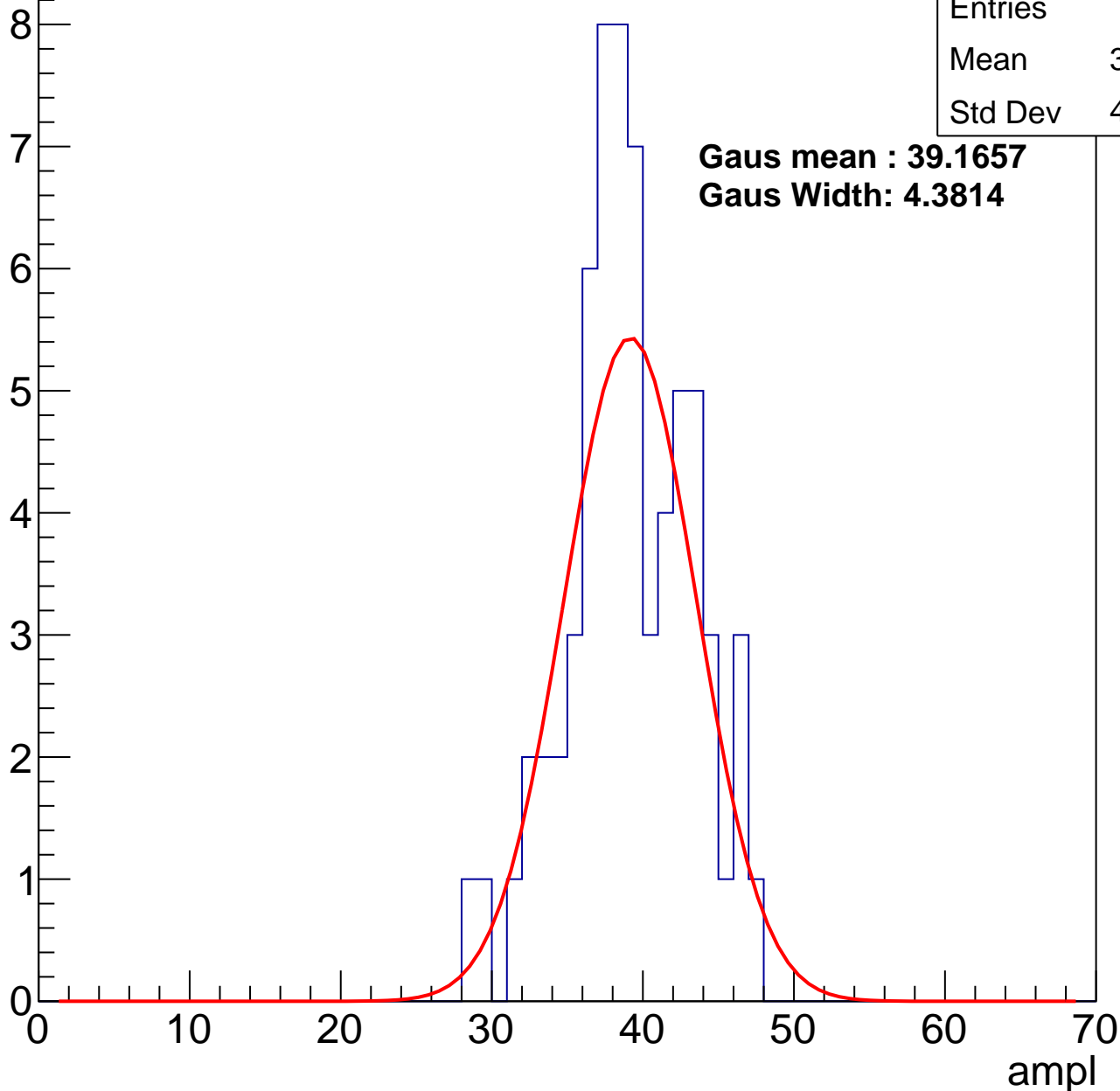
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.65
Std Dev	4.092

Gaus mean : 39.1657

Gaus Width: 4.3814



B0L001S, U25-ch103, adc2

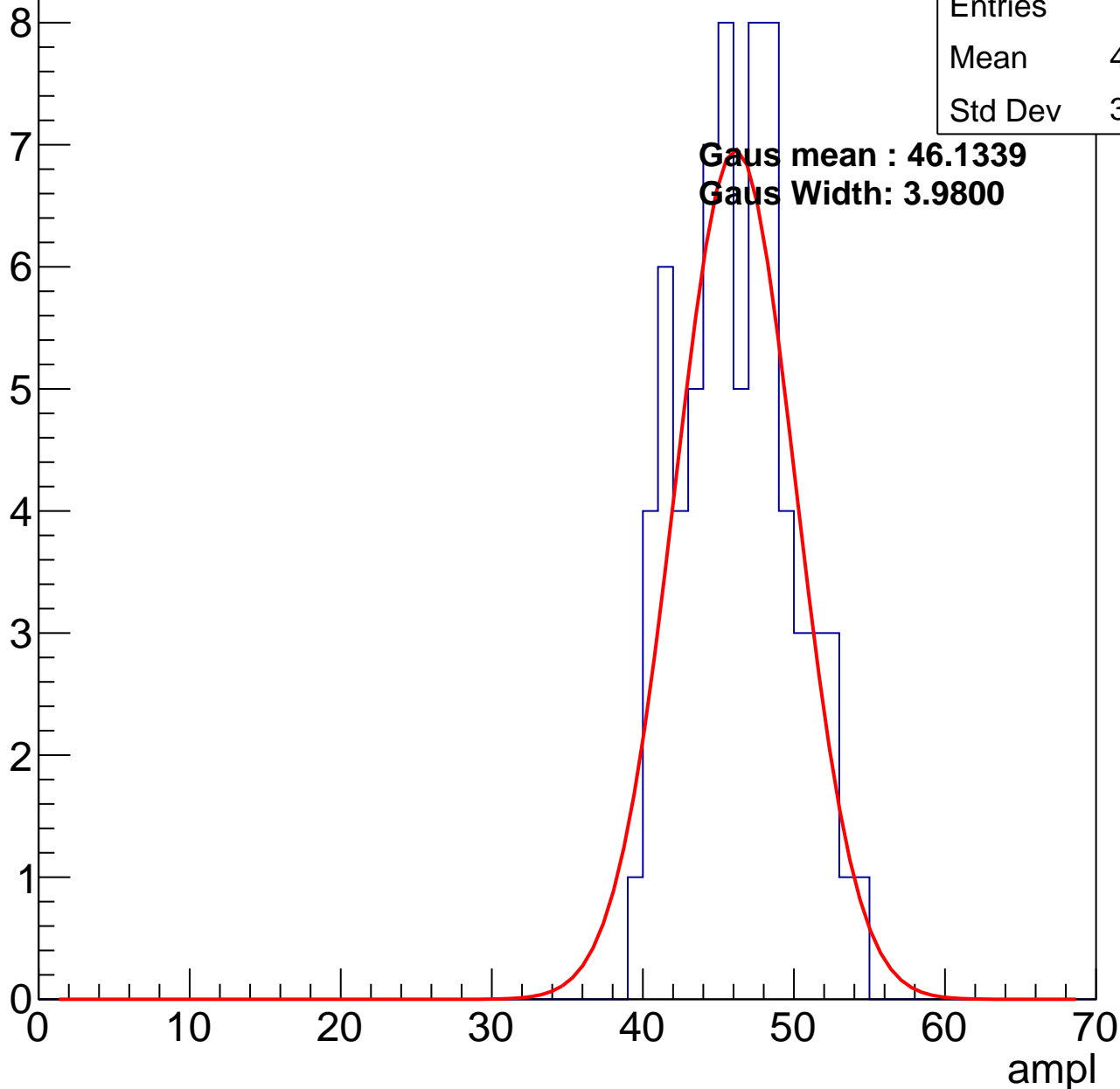
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	45.75
Std Dev	3.567

Gaus mean : 46.1339

Gaus Width: 3.9800

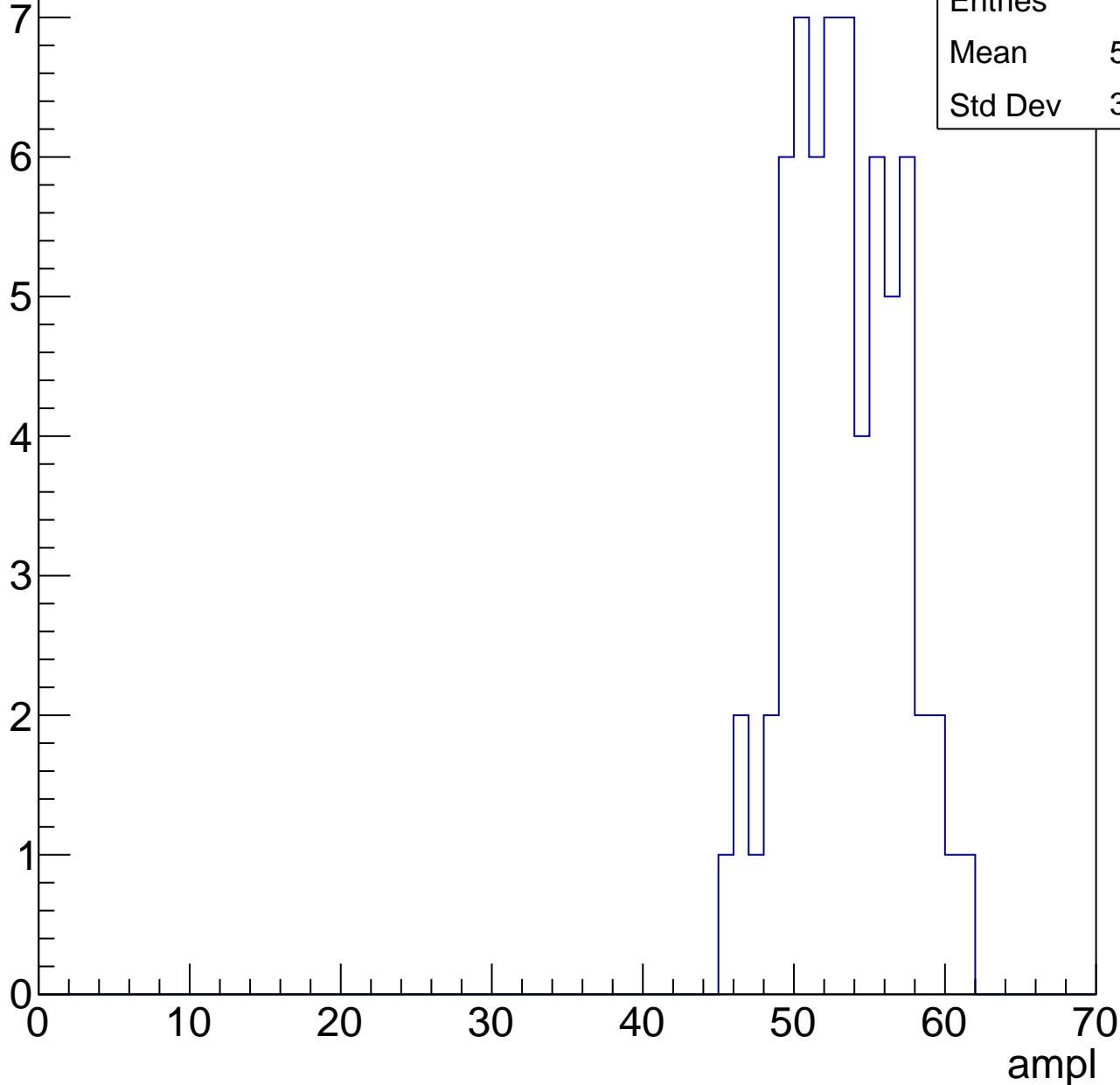


B0L001S, U25-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	52.85
Std Dev	3.569

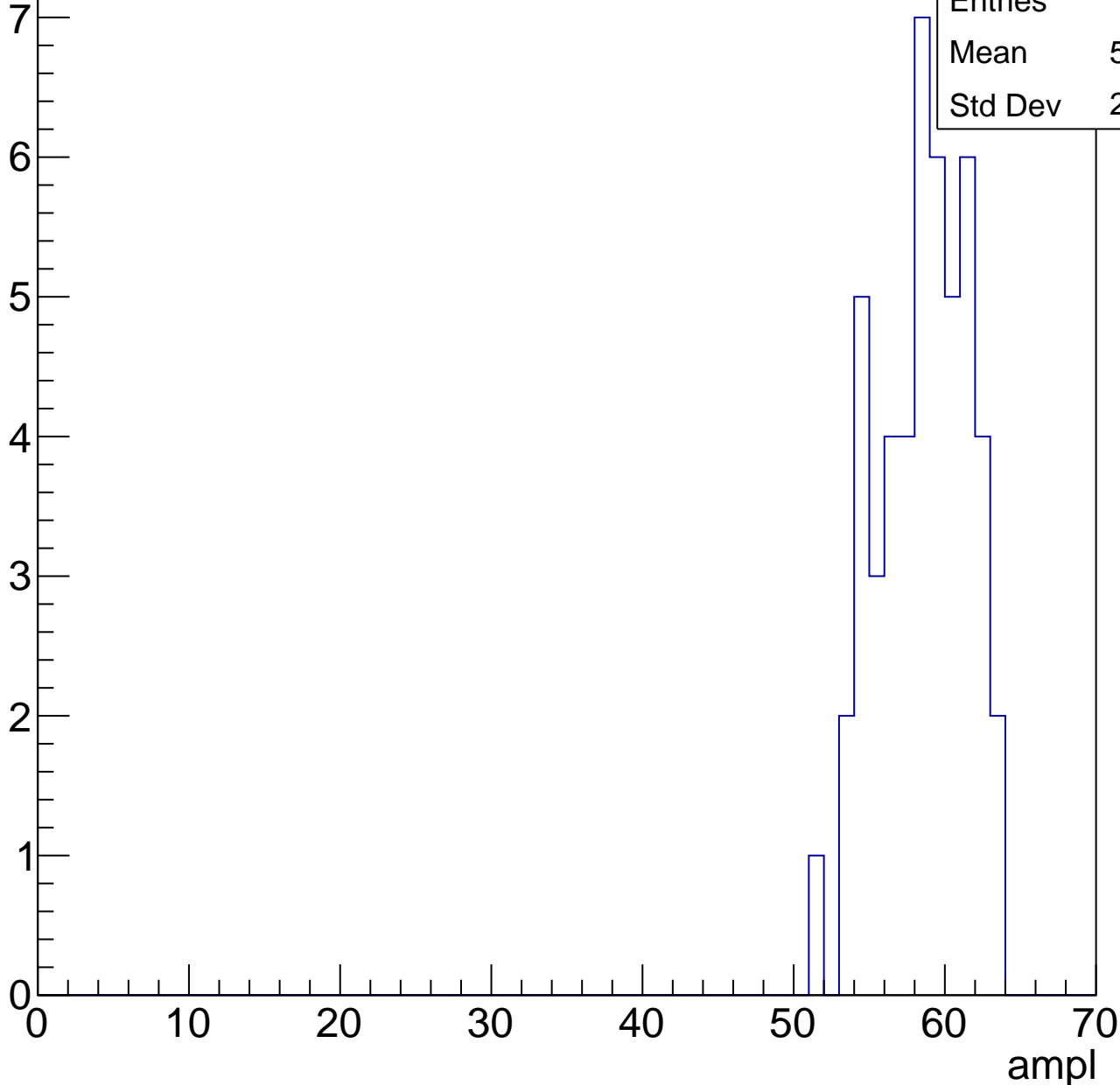


B0L001S, U25-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.04
Std Dev	2.927

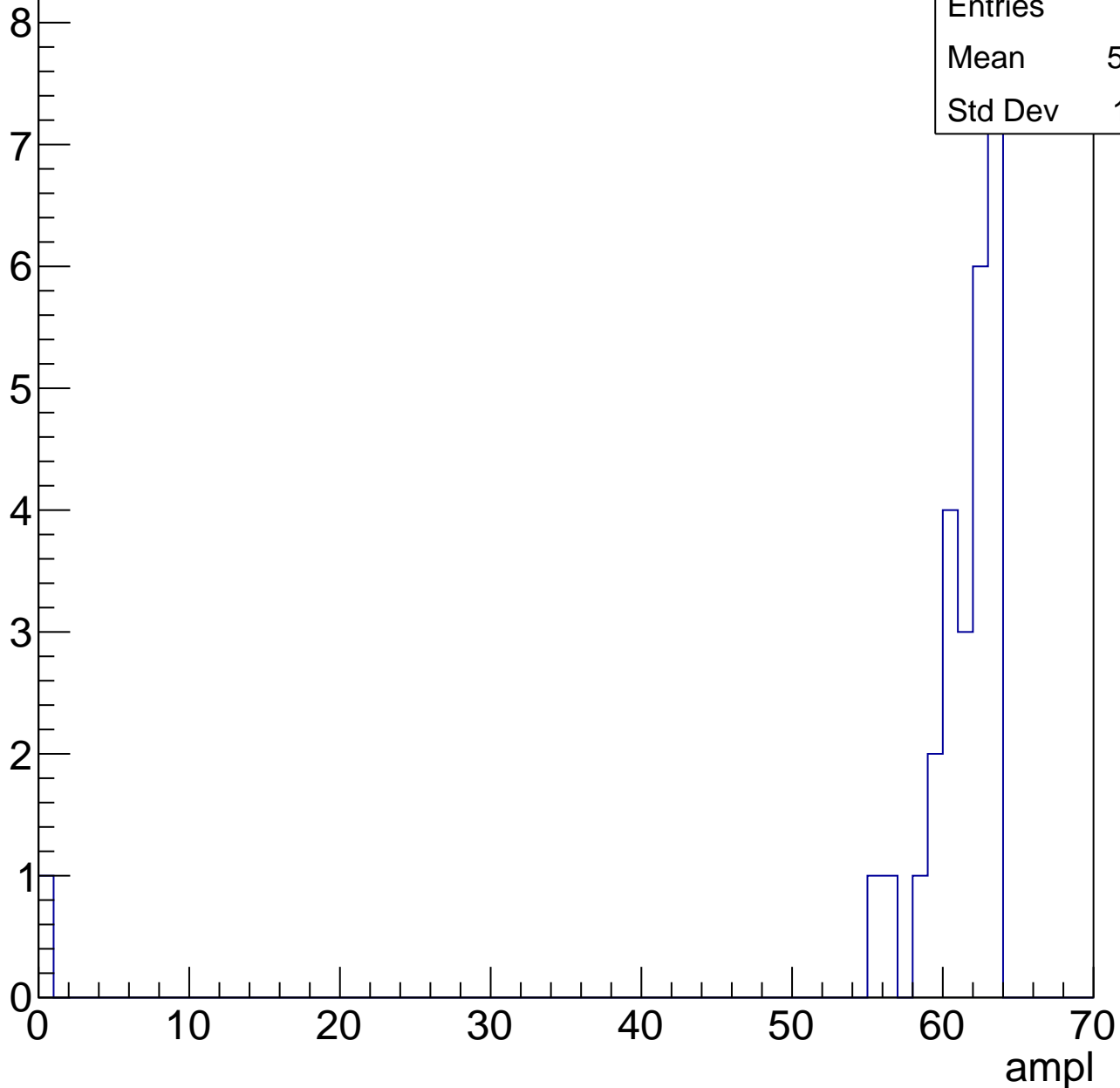


B0L001S, U25-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.74
Std Dev	11.71



B0L001S, U25-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch104, adc0

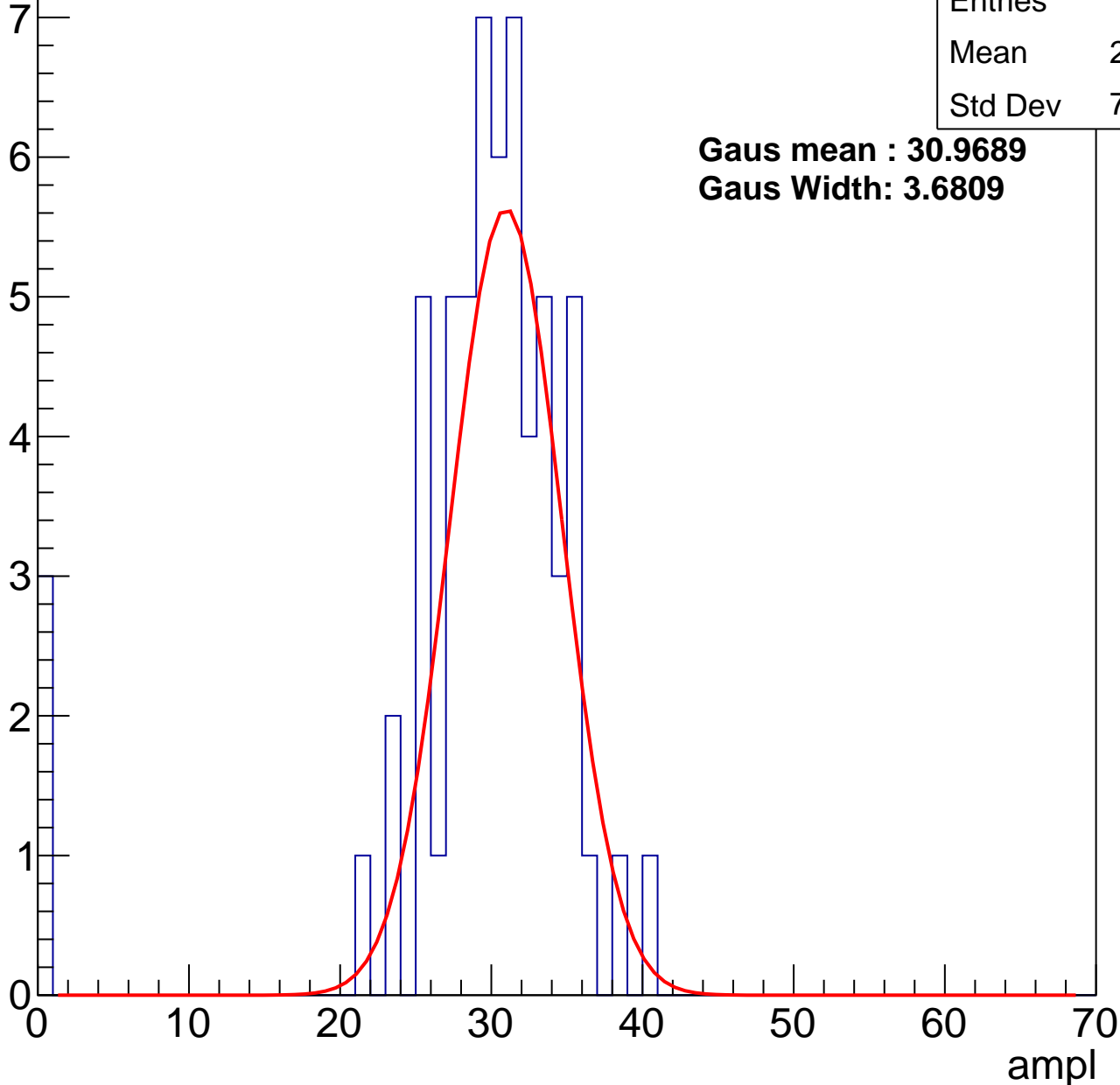
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.66
Std Dev	7.429

Gaus mean : 30.9689

Gaus Width: 3.6809



B0L001S, U25-ch104, adc1

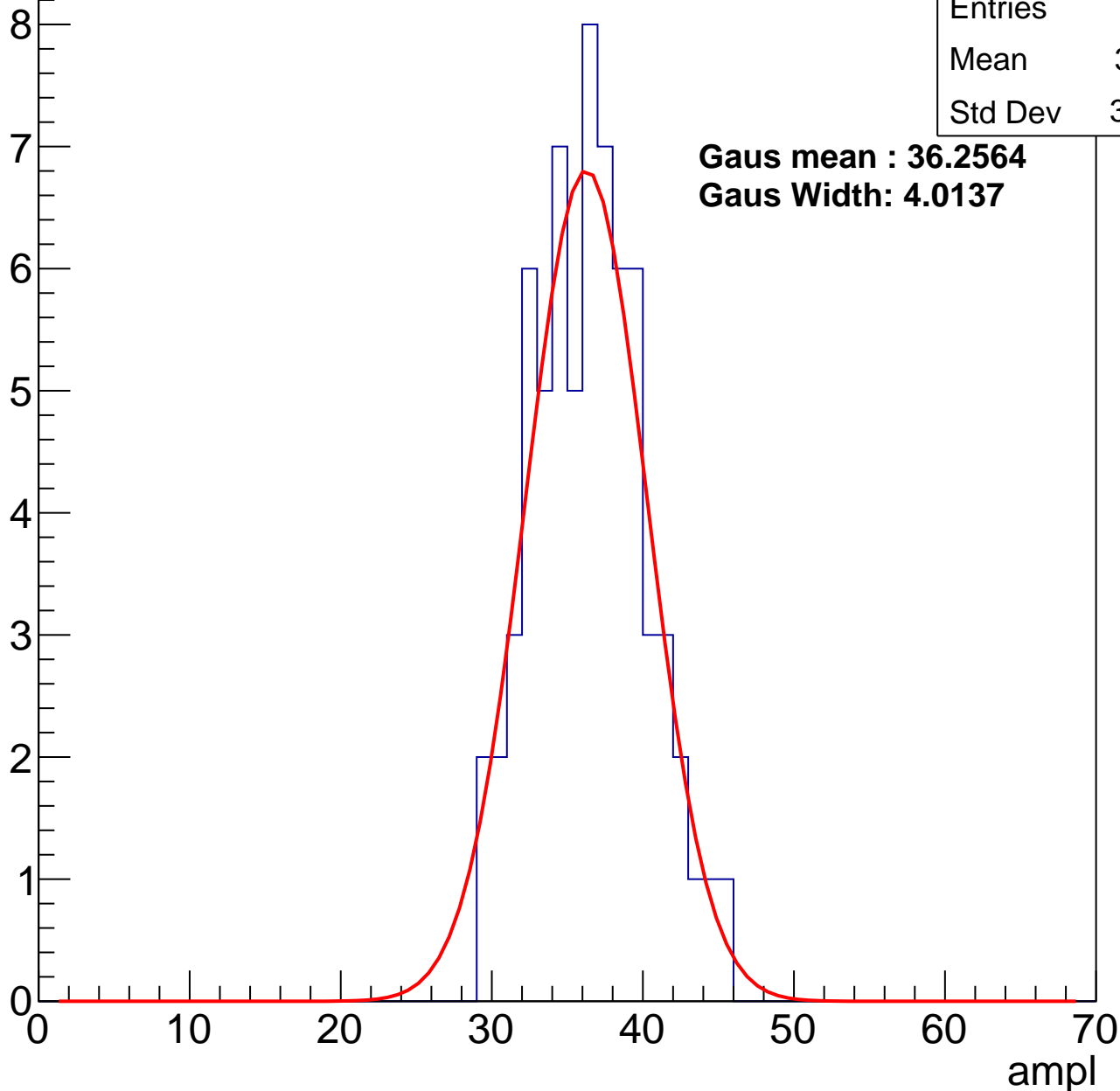
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.01
Std Dev	3.624

Gaus mean : 36.2564

Gaus Width: 4.0137



B0L001S, U25-ch104, adc2

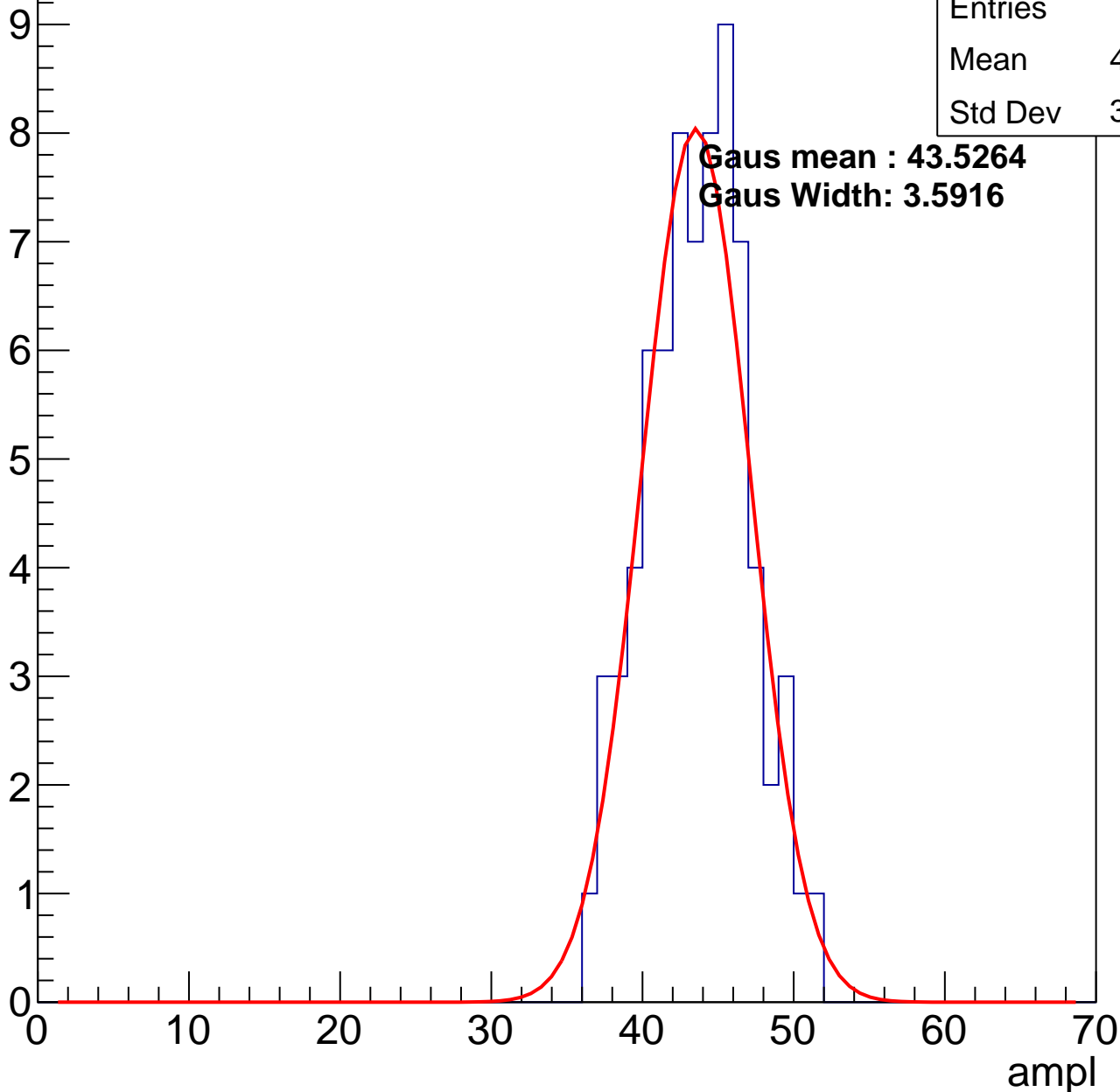
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.16
Std Dev	3.356

Gaus mean : 43.5264

Gaus Width: 3.5916

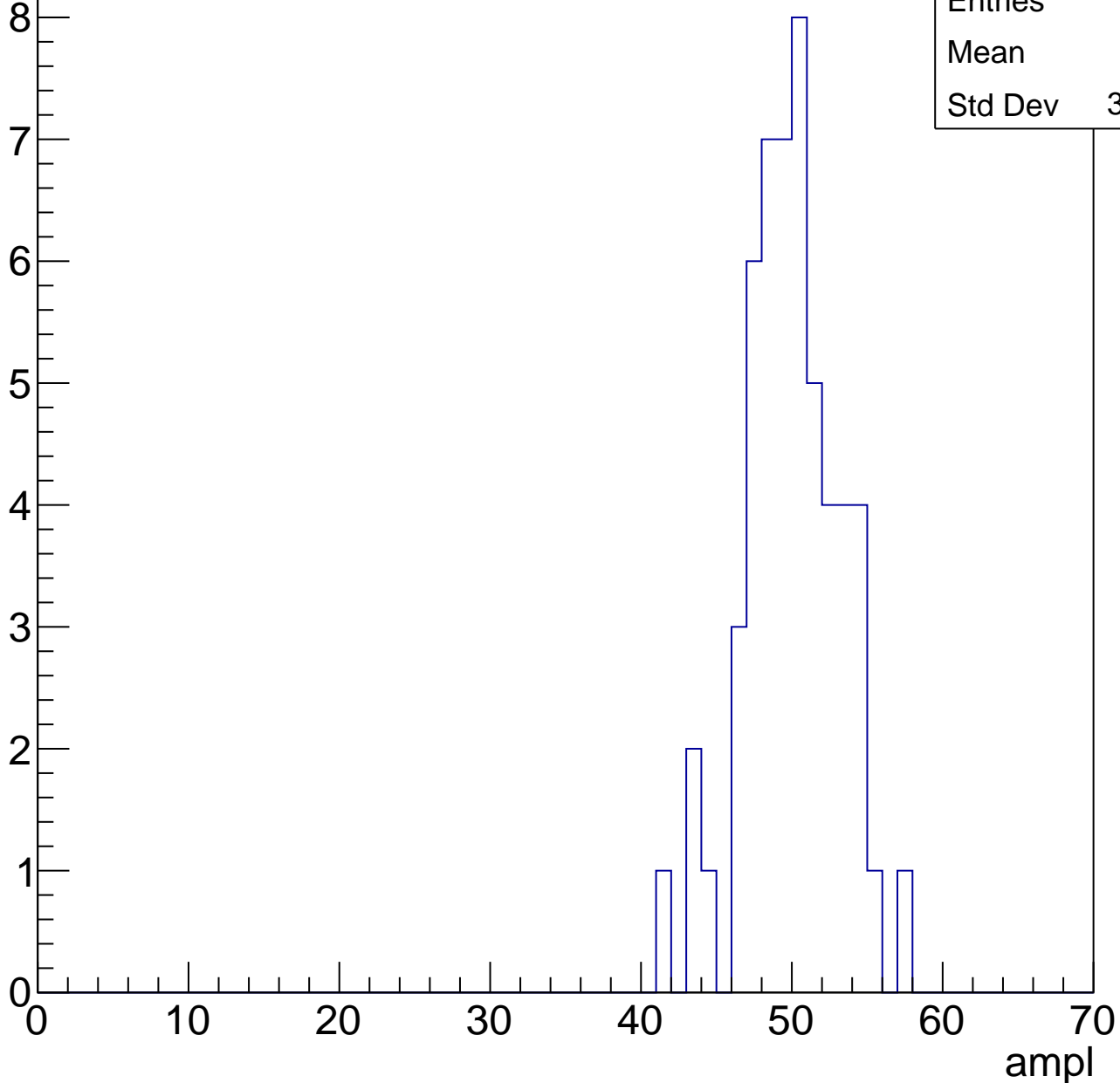


B0L001S, U25-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	49.5
Std Dev	3.143



B0L001S, U25-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

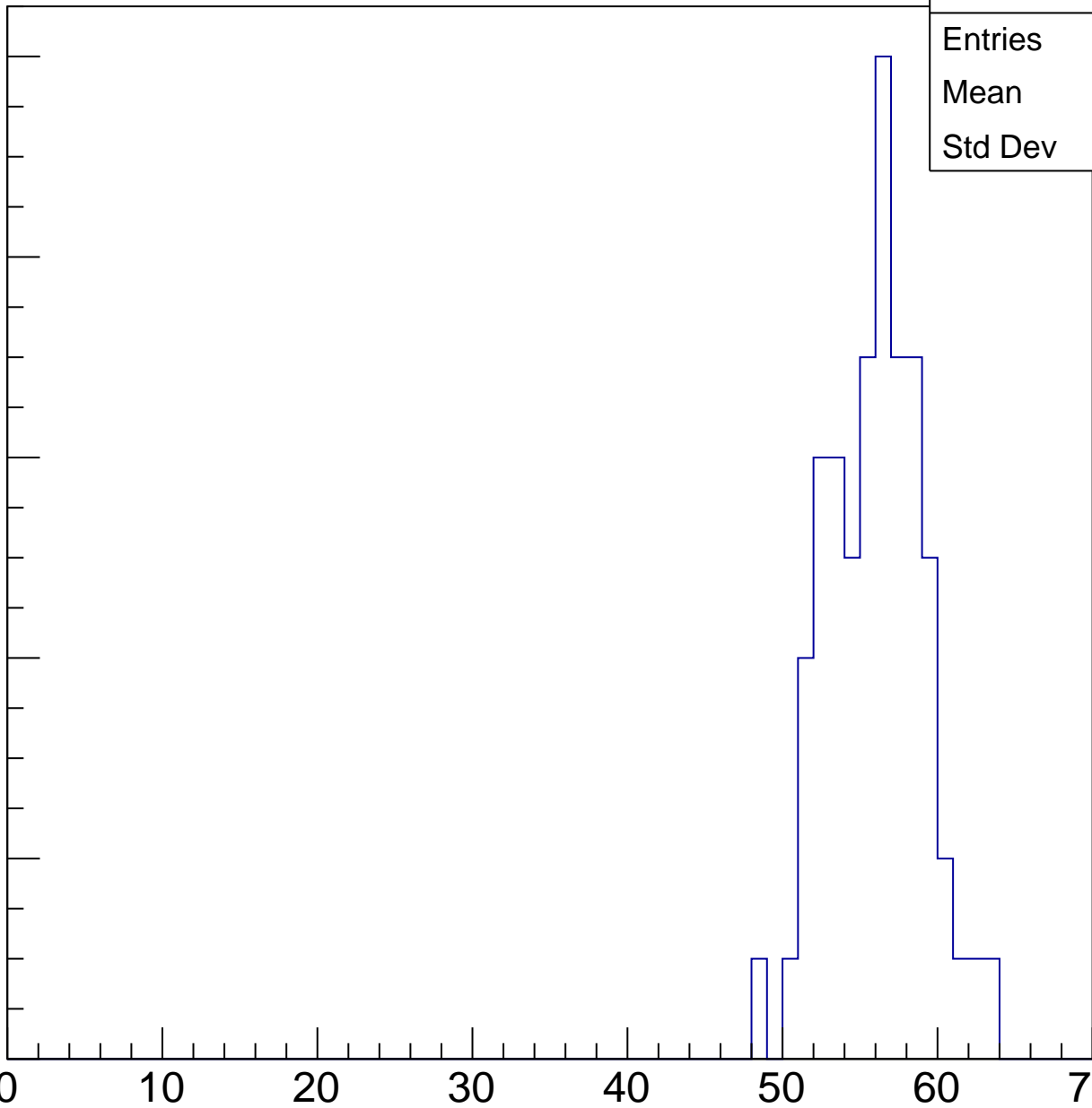
Entries	64
Mean	55.52
Std Dev	3.021

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

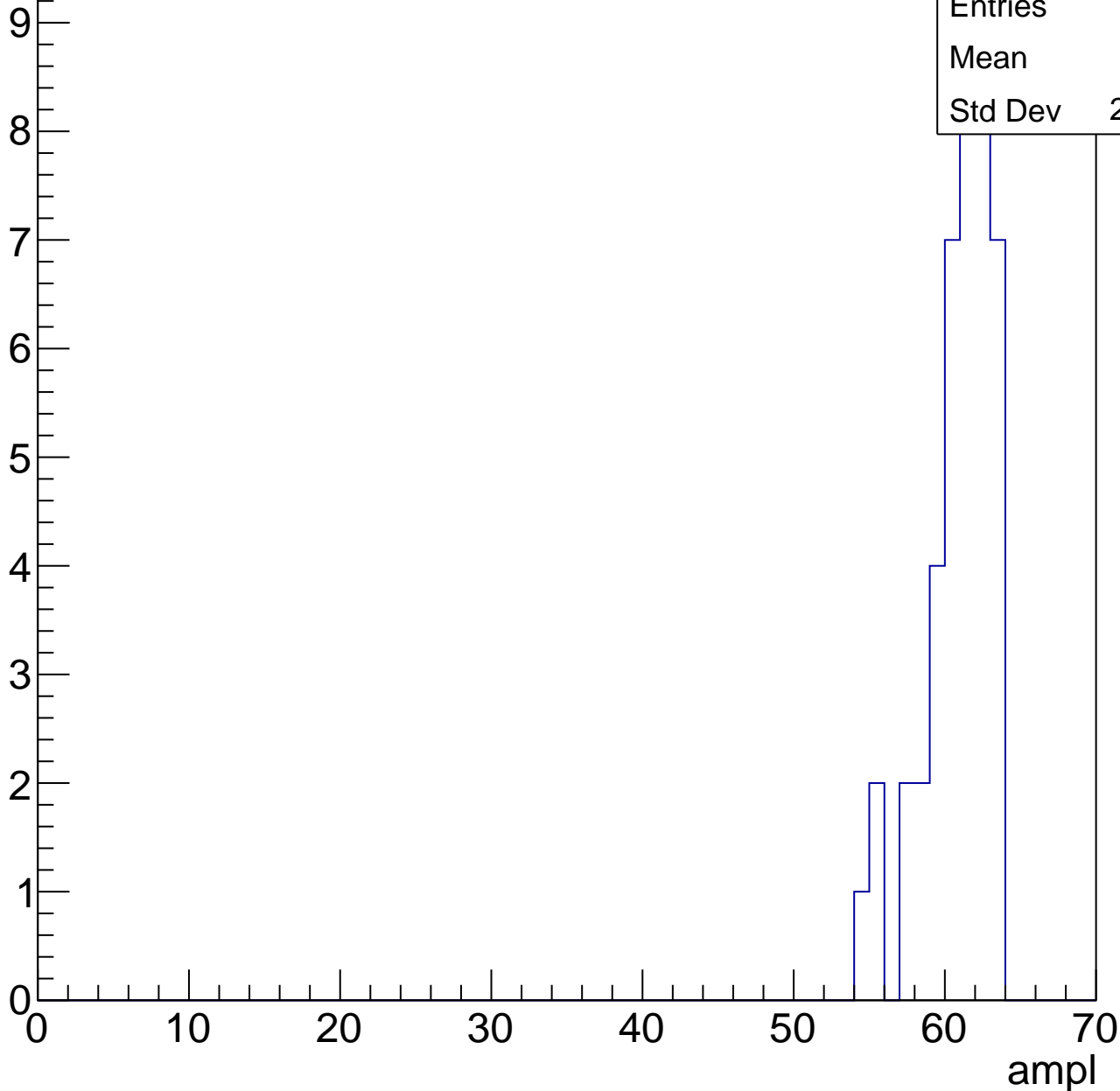


B0L001S, U25-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	60.4
Std Dev	2.268



B0L001S, U25-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

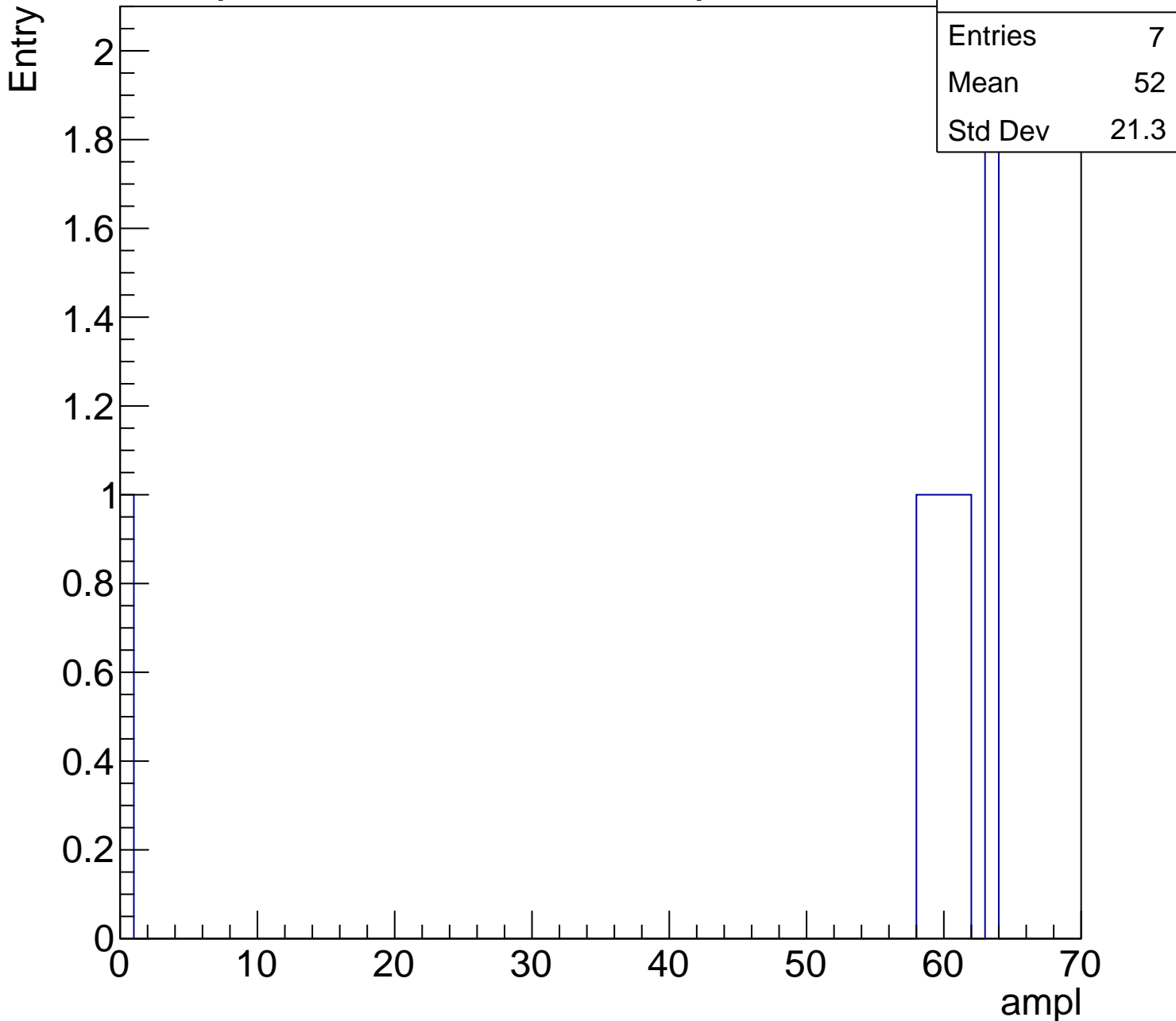
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52
Std Dev	21.3

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch105, adc0

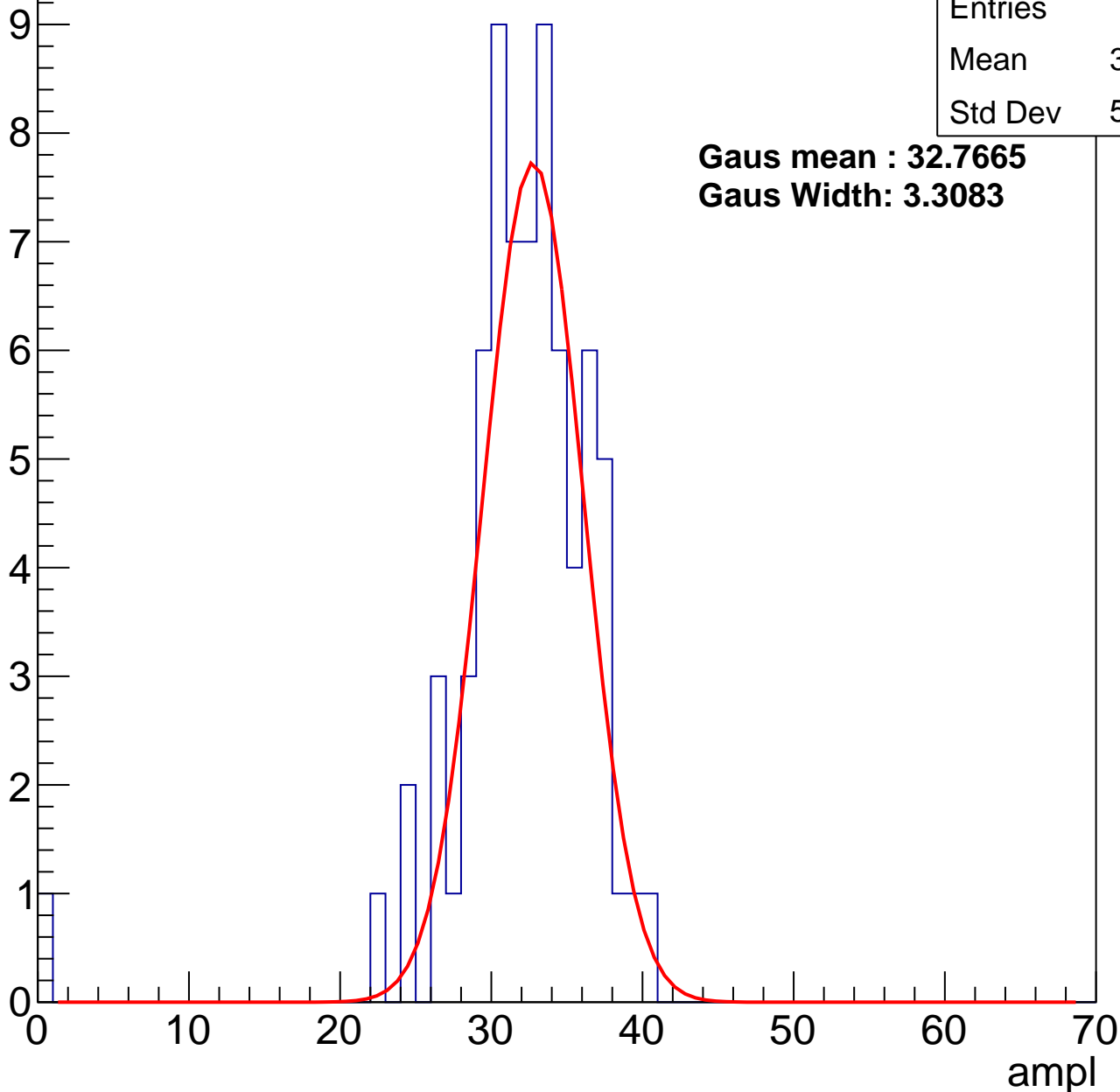
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	31.55
Std Dev	5.174

Gaus mean : 32.7665

Gaus Width: 3.3083



B0L001S, U25-ch105, adc1

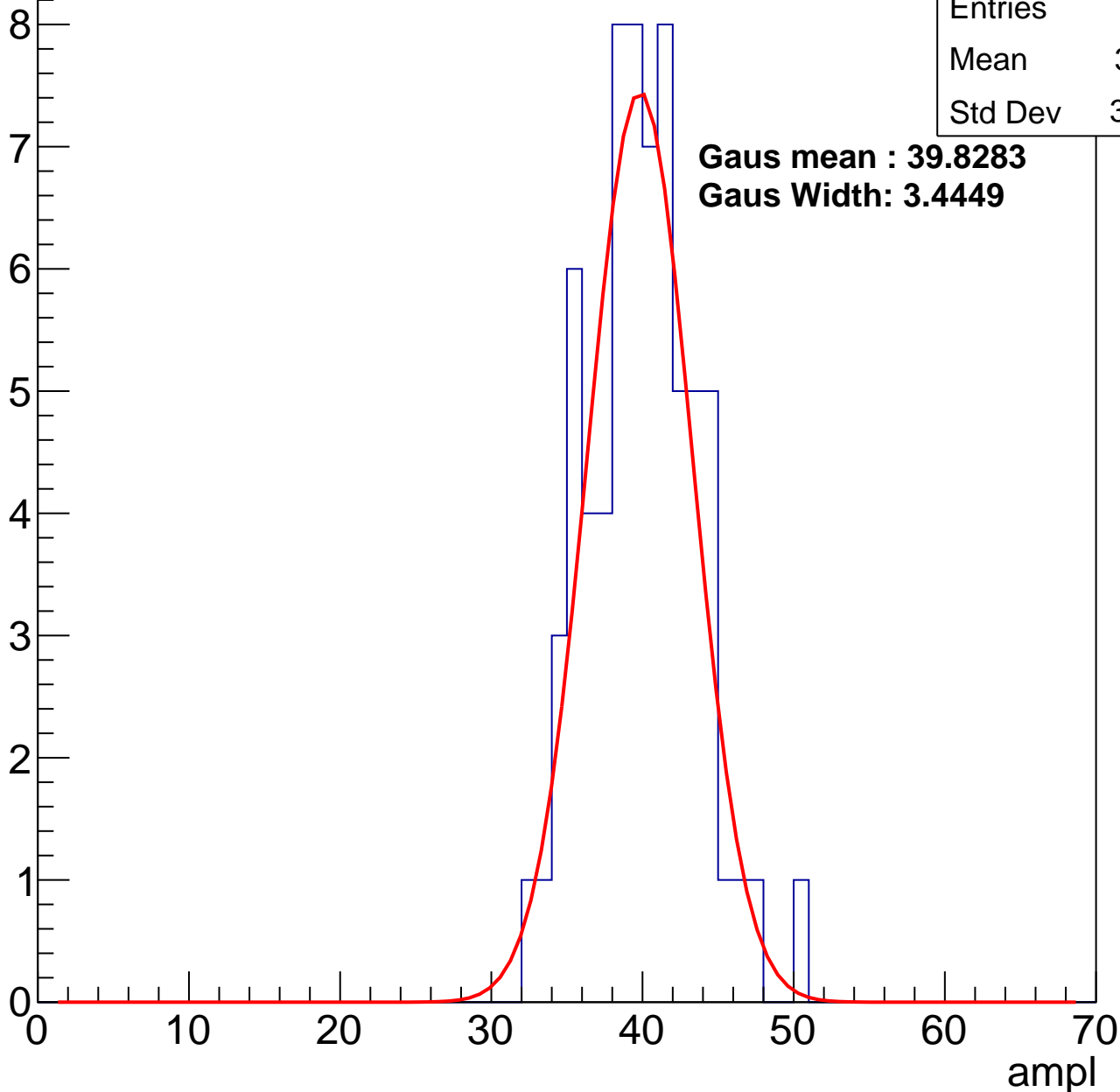
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	39.51
Std Dev	3.525

Gaus mean : 39.8283

Gaus Width: 3.4449



B0L001S, U25-ch105, adc2

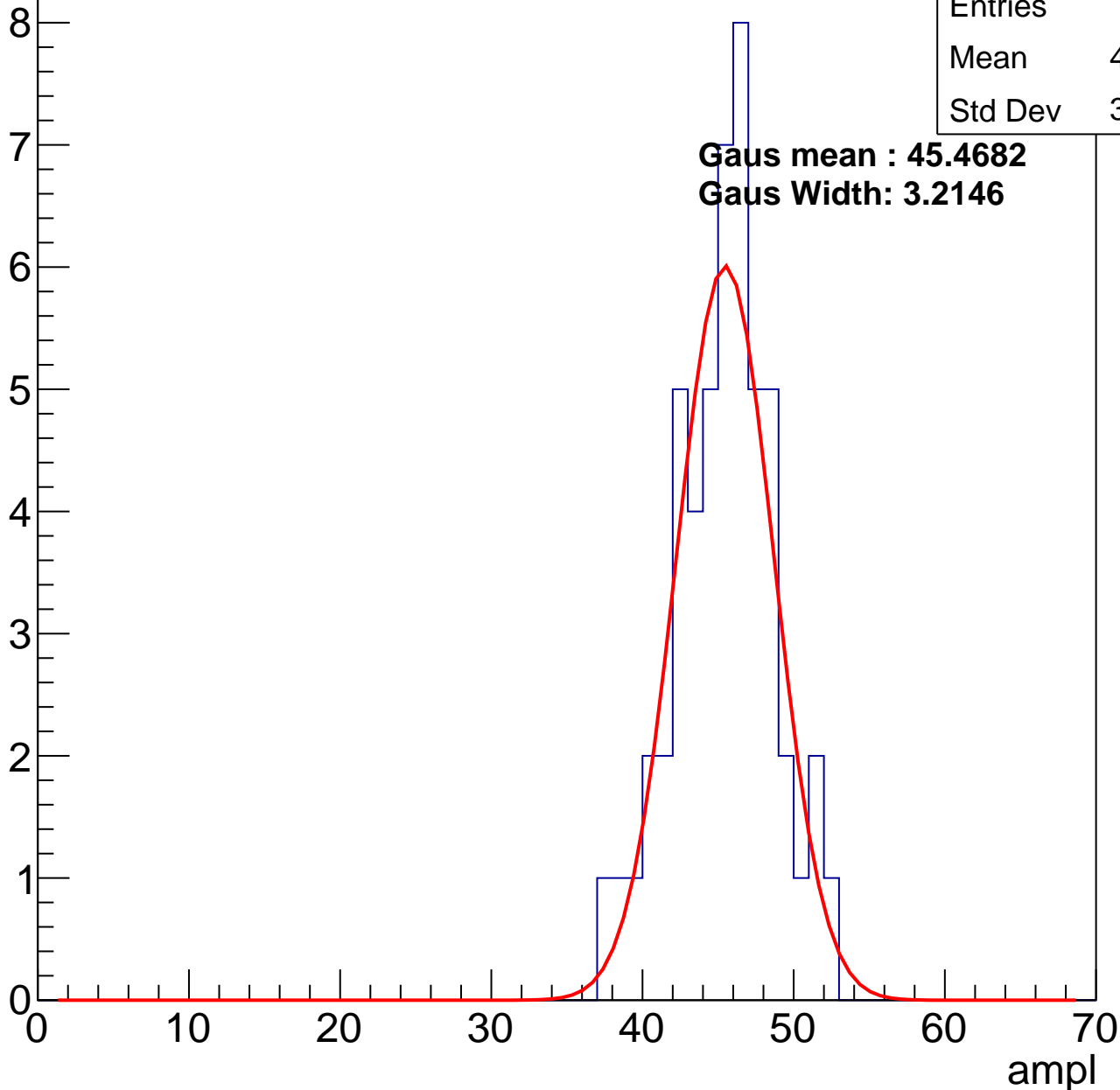
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.96
Std Dev	3.246

Gaus mean : 45.4682

Gaus Width: 3.2146

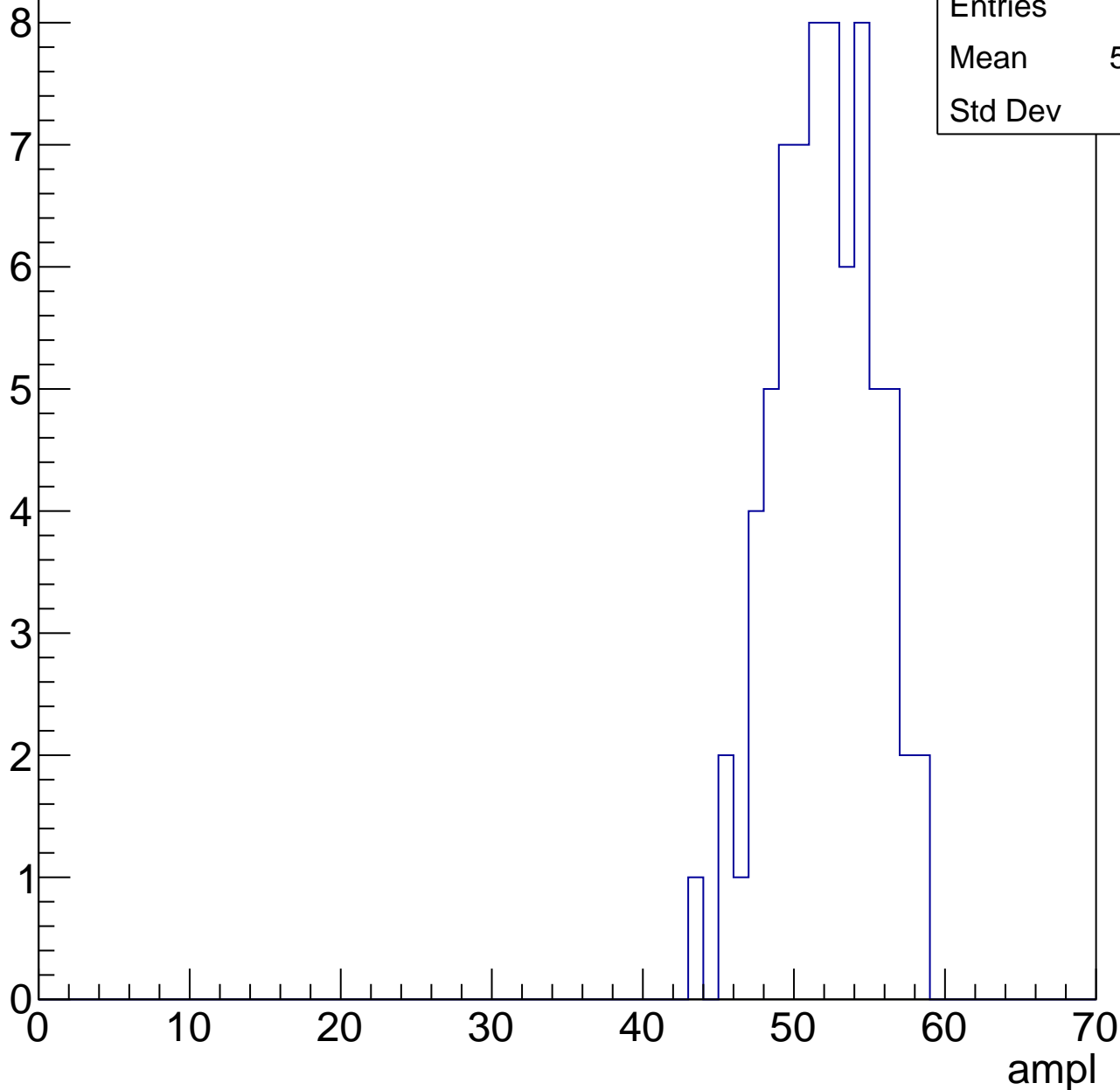


B0L001S, U25-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.54
Std Dev	3.28

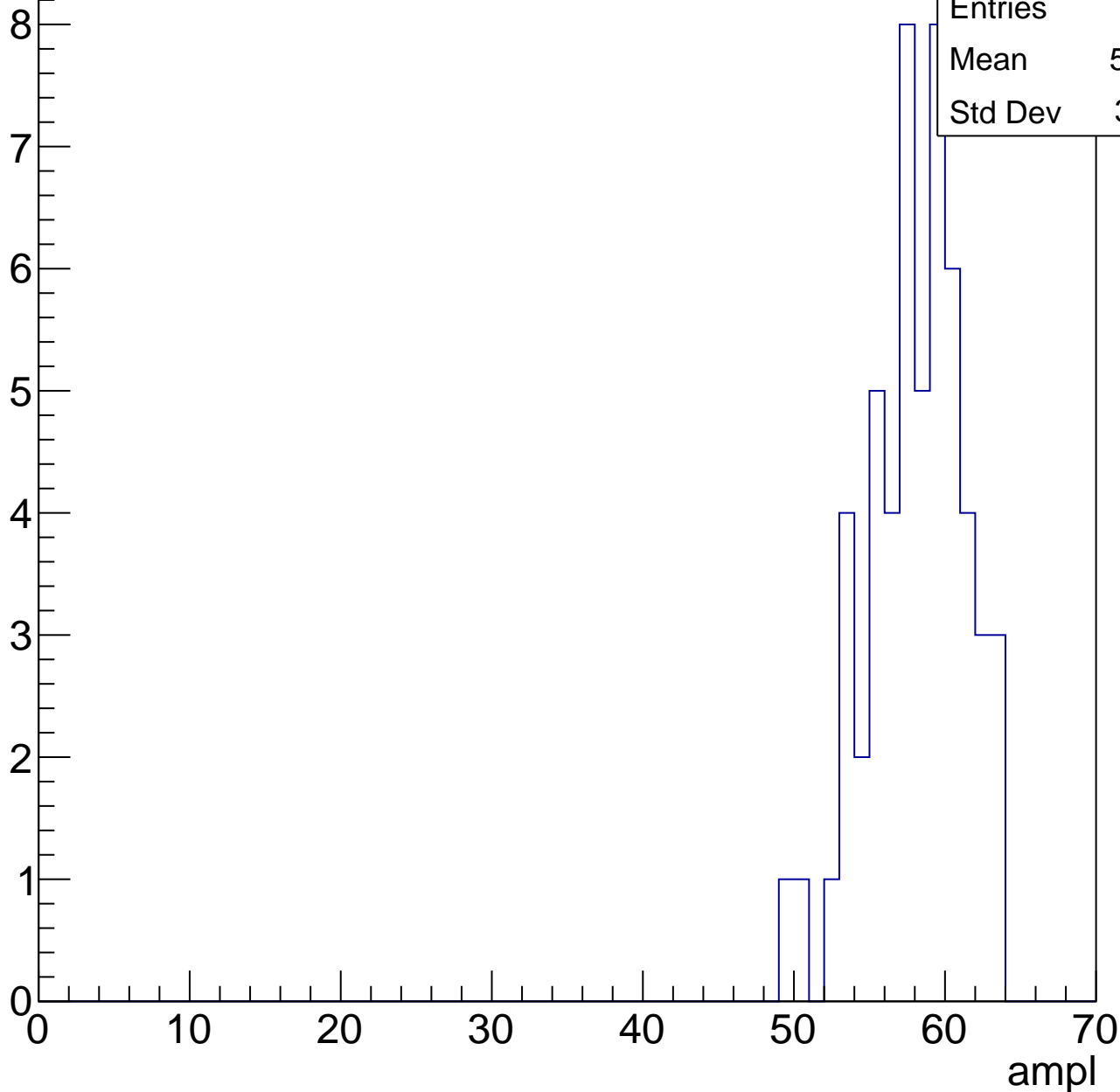


B0L001S, U25-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.58
Std Dev	3.201

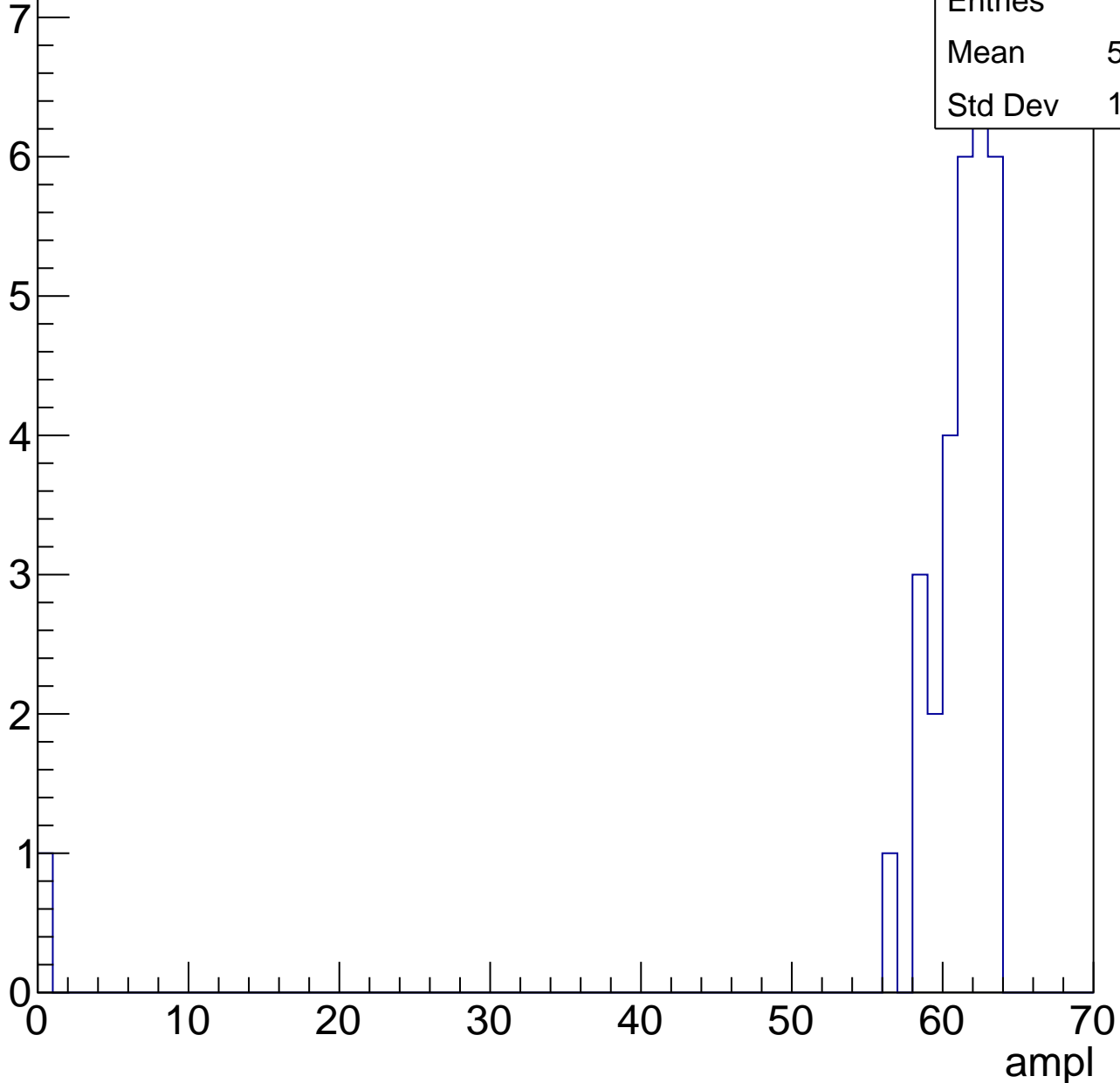


B0L001S, U25-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.87
Std Dev	11.07



B0L001S, U25-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U25-ch106, adc0

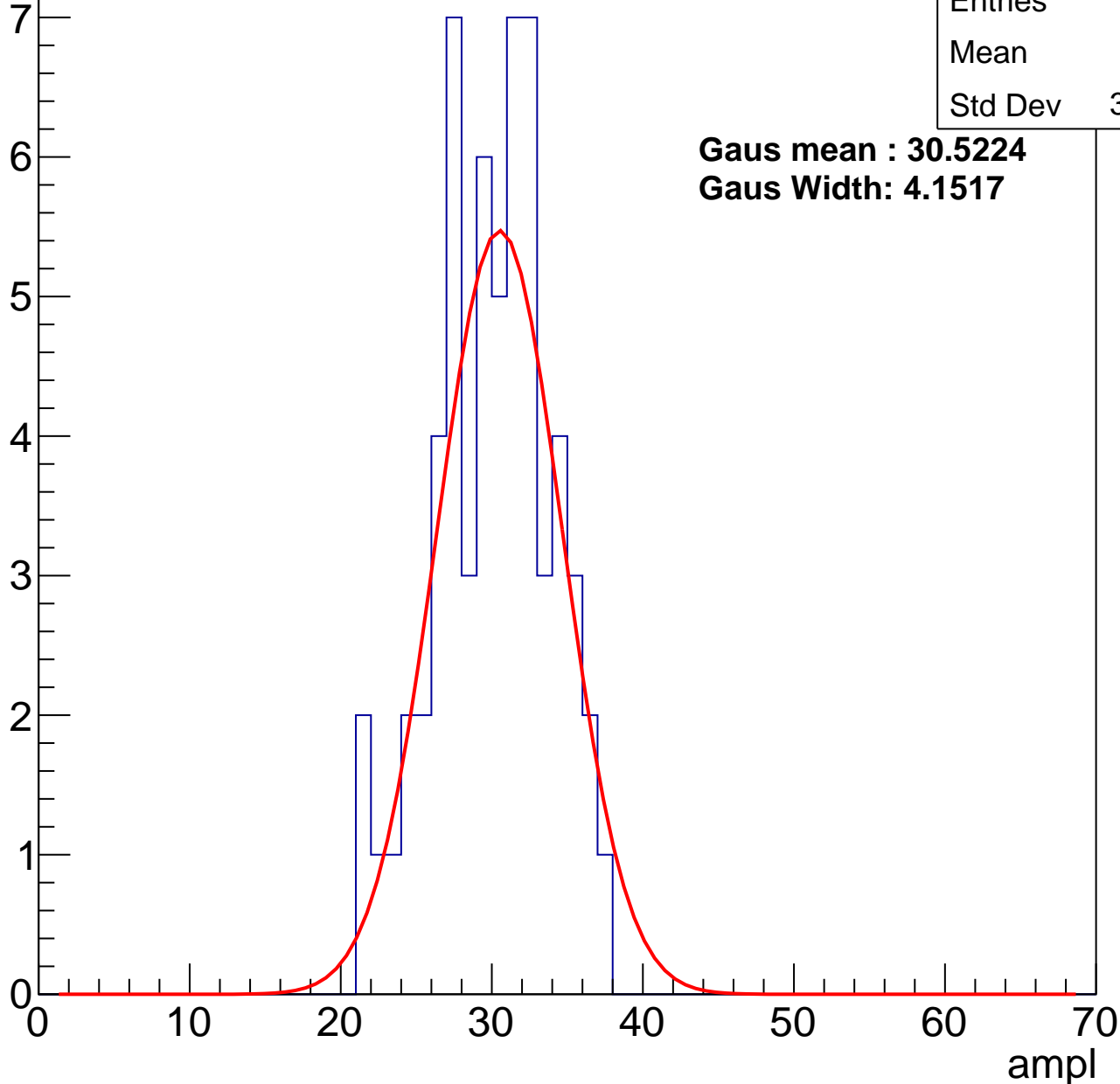
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.6
Std Dev	3.774

Gaus mean : 30.5224

Gaus Width: 4.1517



B0L001S, U25-ch106, adc1

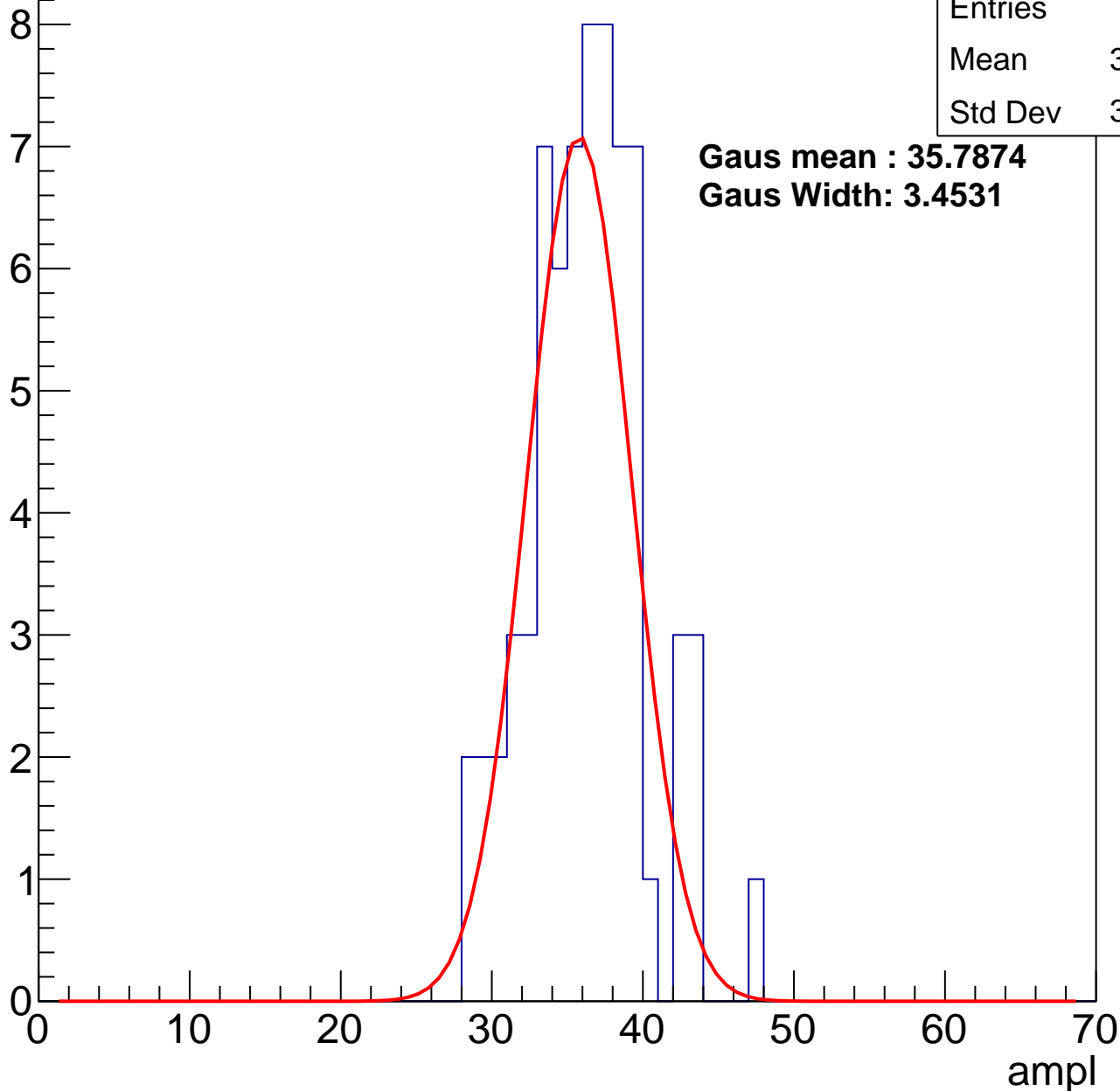
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	35.83
Std Dev	3.787

Gaus mean : 35.7874

Gaus Width: 3.4531



B0L001S, U25-ch106, adc2

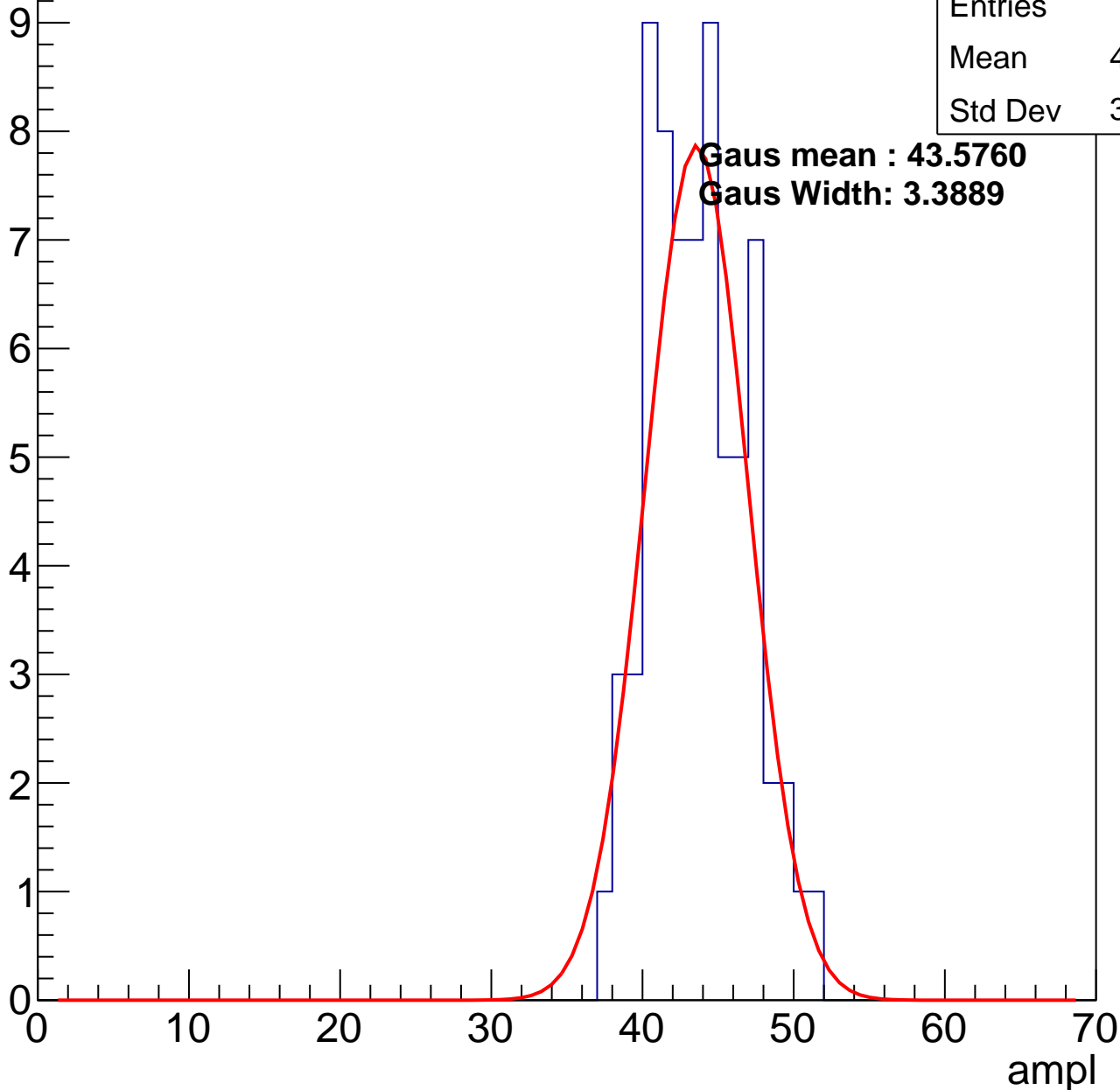
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.23
Std Dev	3.154

Gaus mean : 43.5760

Gaus Width: 3.3889

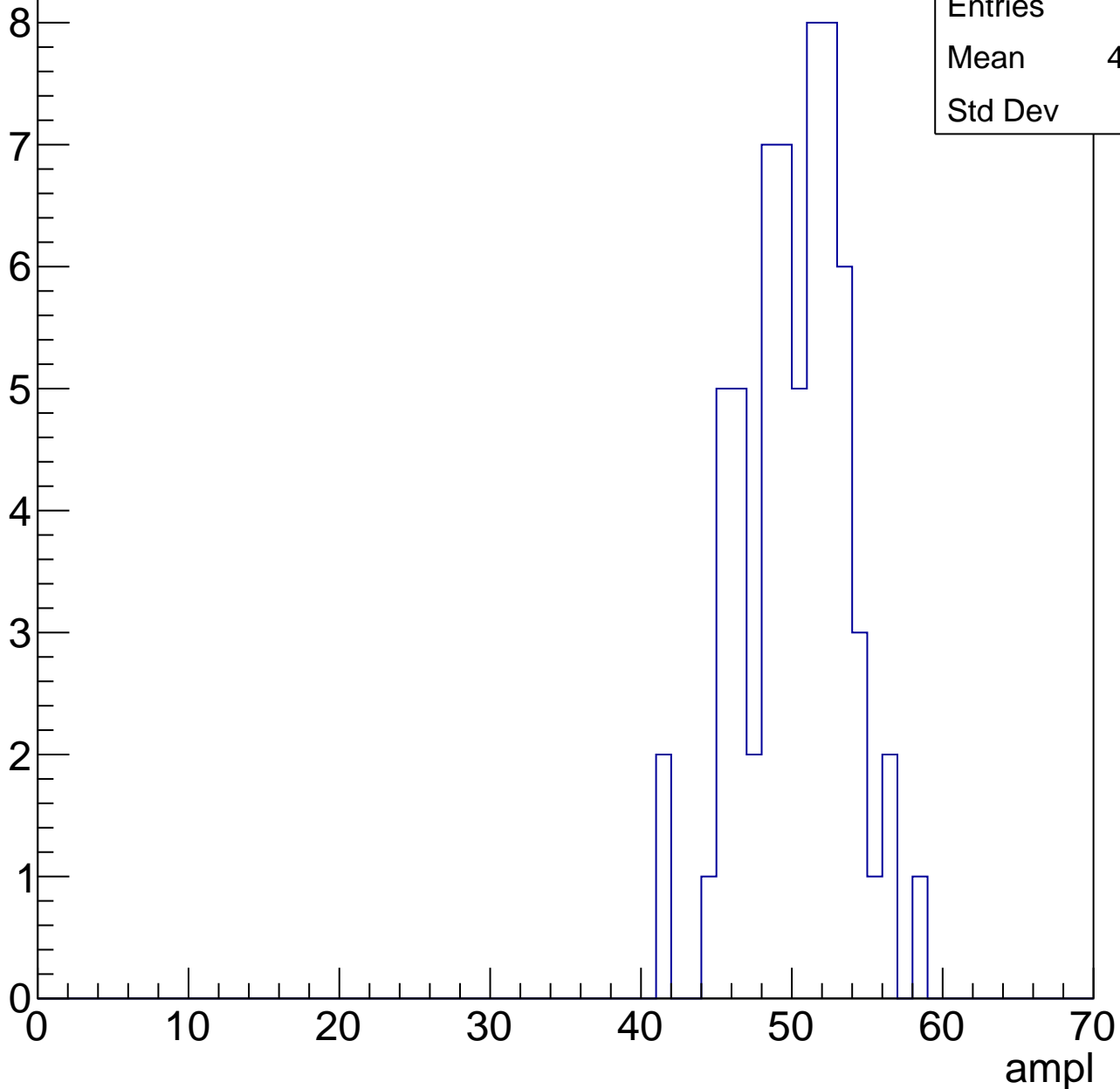


B0L001S, U25-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	49.73
Std Dev	3.46

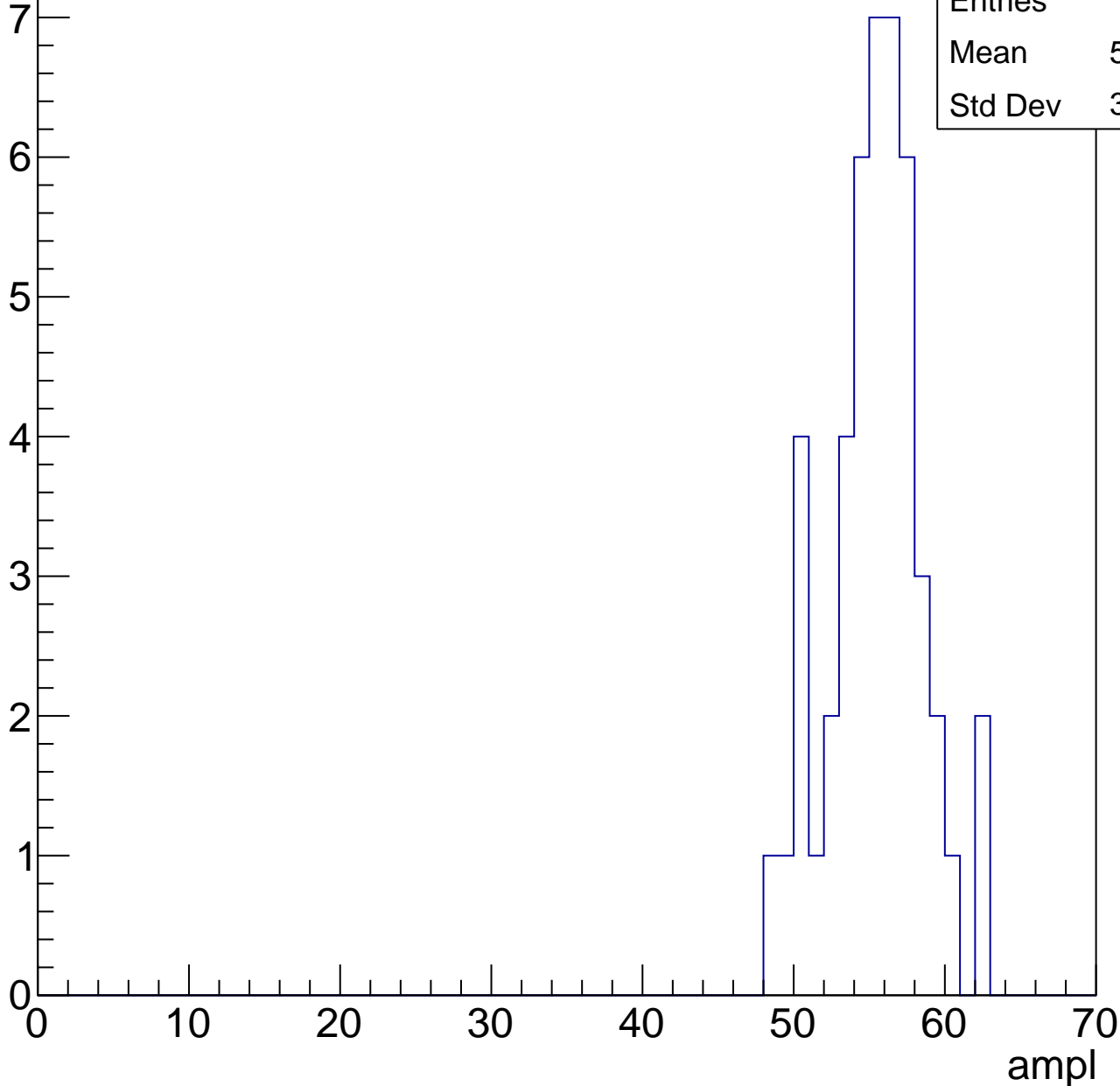


B0L001S, U25-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

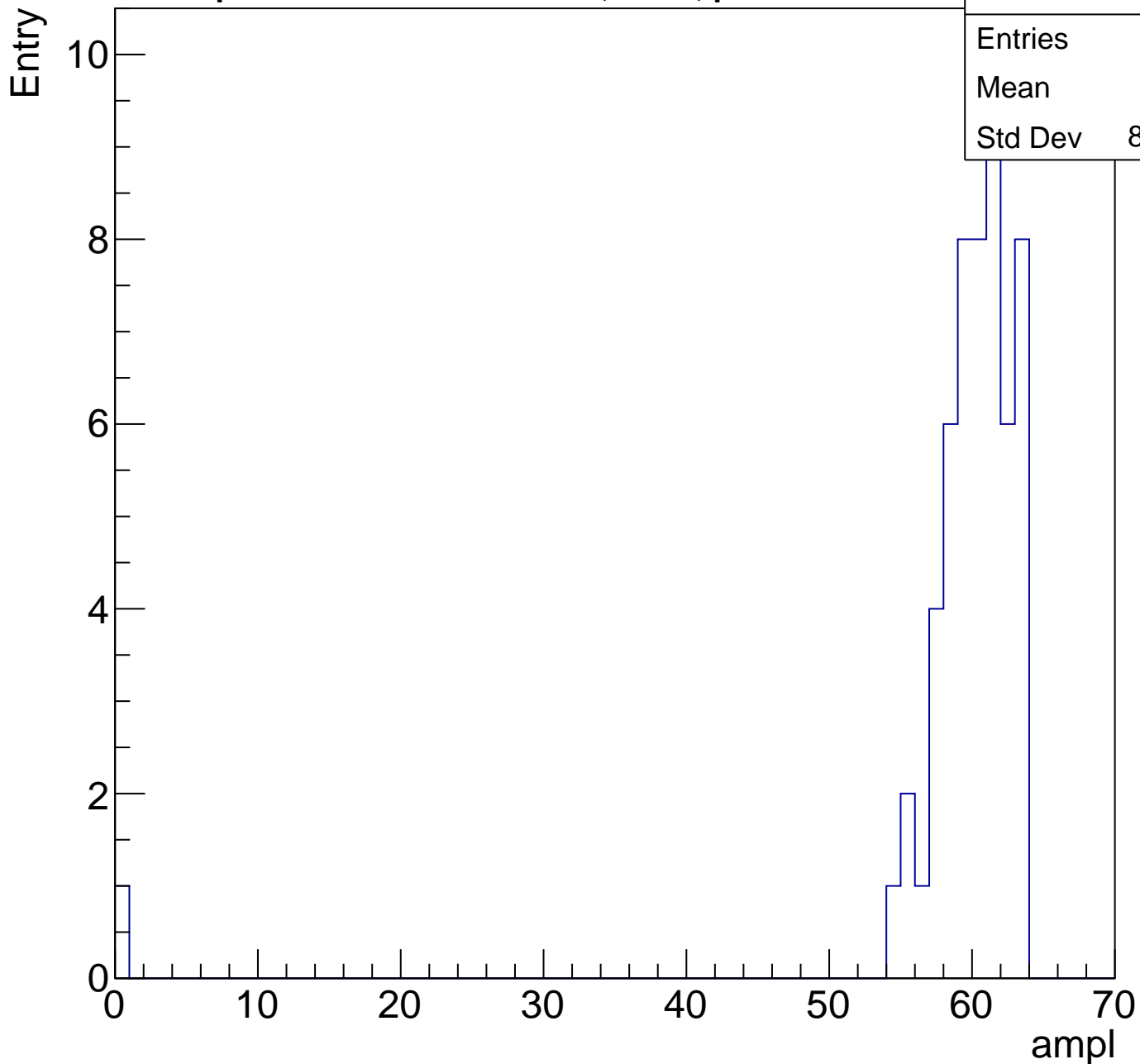
Entries	47
Mean	54.96
Std Dev	3.108



B0L001S, U25-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	55
Mean	58.8
Std Dev	8.309



B0L001S, U25-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

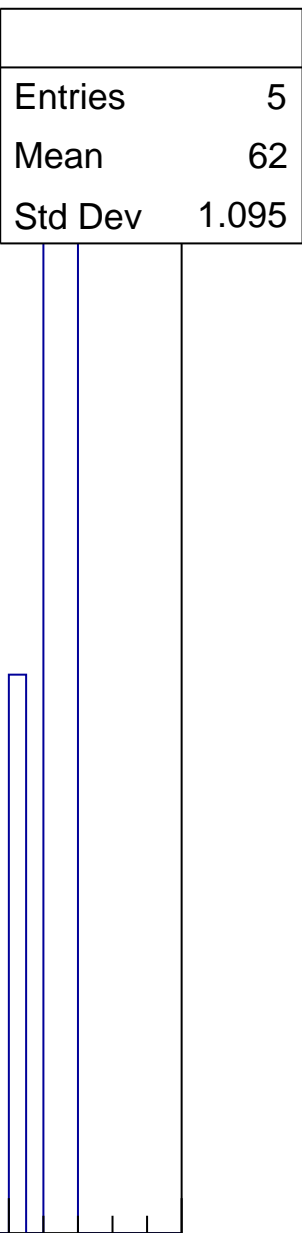
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	1.095

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch107, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	29.37
Std Dev	5.913

Gaus mean : 30.6787

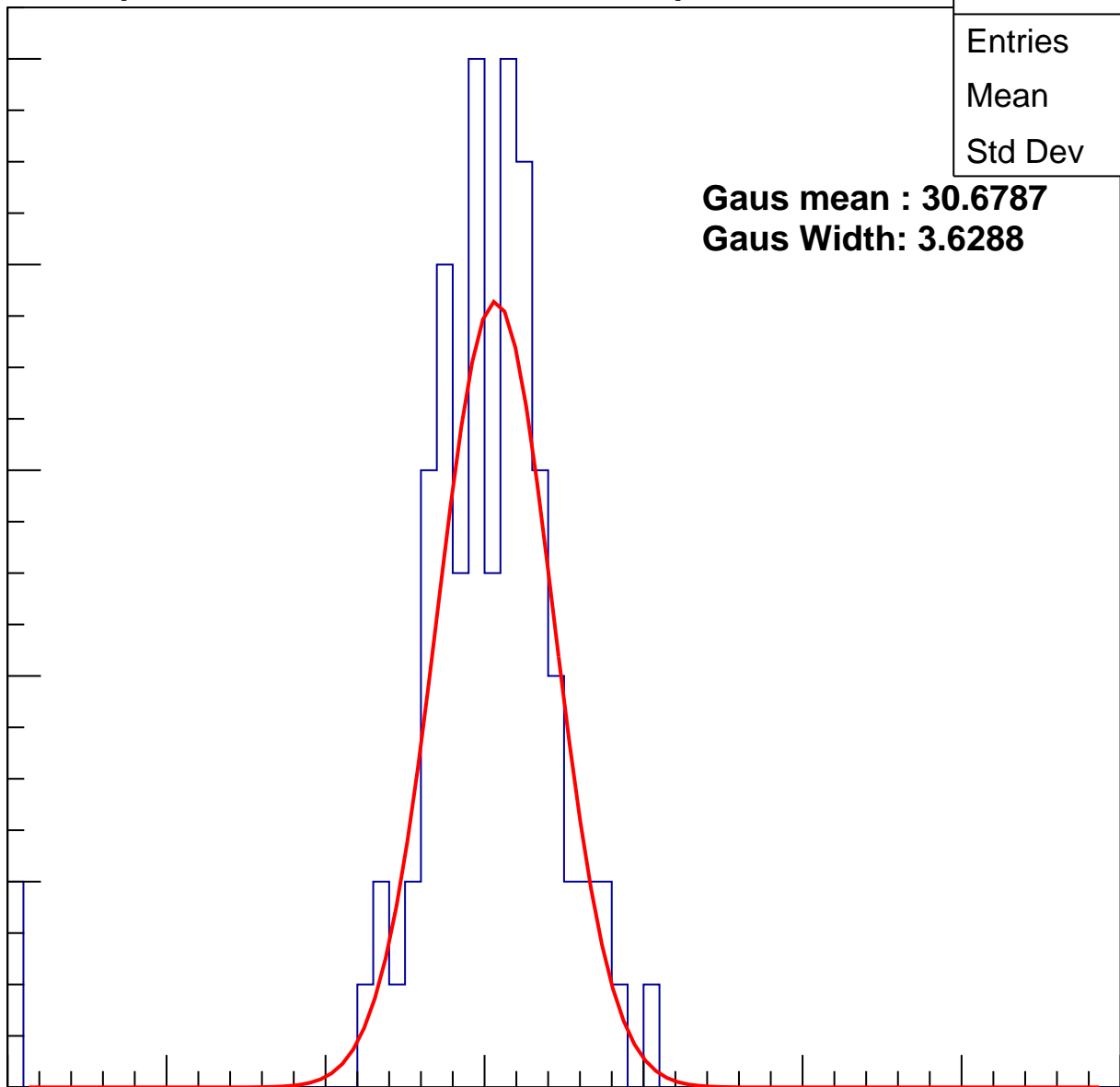
Gaus Width: 3.6288

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch107, adc1

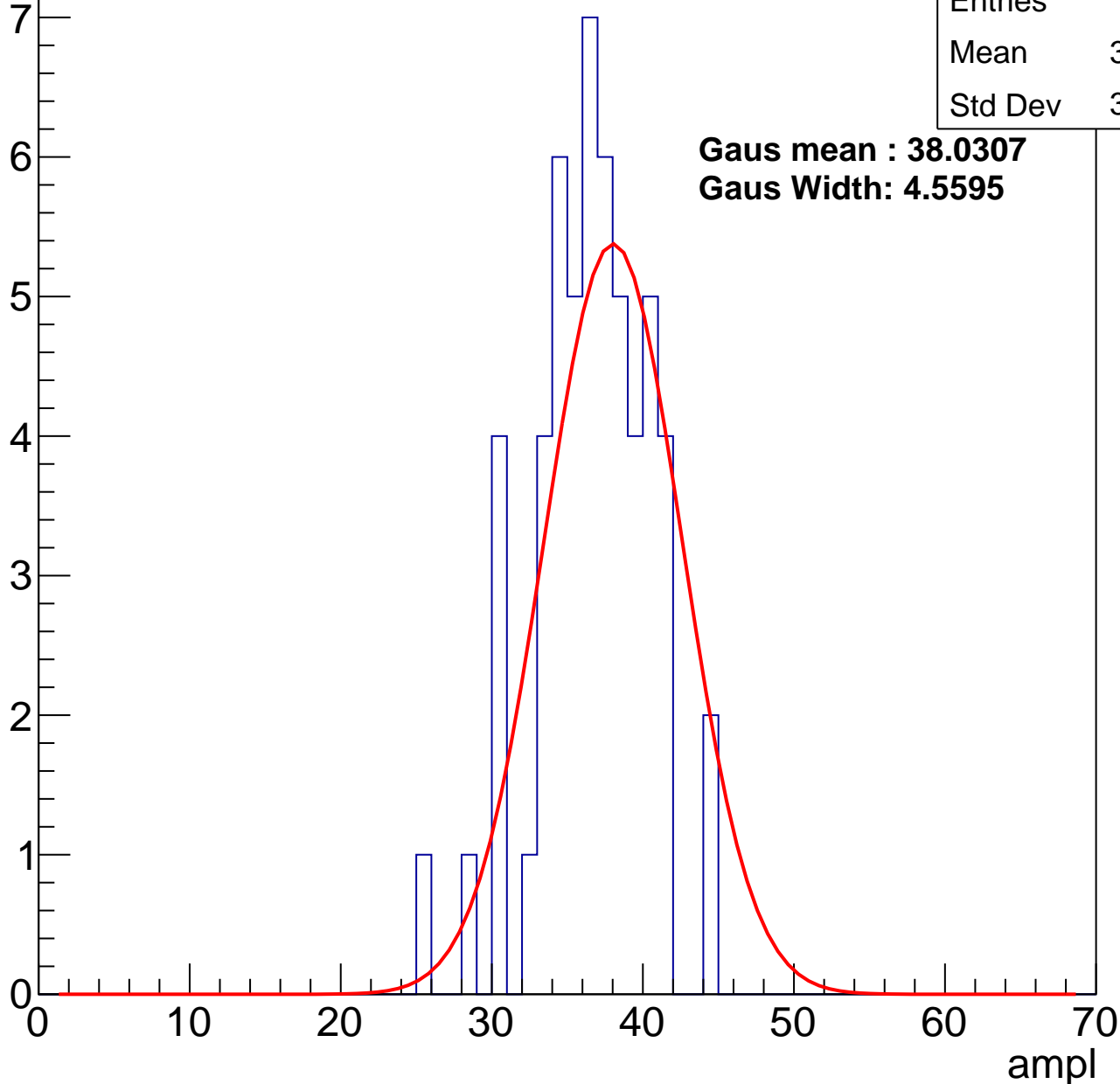
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	36.15
Std Dev	3.763

Gaus mean : 38.0307

Gaus Width: 4.5595



B0L001S, U25-ch107, adc2

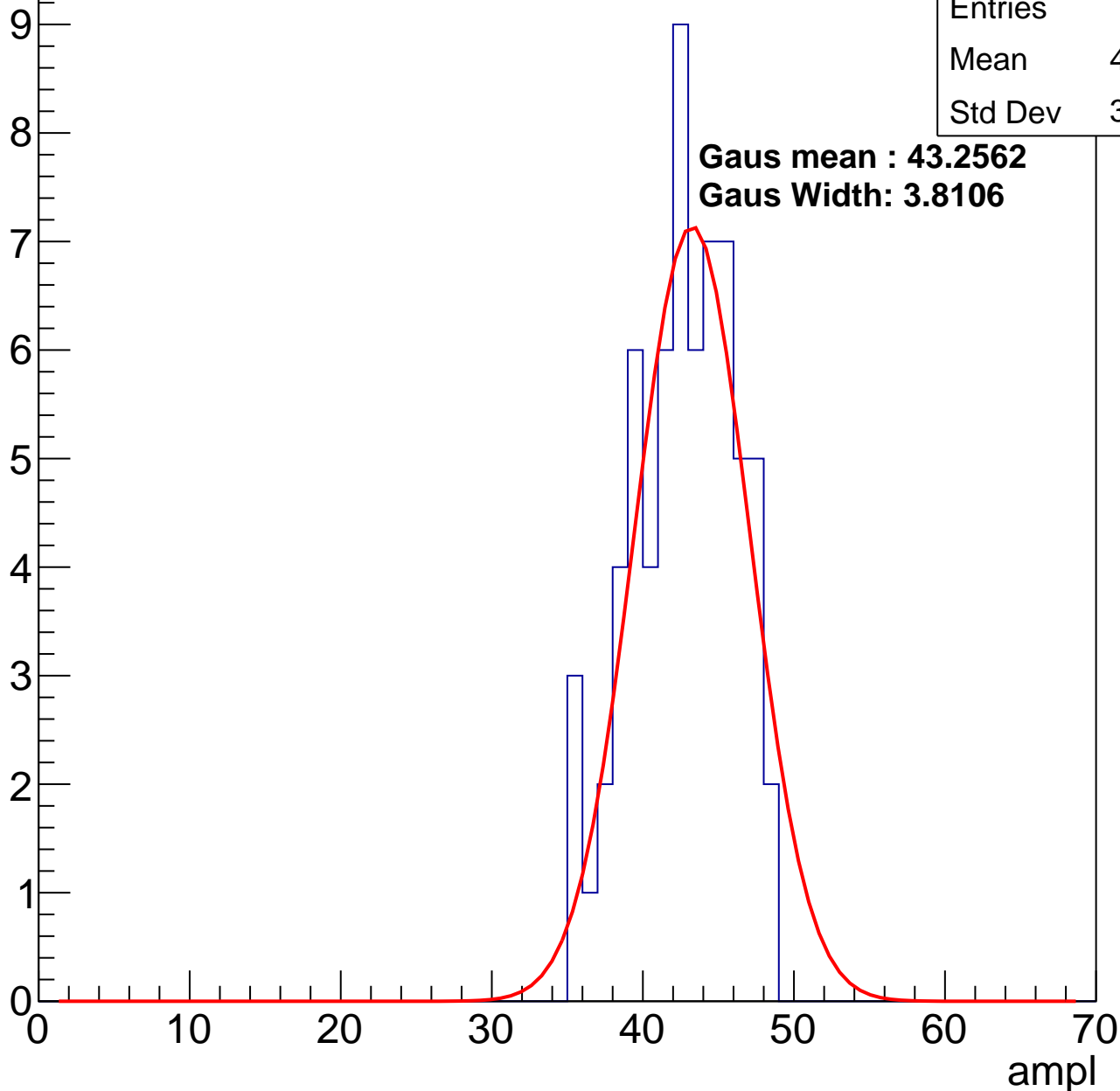
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.19
Std Dev	3.333

Gaus mean : 43.2562

Gaus Width: 3.8106

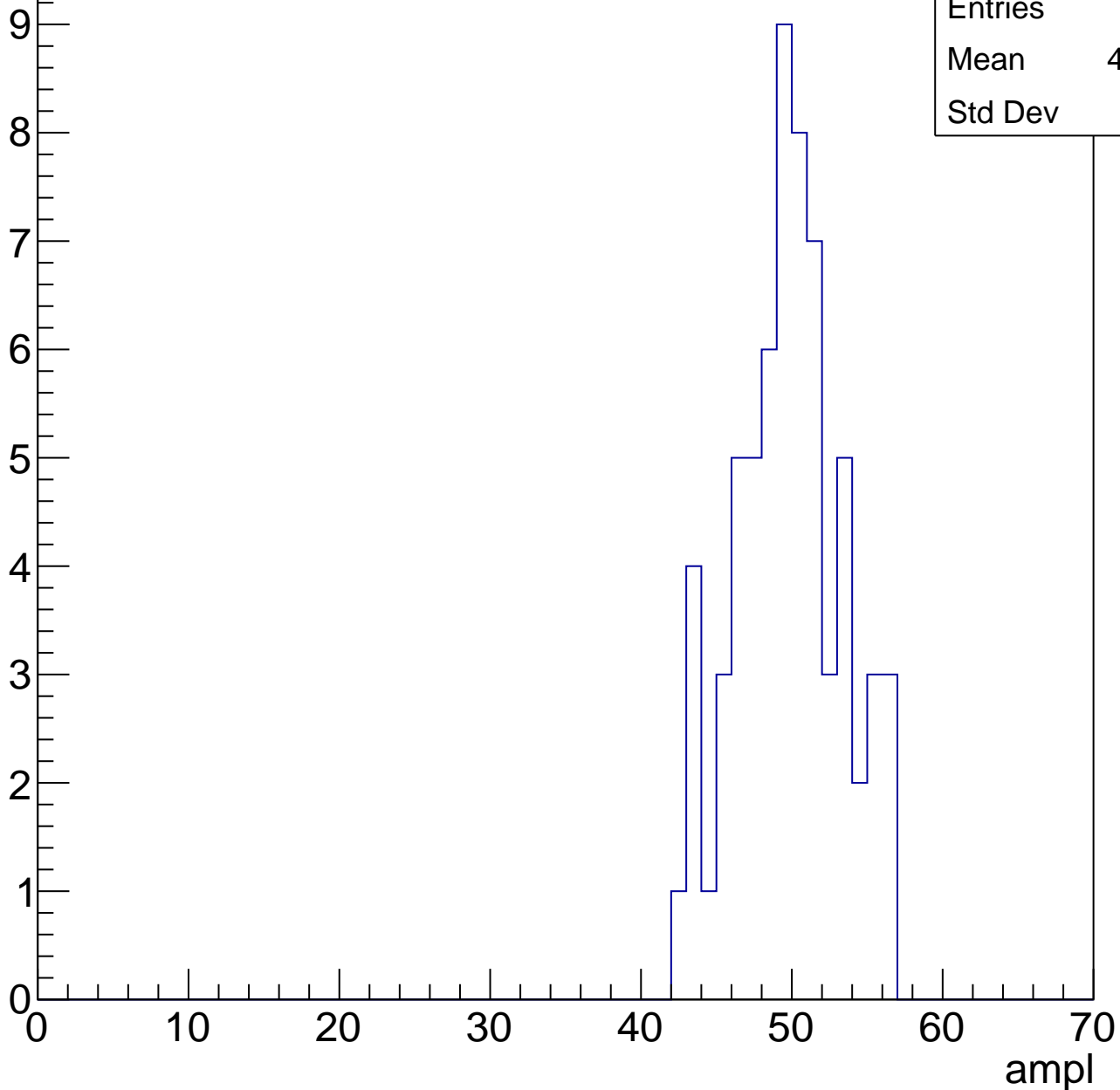


B0L001S, U25-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.32
Std Dev	3.46

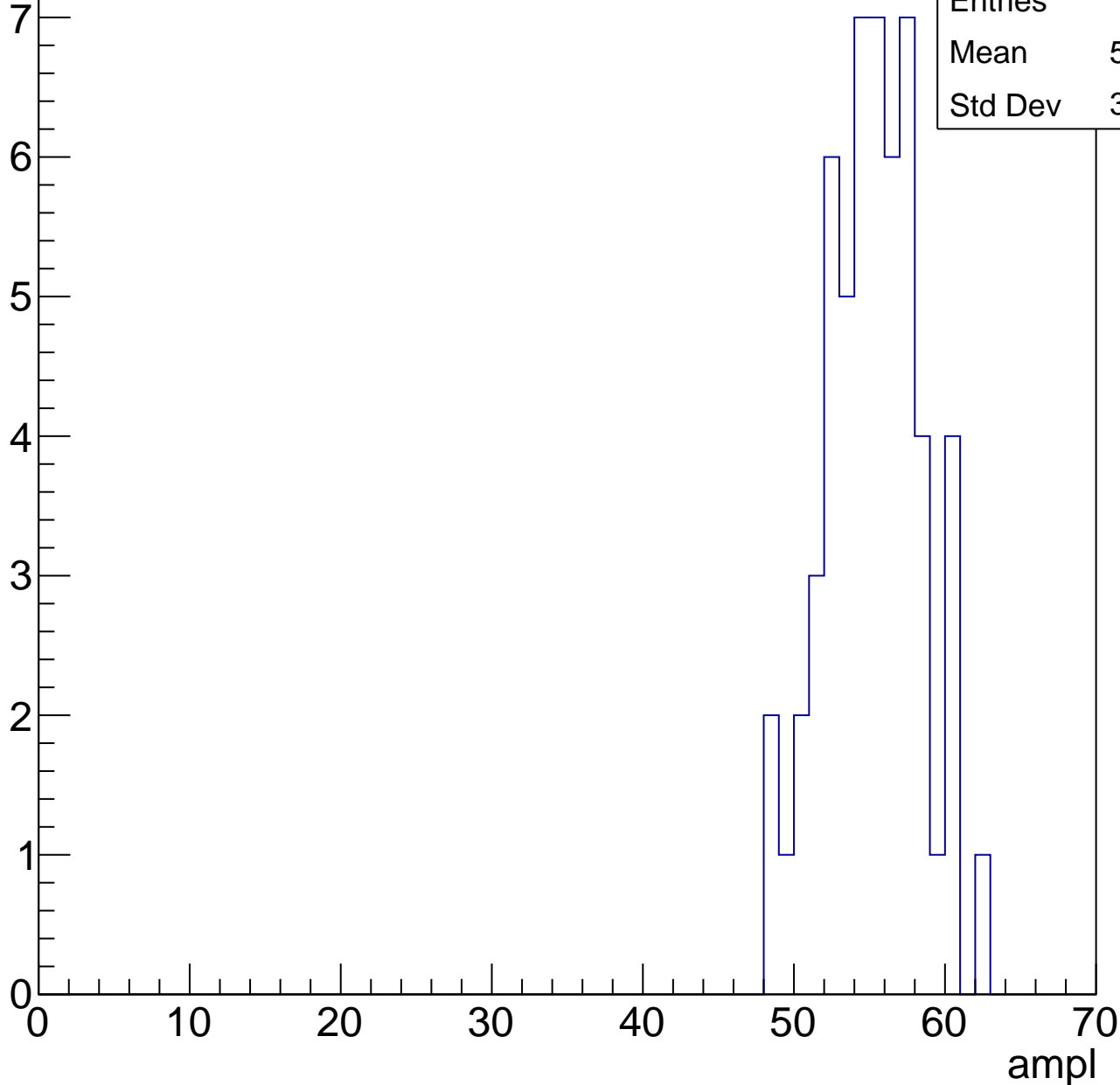


B0L001S, U25-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	54.75
Std Dev	3.118

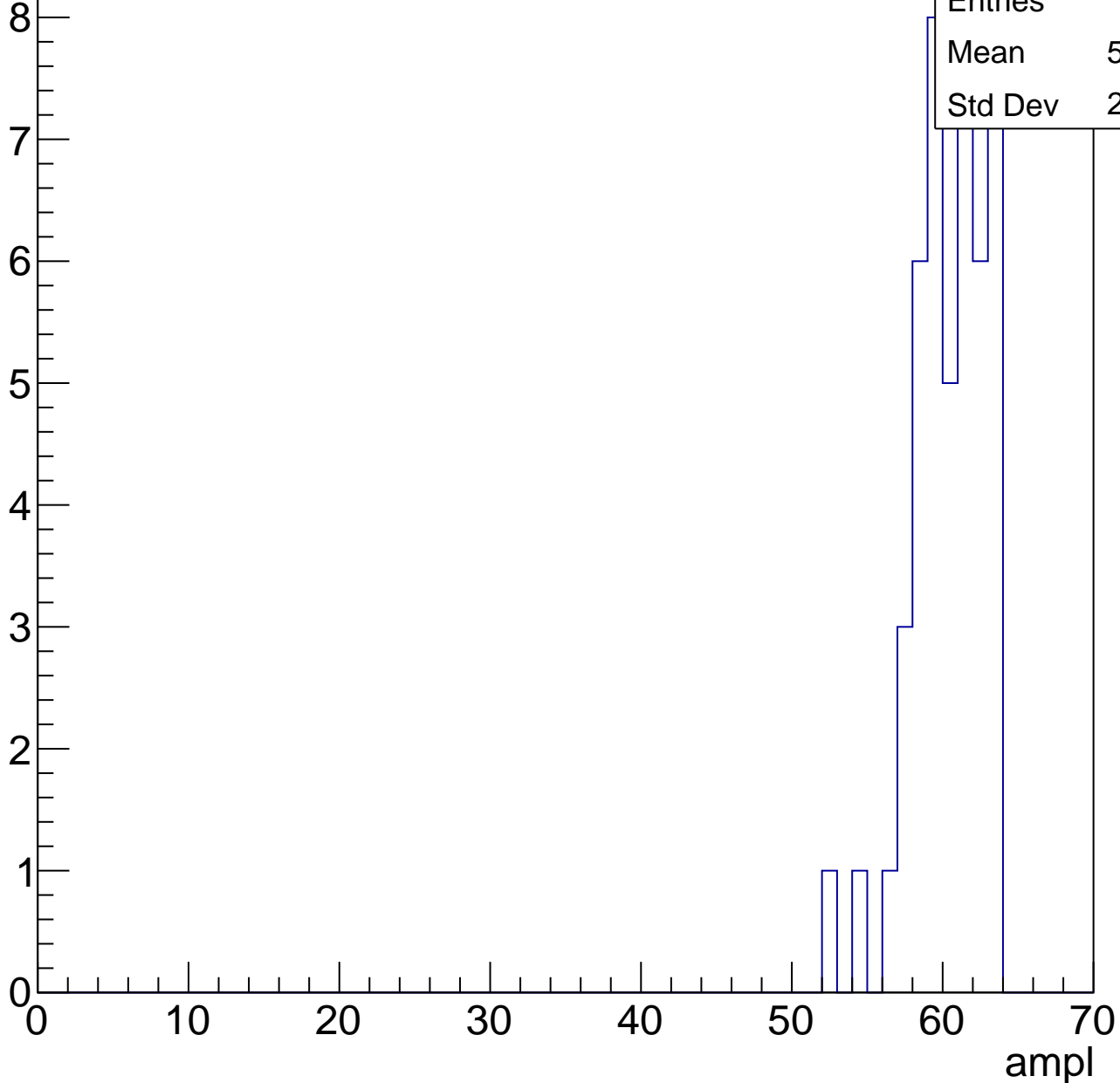


B0L001S, U25-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

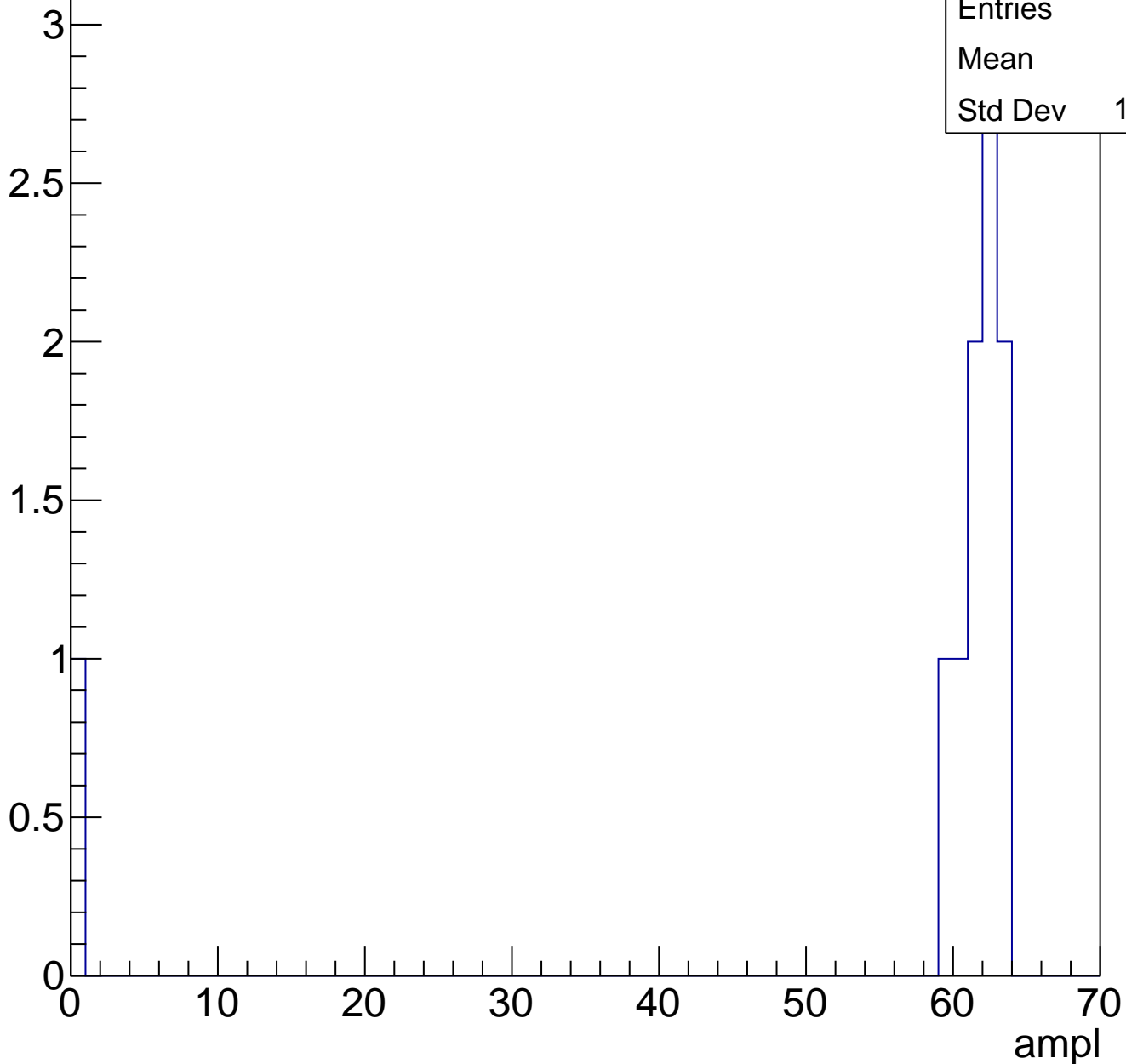
Entries	47
Mean	59.94
Std Dev	2.436



B0L001S, U25-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch108, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	30.09
Std Dev	7.037

Gaus mean : 33.0358

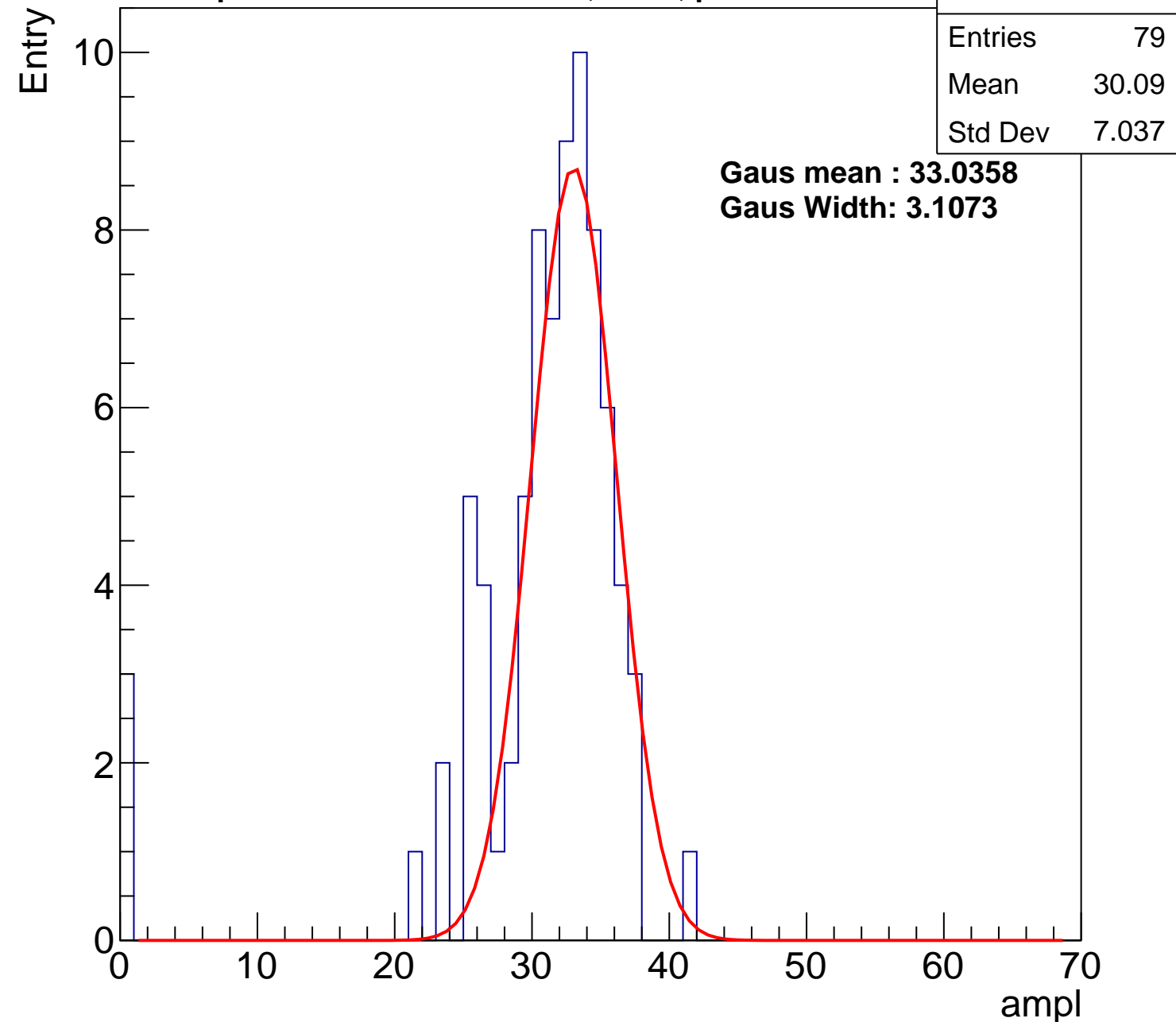
Gaus Width: 3.1073

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch108, adc1

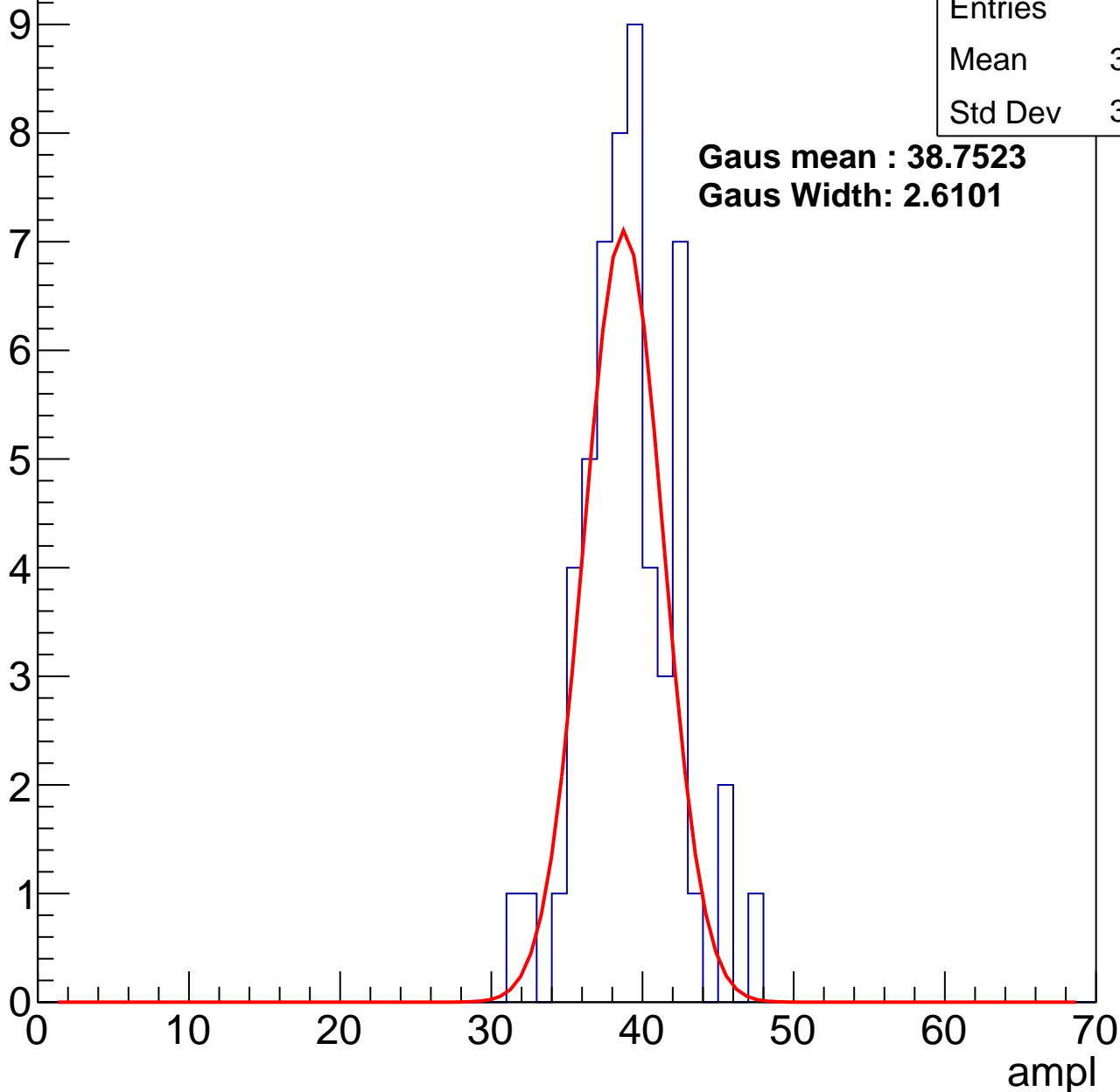
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	38.67
Std Dev	3.067

Gaus mean : 38.7523

Gaus Width: 2.6101



B0L001S, U25-ch108, adc2

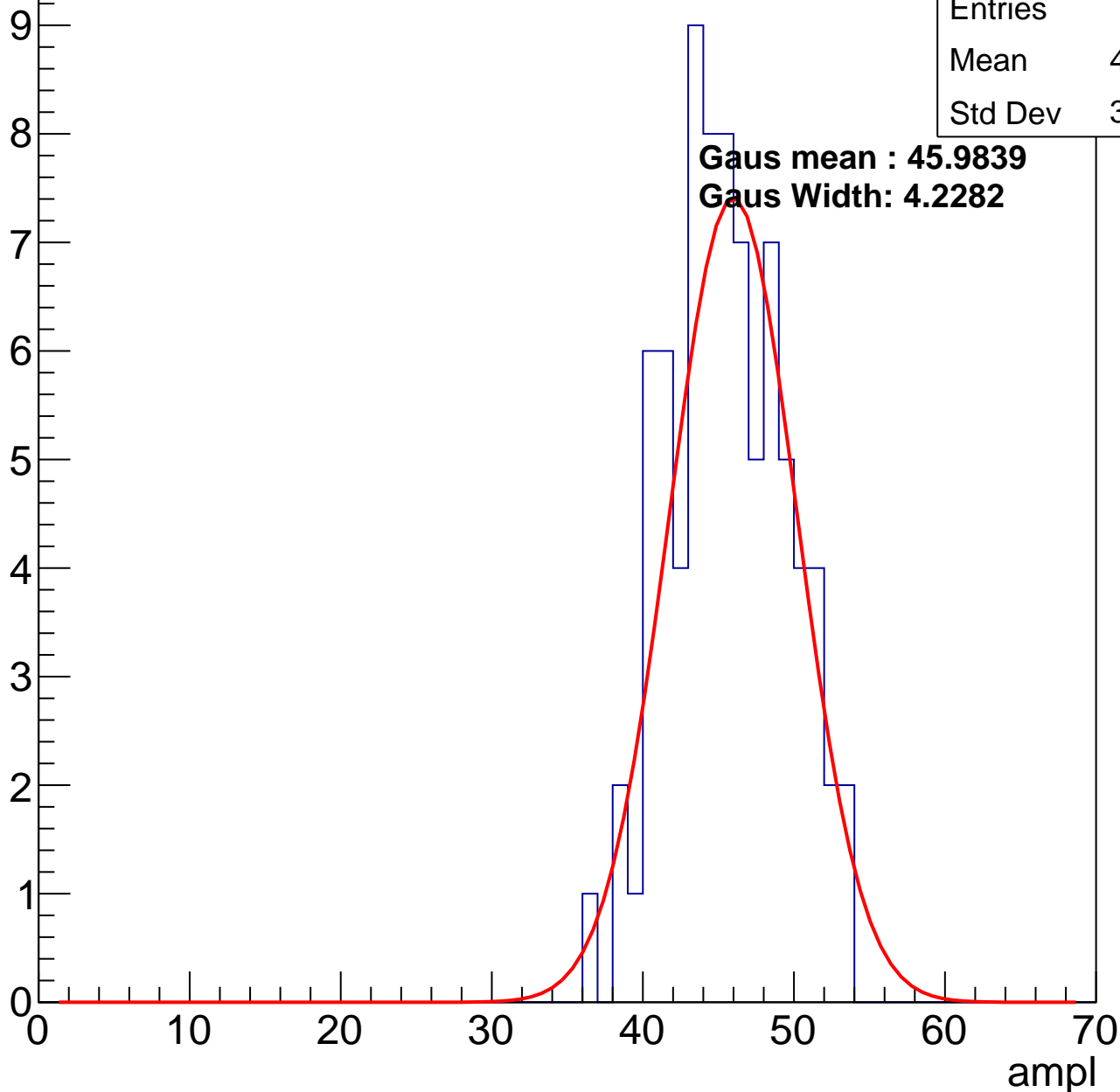
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	45.14
Std Dev	3.816

Gaus mean : 45.9839

Gaus Width: 4.2282

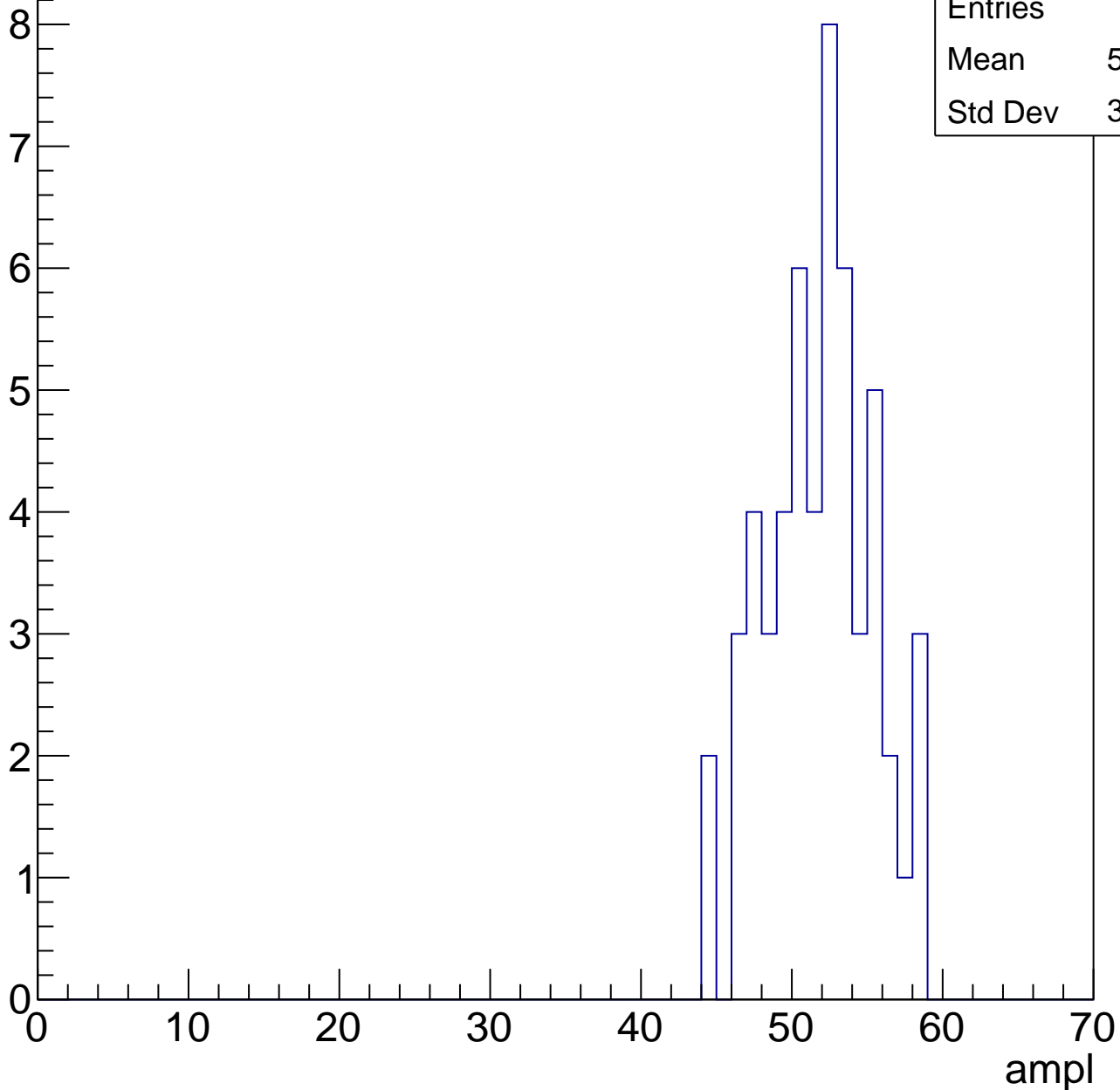


B0L001S, U25-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	51.33
Std Dev	3.475

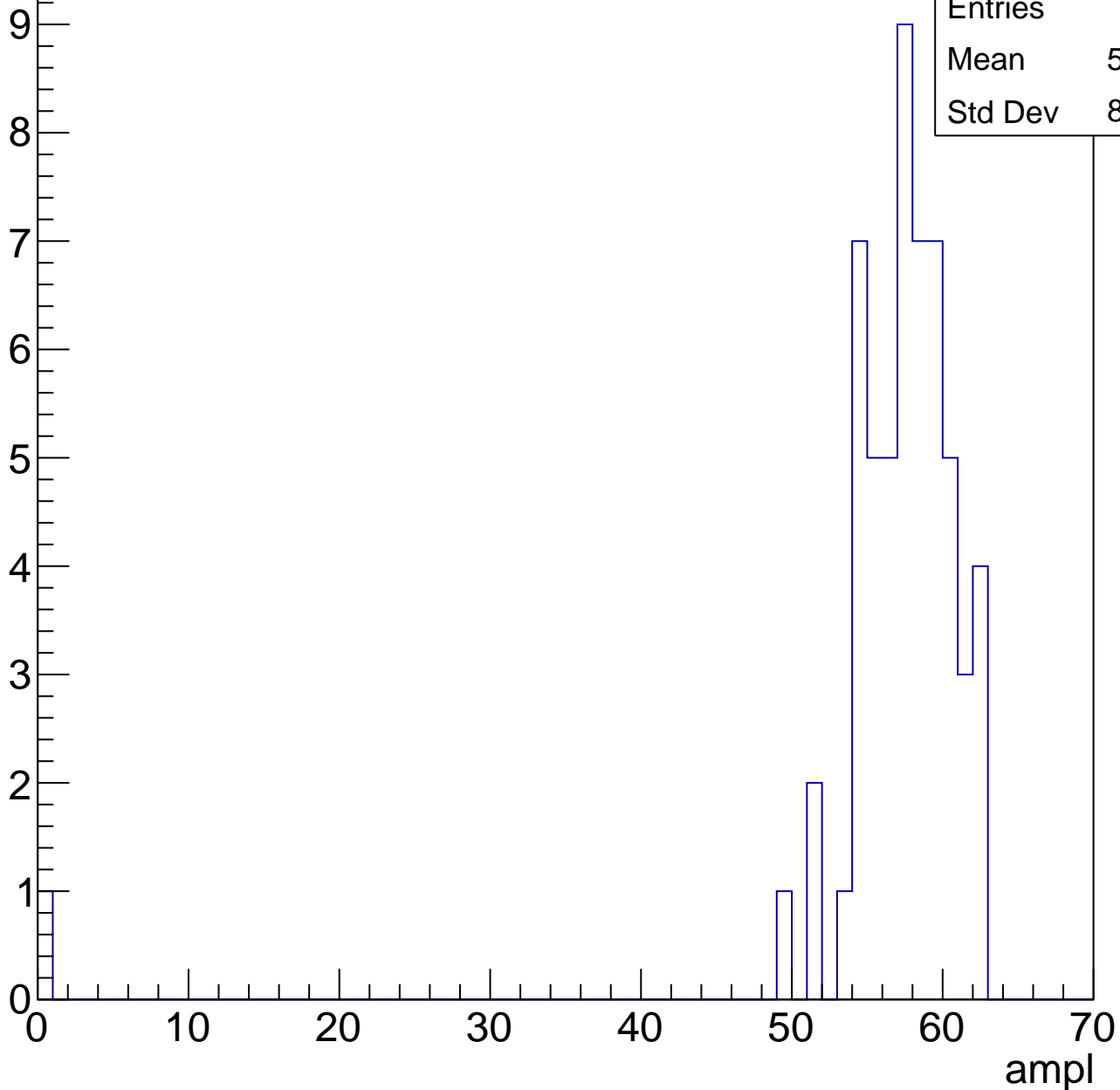


B0L001S, U25-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.14
Std Dev	8.029

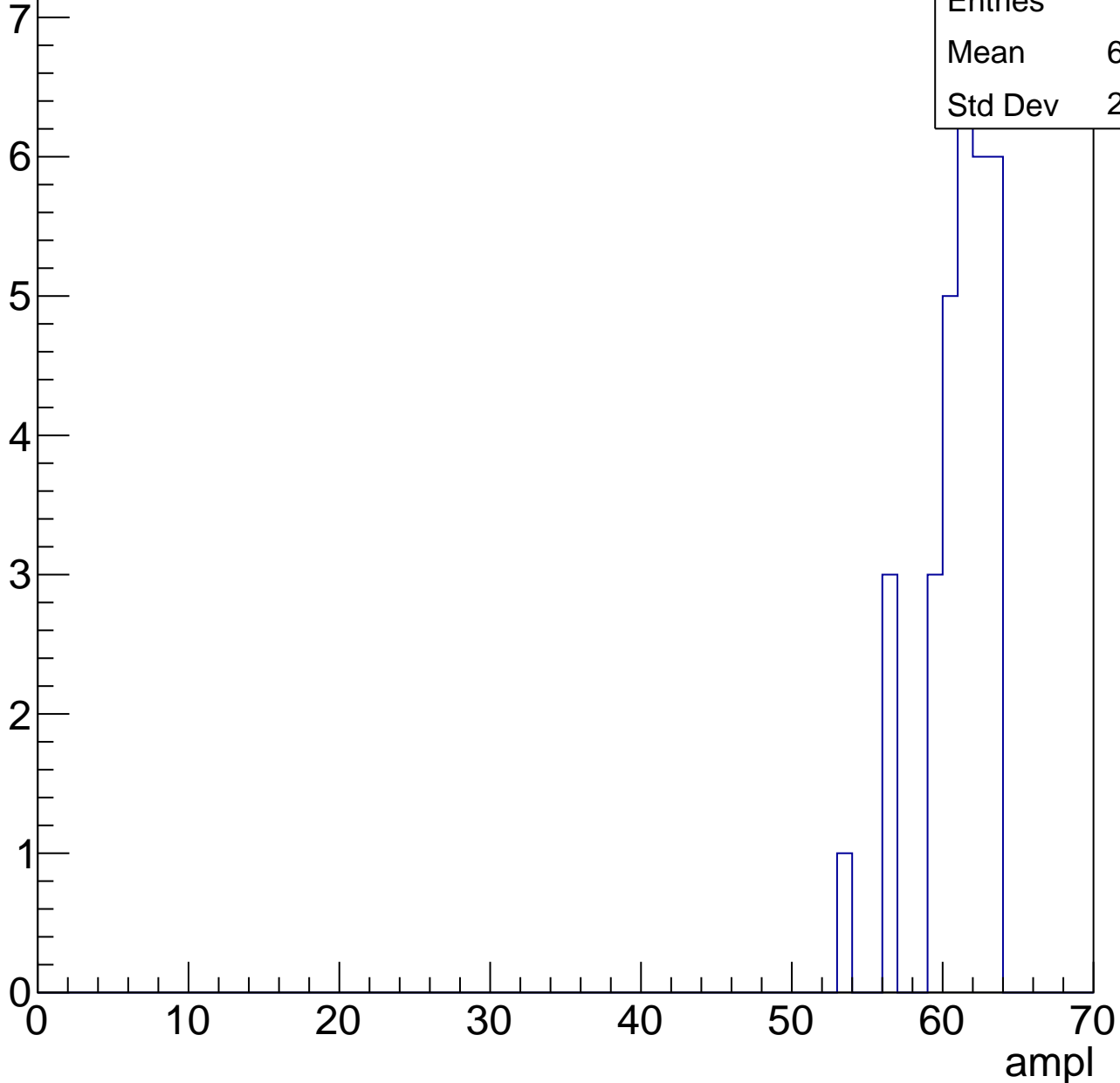


B0L001S, U25-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	60.48
Std Dev	2.394



B0L001S, U25-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch109, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	28.16
Std Dev	7.33

Gaus mean : 29.9792

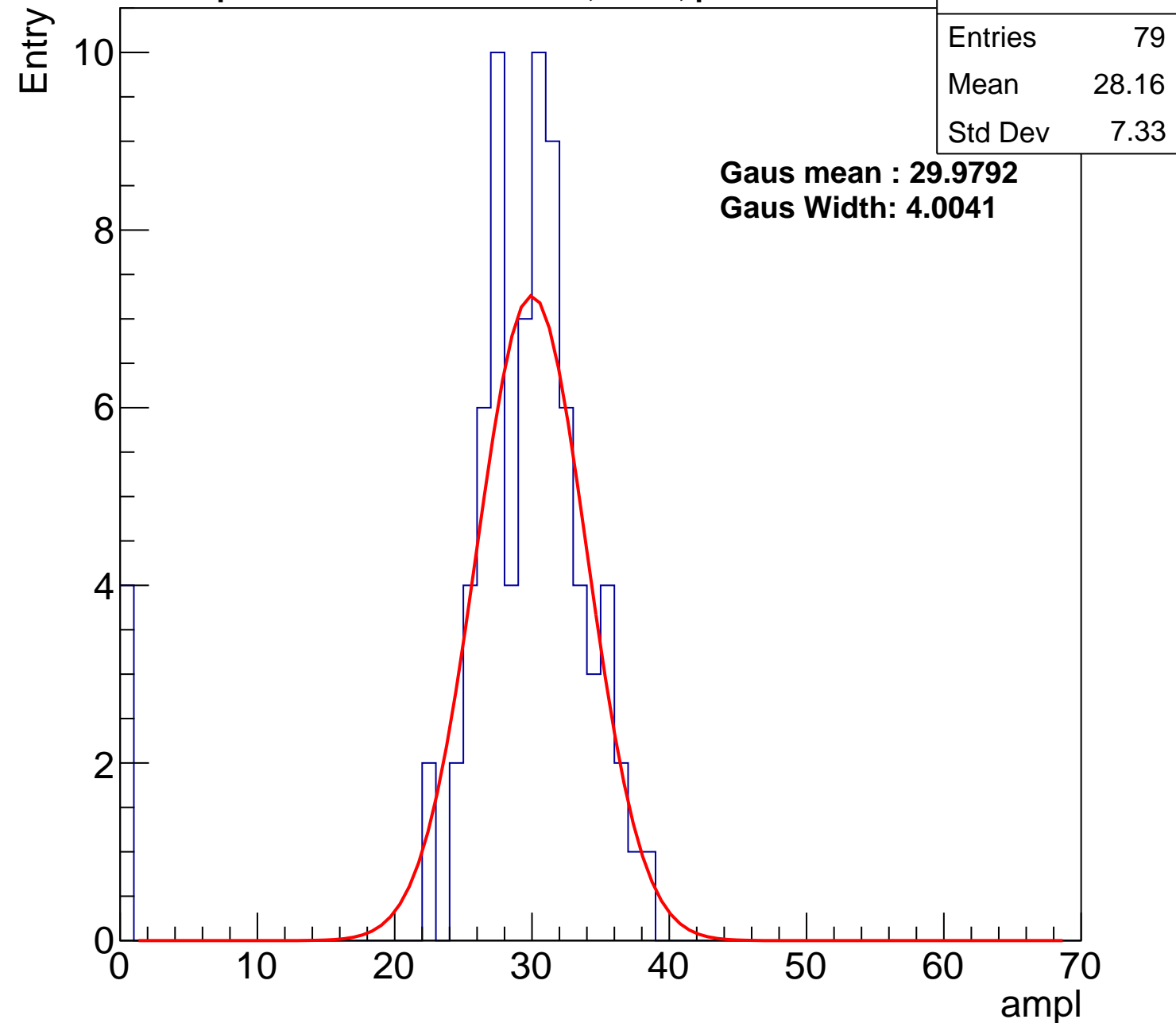
Gaus Width: 4.0041

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch109, adc1

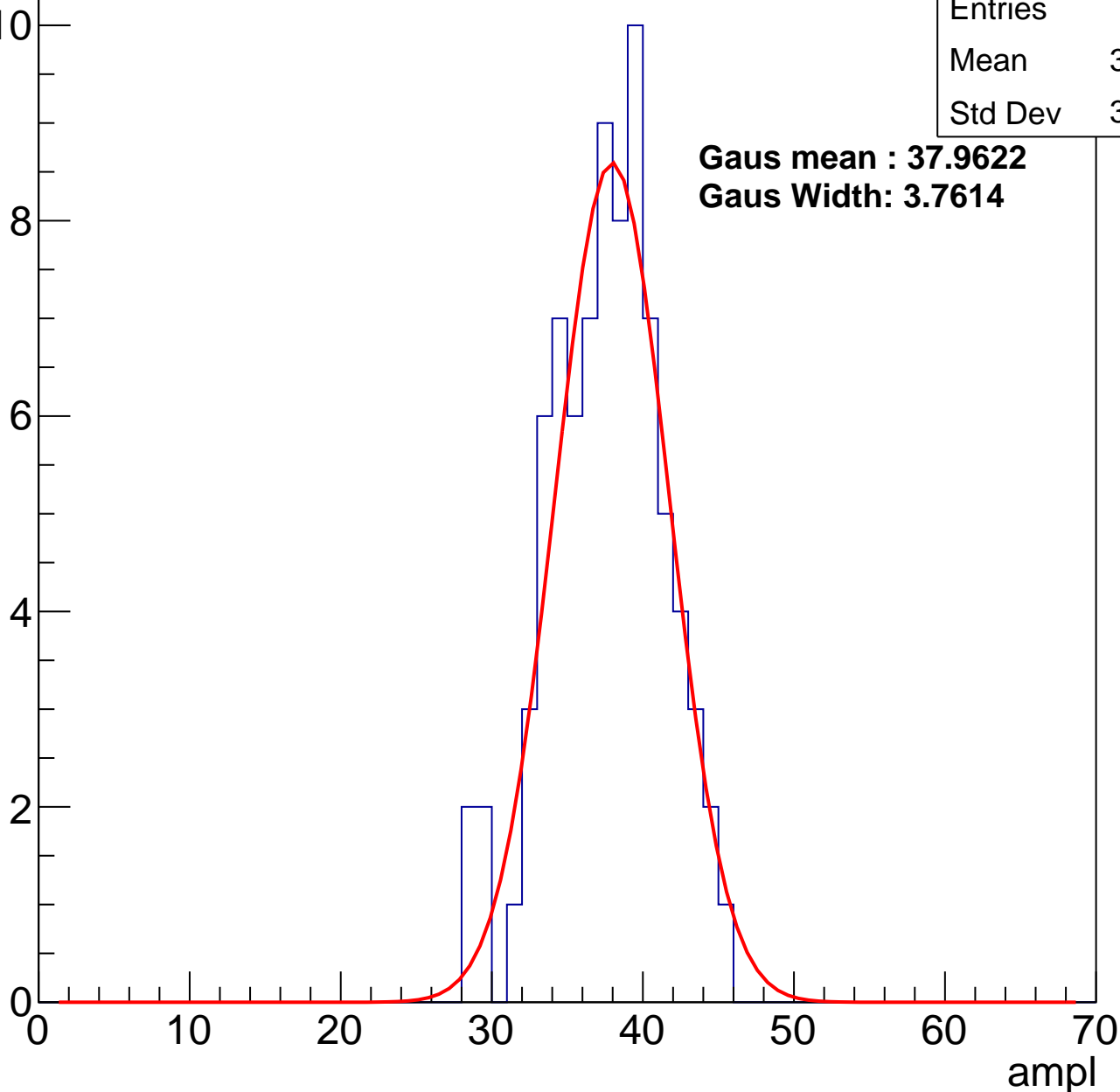
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	37.12
Std Dev	3.724

Gaus mean : 37.9622

Gaus Width: 3.7614



B0L001S, U25-ch109, adc2

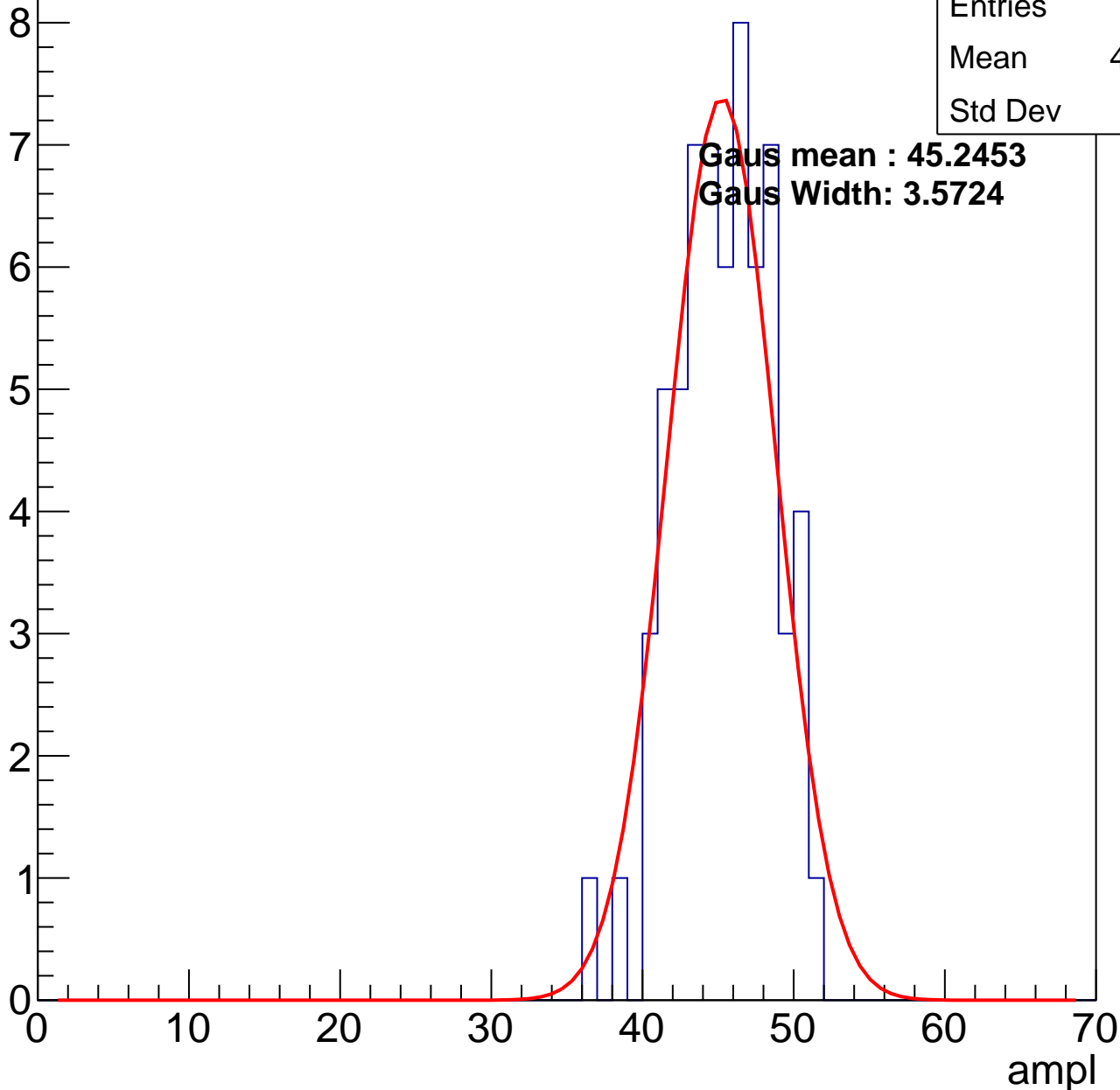
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.88
Std Dev	3.17

Gaus mean : 45.2453

Gaus Width: 3.5724

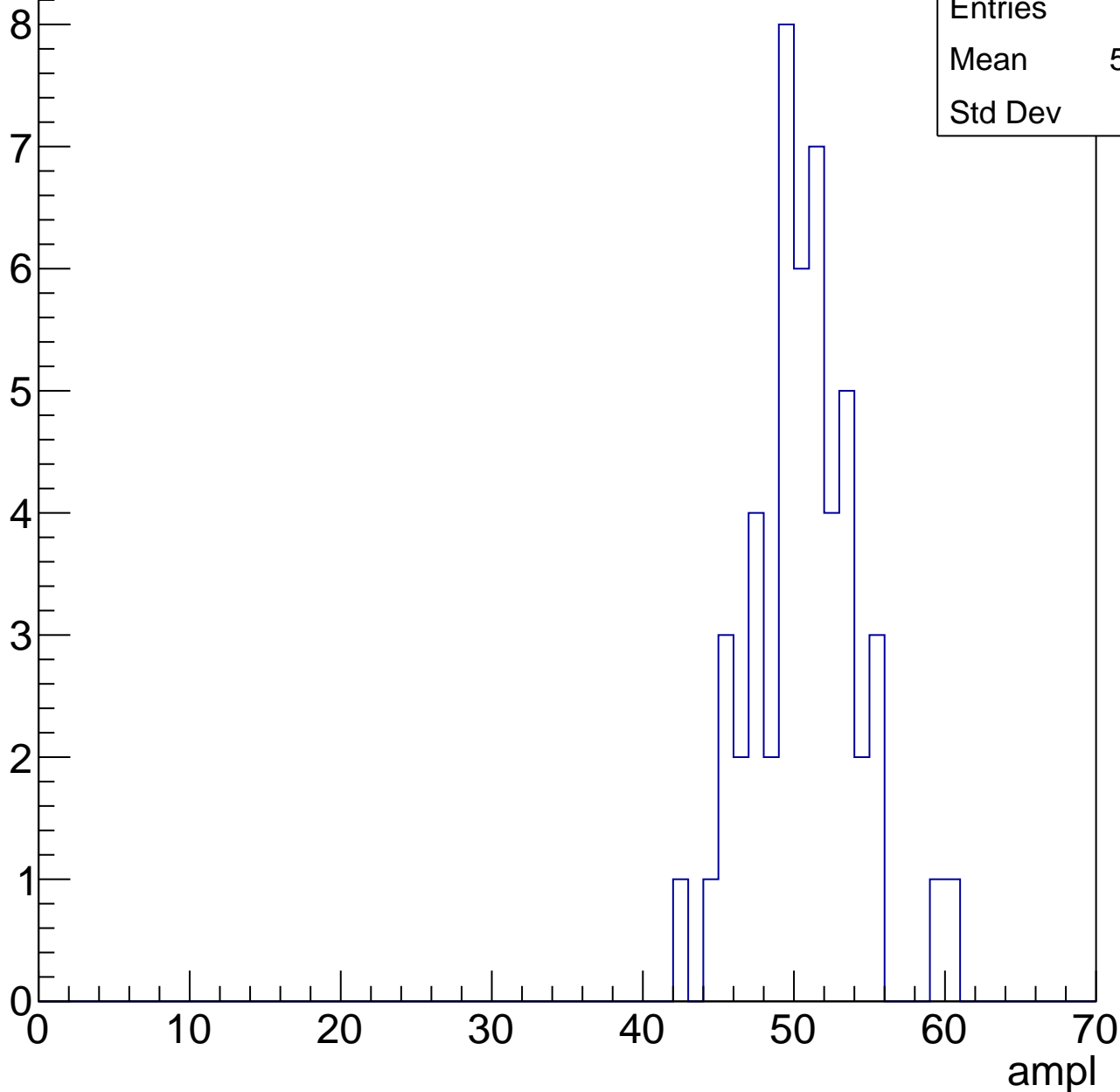


B0L001S, U25-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	50.22
Std Dev	3.5

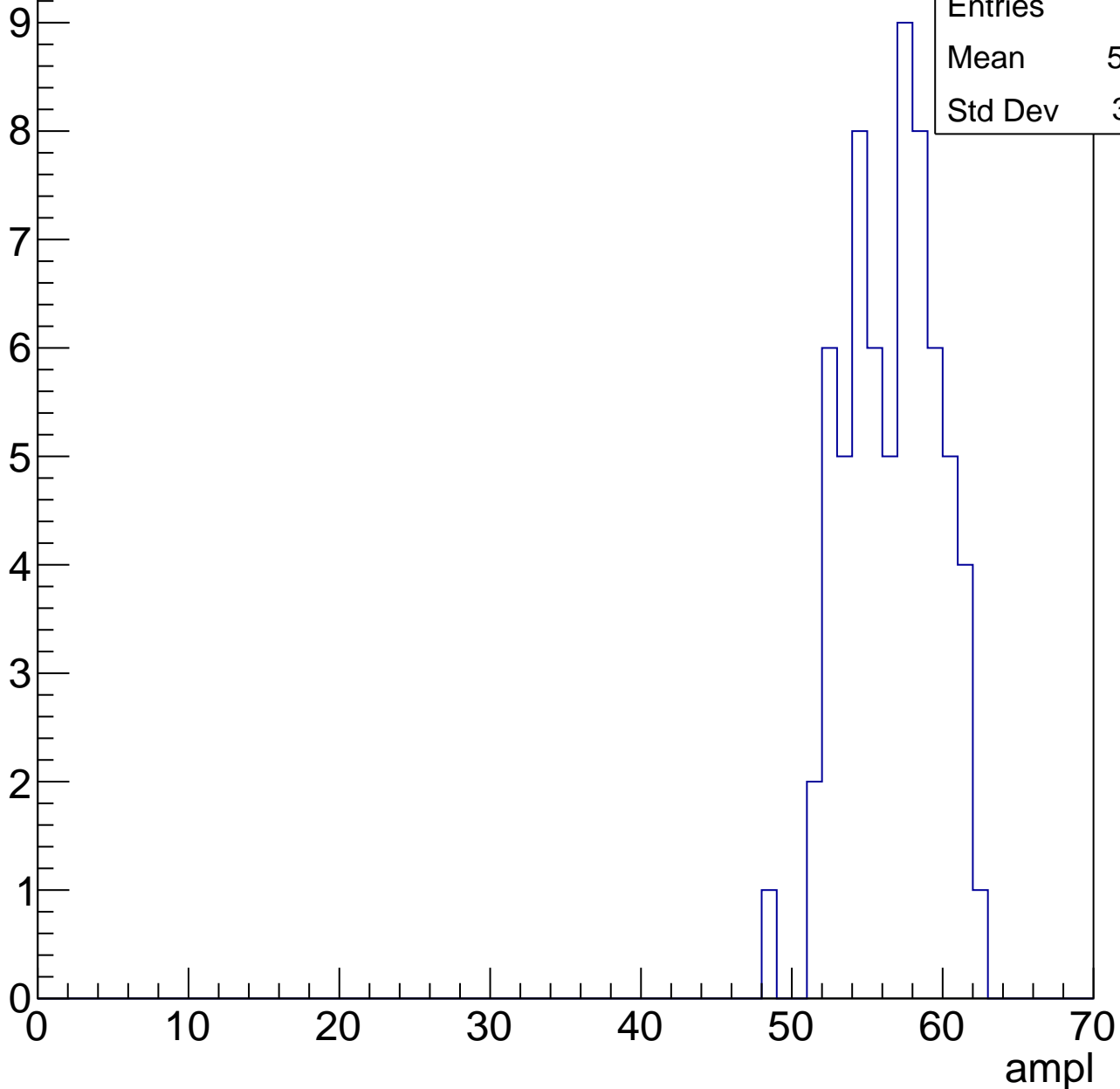


B0L001S, U25-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.15
Std Dev	3.021

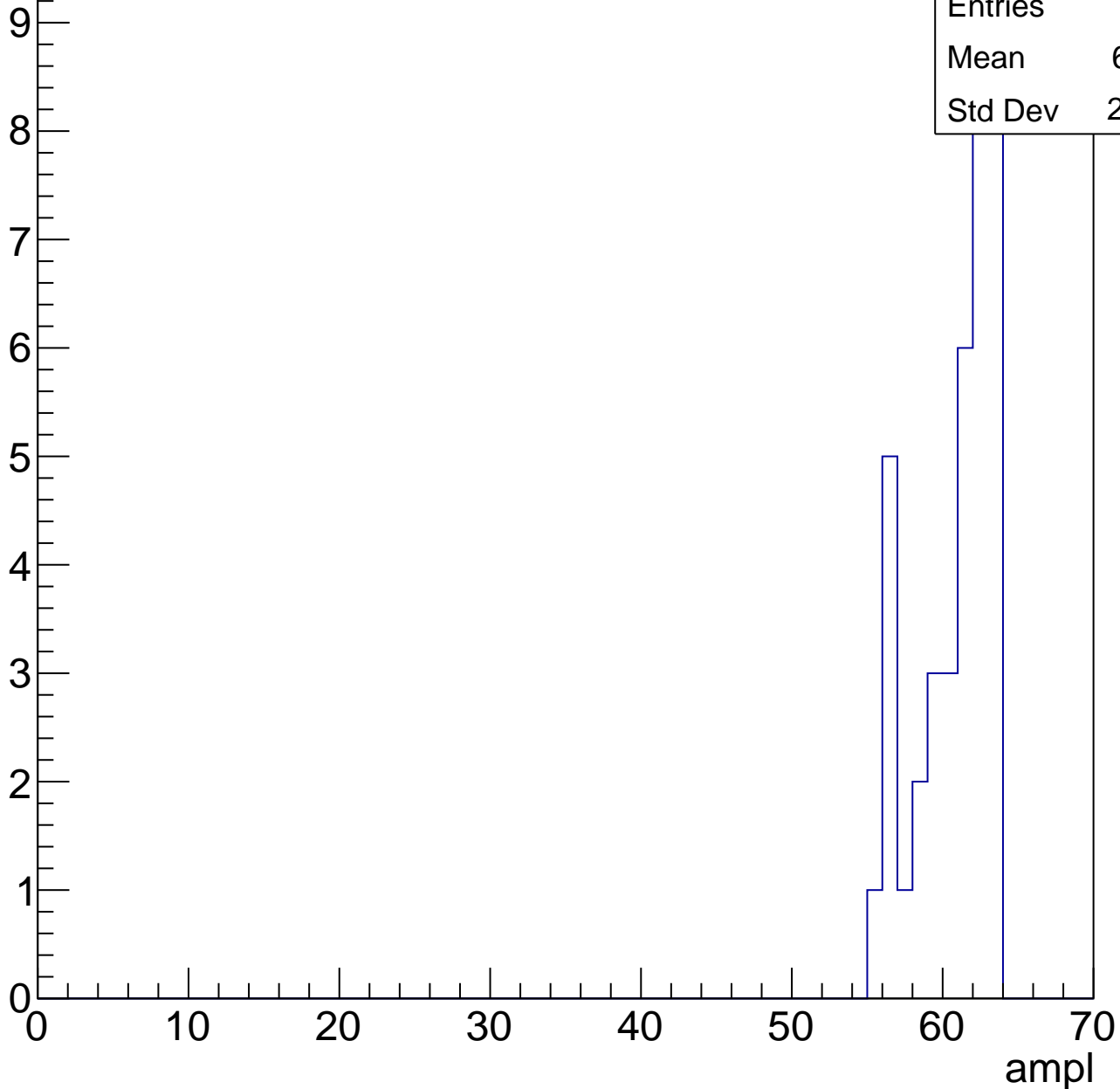


B0L001S, U25-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

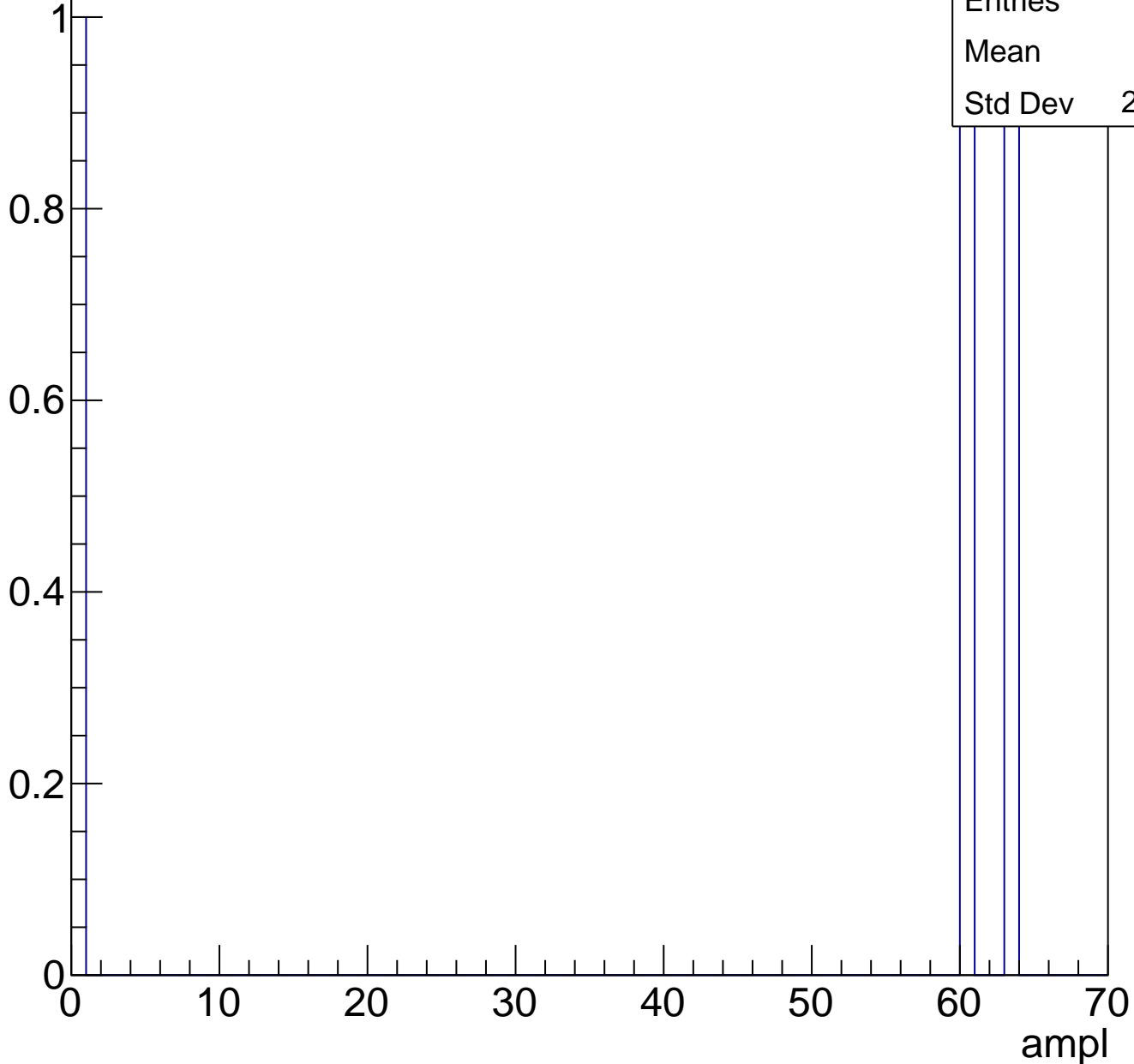
Entries	39
Mean	60.41
Std Dev	2.488



B0L001S, U25-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch110, adc0

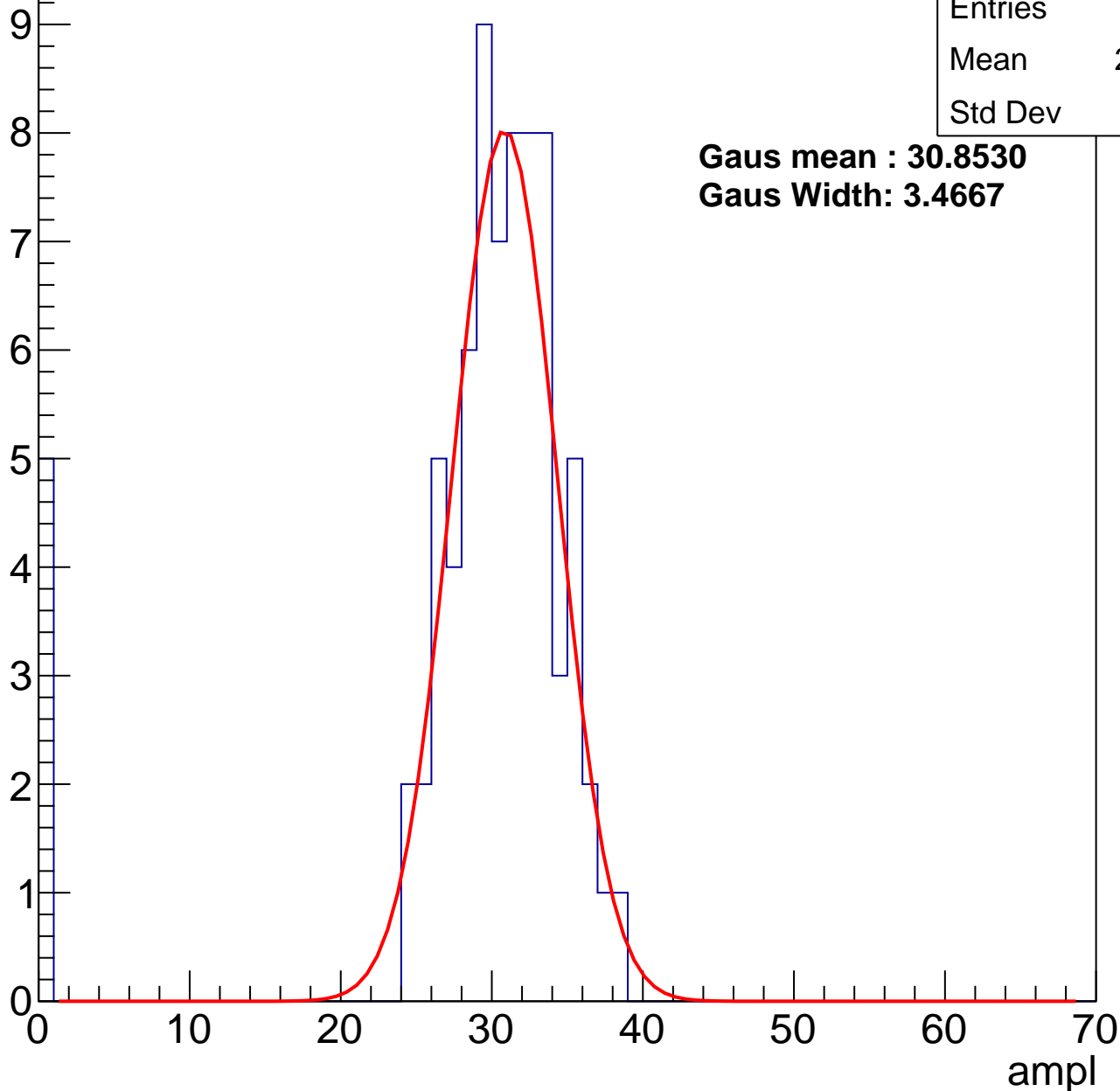
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	28.51
Std Dev	8.17

Gaus mean : 30.8530

Gaus Width: 3.4667



B0L001S, U25-ch110, adc1

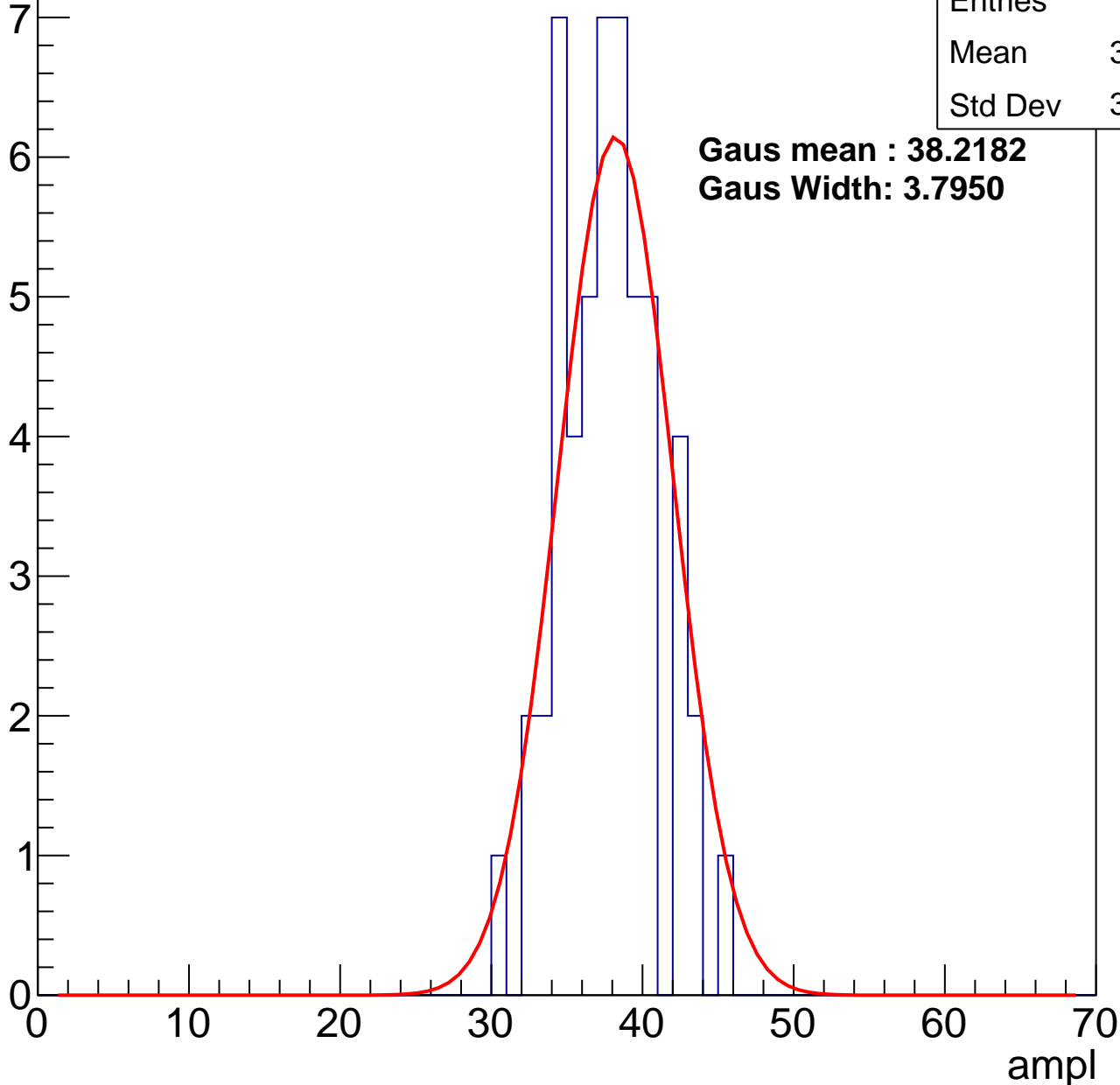
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	37.25
Std Dev	3.162

Gaus mean : 38.2182

Gaus Width: 3.7950



B0L001S, U25-ch110, adc2

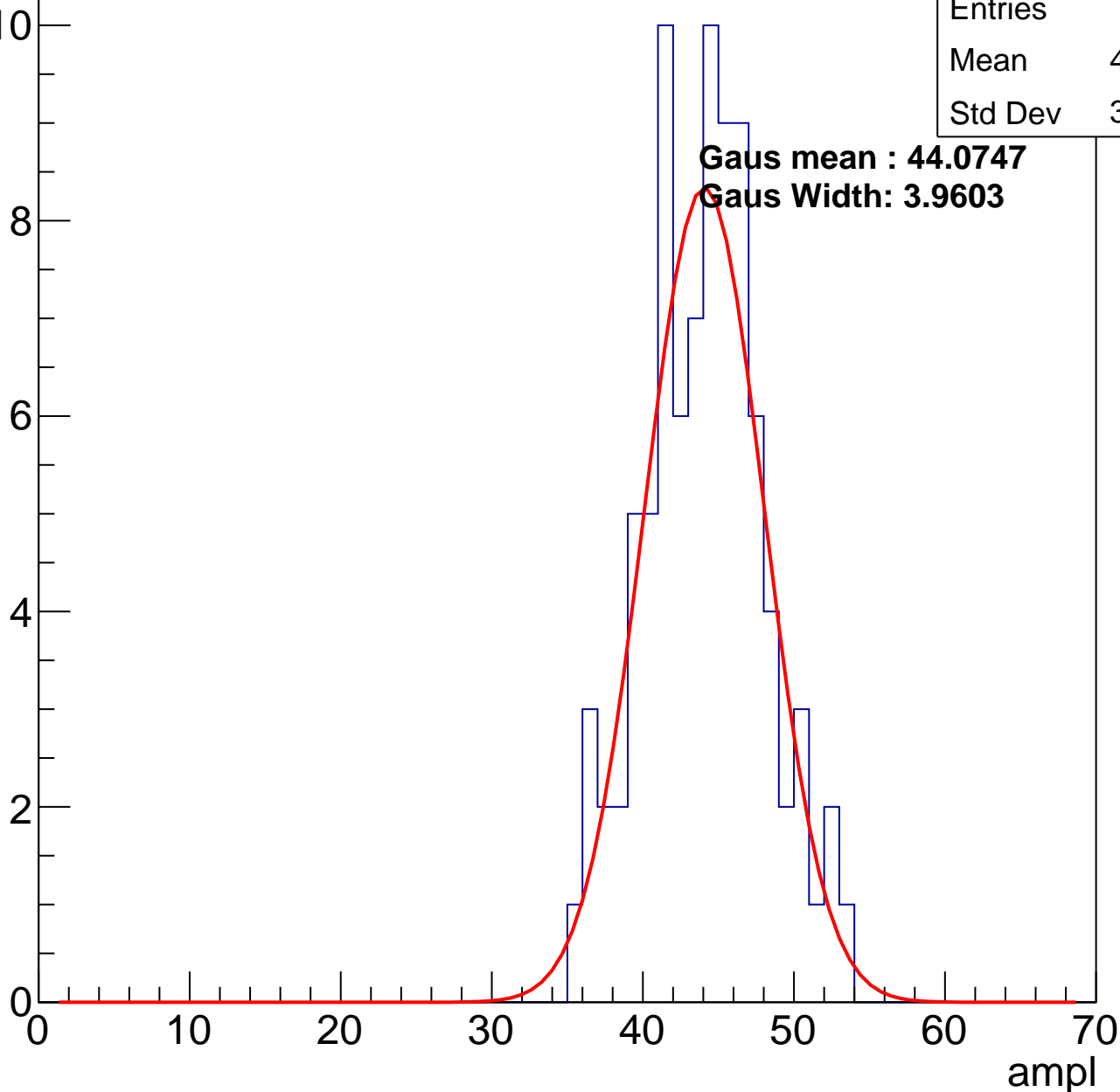
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	43.64
Std Dev	3.897

Gaus mean : 44.0747

Gaus Width: 3.9603

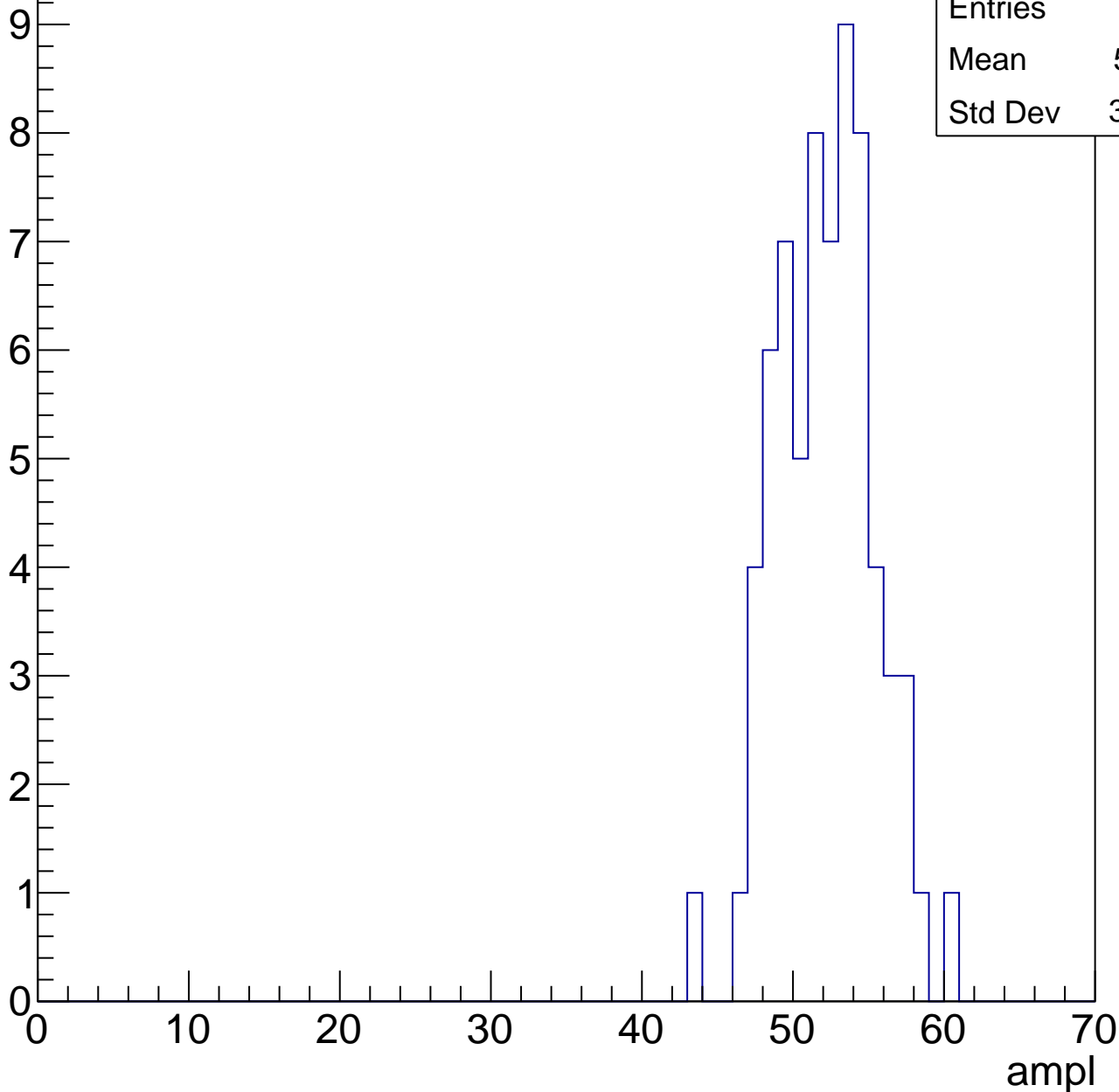


B0L001S, U25-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.71
Std Dev	3.204

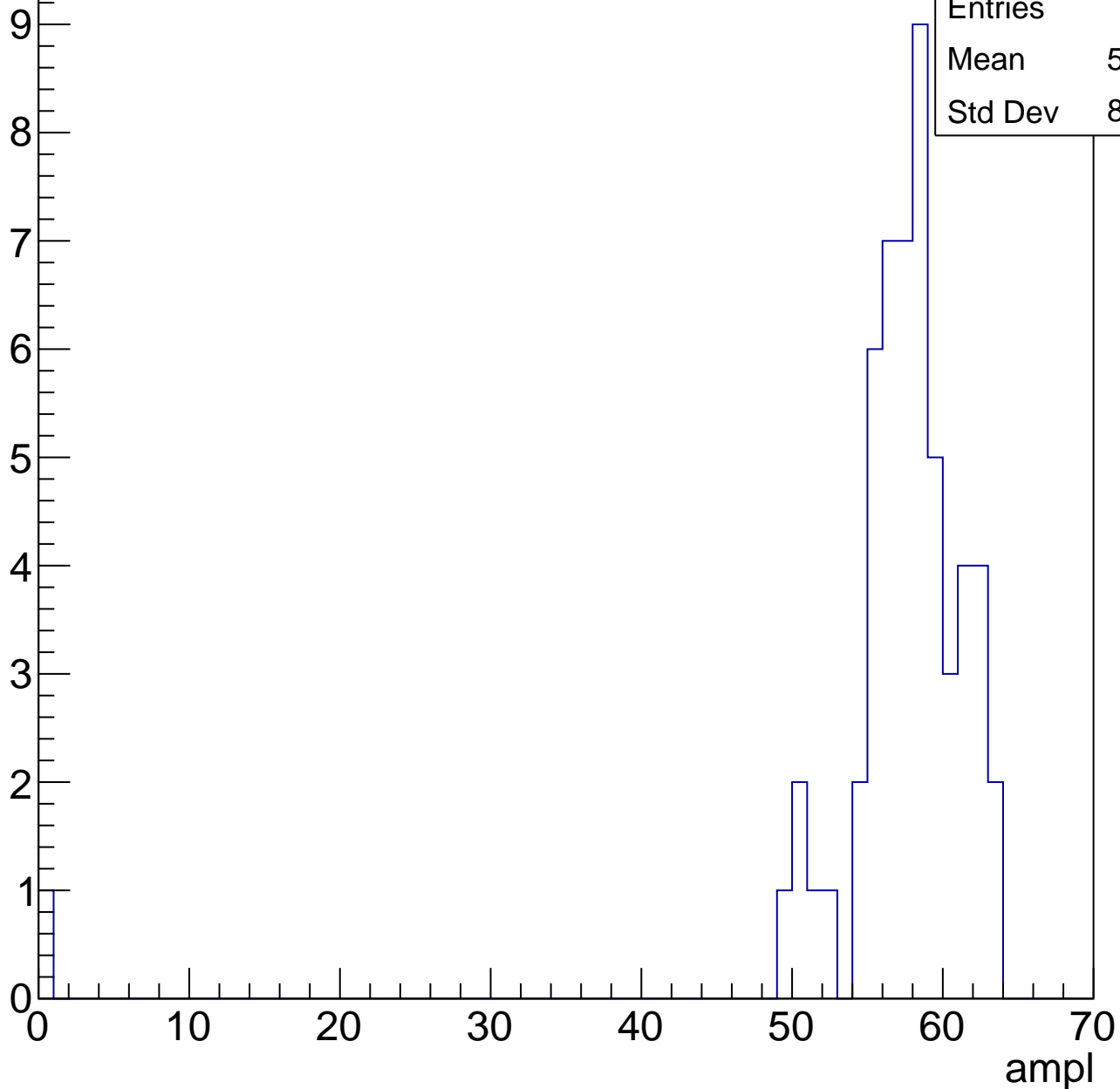


B0L001S, U25-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.29
Std Dev	8.296

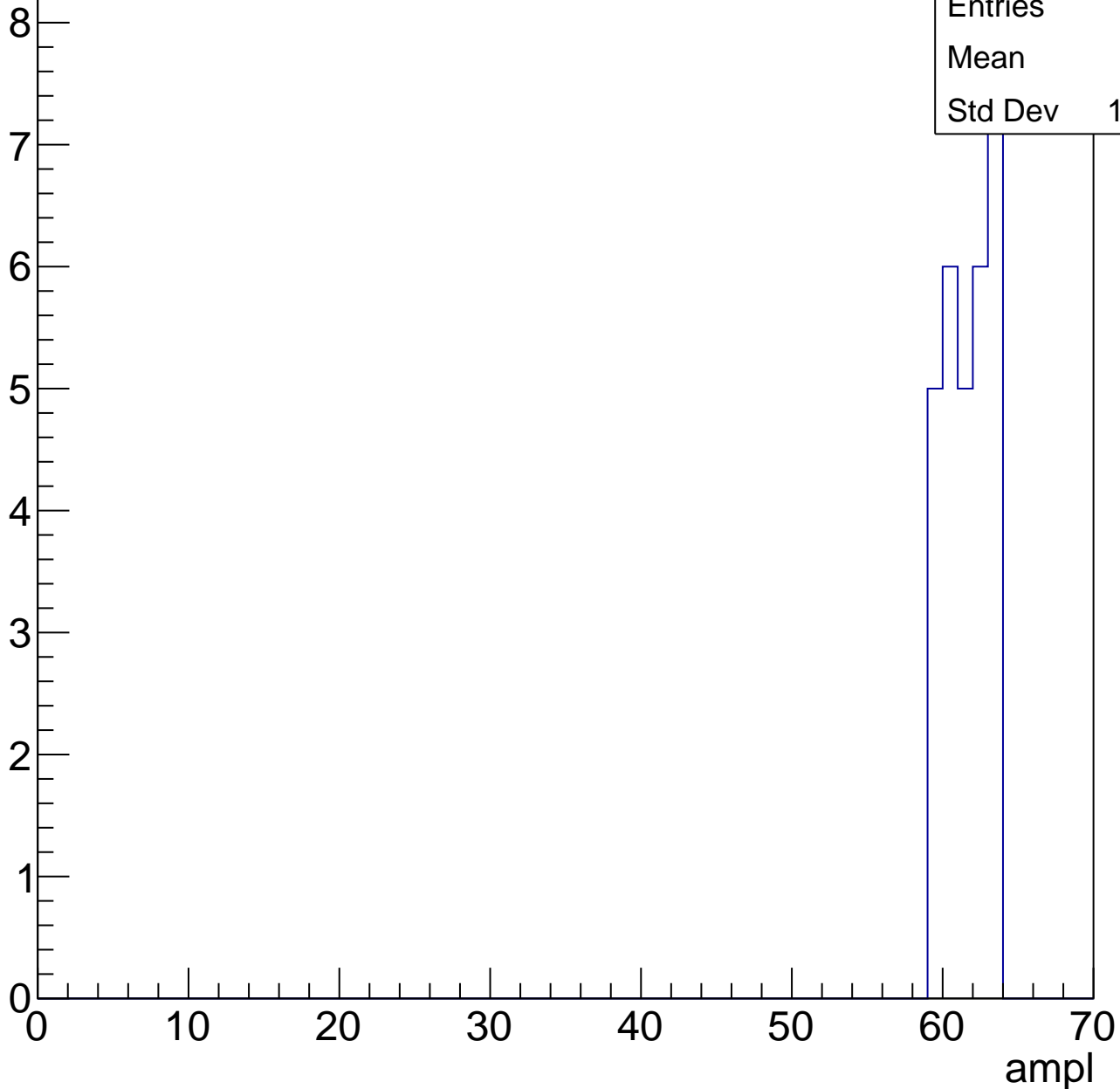


B0L001S, U25-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	61.2
Std Dev	1.447



B0L001S, U25-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch111, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	29.24
Std Dev	5.846

Gaus mean : 30.2674

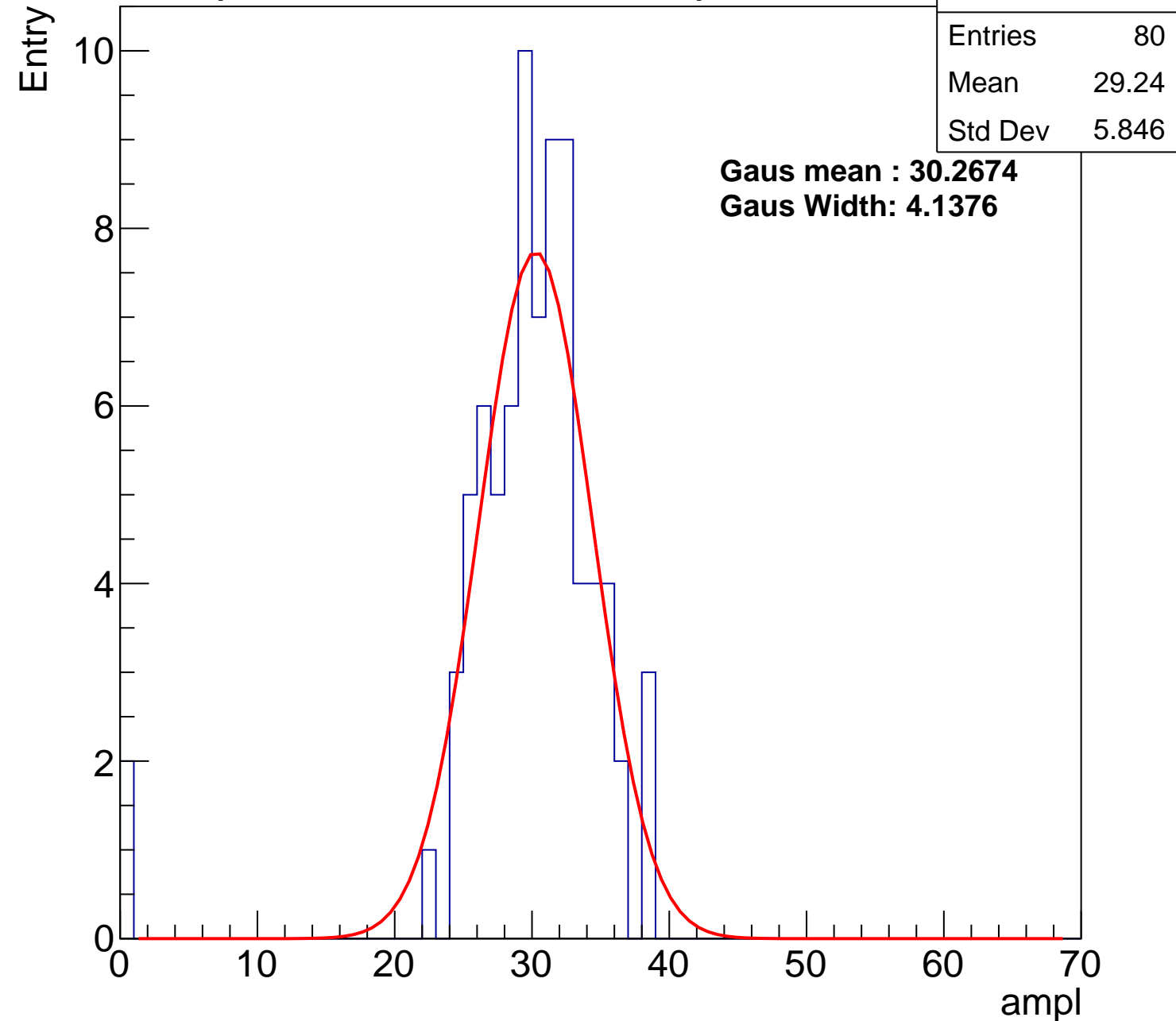
Gaus Width: 4.1376

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch111, adc1

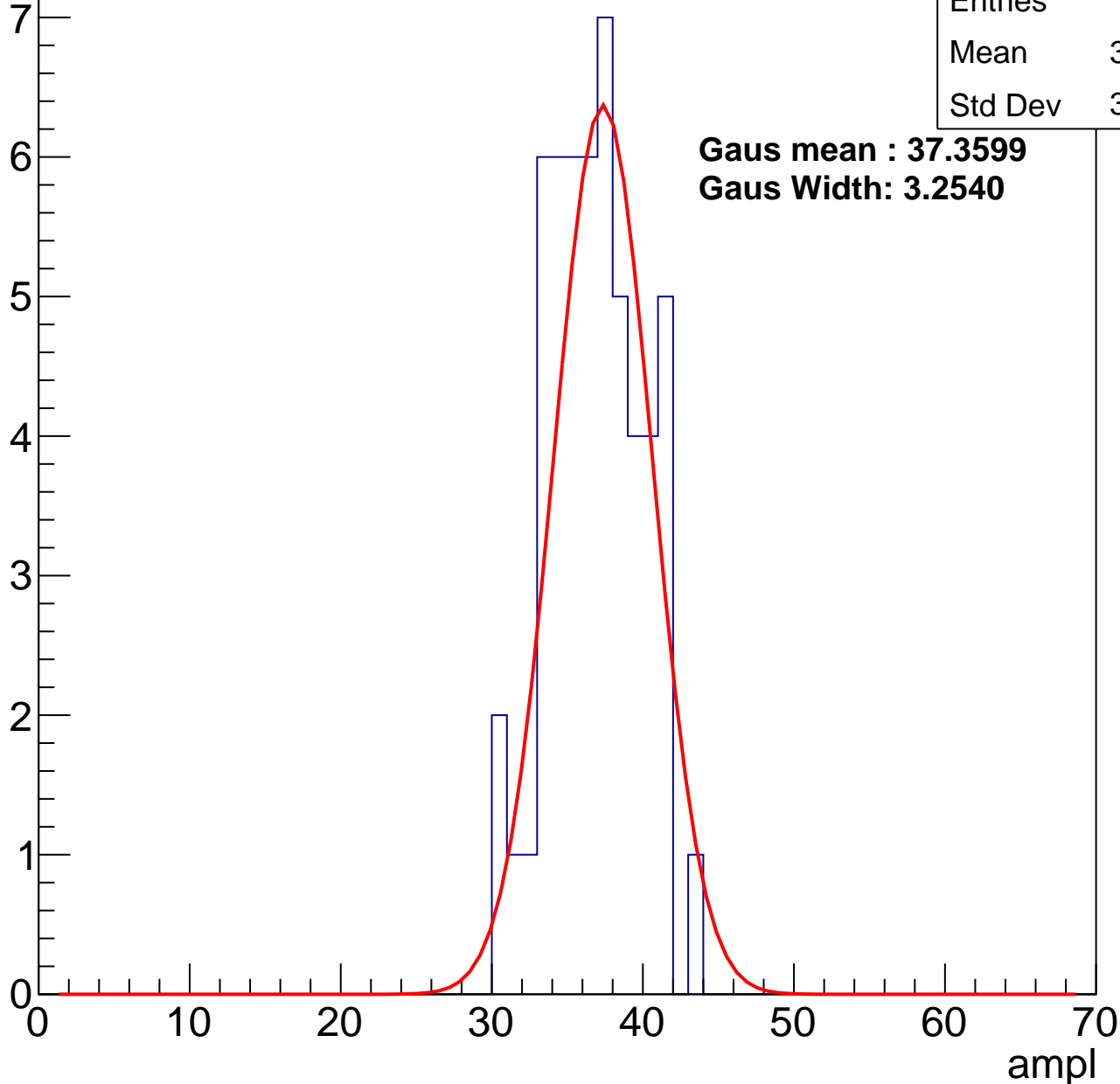
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	36.37
Std Dev	3.014

Gaus mean : 37.3599

Gaus Width: 3.2540



B0L001S, U25-ch111, adc2

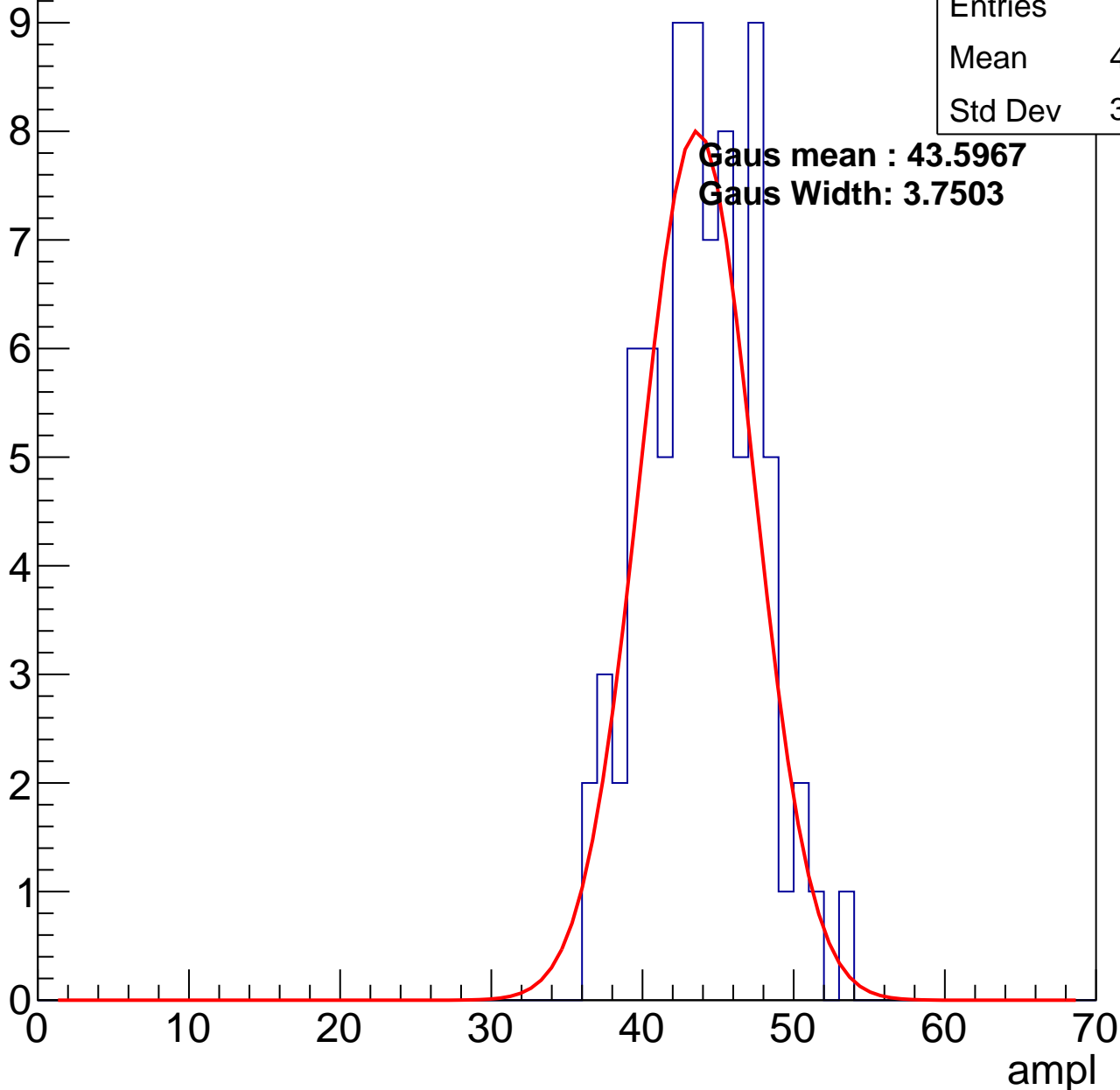
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	43.42
Std Dev	3.637

Gaus mean : 43.5967

Gaus Width: 3.7503

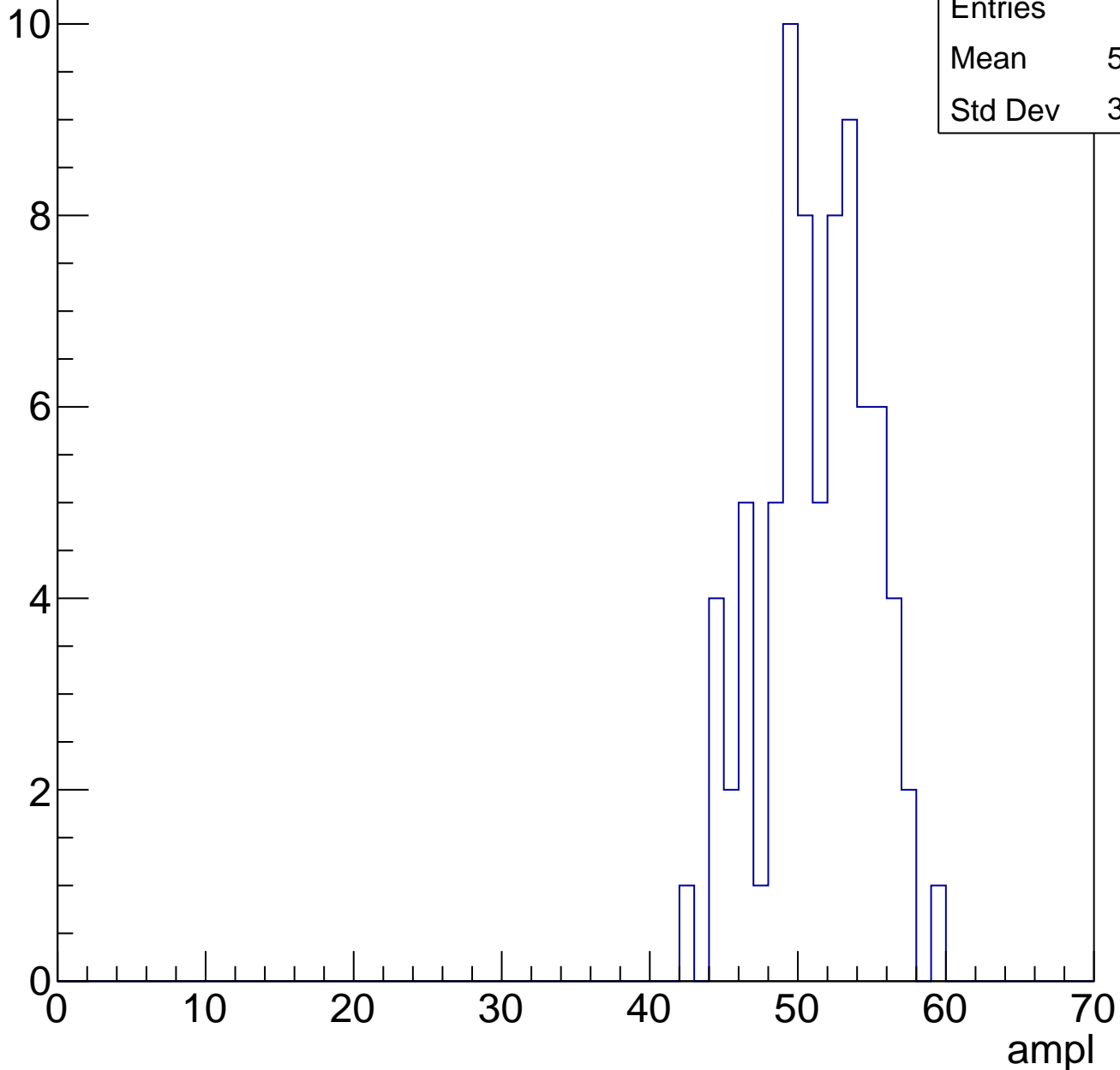


B0L001S, U25-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	50.83
Std Dev	3.623

Entry

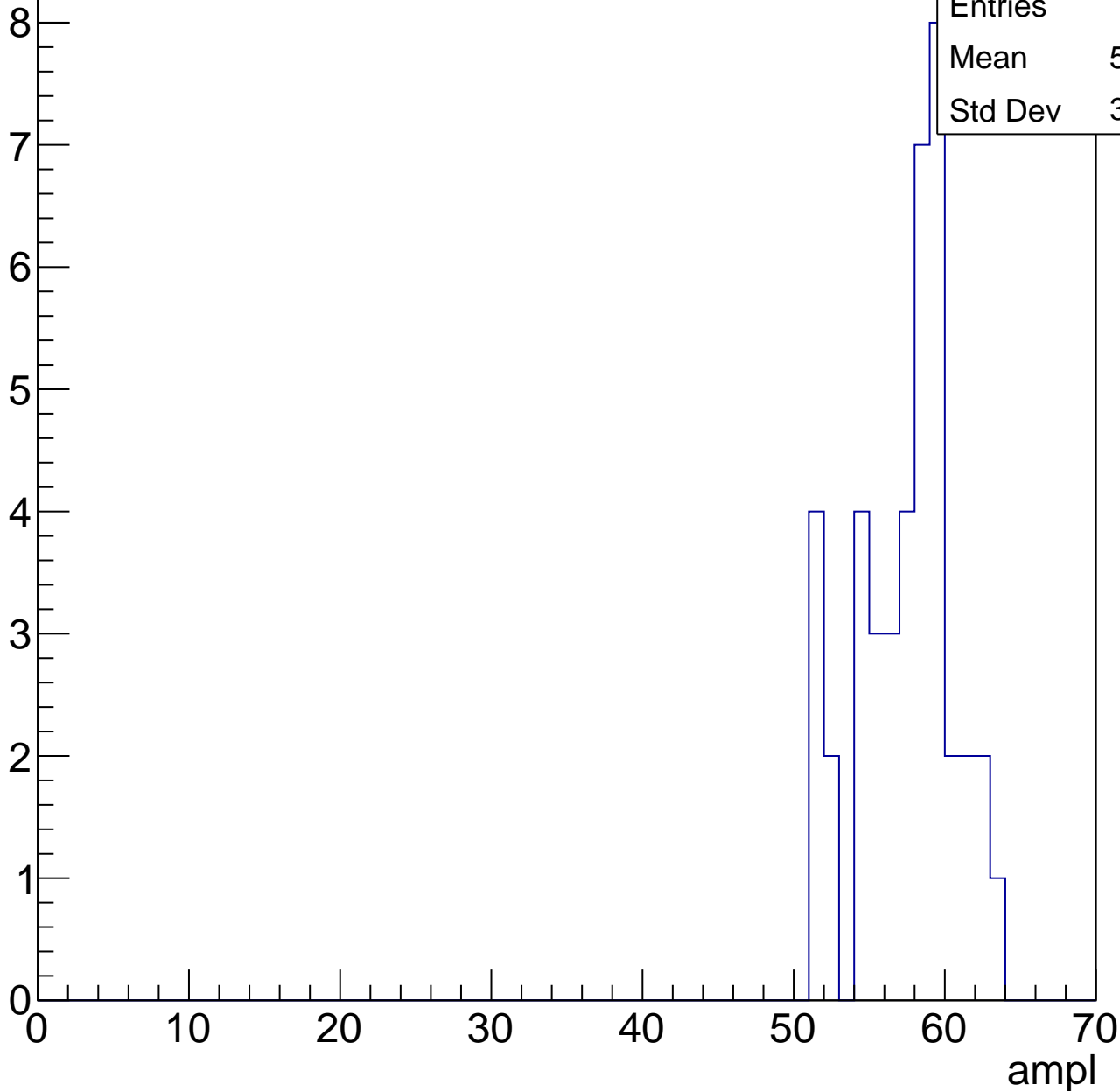


B0L001S, U25-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	56.95
Std Dev	3.162

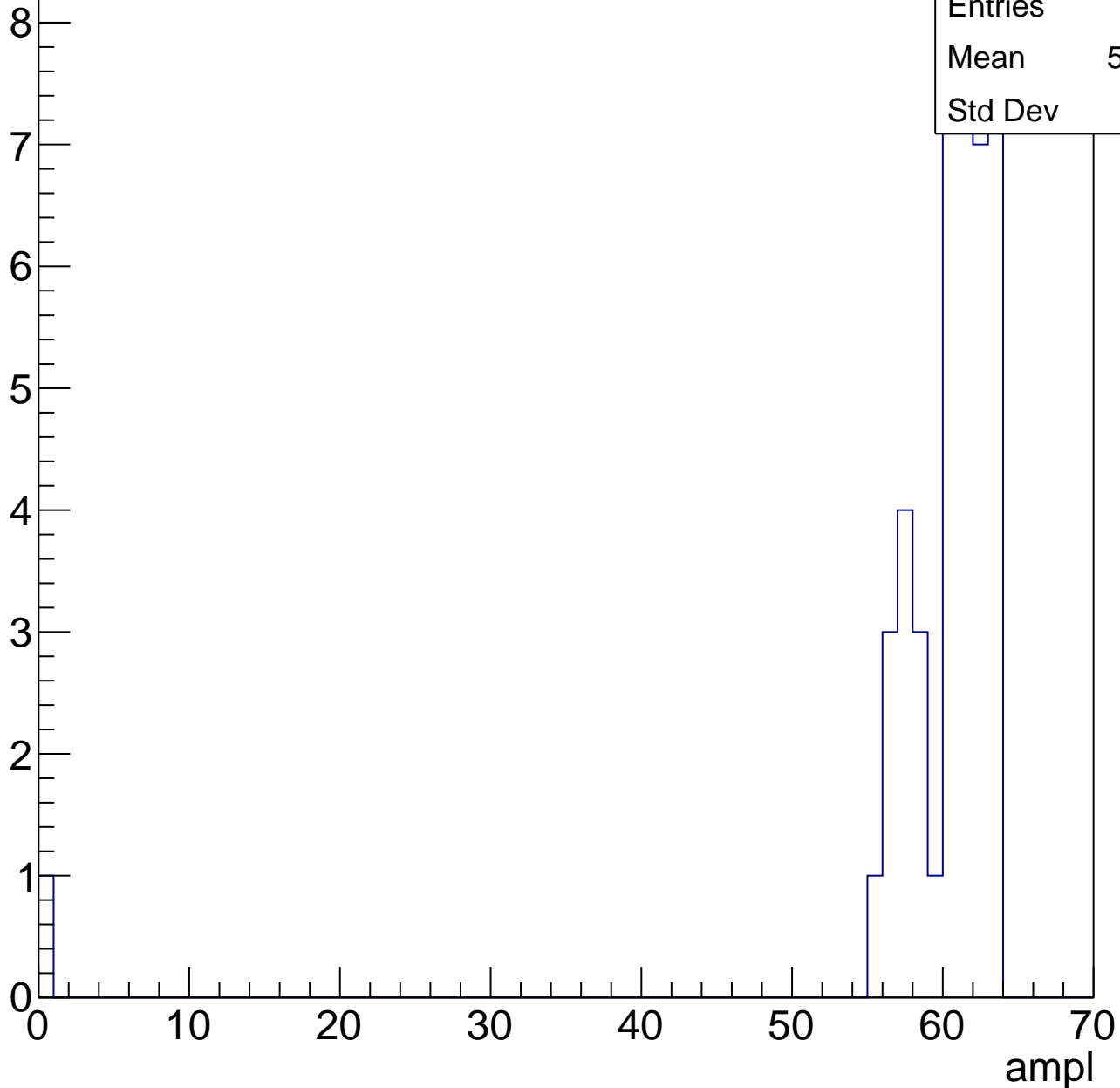


B0L001S, U25-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.86
Std Dev	9.26



B0L001S, U25-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch112, adc0

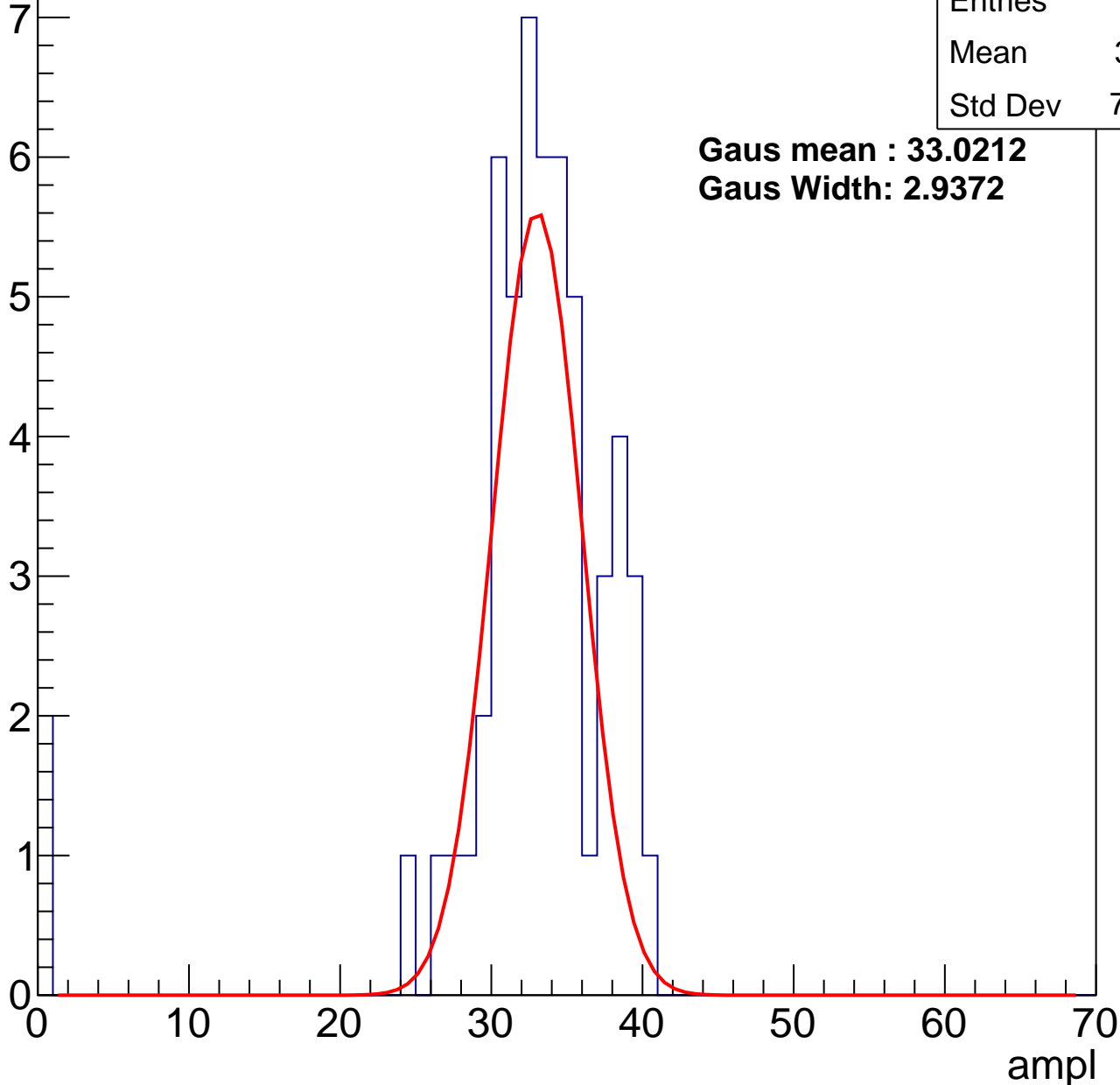
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	31.91
Std Dev	7.079

Gaus mean : 33.0212

Gaus Width: 2.9372



B0L001S, U25-ch112, adc1

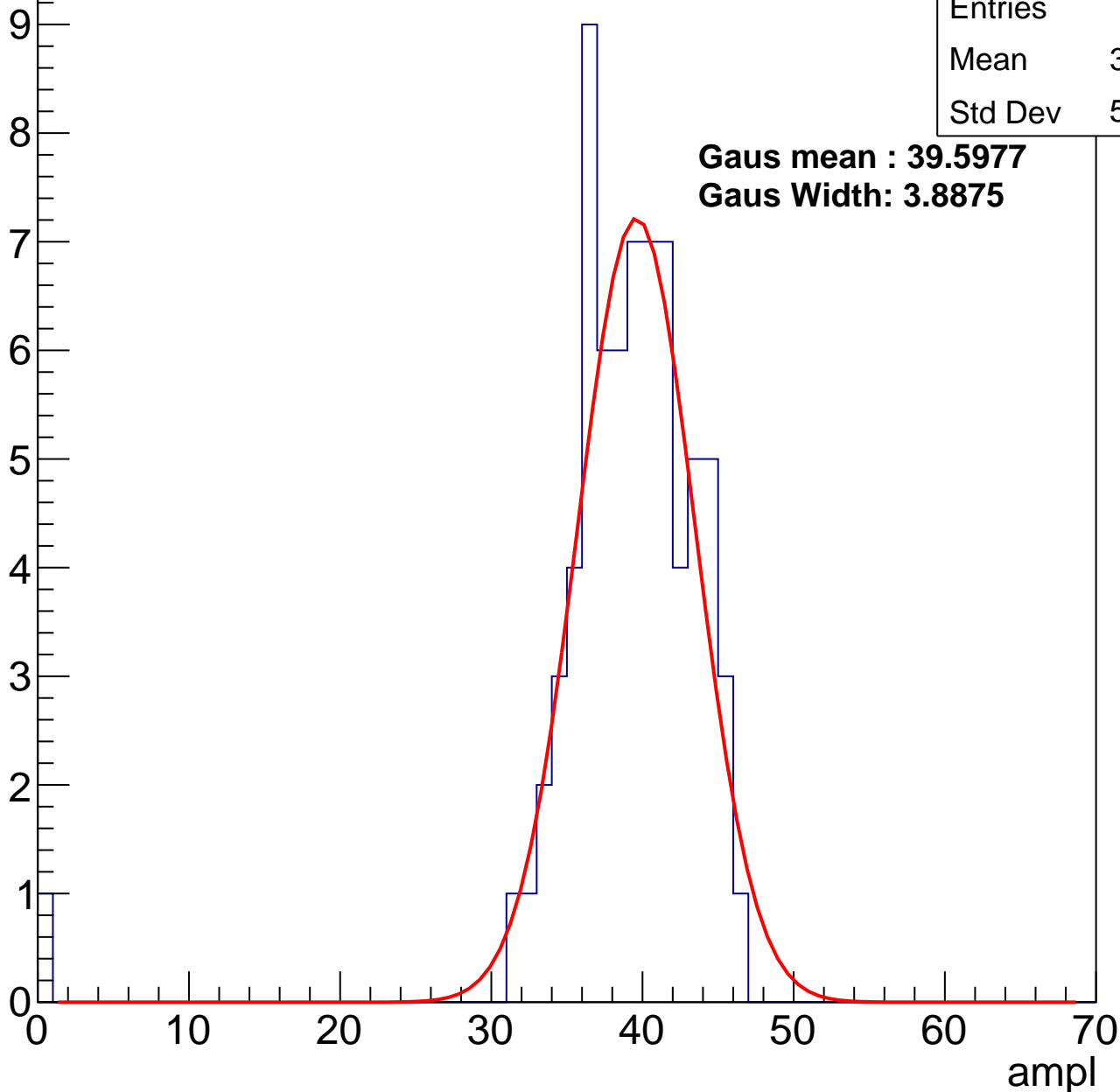
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.46
Std Dev	5.735

Gaus mean : 39.5977

Gaus Width: 3.8875



B0L001S, U25-ch112, adc2

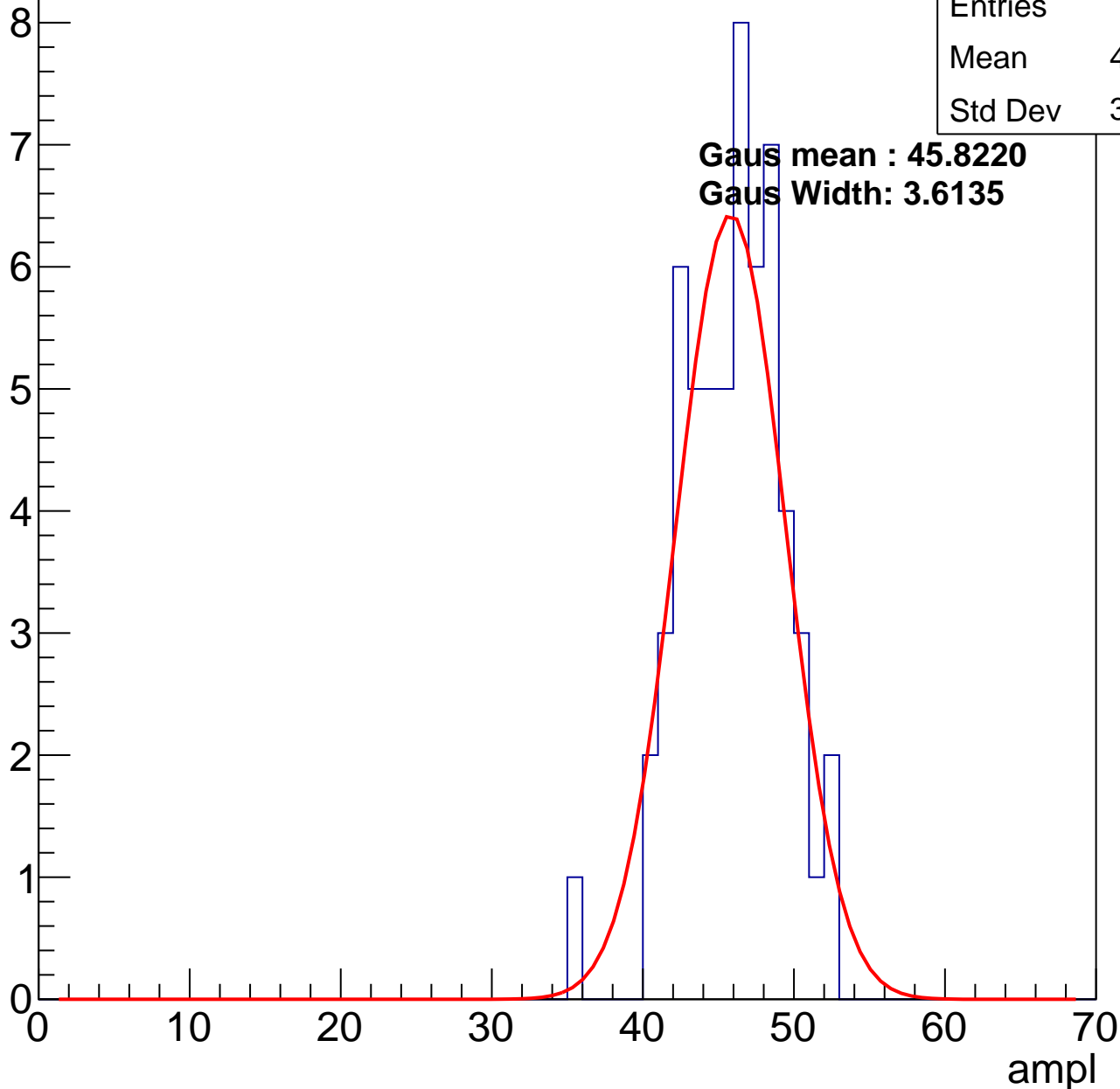
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.47
Std Dev	3.302

Gaus mean : 45.8220

Gaus Width: 3.6135

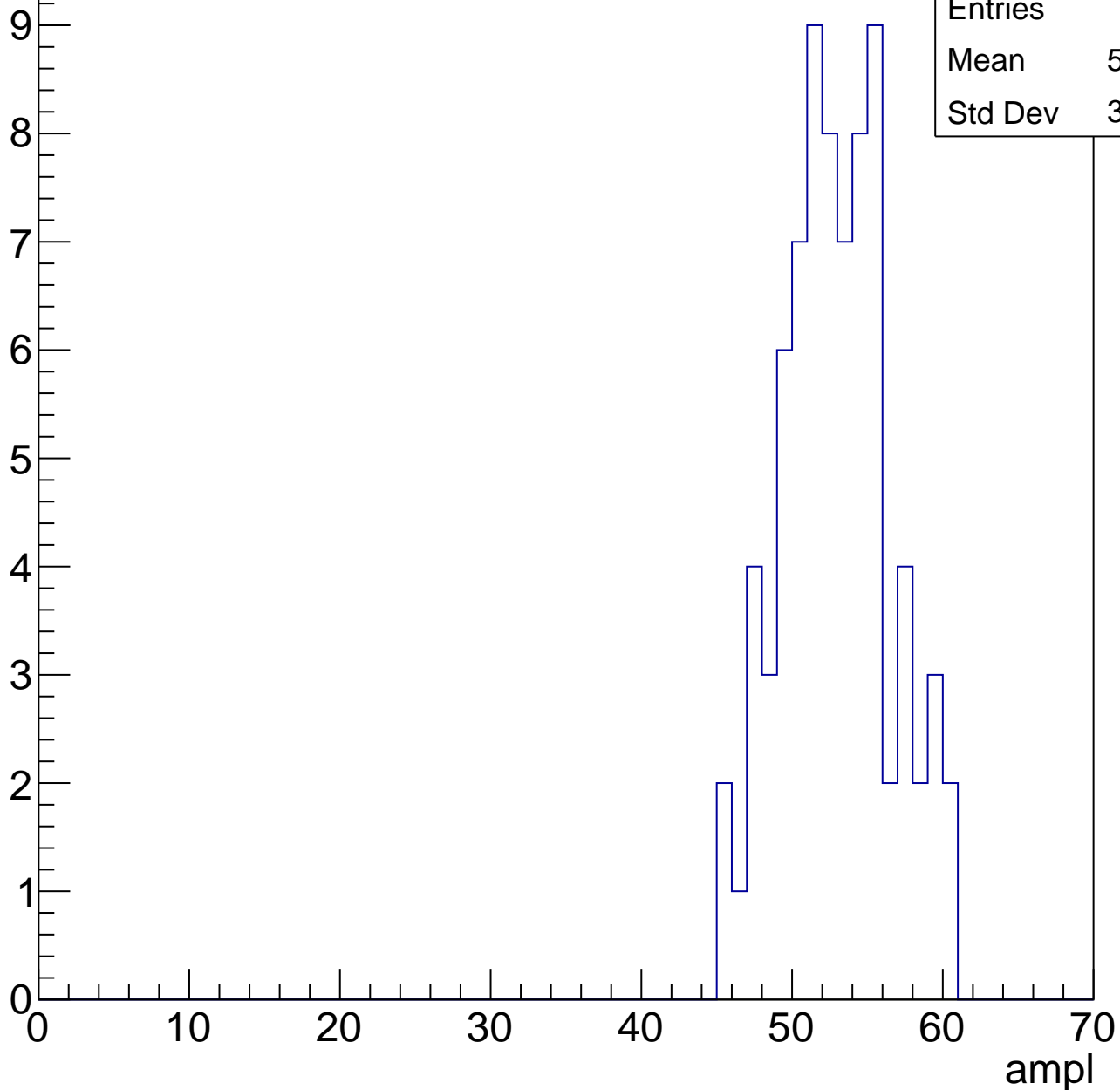


B0L001S, U25-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	52.44
Std Dev	3.525

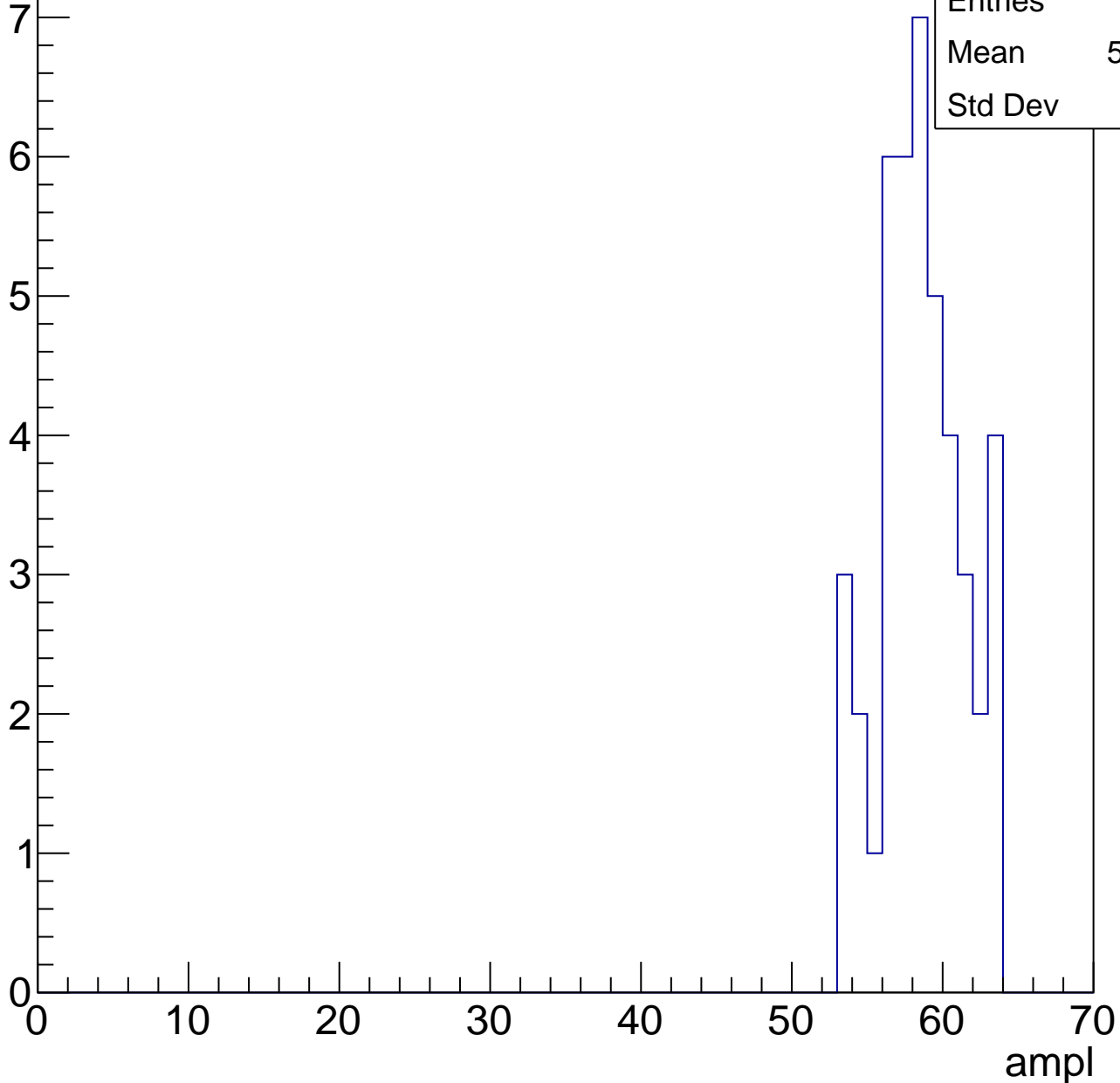


B0L001S, U25-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.14
Std Dev	2.75

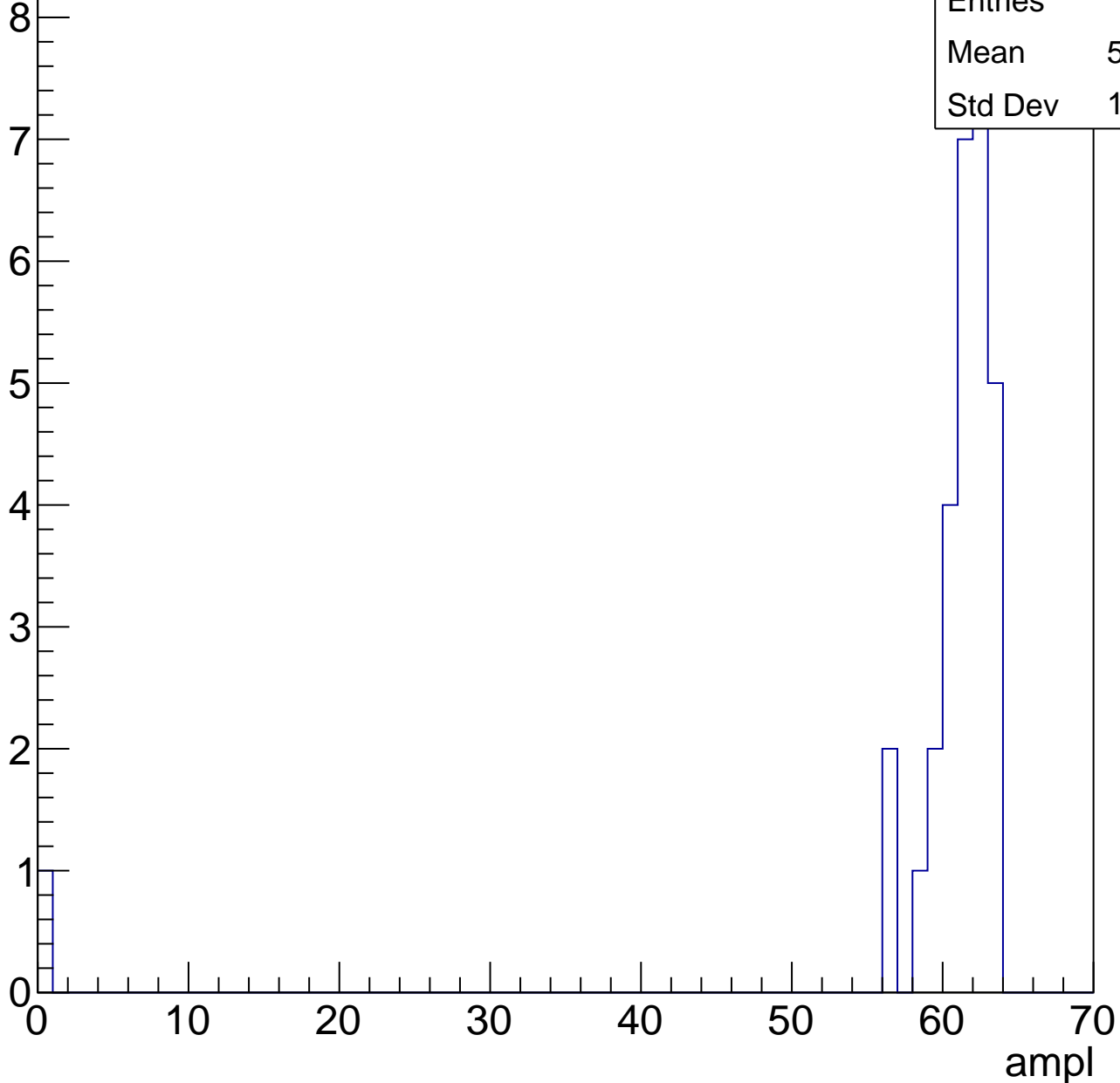


B0L001S, U25-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.87
Std Dev	11.08



B0L001S, U25-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch113, adc0

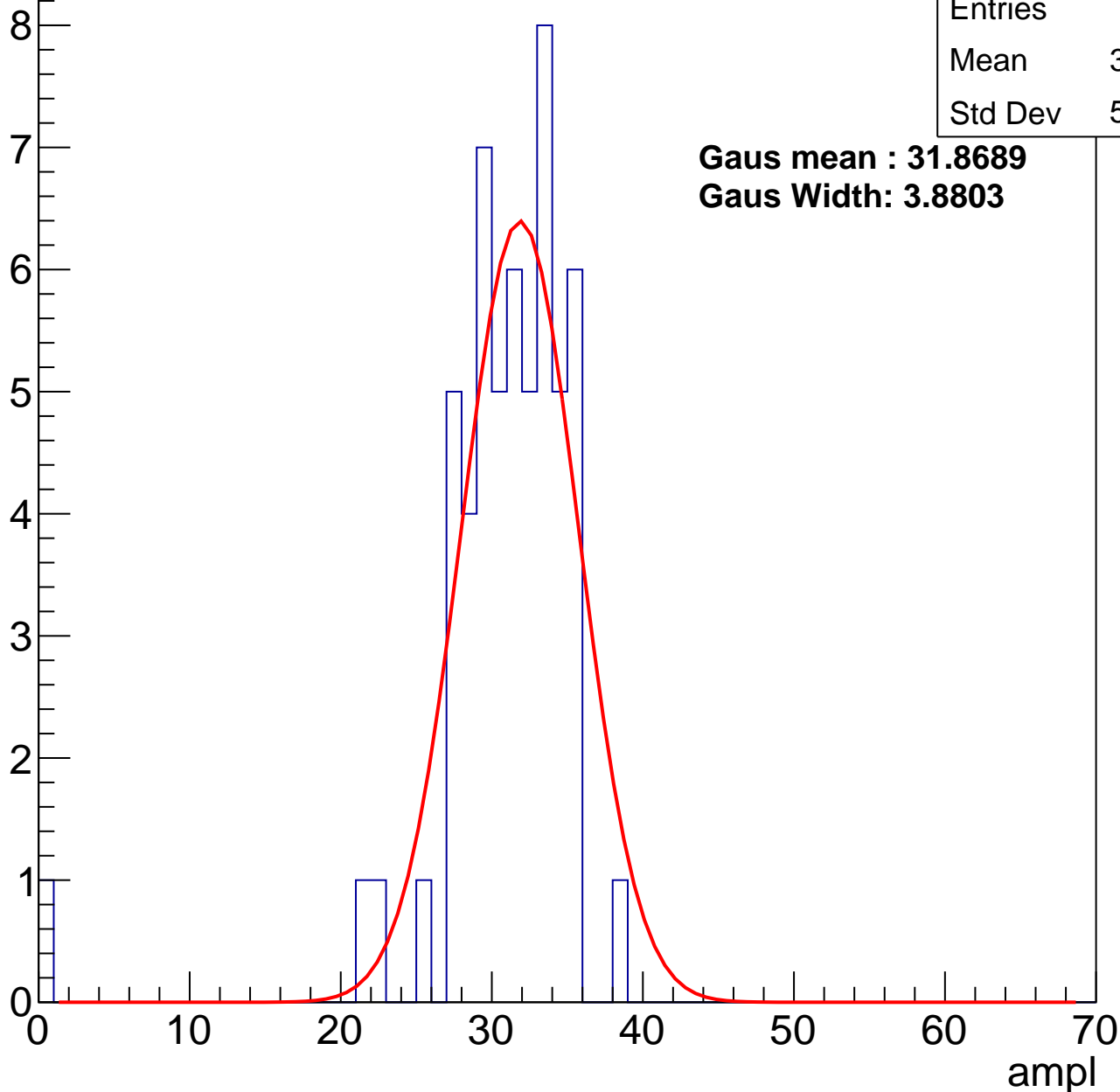
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.29
Std Dev	5.219

Gaus mean : 31.8689

Gaus Width: 3.8803



B0L001S, U25-ch113, adc1

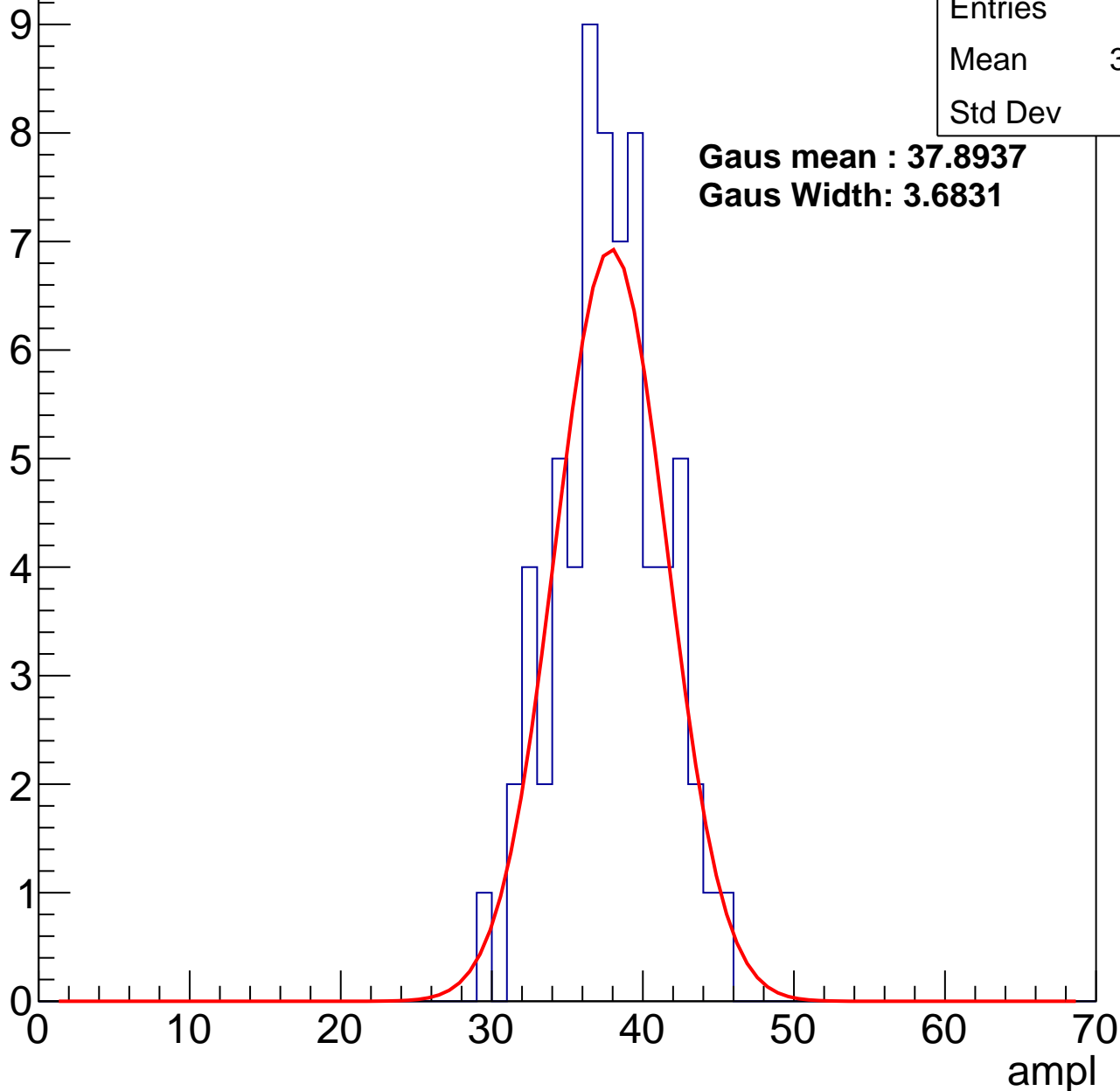
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.34
Std Dev	3.41

Gaus mean : 37.8937

Gaus Width: 3.6831



B0L001S, U25-ch113, adc2

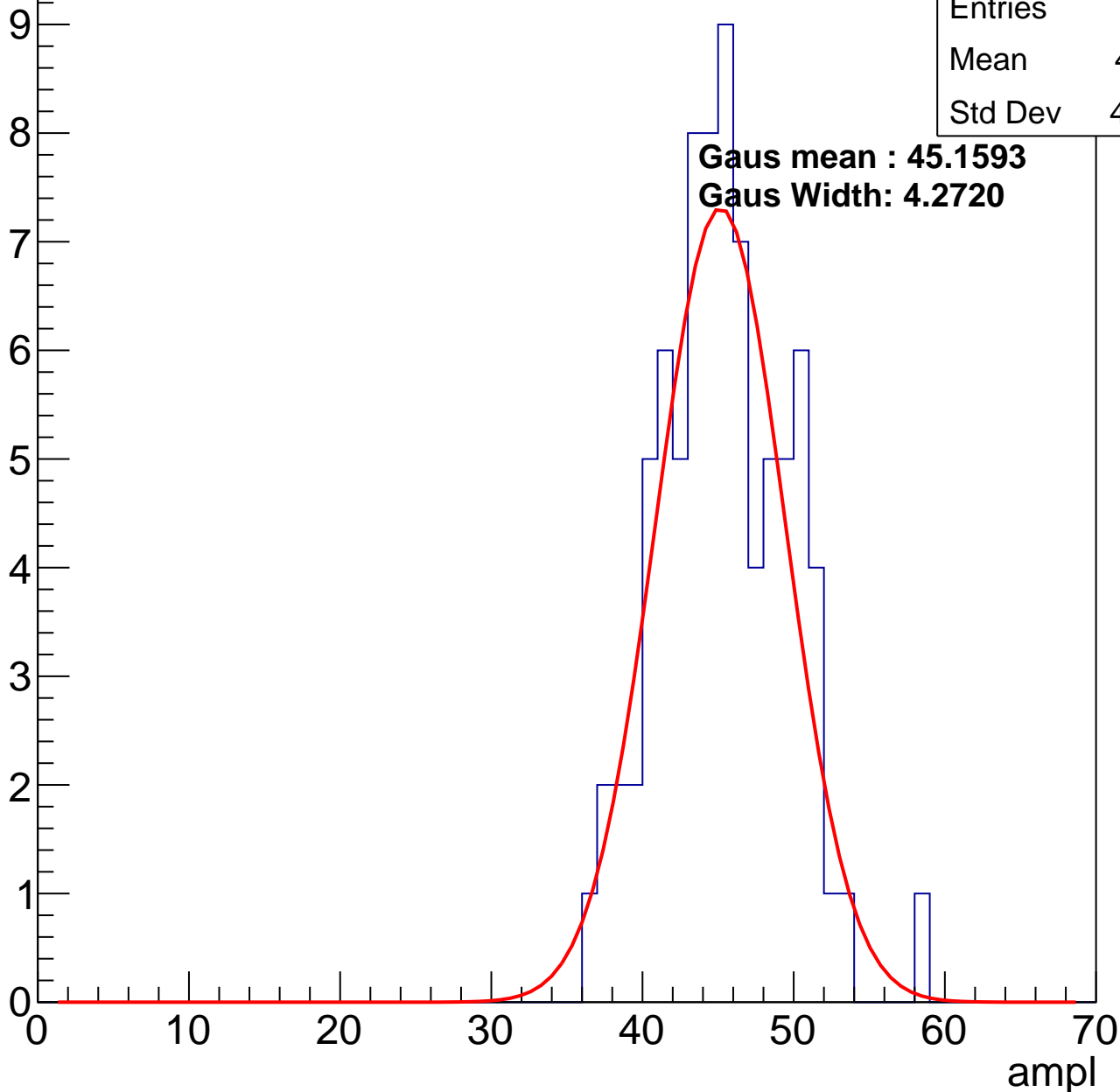
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	44.91
Std Dev	4.144

Gaus mean : 45.1593

Gaus Width: 4.2720

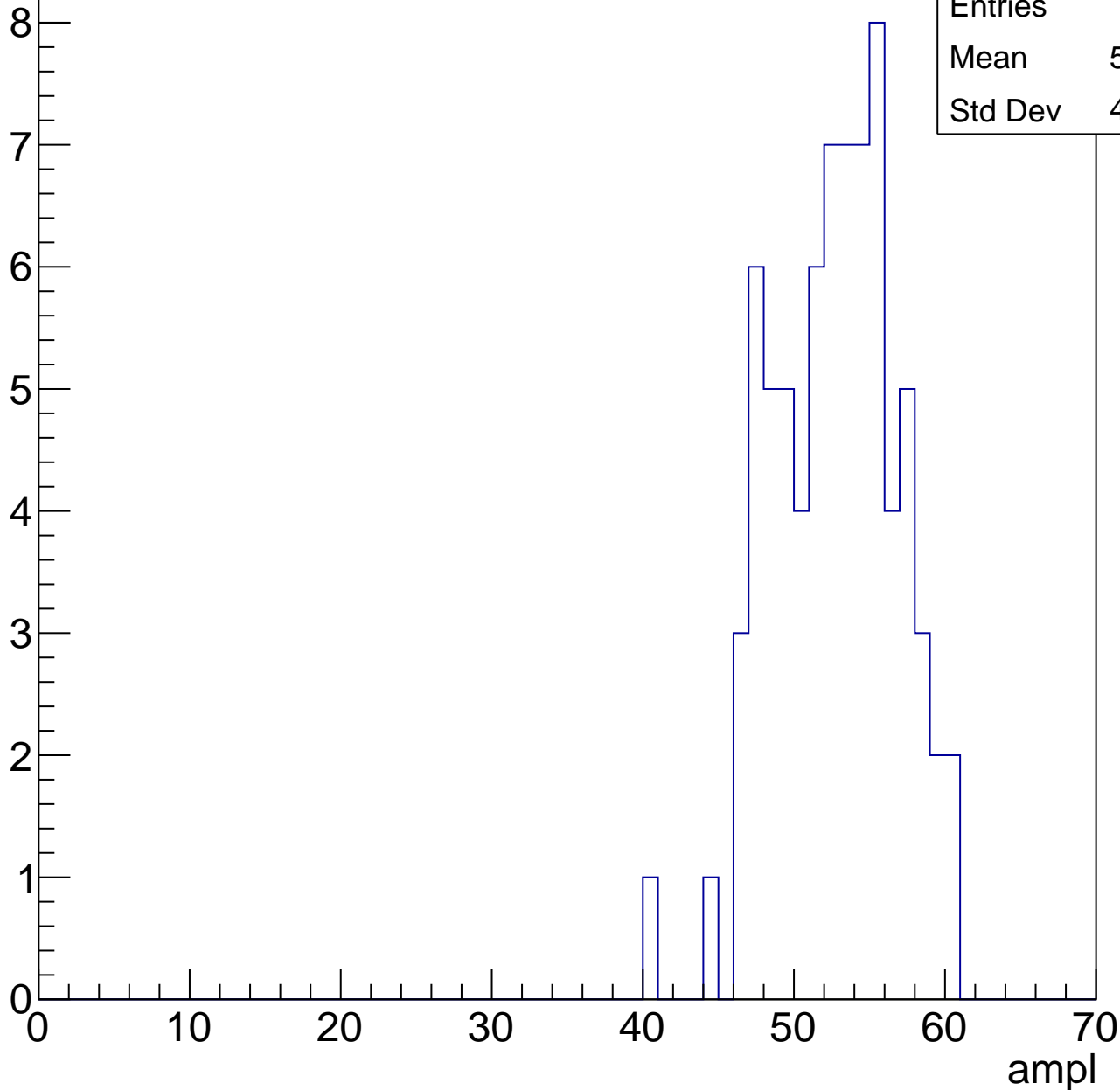


B0L001S, U25-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	52.22
Std Dev	4.038

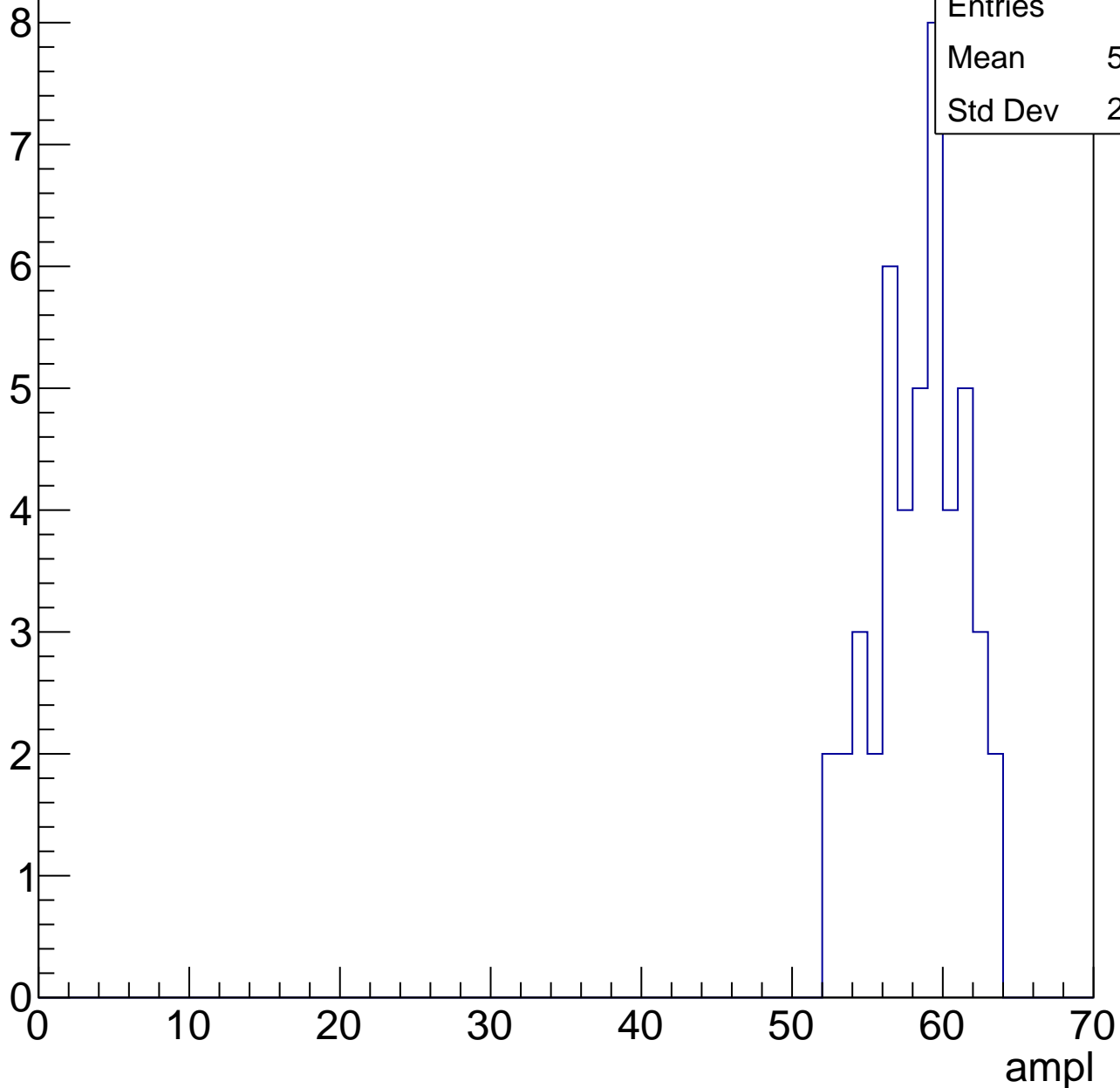


B0L001S, U25-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.93
Std Dev	2.885

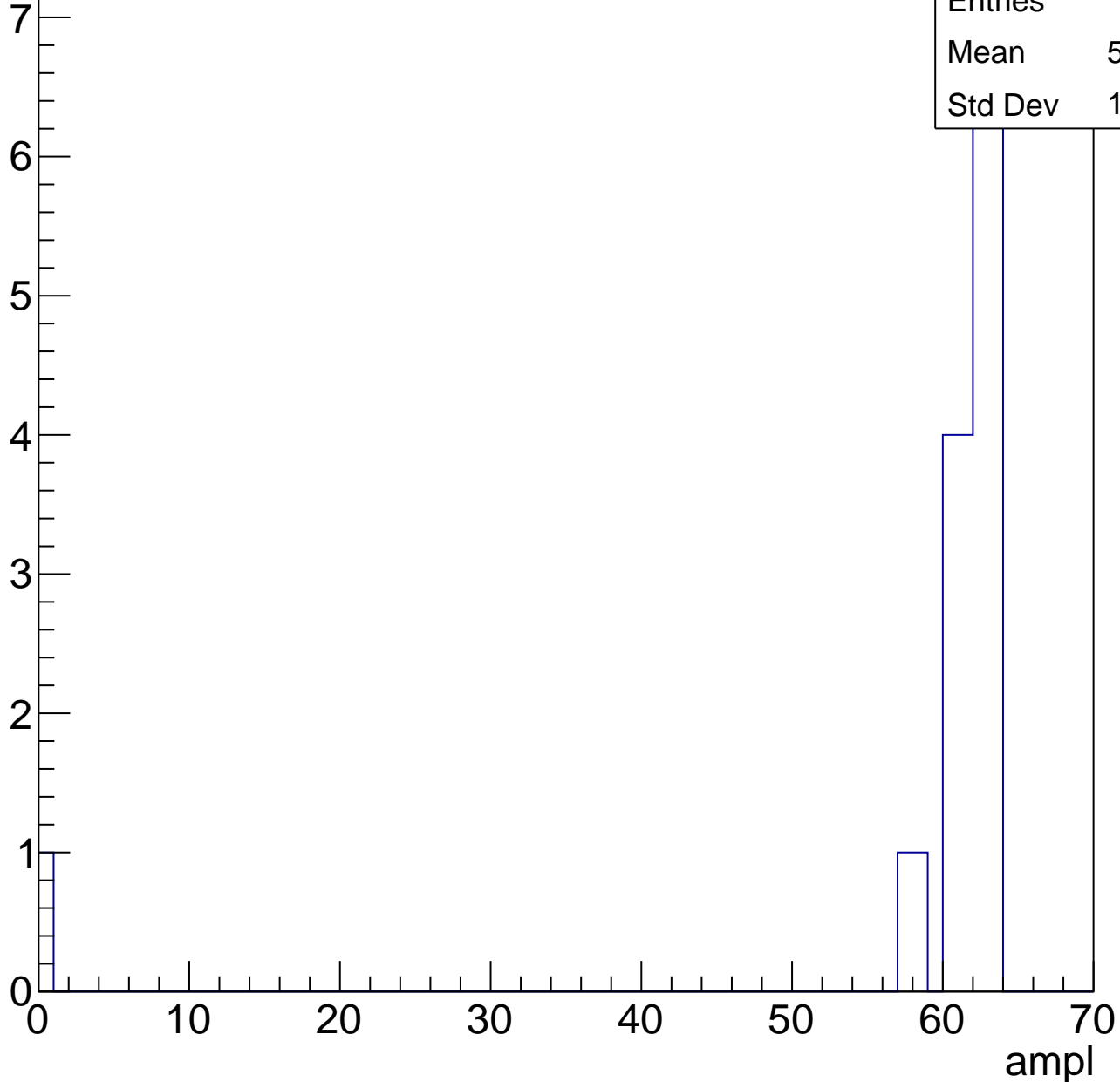


B0L001S, U25-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.96
Std Dev	12.13



B0L001S, U25-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch114, adc0

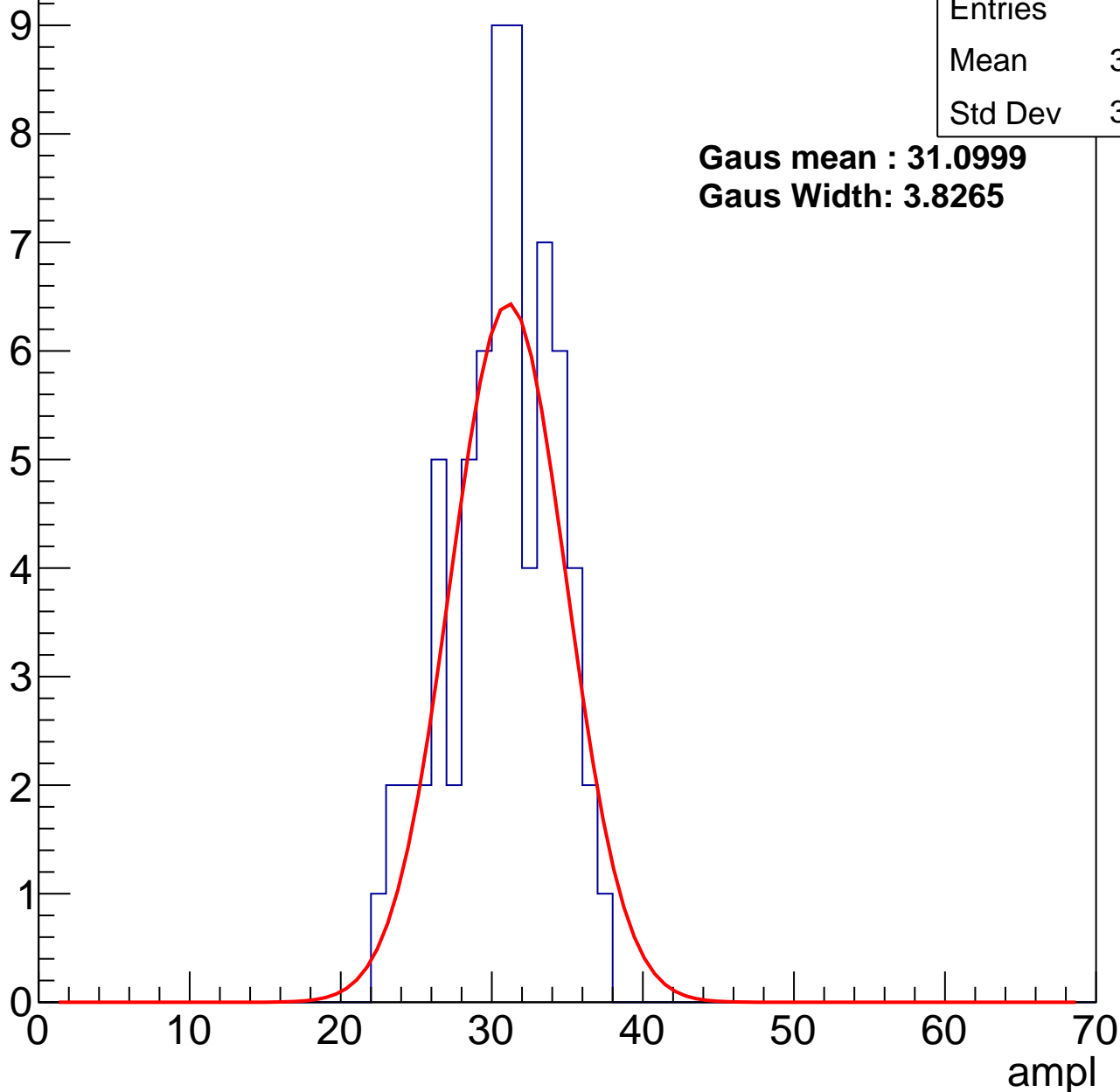
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.22
Std Dev	3.476

Gaus mean : 31.0999

Gaus Width: 3.8265



B0L001S, U25-ch114, adc1

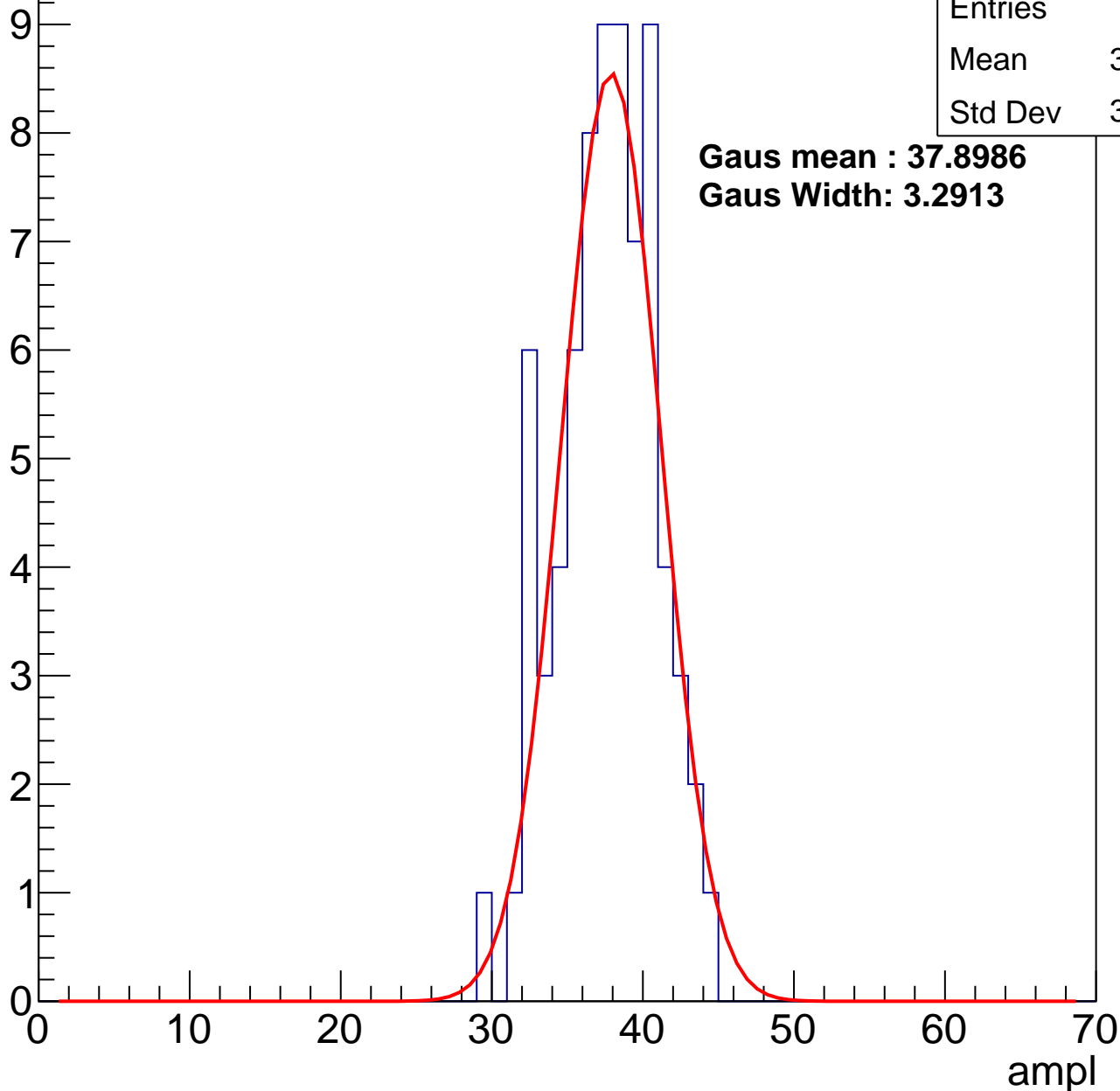
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.16
Std Dev	3.188

Gaus mean : 37.8986

Gaus Width: 3.2913



B0L001S, U25-ch114, adc2

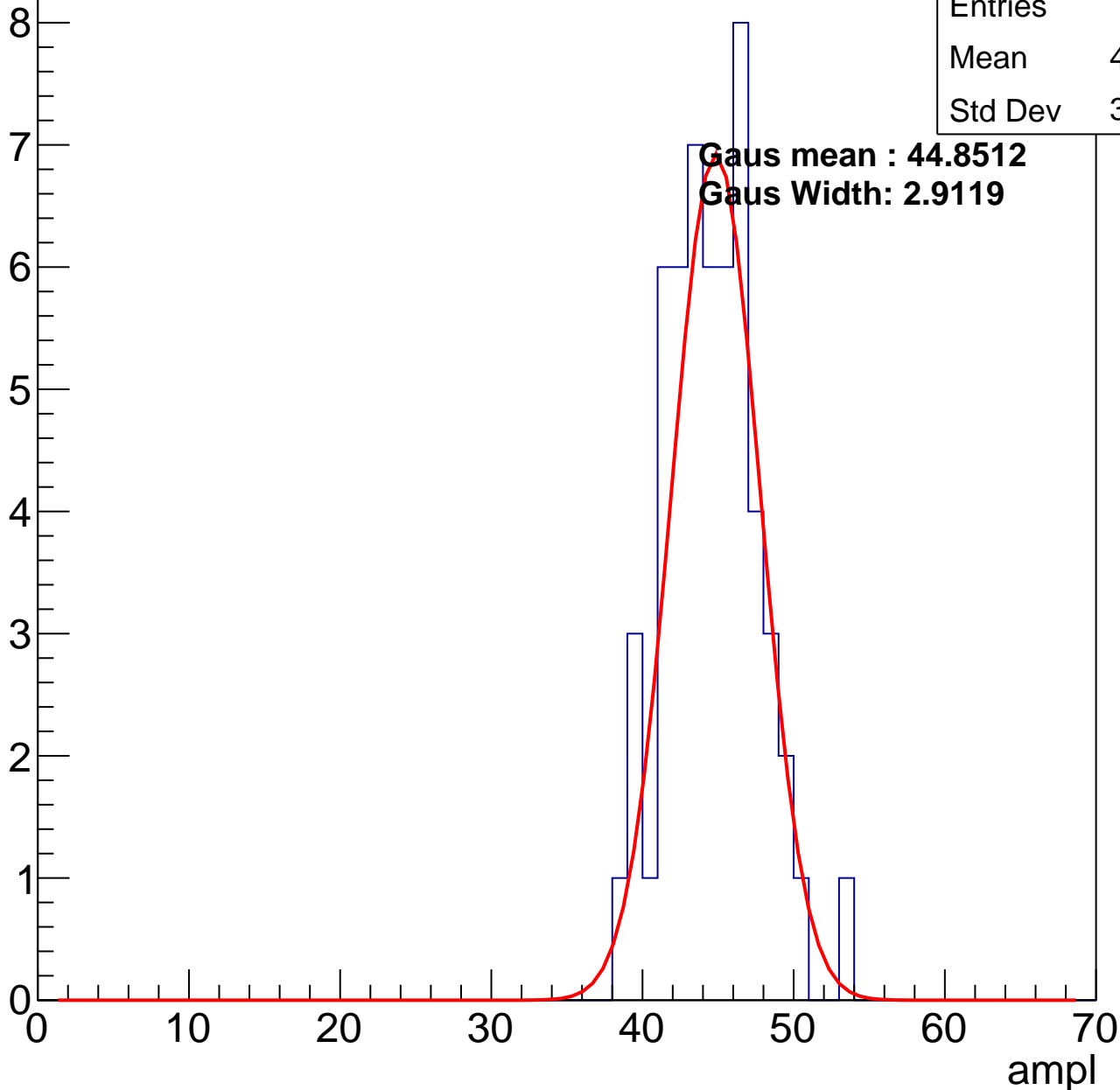
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.16
Std Dev	3.014

Gaus mean : 44.8512

Gaus Width: 2.9119

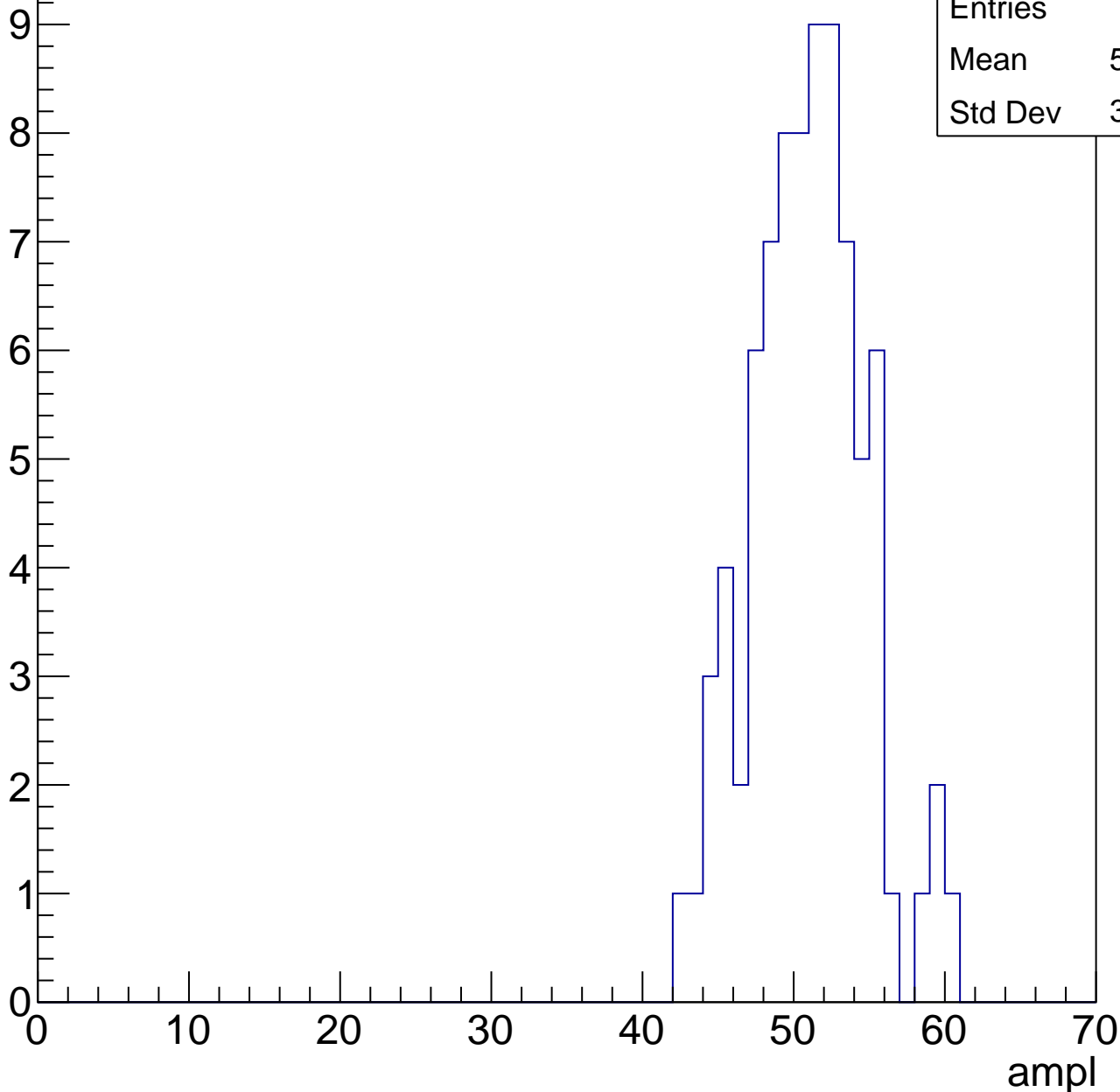


B0L001S, U25-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	50.48
Std Dev	3.725

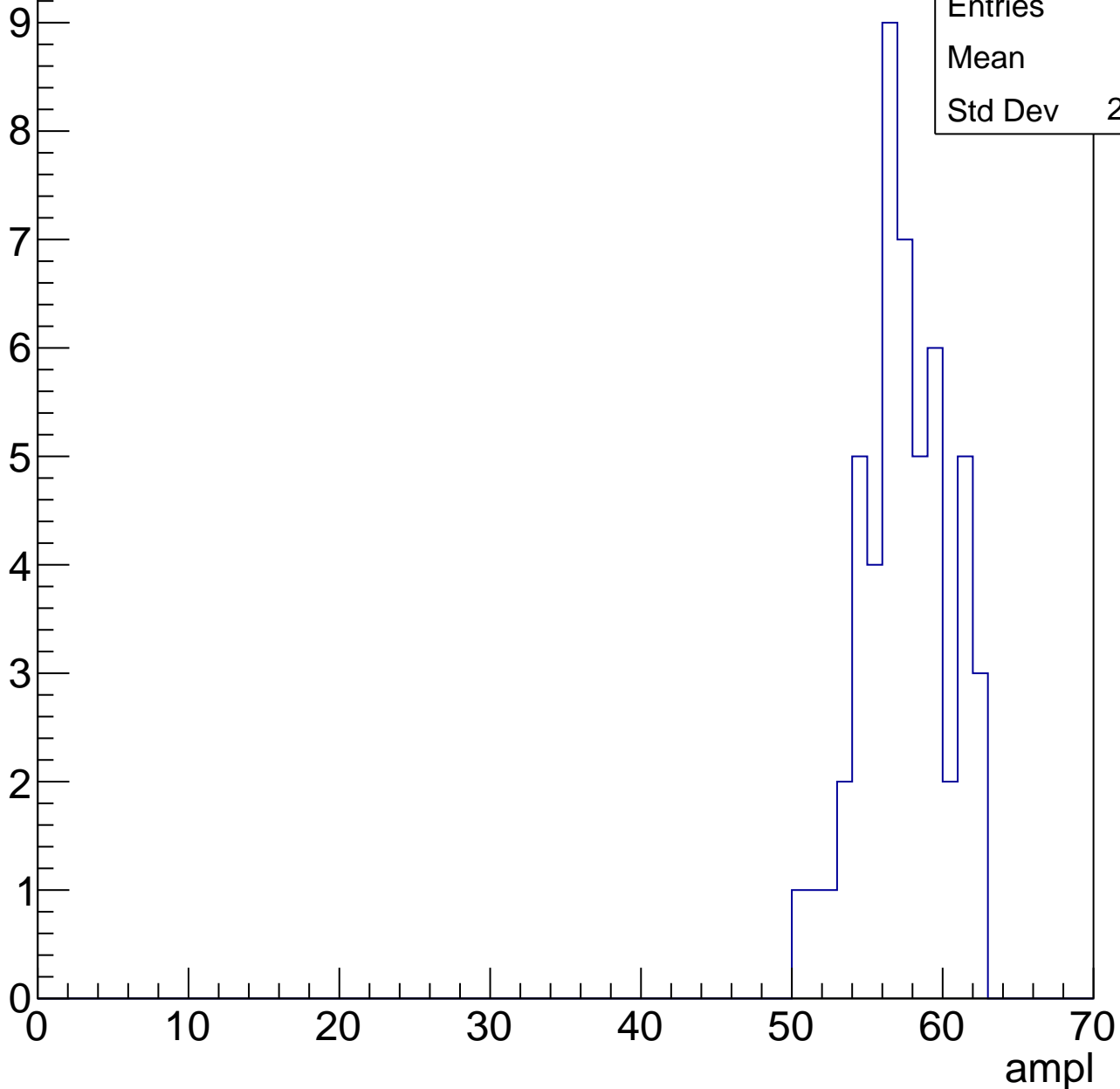


B0L001S, U25-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57
Std Dev	2.849

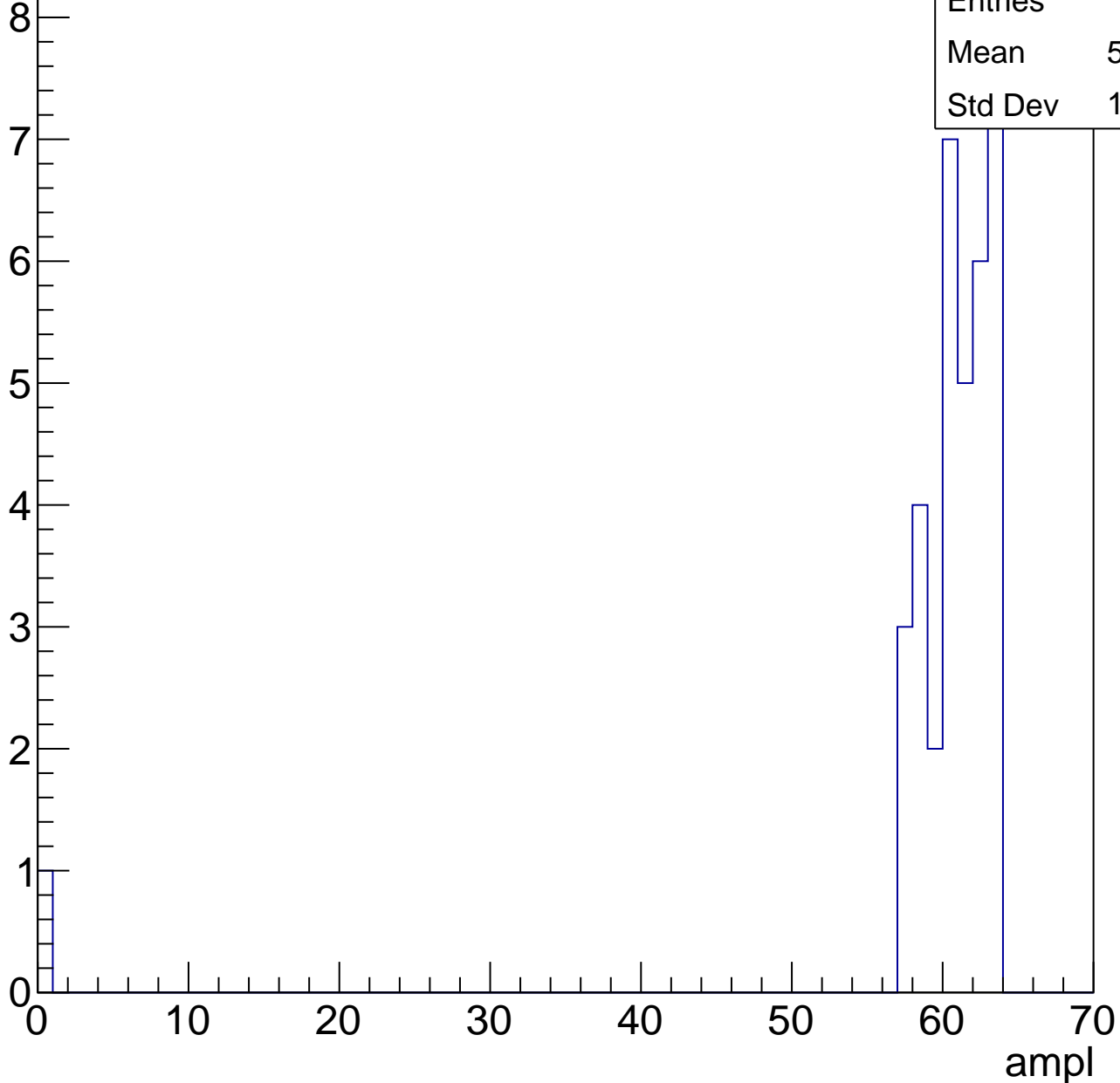


B0L001S, U25-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.94
Std Dev	10.15



B0L001S, U25-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U25-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch115, adc0

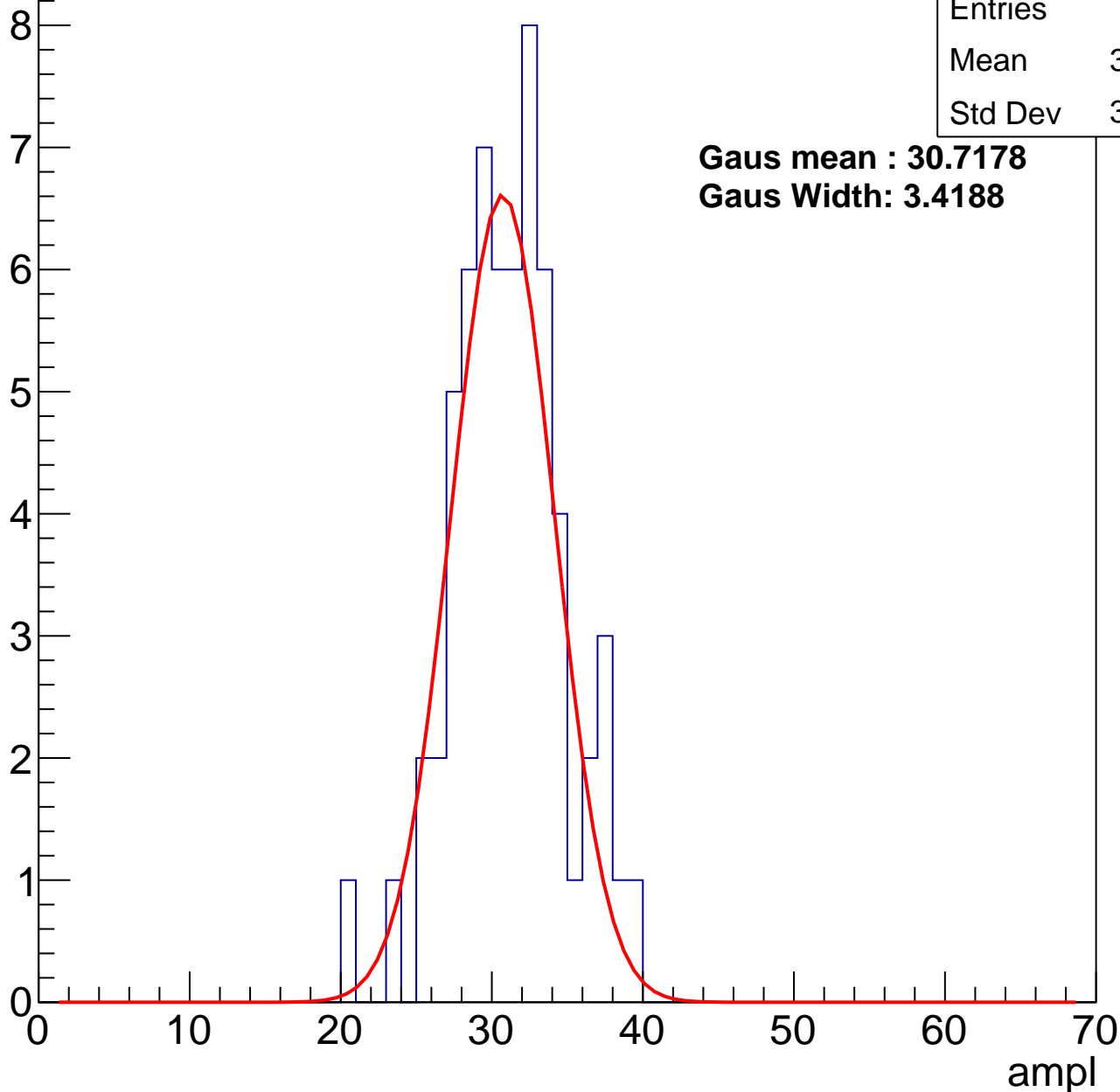
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.68
Std Dev	3.645

Gaus mean : 30.7178

Gaus Width: 3.4188



B0L001S, U25-ch115, adc1

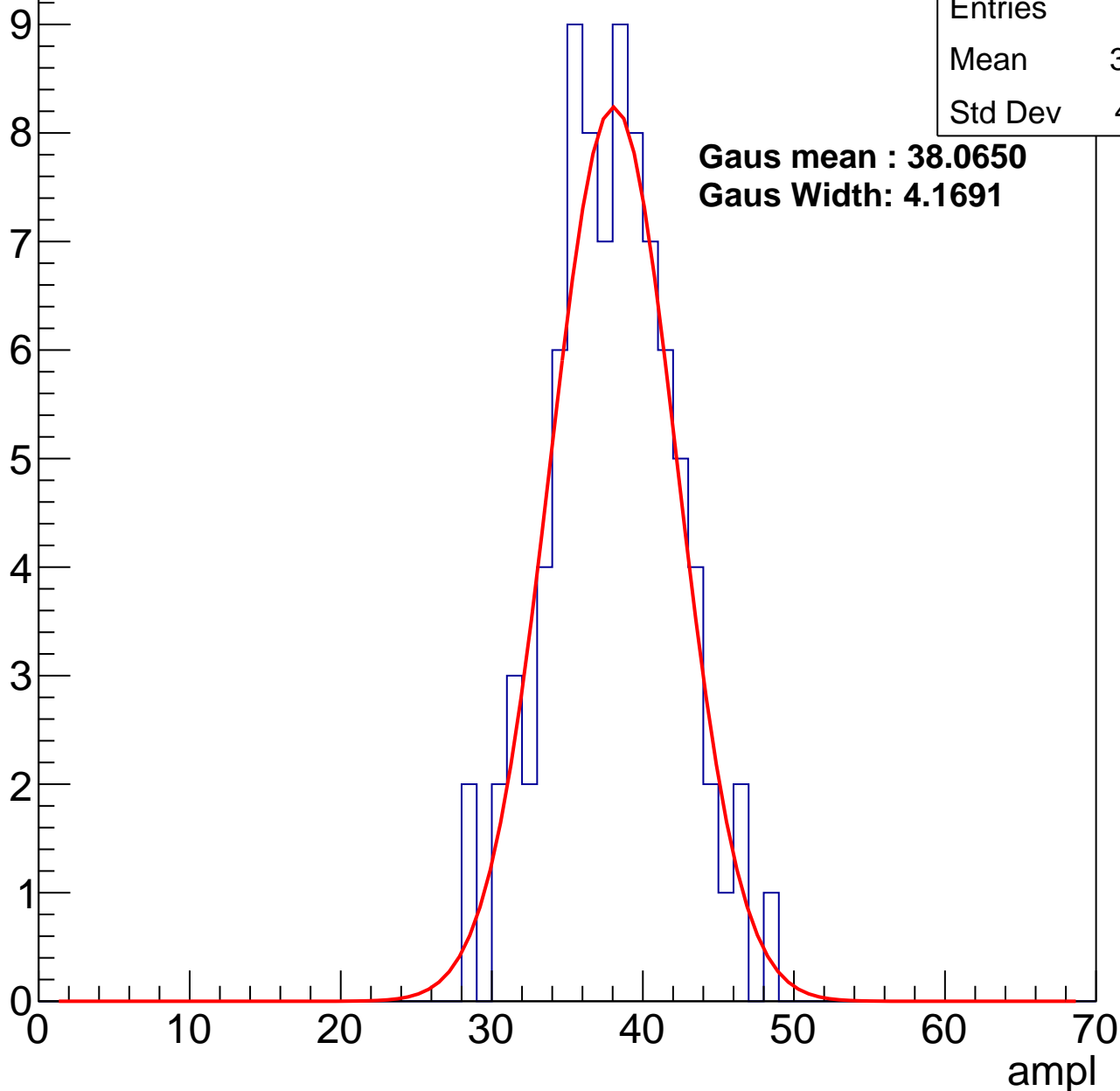
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	37.57
Std Dev	4.081

Gaus mean : 38.0650

Gaus Width: 4.1691



B0L001S, U25-ch115, adc2

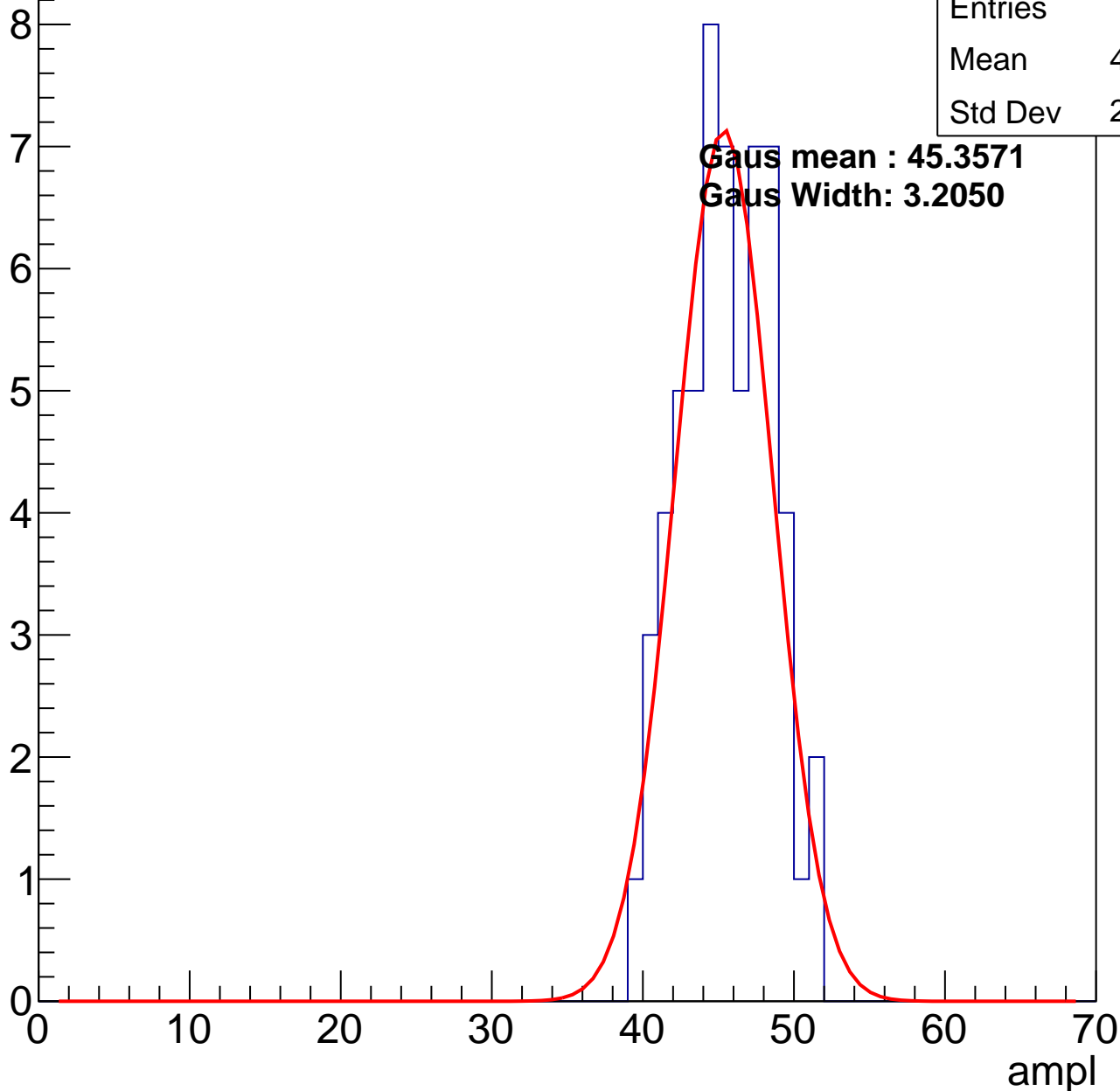
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	45.05
Std Dev	2.925

Gaus mean : 45.3571

Gaus Width: 3.2050

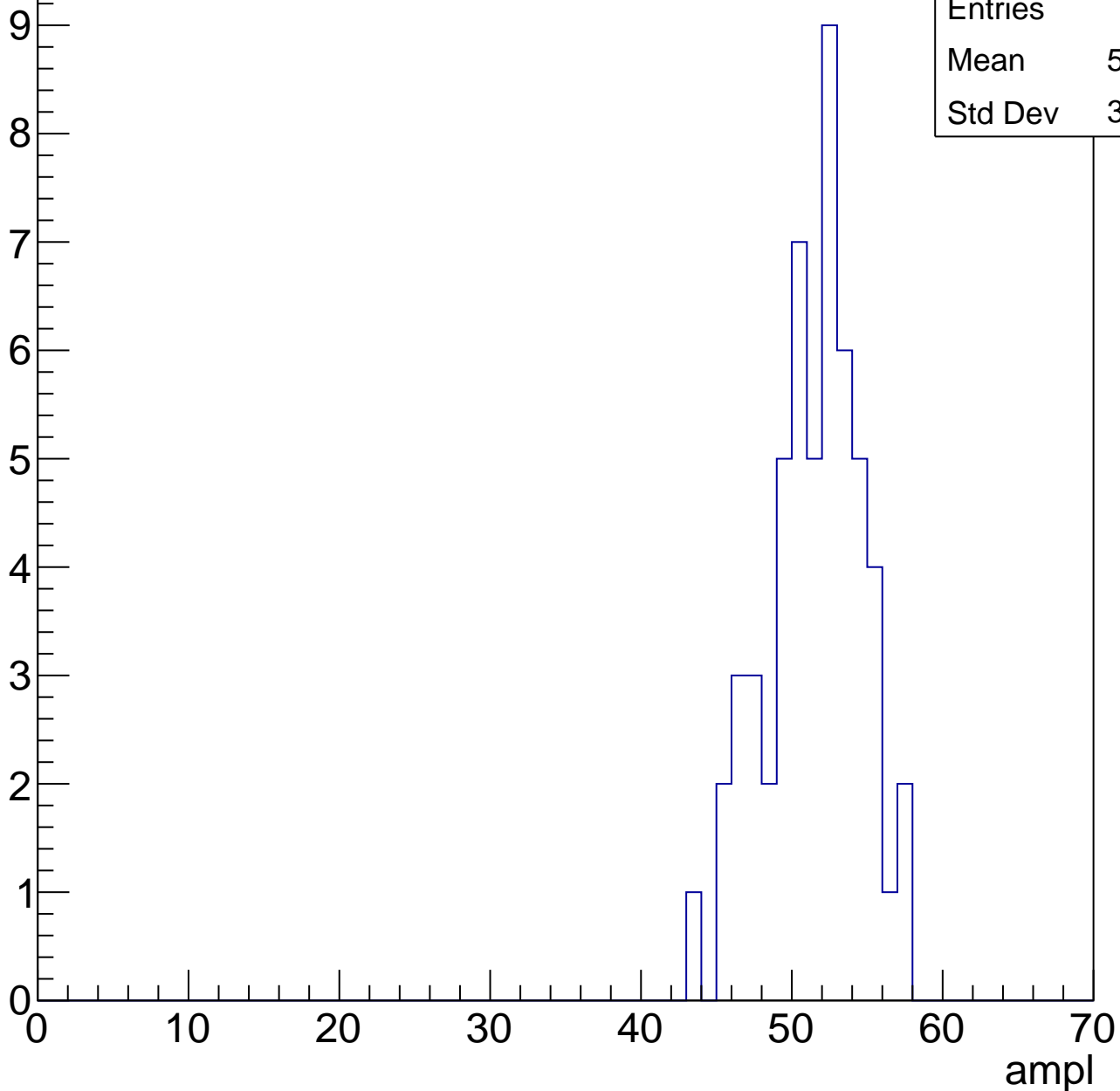


B0L001S, U25-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

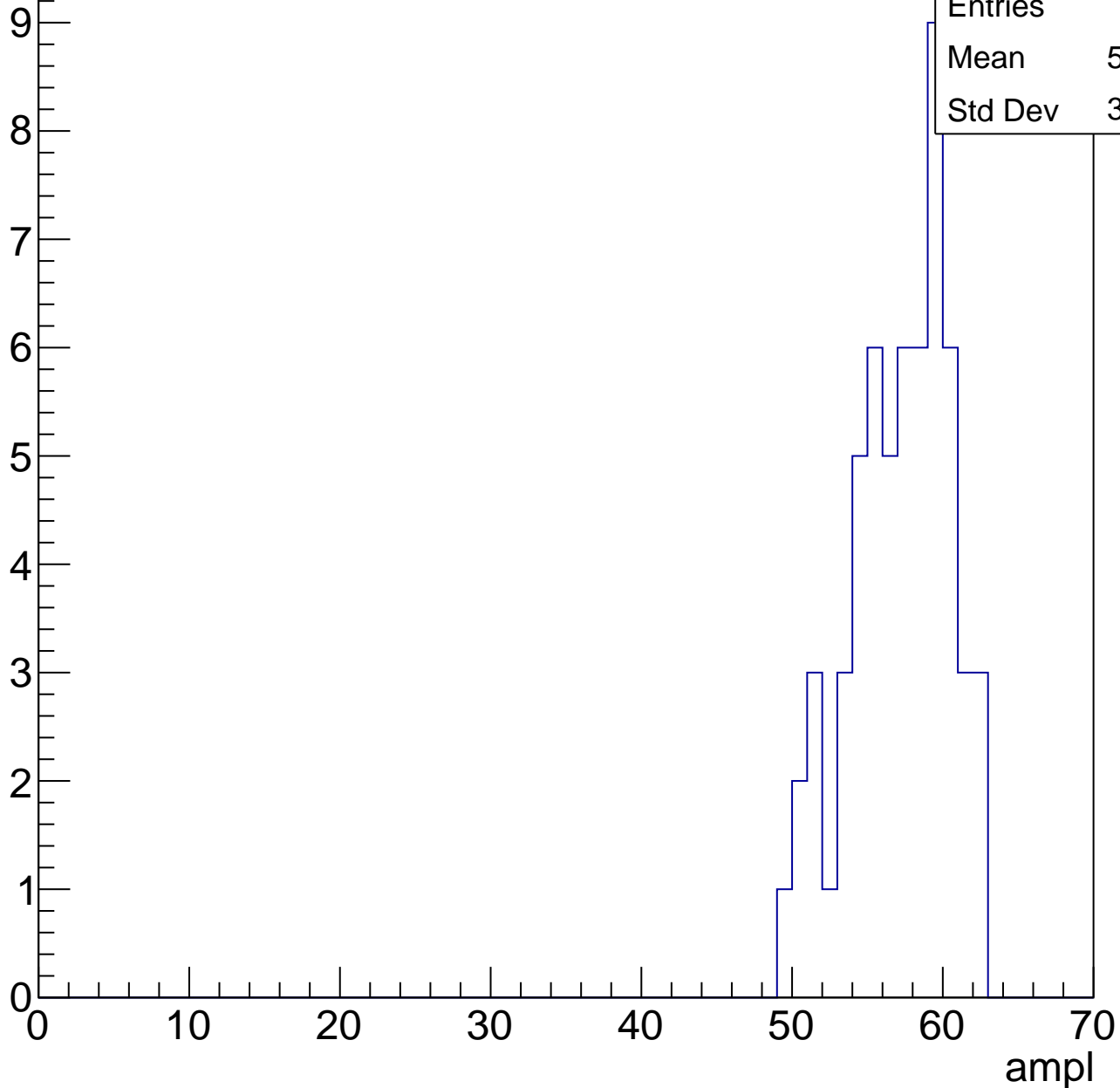
Entries	55
Mean	50.98
Std Dev	3.142



B0L001S, U25-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



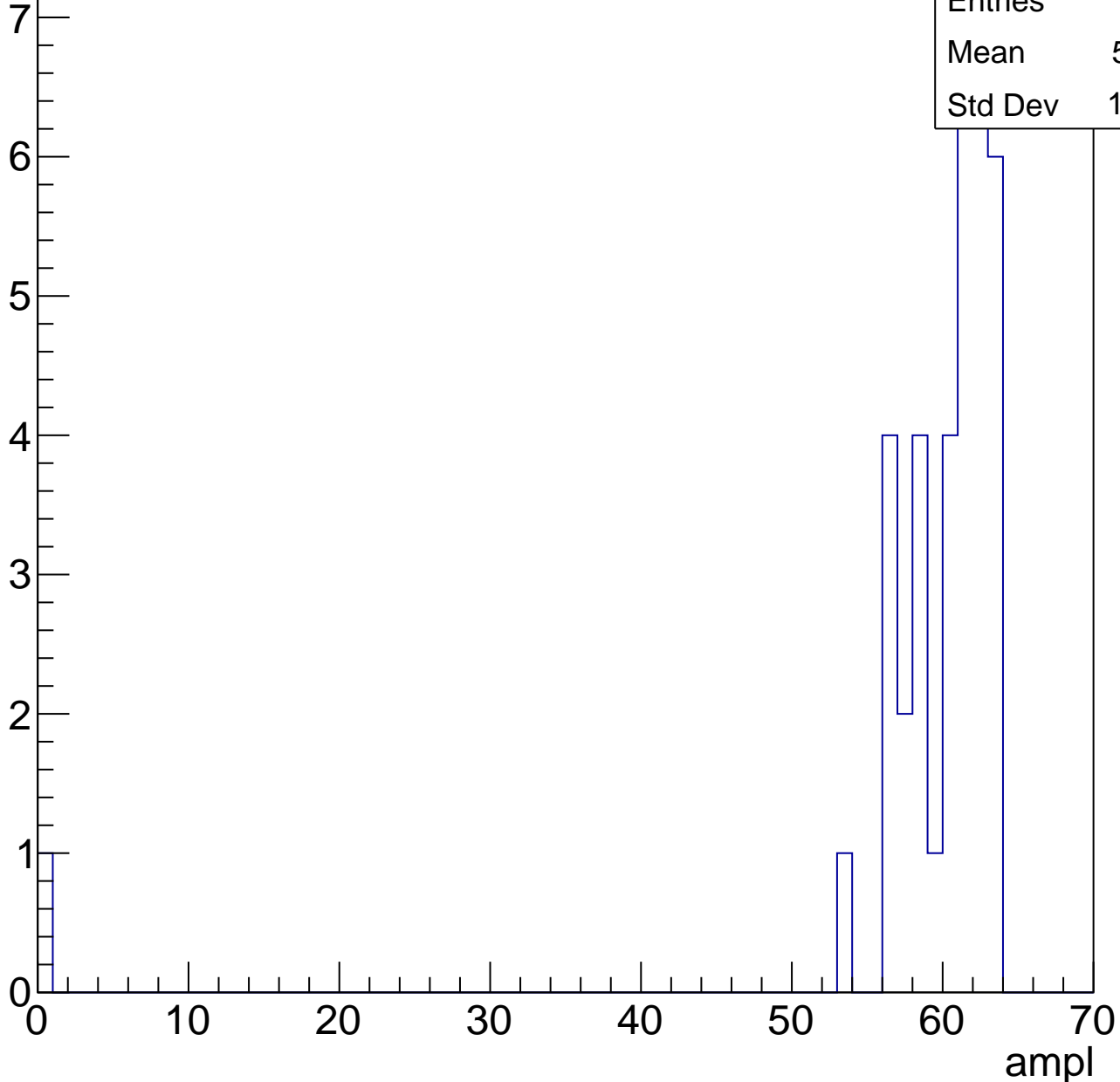
Entries	59
Mean	56.66
Std Dev	3.266

B0L001S, U25-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.41
Std Dev	10.06



B0L001S, U25-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch116, adc0

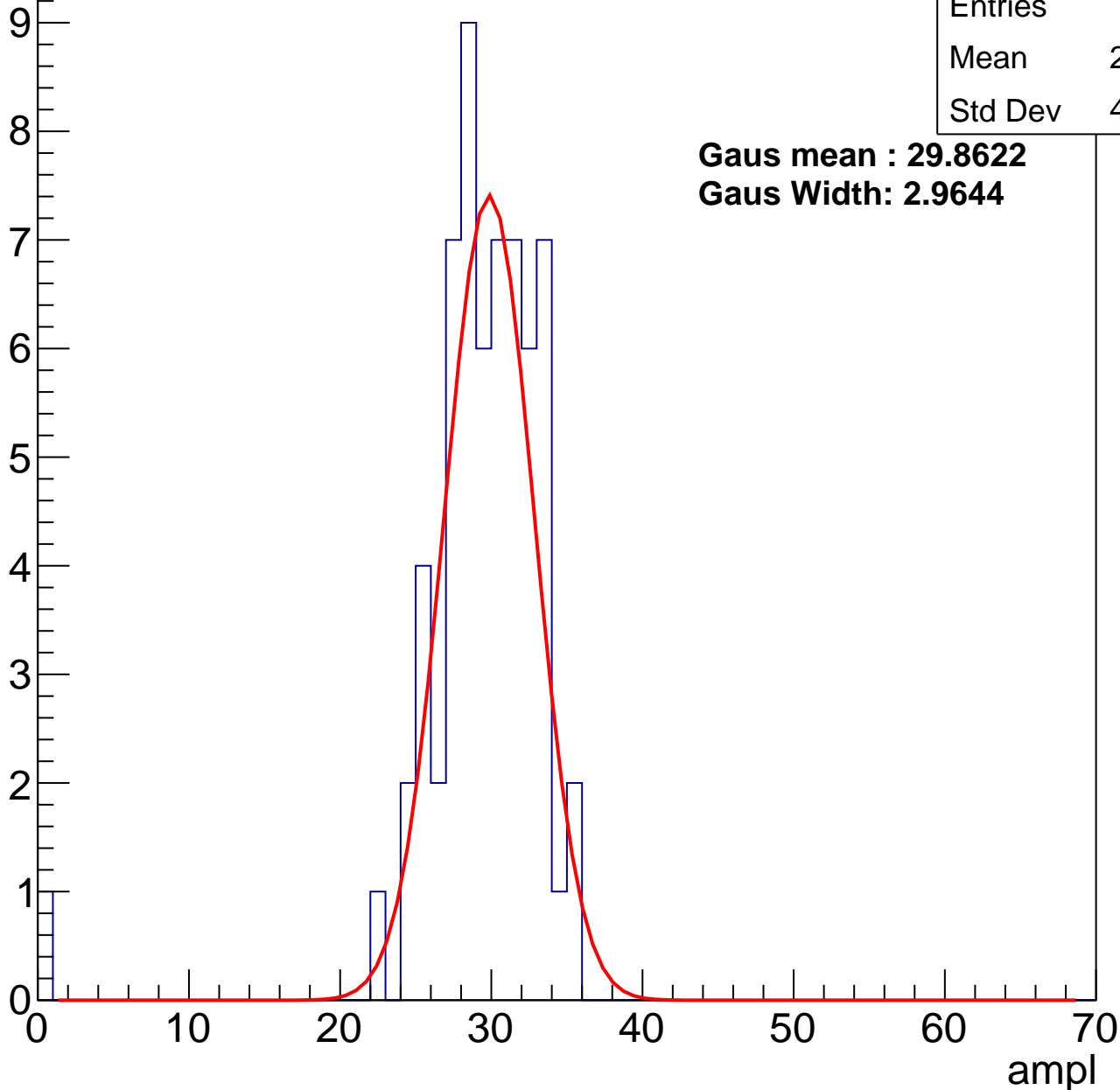
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.89
Std Dev	4.677

Gaus mean : 29.8622

Gaus Width: 2.9644



B0L001S, U25-ch116, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	36.38
Std Dev	3.297

Gaus mean : 36.8724

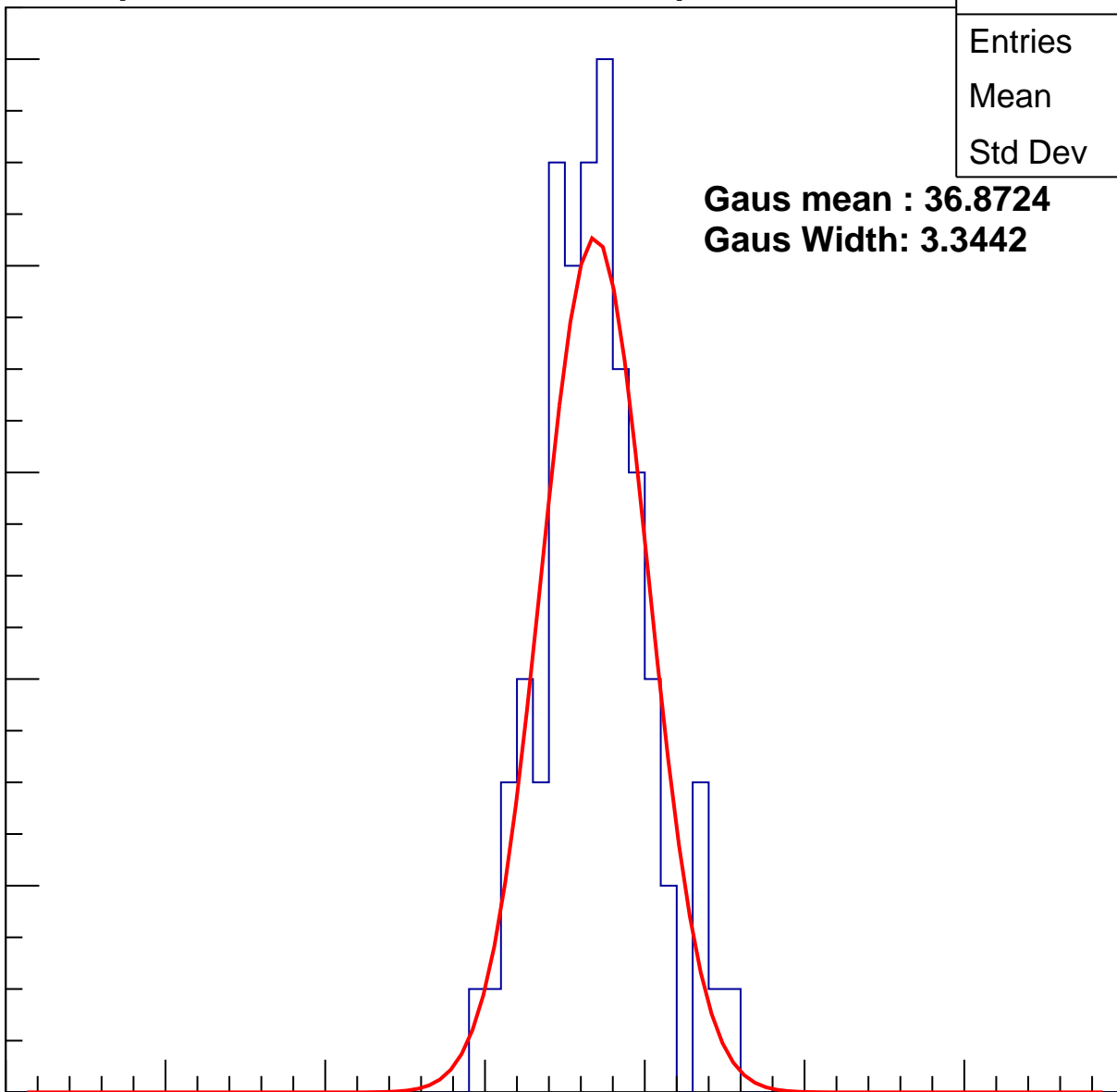
Gaus Width: 3.3442

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch116, adc2

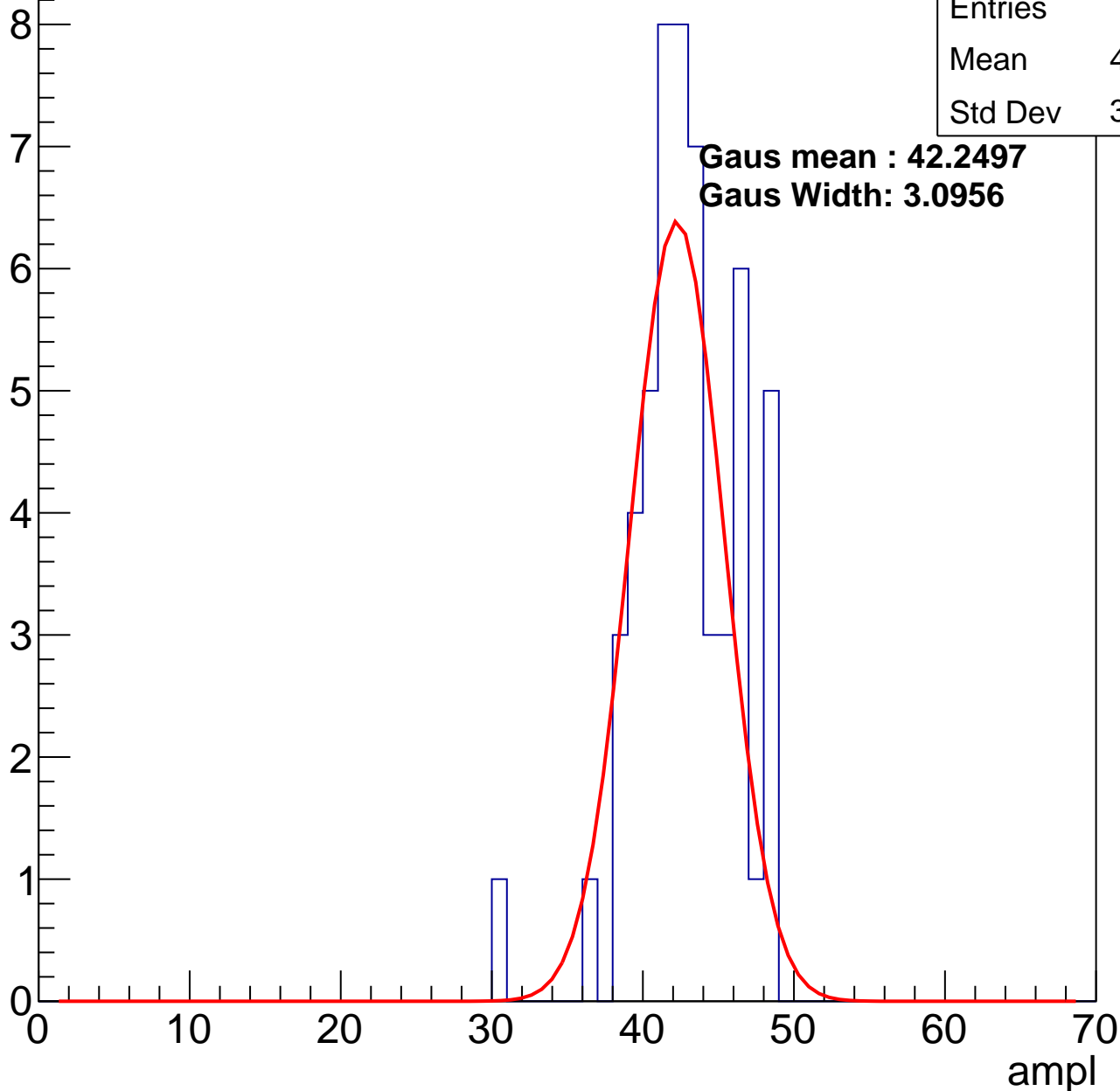
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.38
Std Dev	3.387

Gaus mean : 42.2497

Gaus Width: 3.0956

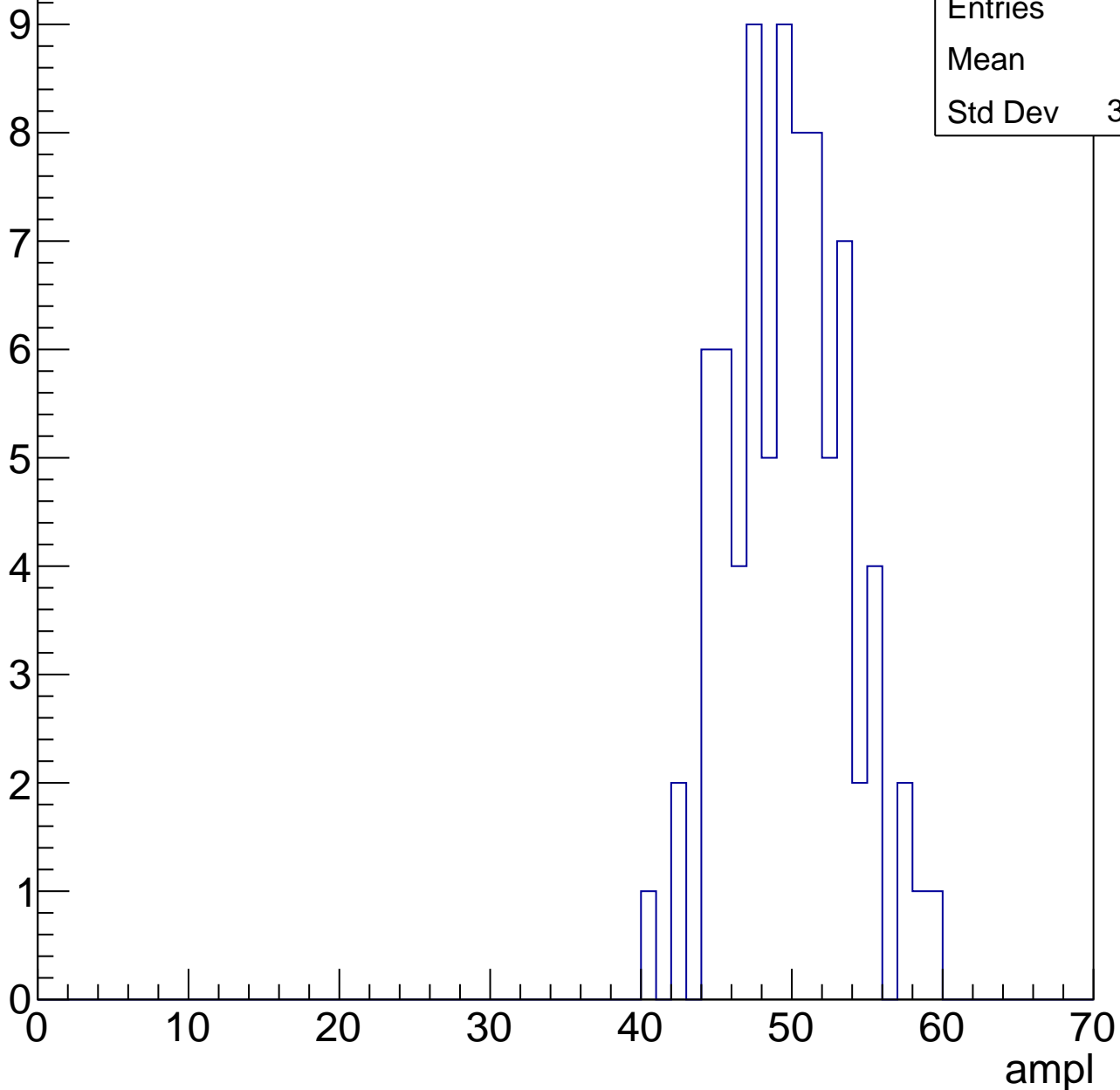


B0L001S, U25-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	49.3
Std Dev	3.865

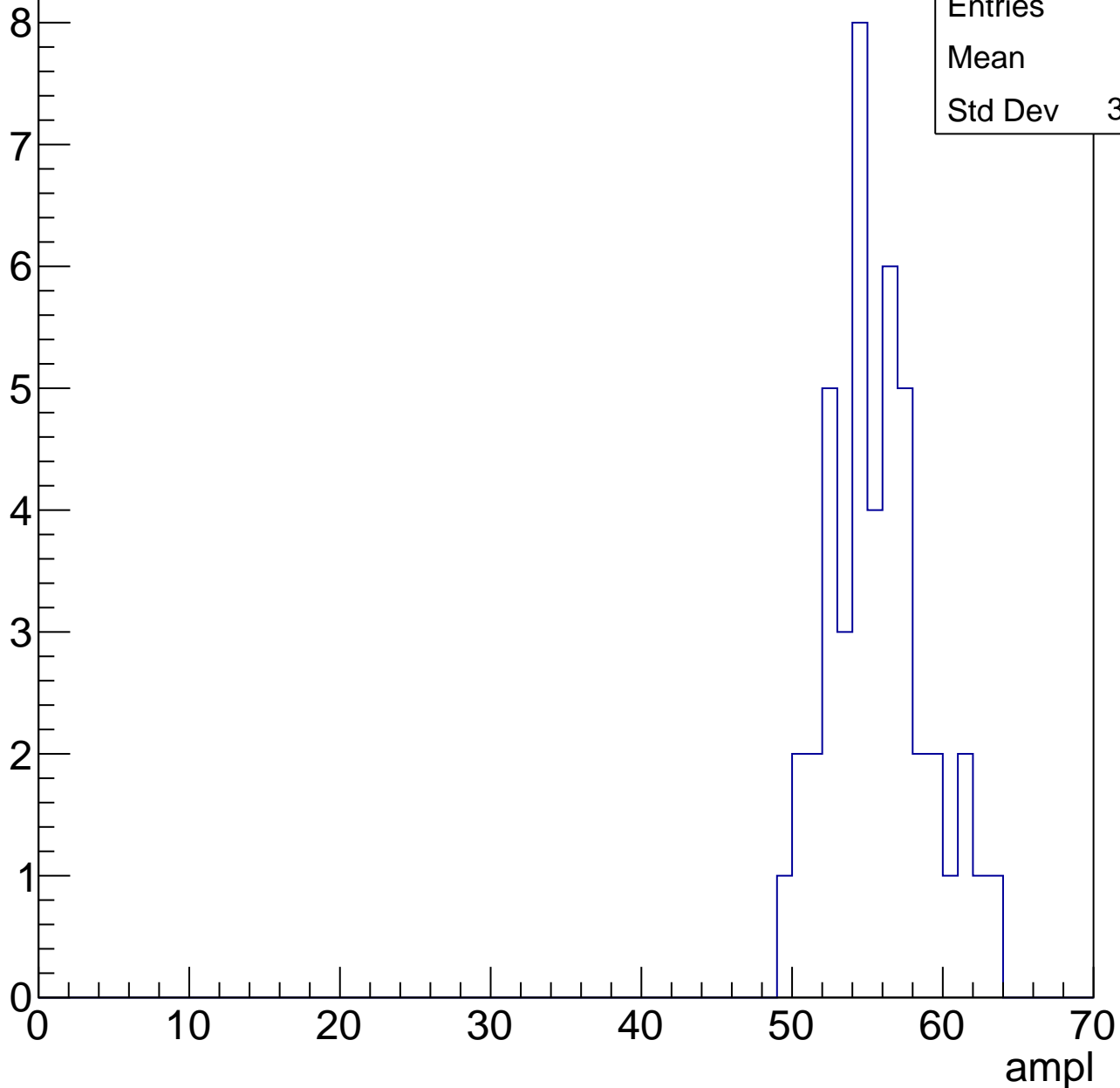


B0L001S, U25-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

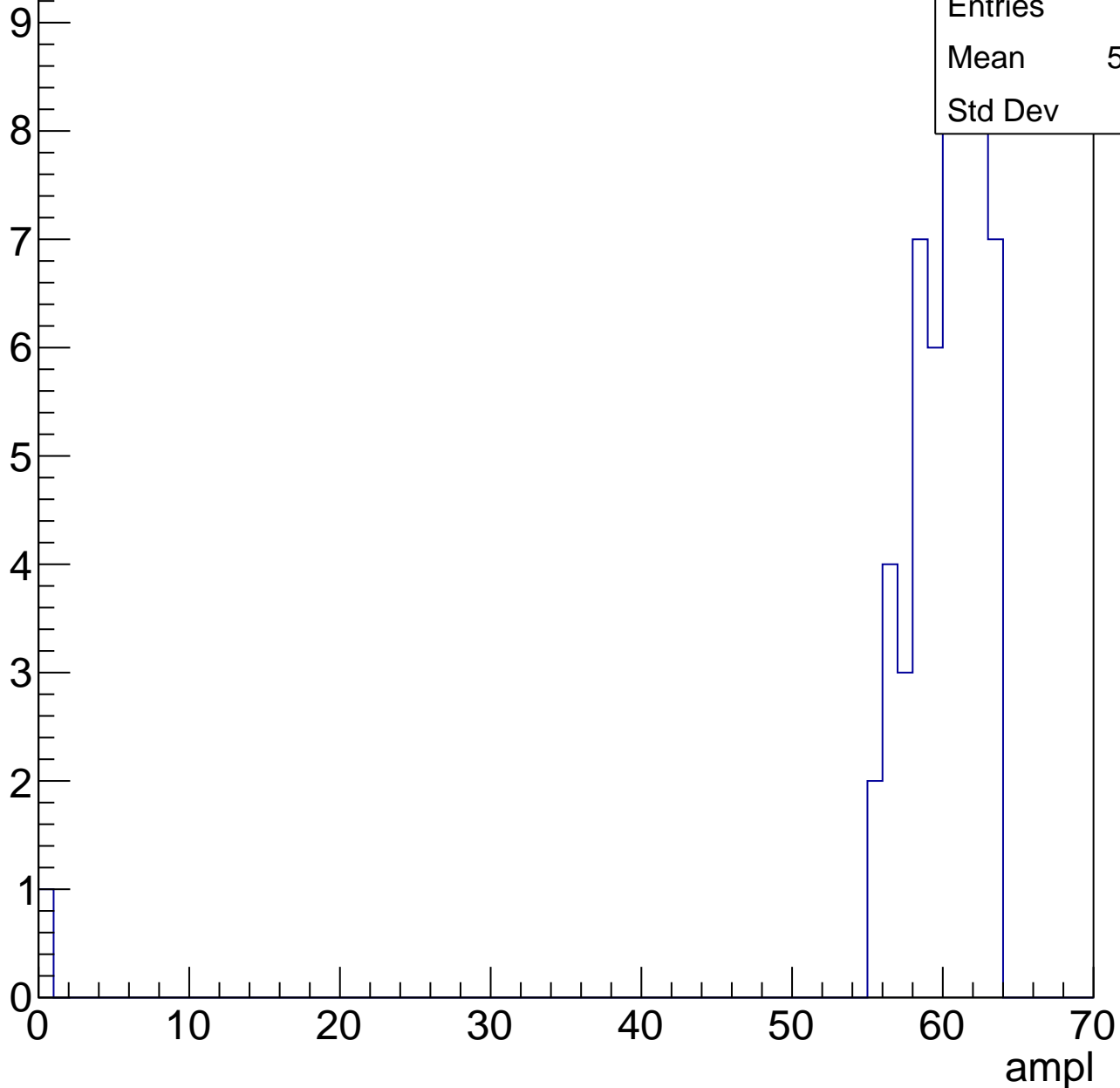
Entries	45
Mean	55.2
Std Dev	3.222



B0L001S, U25-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

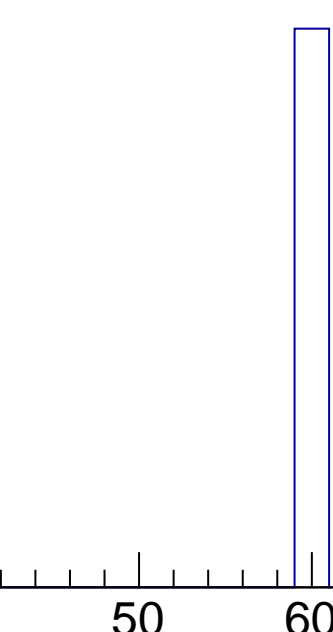
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.785

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch117, adc0

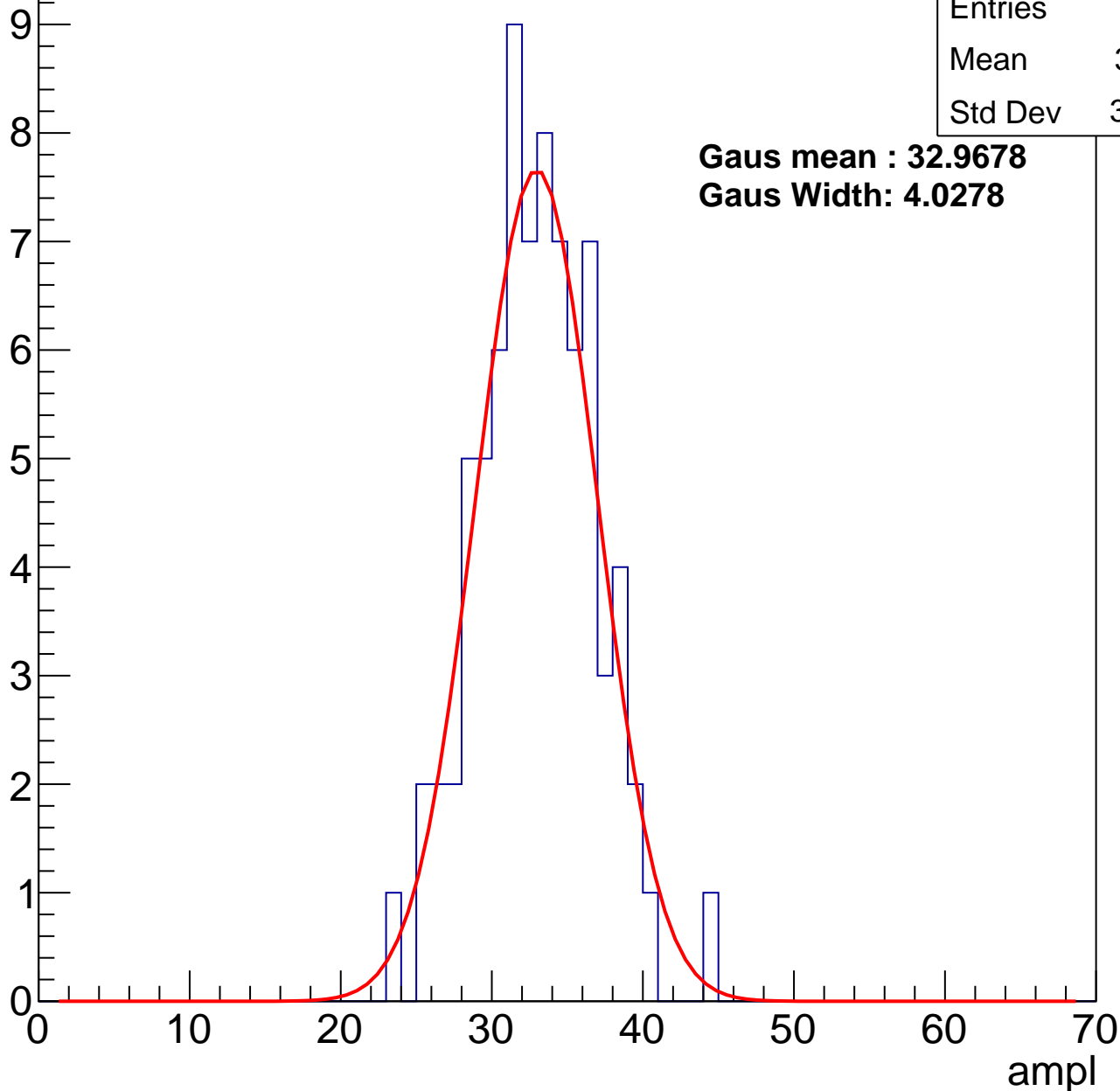
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	32.51
Std Dev	3.866

Gaus mean : 32.9678

Gaus Width: 4.0278



B0L001S, U25-ch117, adc1

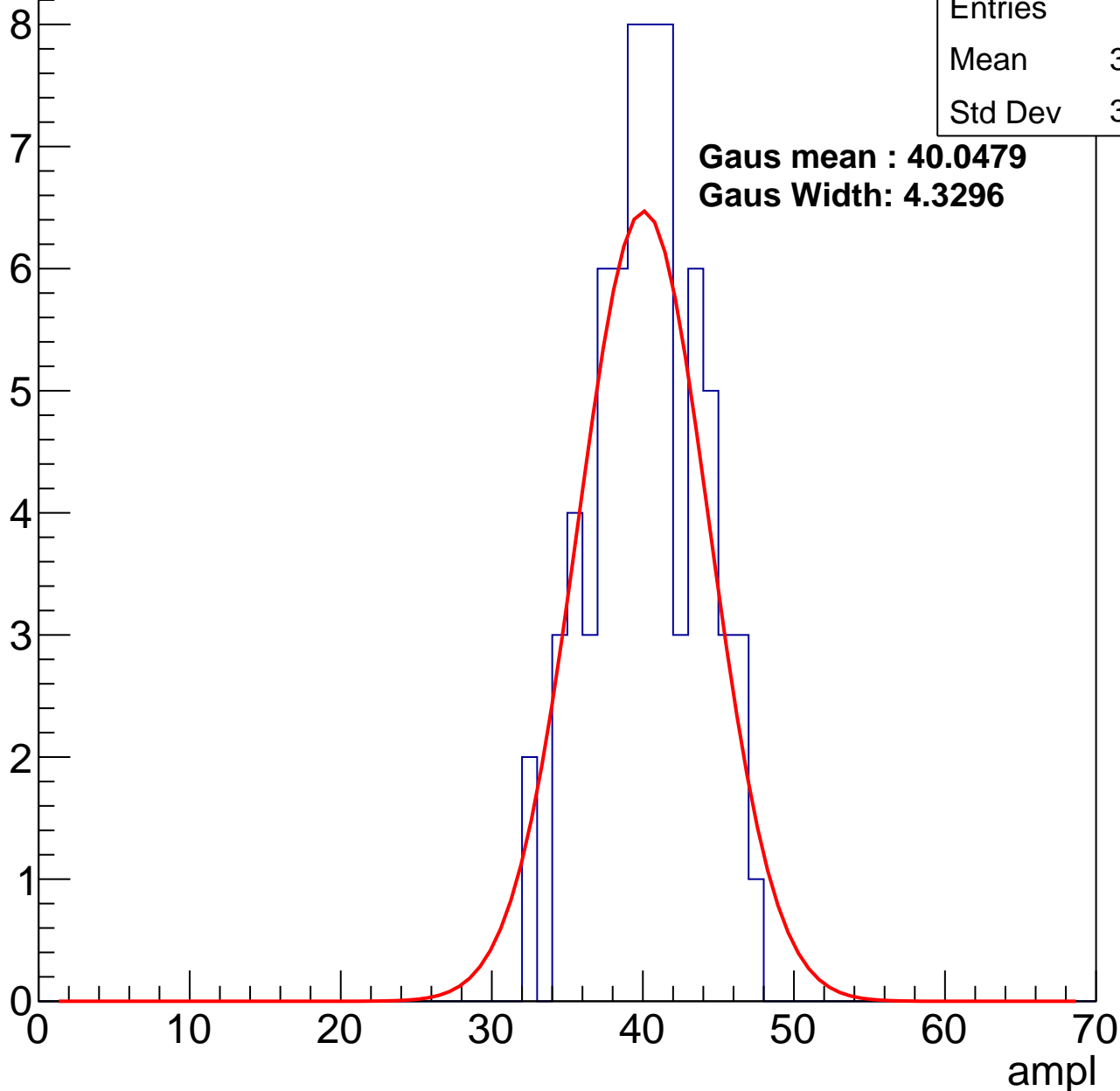
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	39.83
Std Dev	3.518

Gaus mean : 40.0479

Gaus Width: 4.3296



B0L001S, U25-ch117, adc2

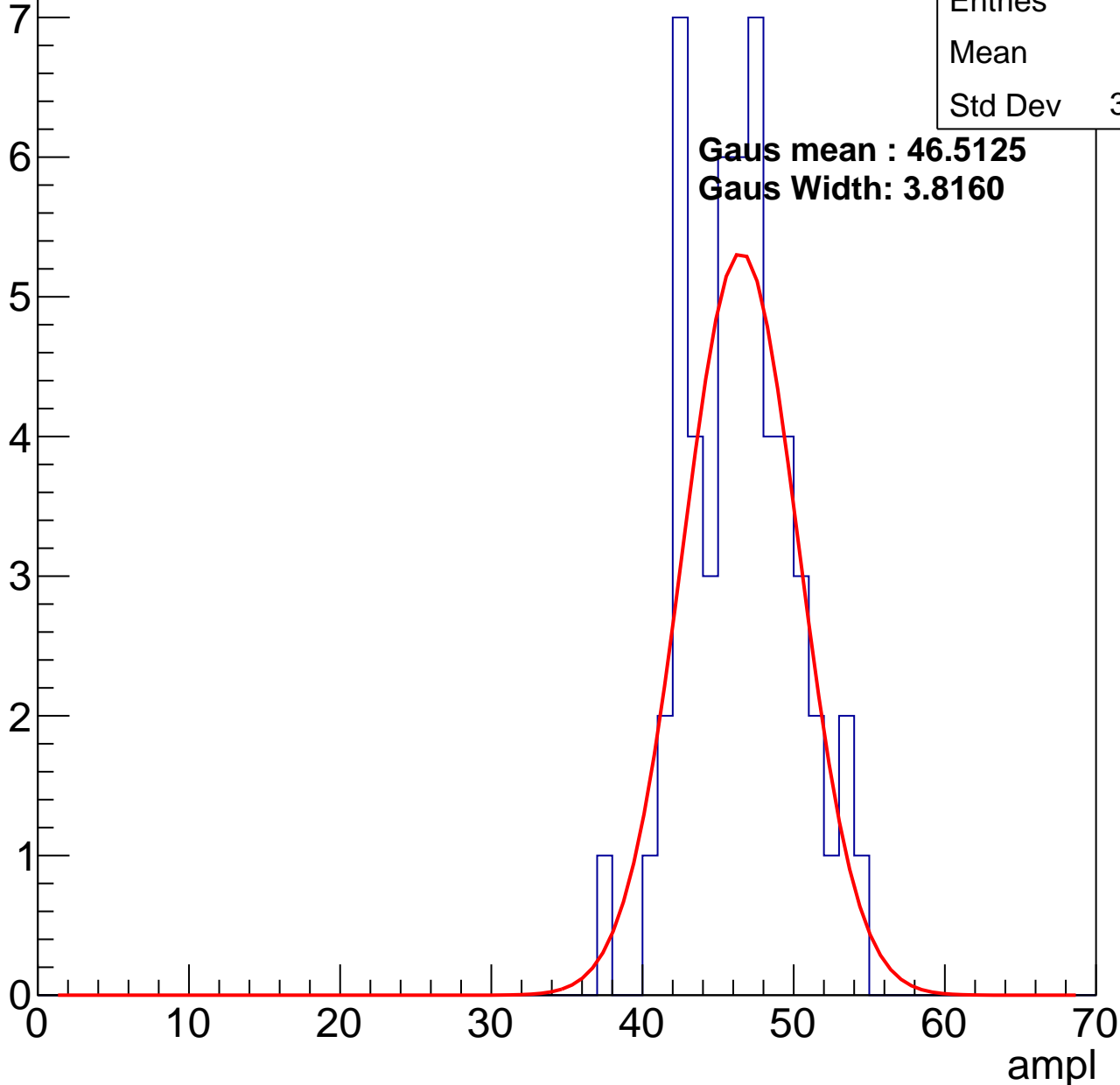
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	46
Std Dev	3.569

Gaus mean : 46.5125

Gaus Width: 3.8160

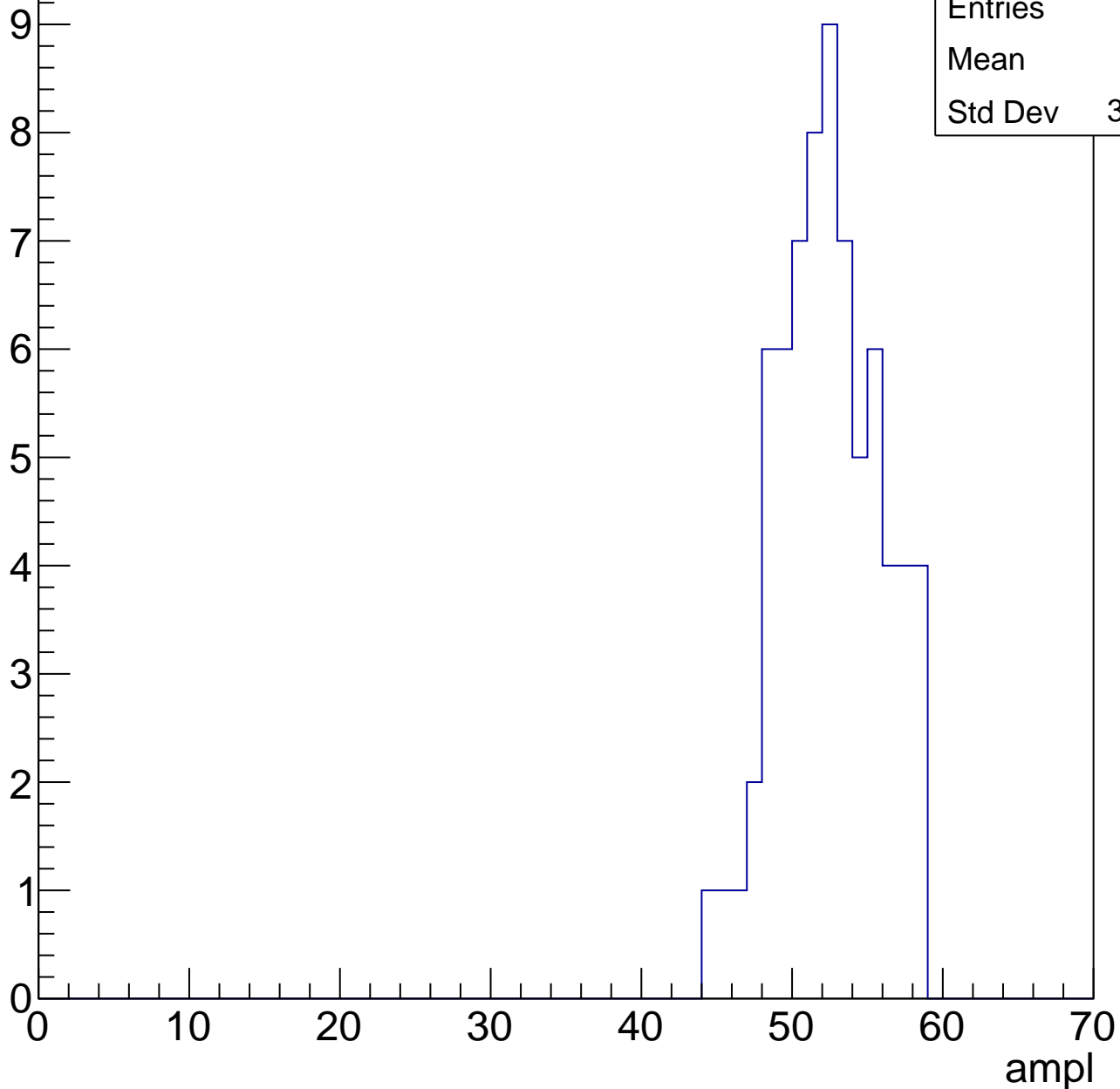


B0L001S, U25-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	52
Std Dev	3.302

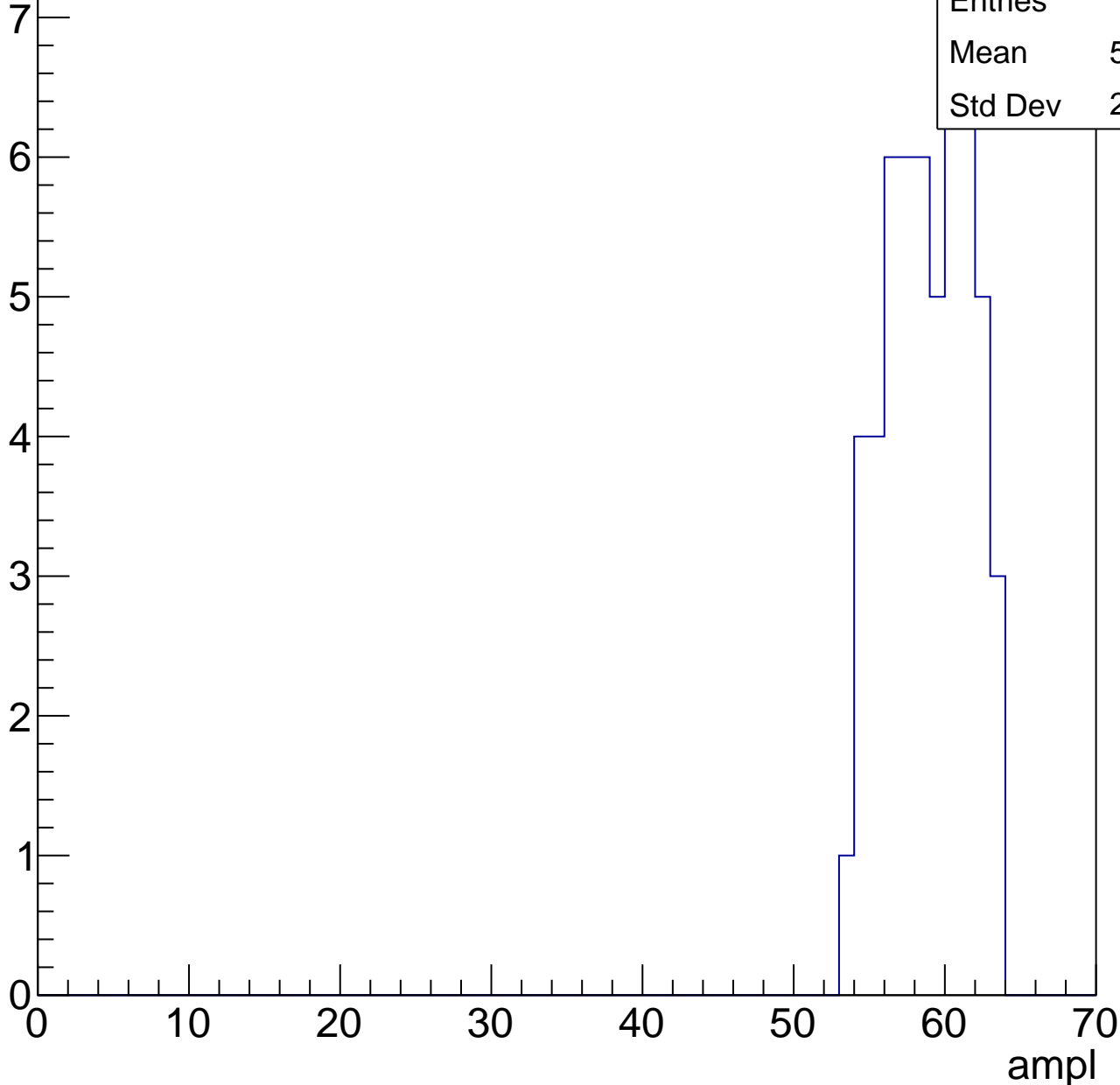


B0L001S, U25-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	58.44
Std Dev	2.706

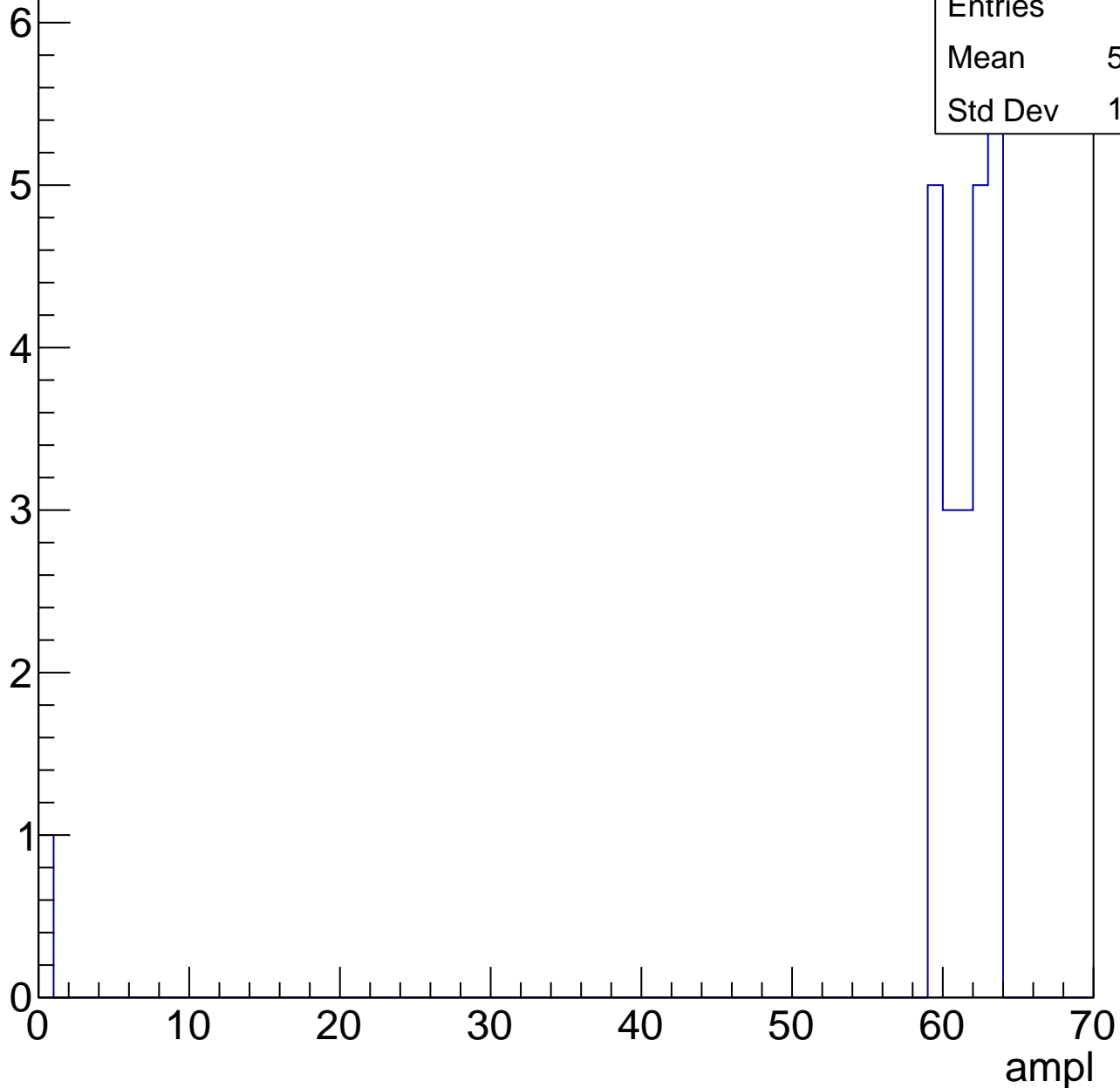


B0L001S, U25-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.52
Std Dev	12.57



B0L001S, U25-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

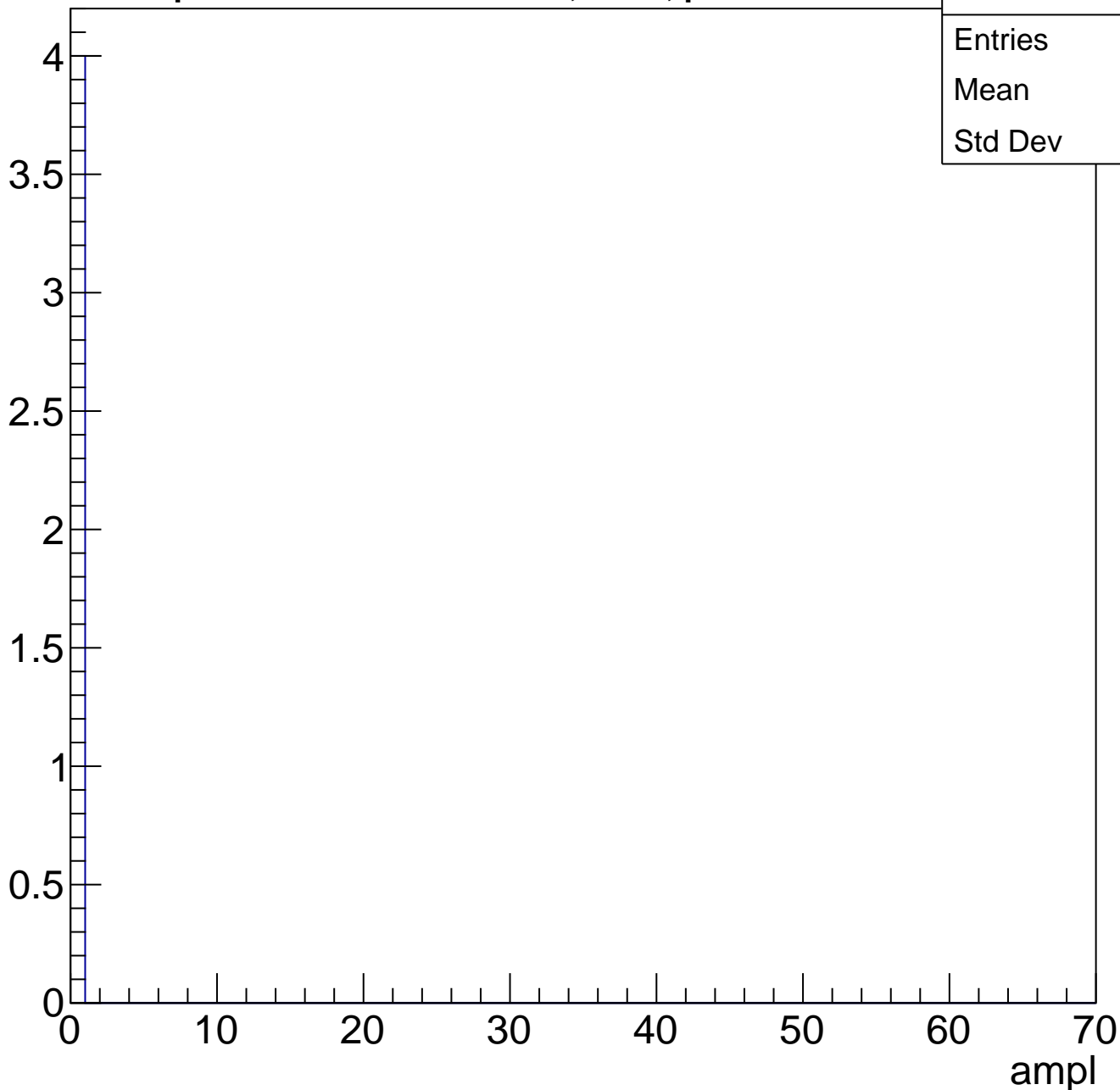


Entries	1
Mean	63
Std Dev	0

B0L001S, U25-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U25-ch118, adc0

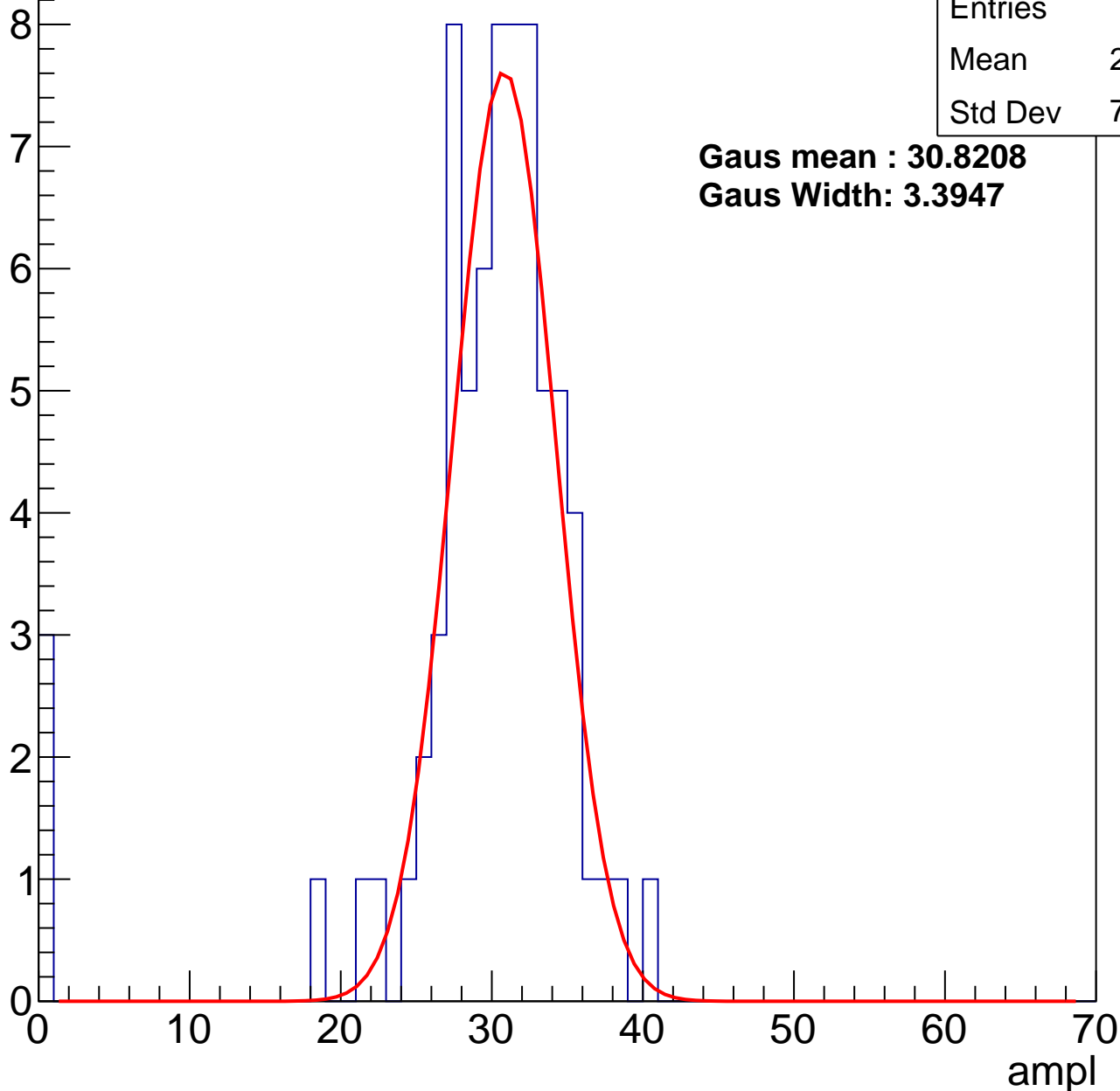
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	28.95
Std Dev	7.075

Gaus mean : 30.8208

Gaus Width: 3.3947



B0L001S, U25-ch118, adc1

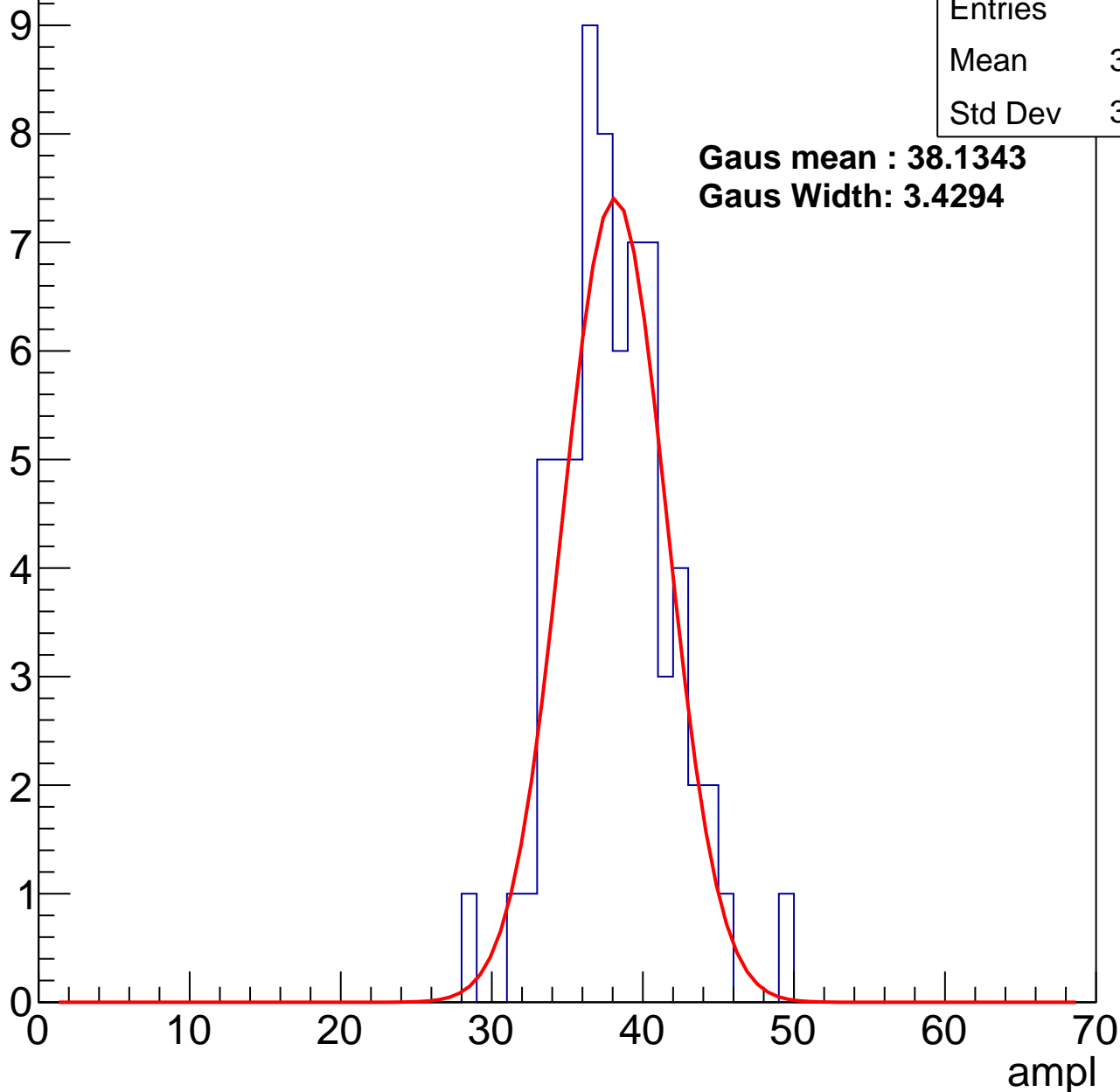
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.66
Std Dev	3.616

Gaus mean : 38.1343

Gaus Width: 3.4294



B0L001S, U25-ch118, adc2

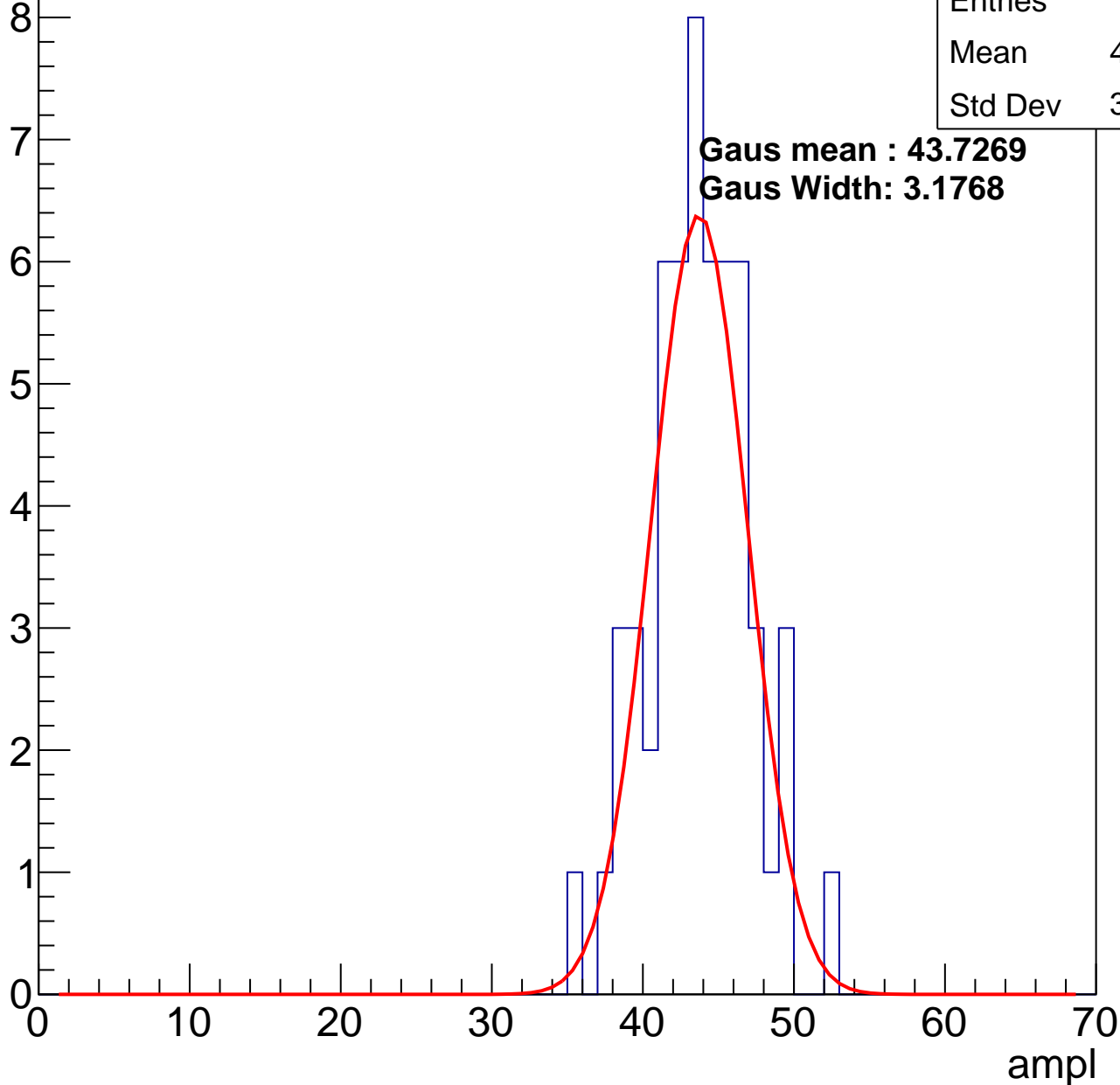
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.27
Std Dev	3.308

Gaus mean : 43.7269

Gaus Width: 3.1768



B0L001S, U25-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

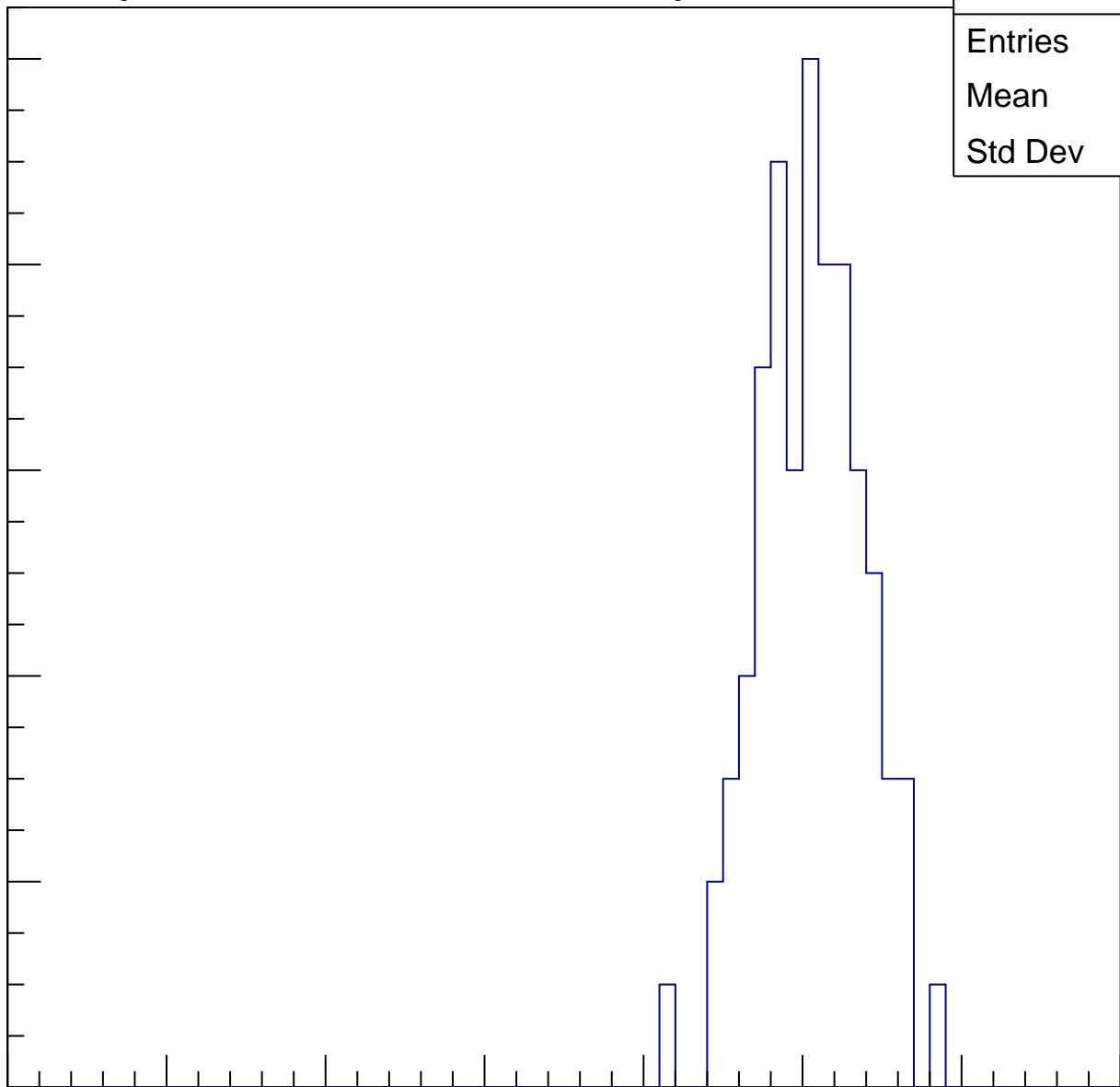
Entries	76
Mean	50.08
Std Dev	3.28

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

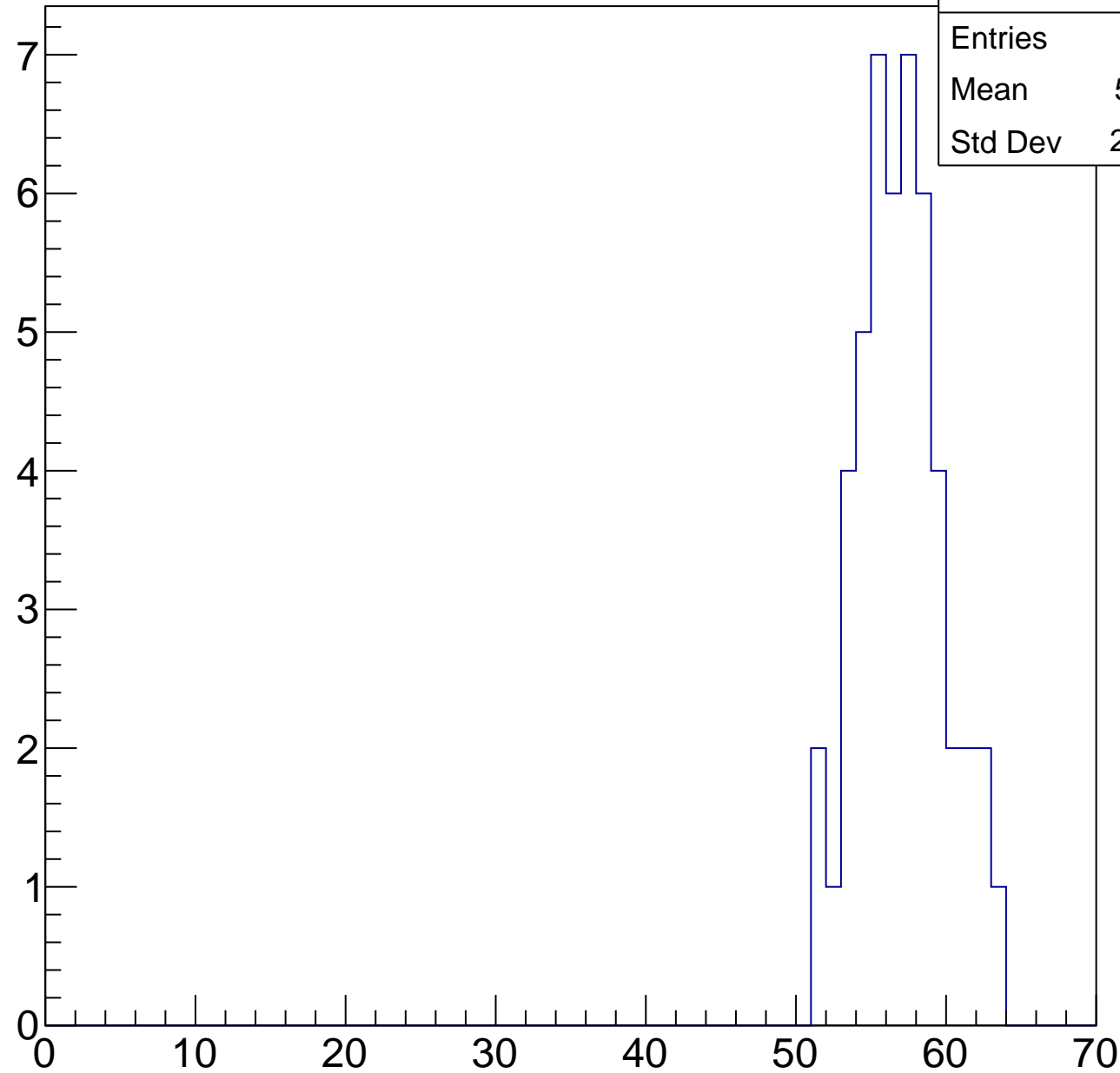
Entry

7
6
5
4
3
2
1
0

Entries	49
Mean	56.51
Std Dev	2.808

ampl

0 10 20 30 40 50 60 70

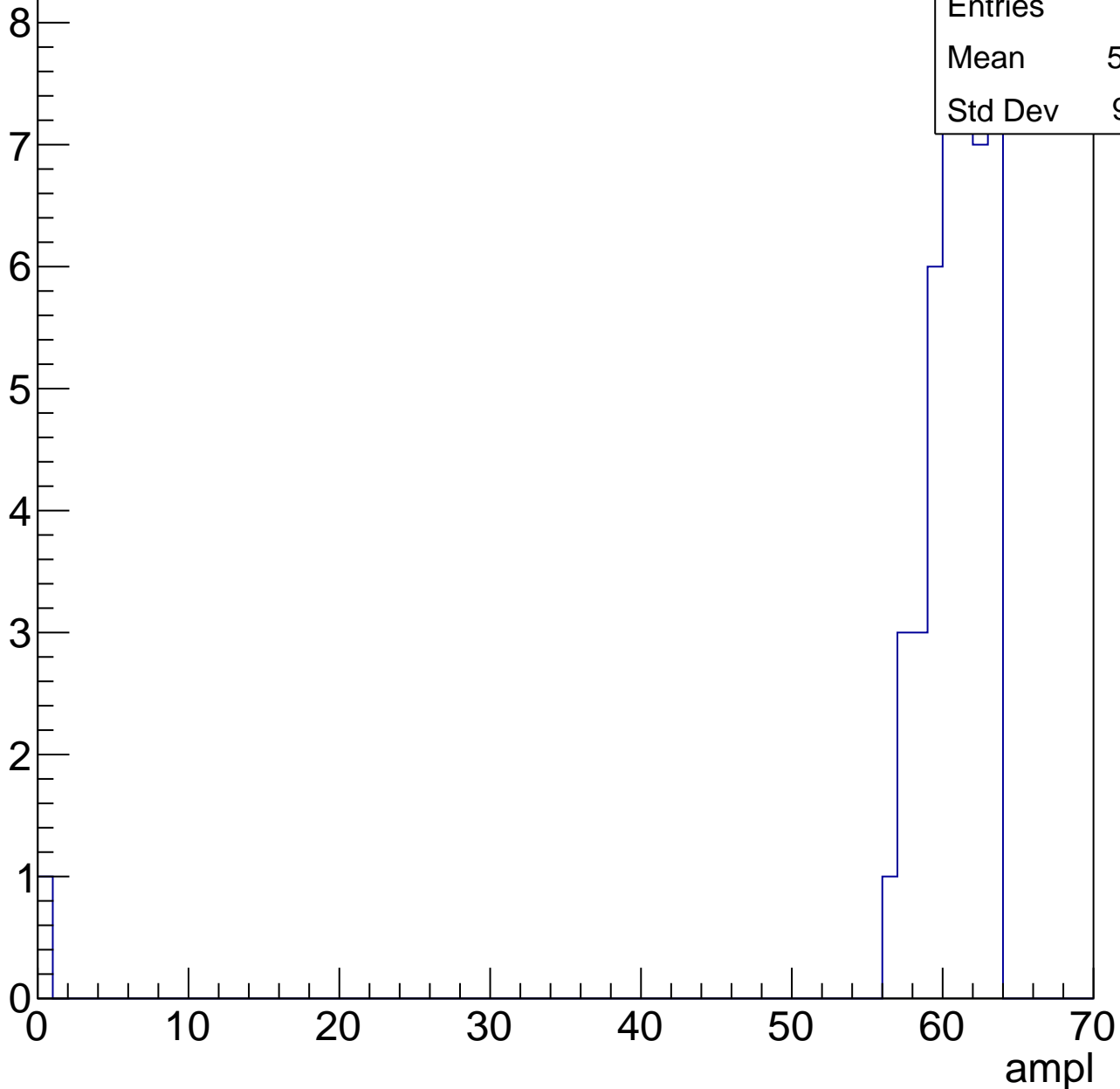


B0L001S, U25-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	59.13
Std Dev	9.111



B0L001S, U25-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch119, adc0

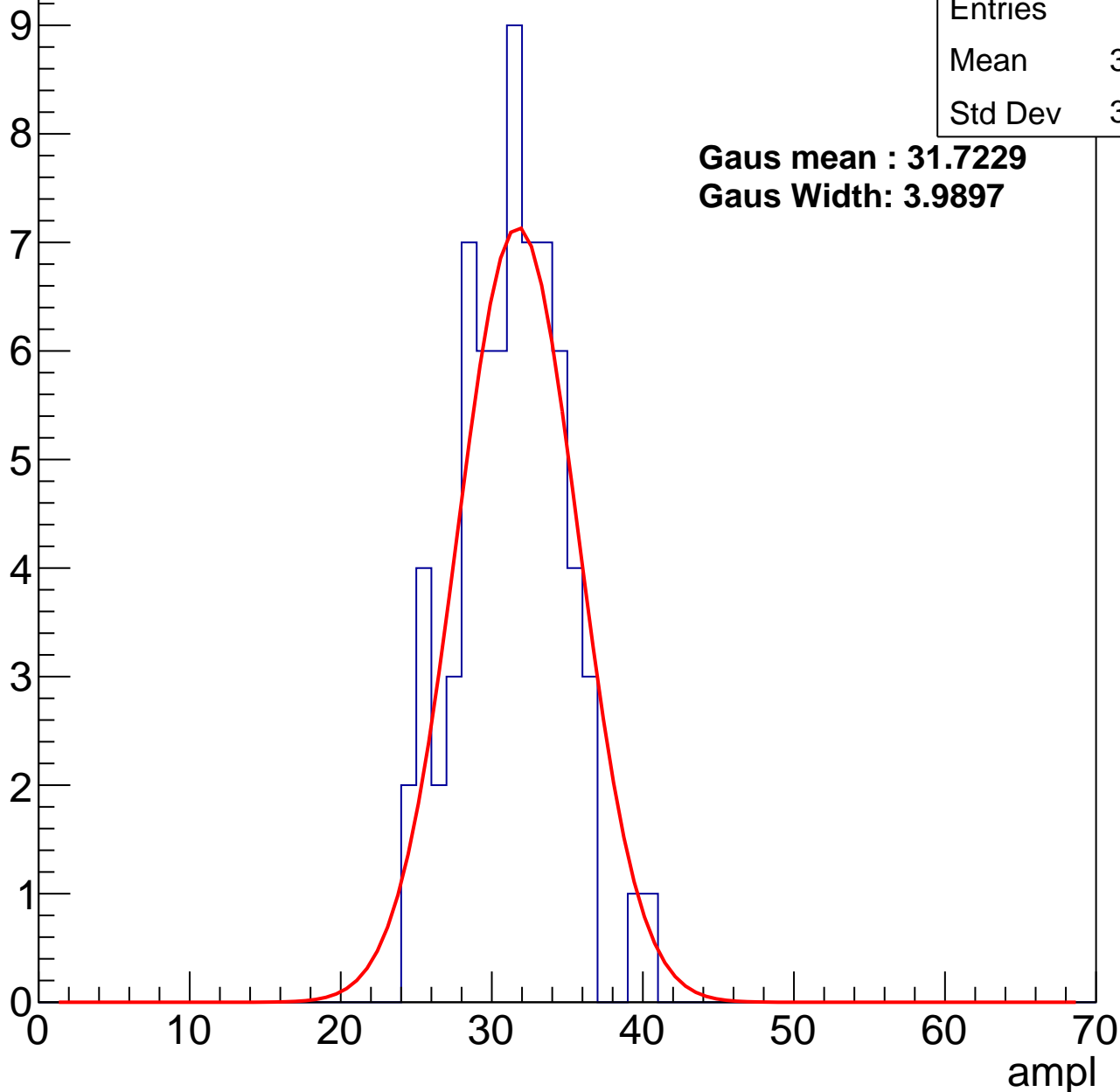
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.82
Std Dev	3.438

Gaus mean : 31.7229

Gaus Width: 3.9897



B0L001S, U25-ch119, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	38.44
Std Dev	4.002

Gaus mean : 38.8899

Gaus Width: 4.0681

Entry

10
8
6
4
2
0

0

10

20

30

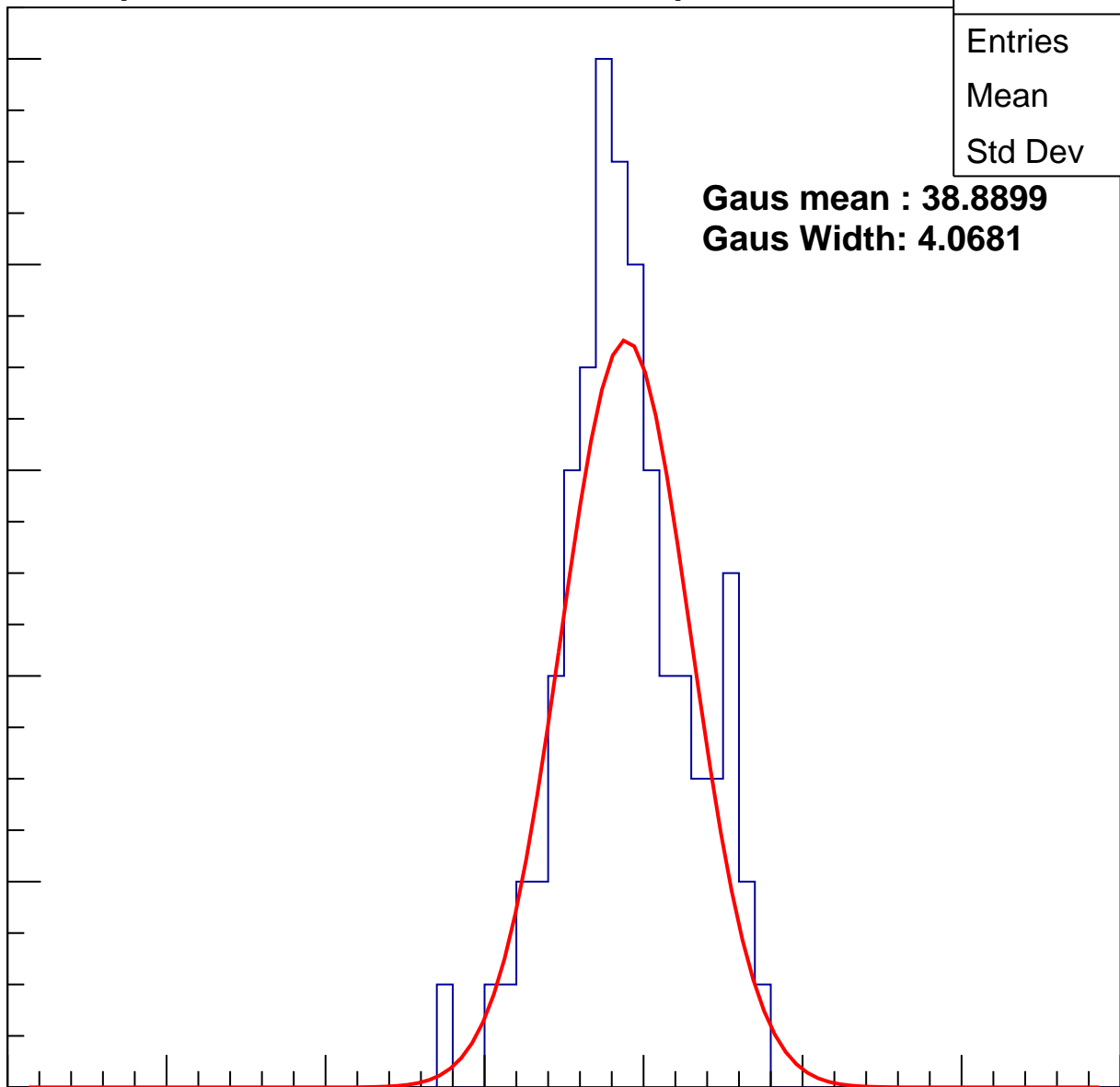
40

50

60

70

ampl

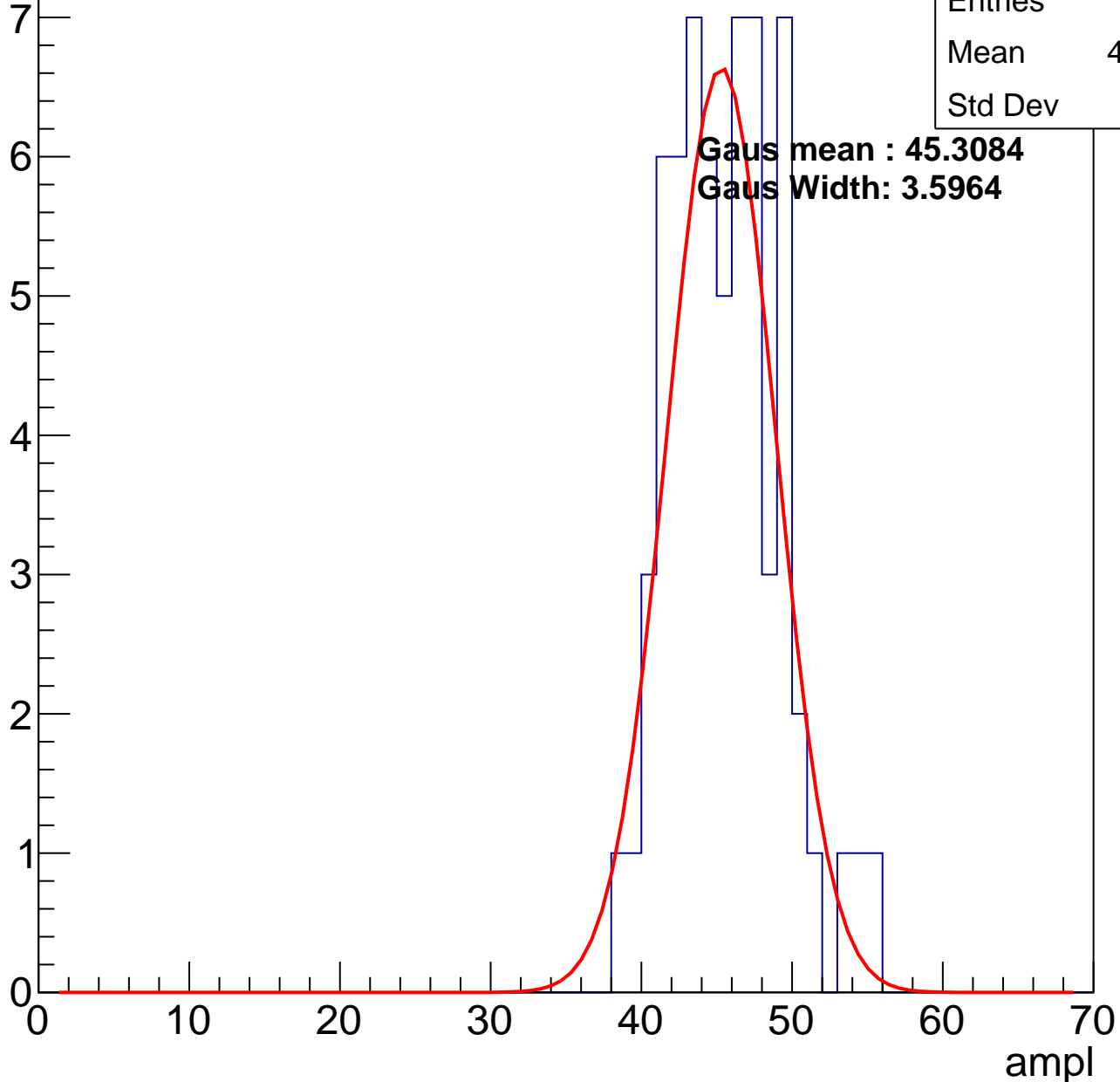


B0L001S, U25-ch119, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.17
Std Dev	3.61

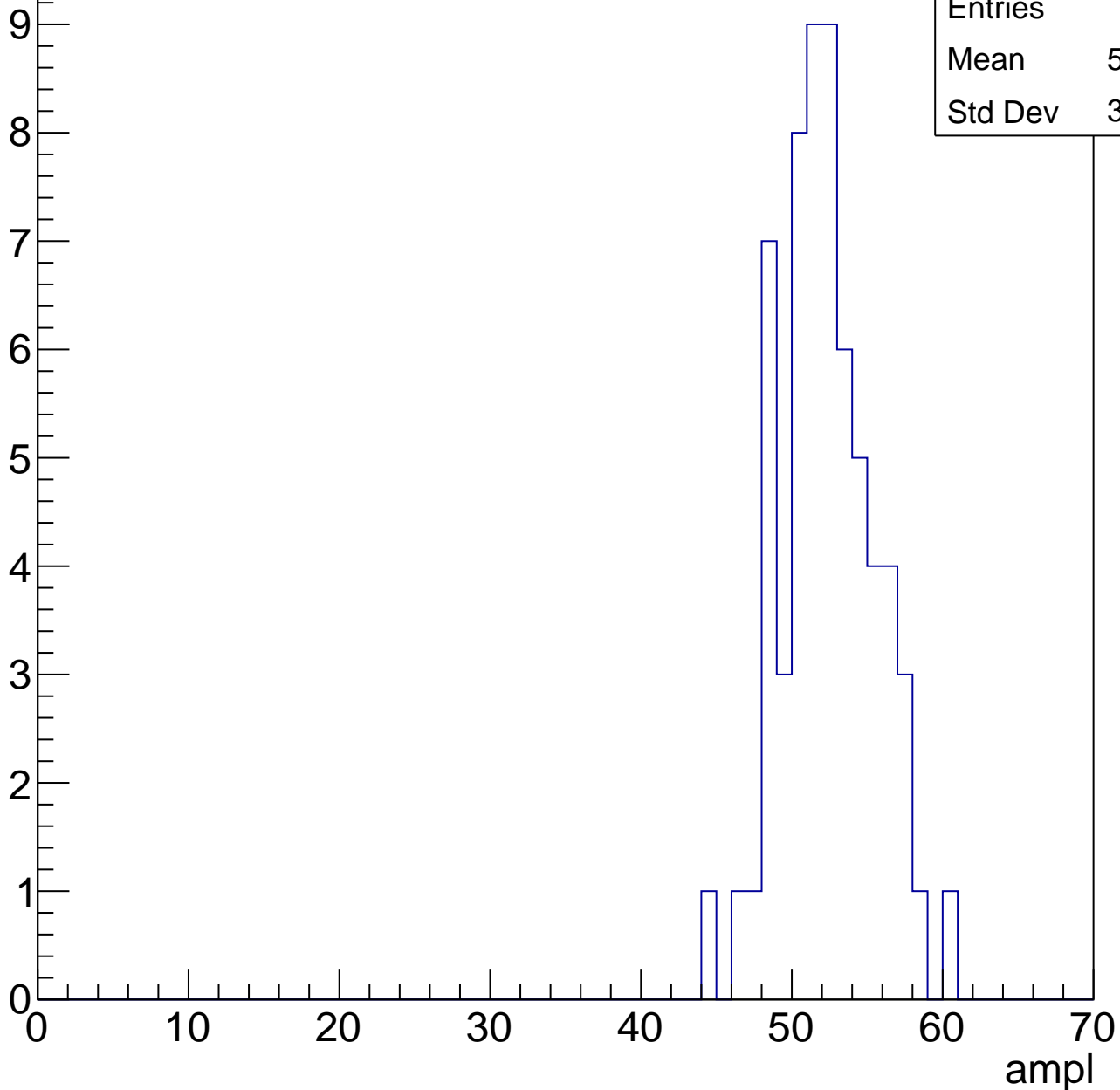


B0L001S, U25-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	51.87
Std Dev	3.099

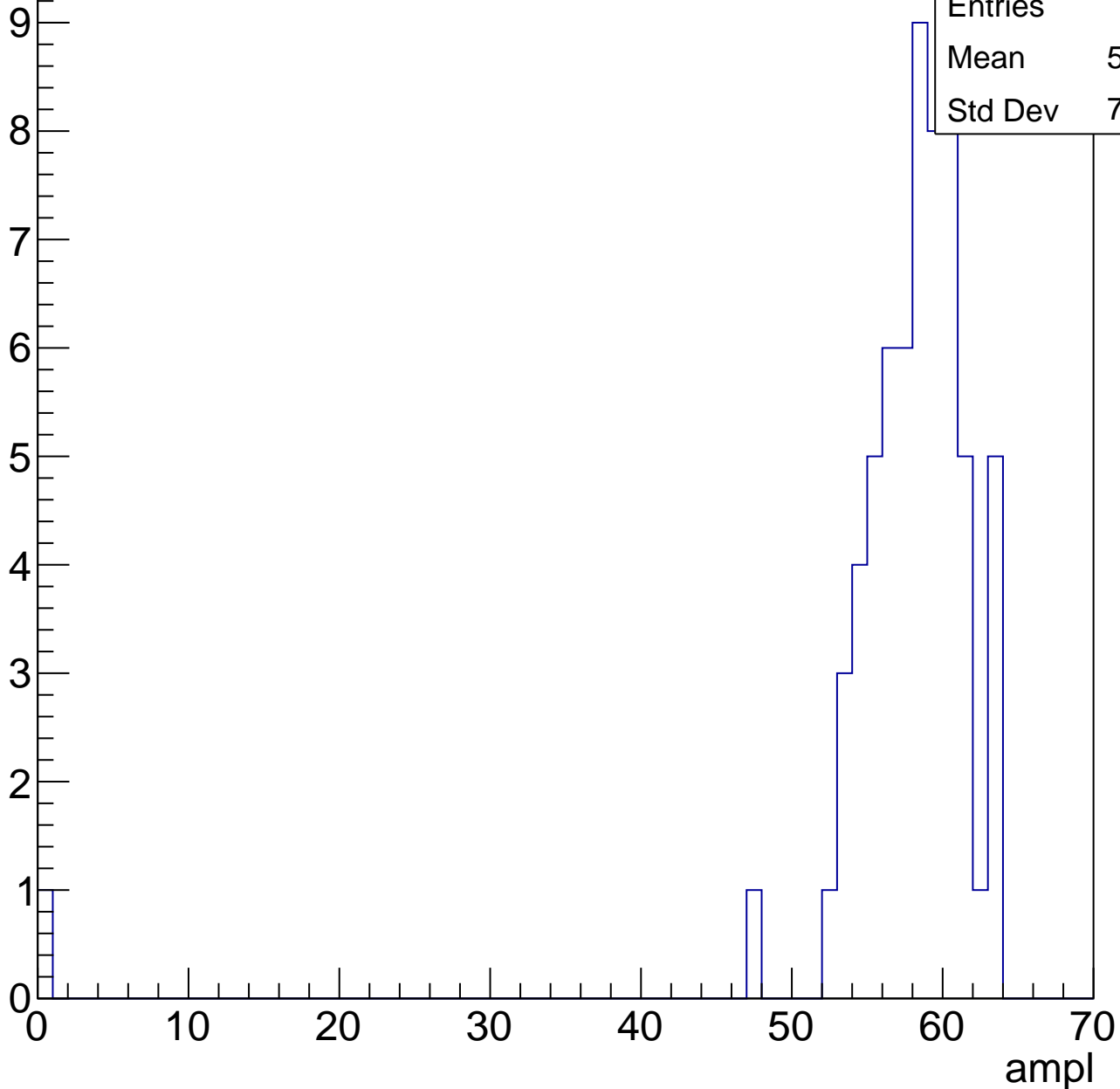


B0L001S, U25-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	56.87
Std Dev	7.849

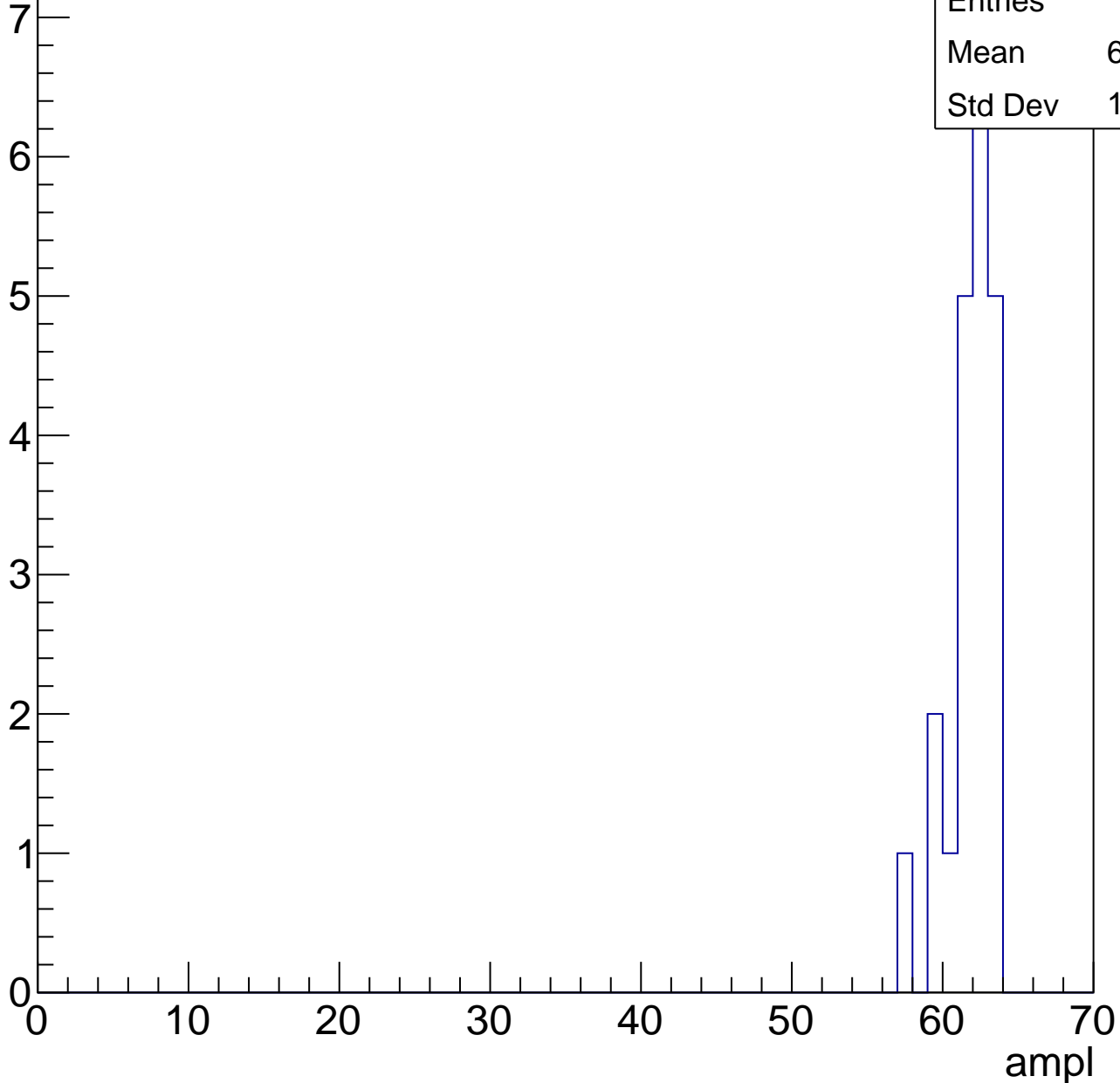


B0L001S, U25-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	61.38
Std Dev	1.527



B0L001S, U25-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch120, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	30.05
Std Dev	5.037

Gaus mean : 30.6479

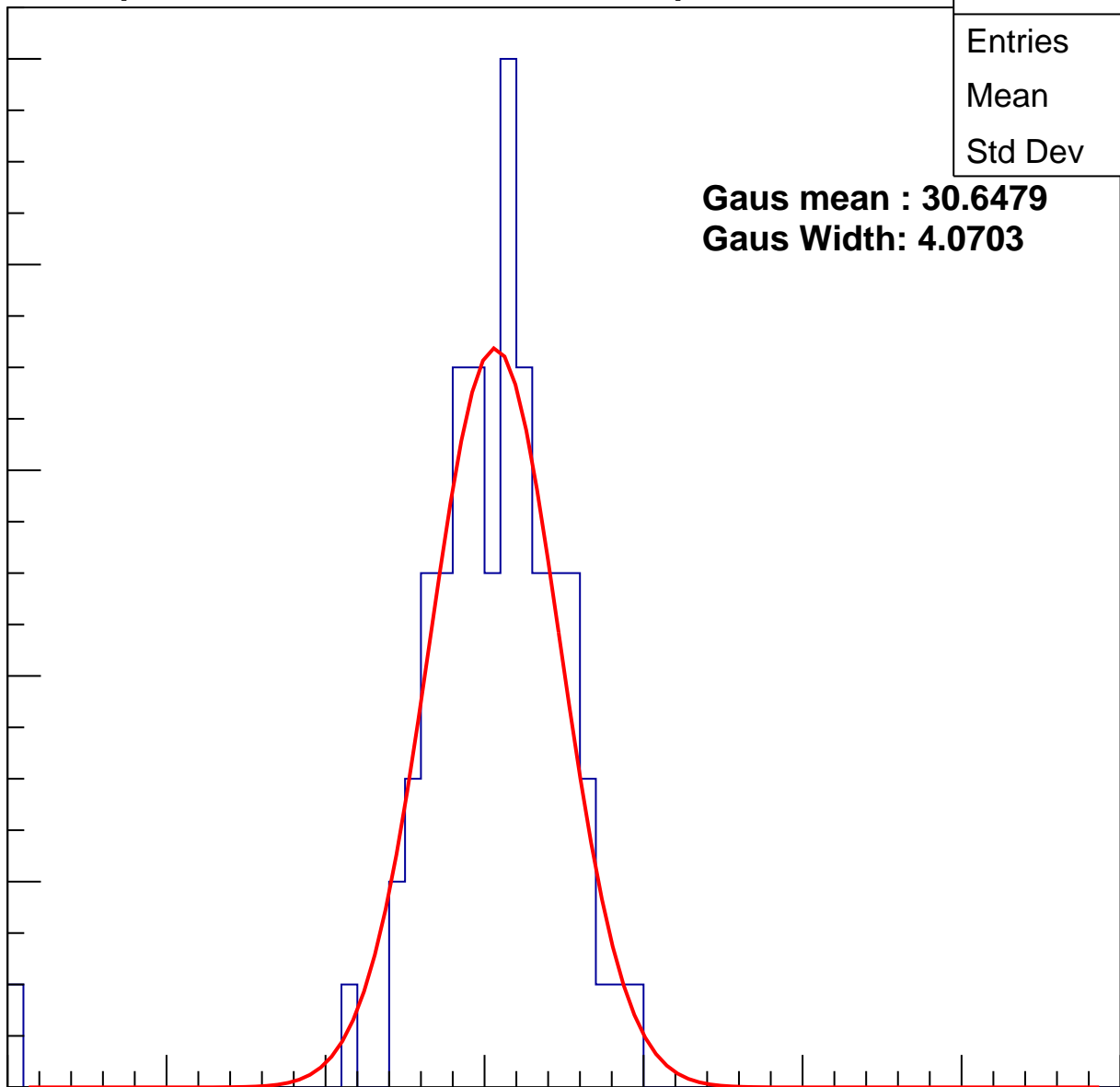
Gaus Width: 4.0703

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U25-ch120, adc1

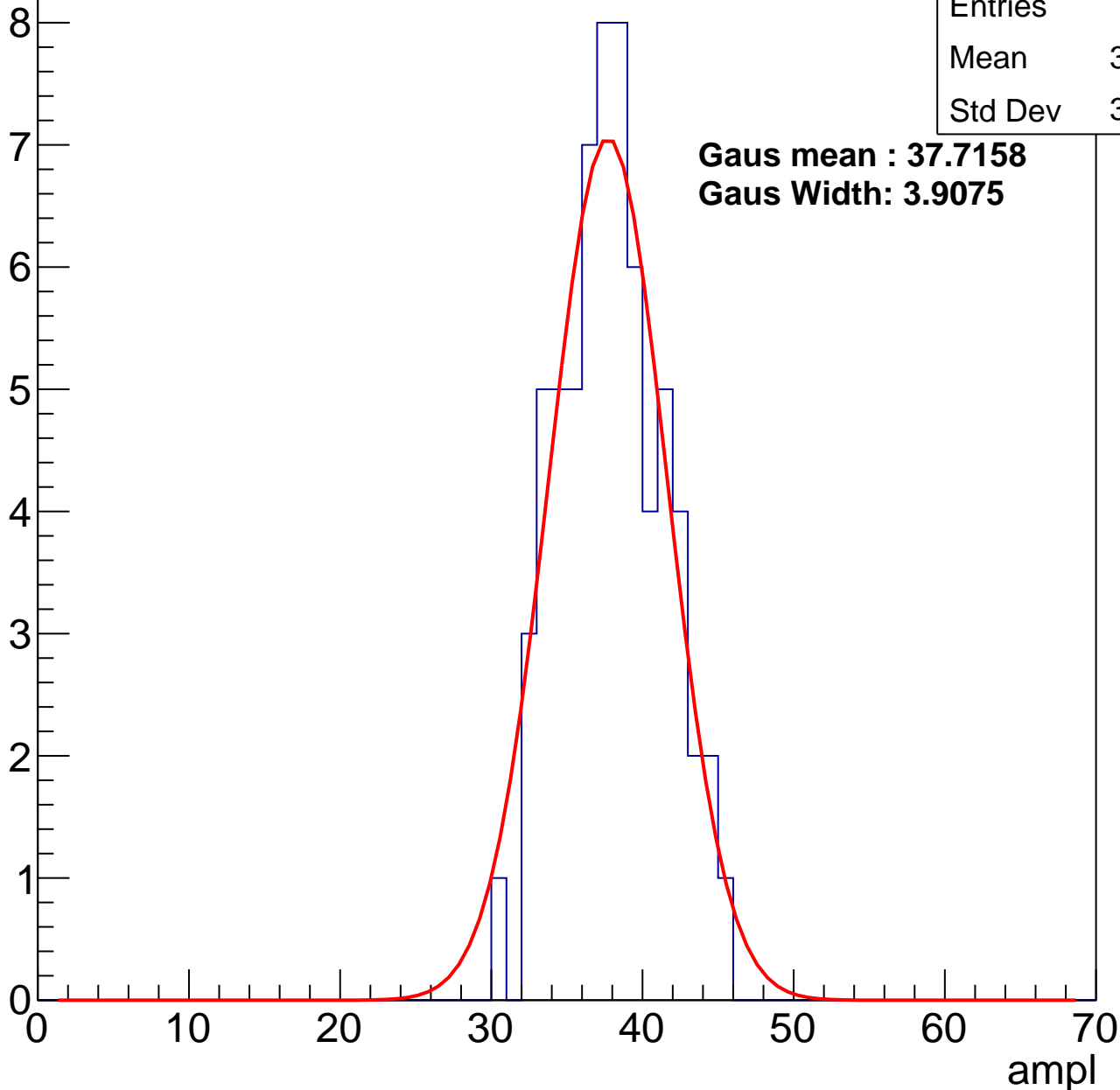
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.48
Std Dev	3.354

Gaus mean : 37.7158

Gaus Width: 3.9075



B0L001S, U25-ch120, adc2

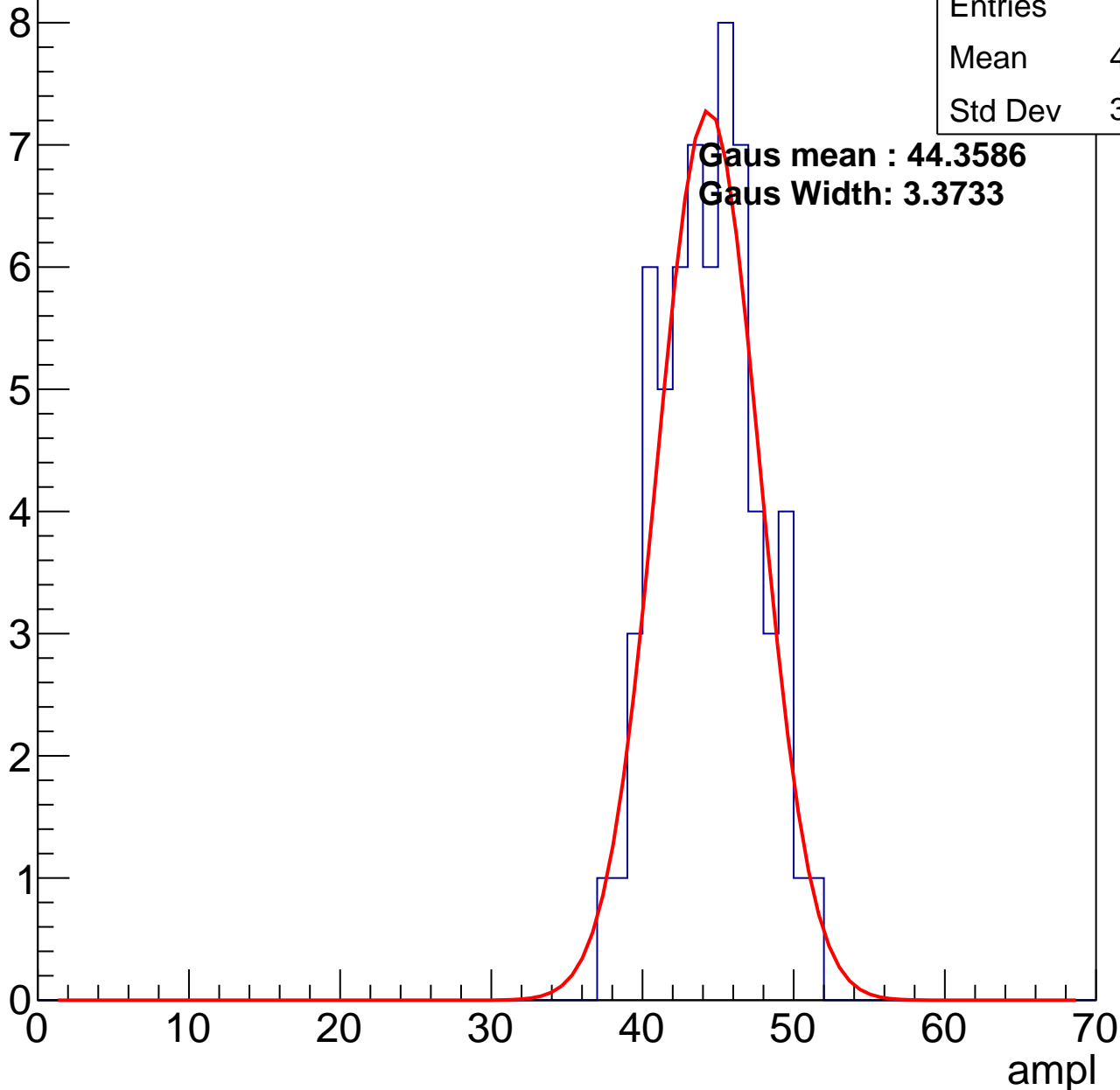
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.89
Std Dev	3.178

Gaus mean : 44.3586

Gaus Width: 3.3733

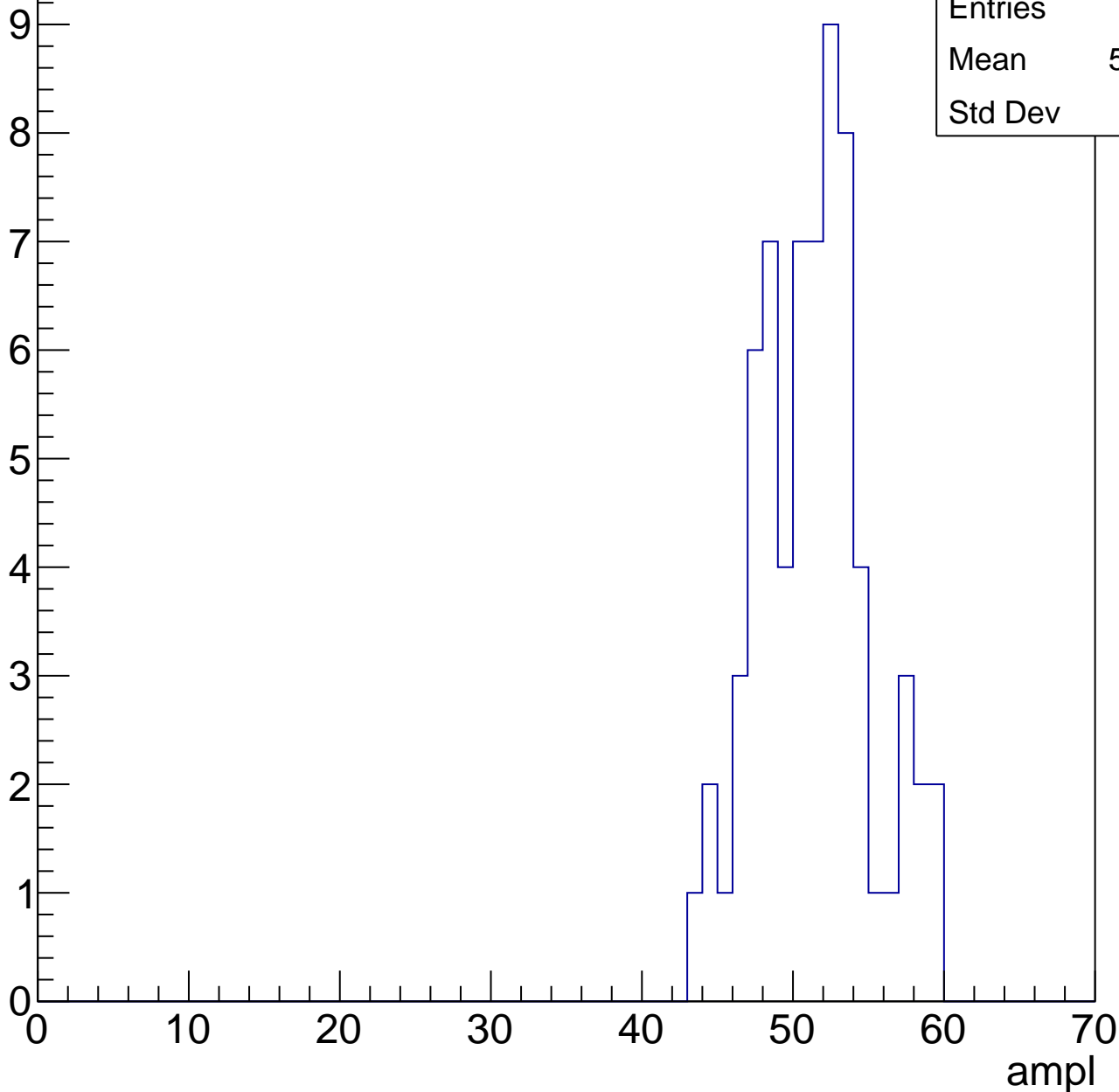


B0L001S, U25-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.87
Std Dev	3.65

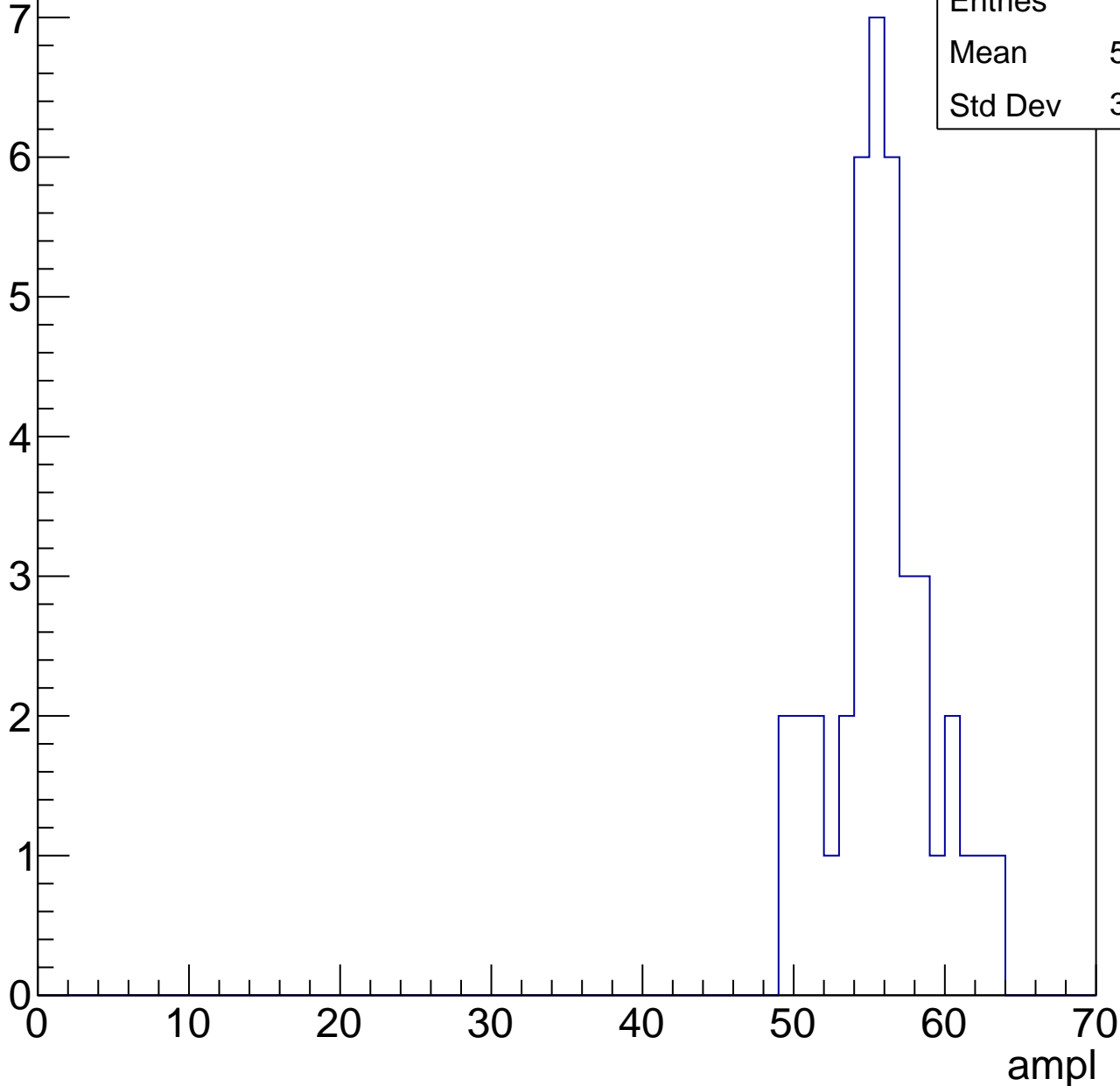


B0L001S, U25-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

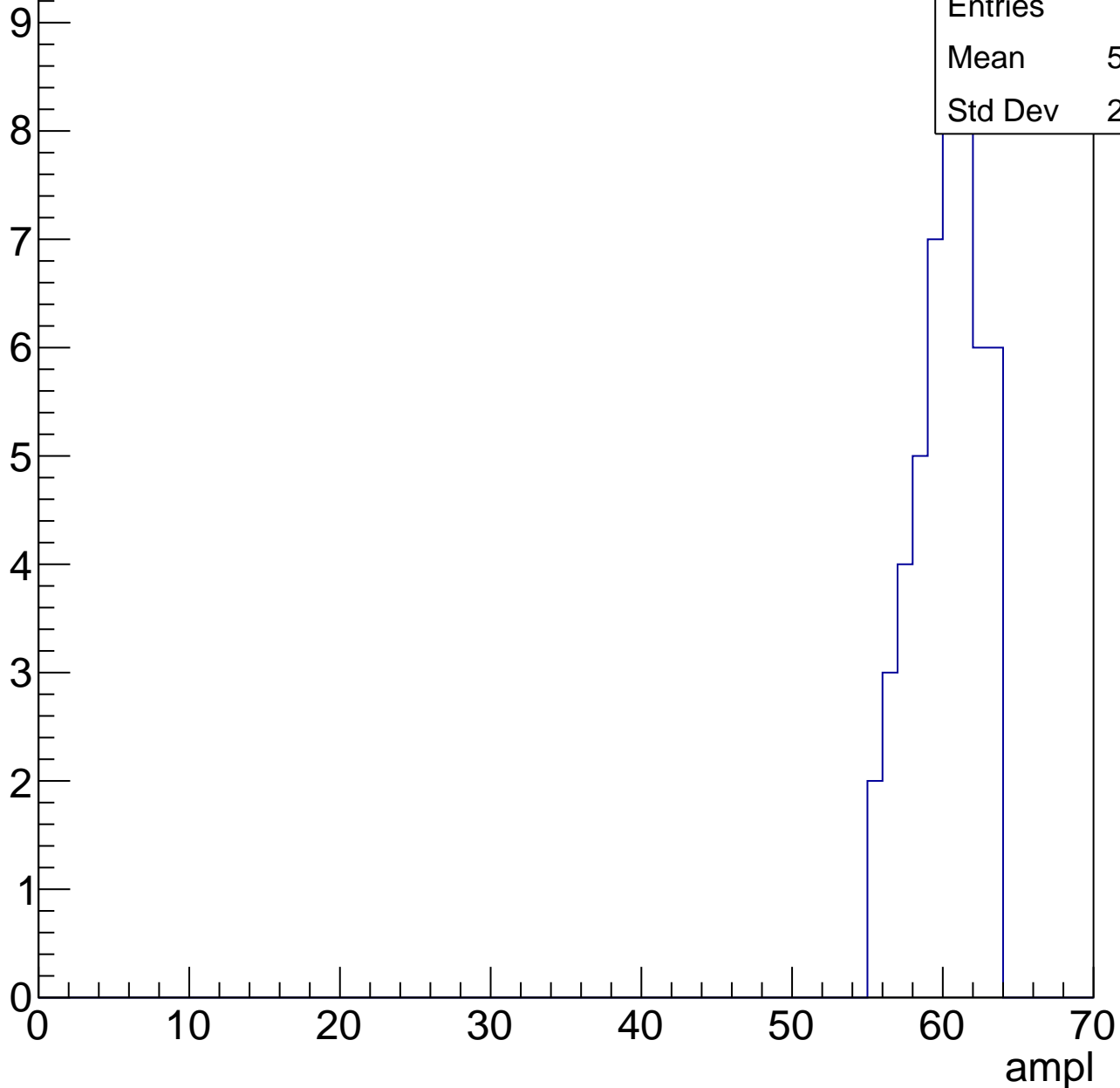
Entries	40
Mean	55.33
Std Dev	3.289



B0L001S, U25-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

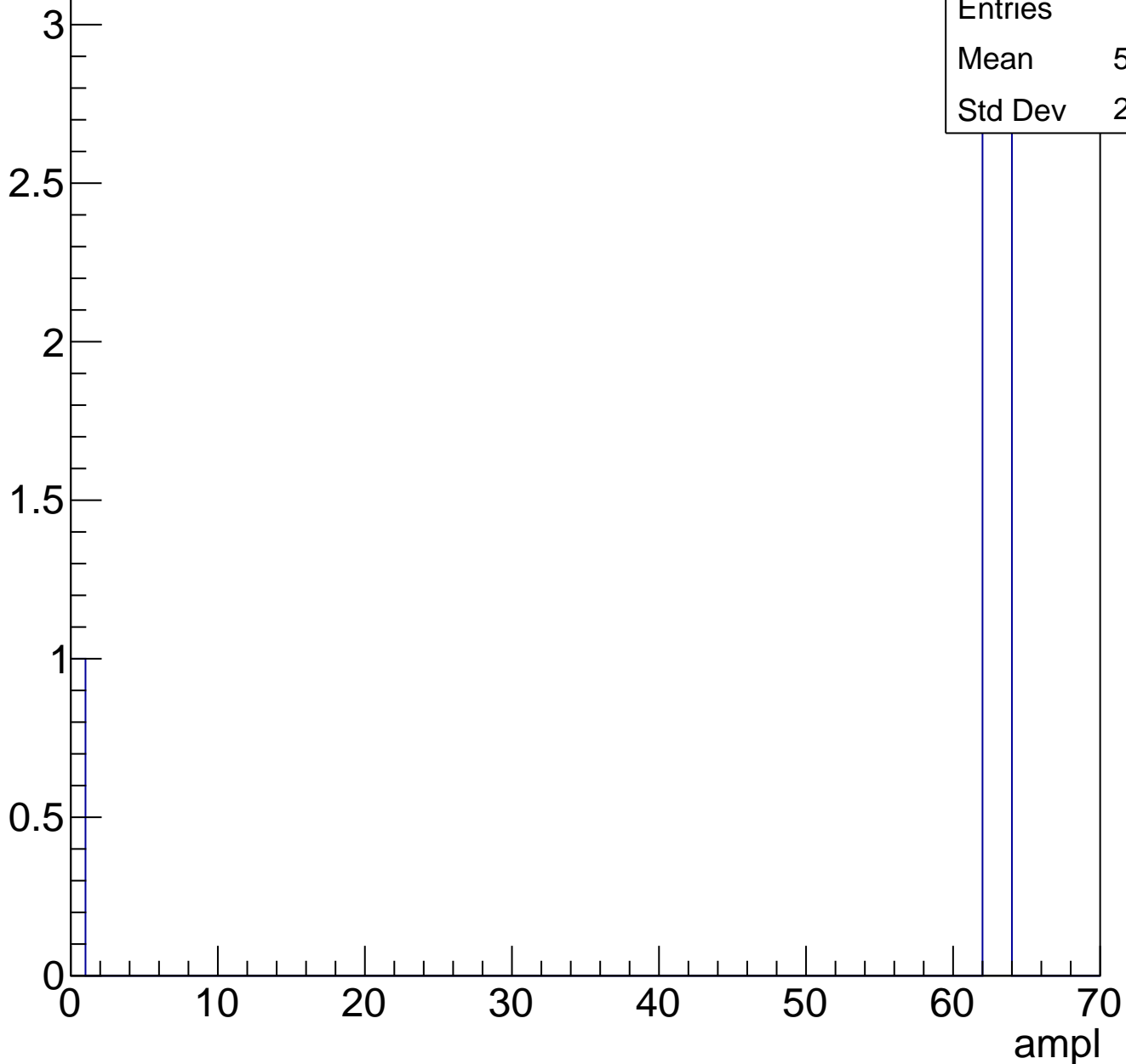
Entry



B0L001S, U25-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch121, adc0

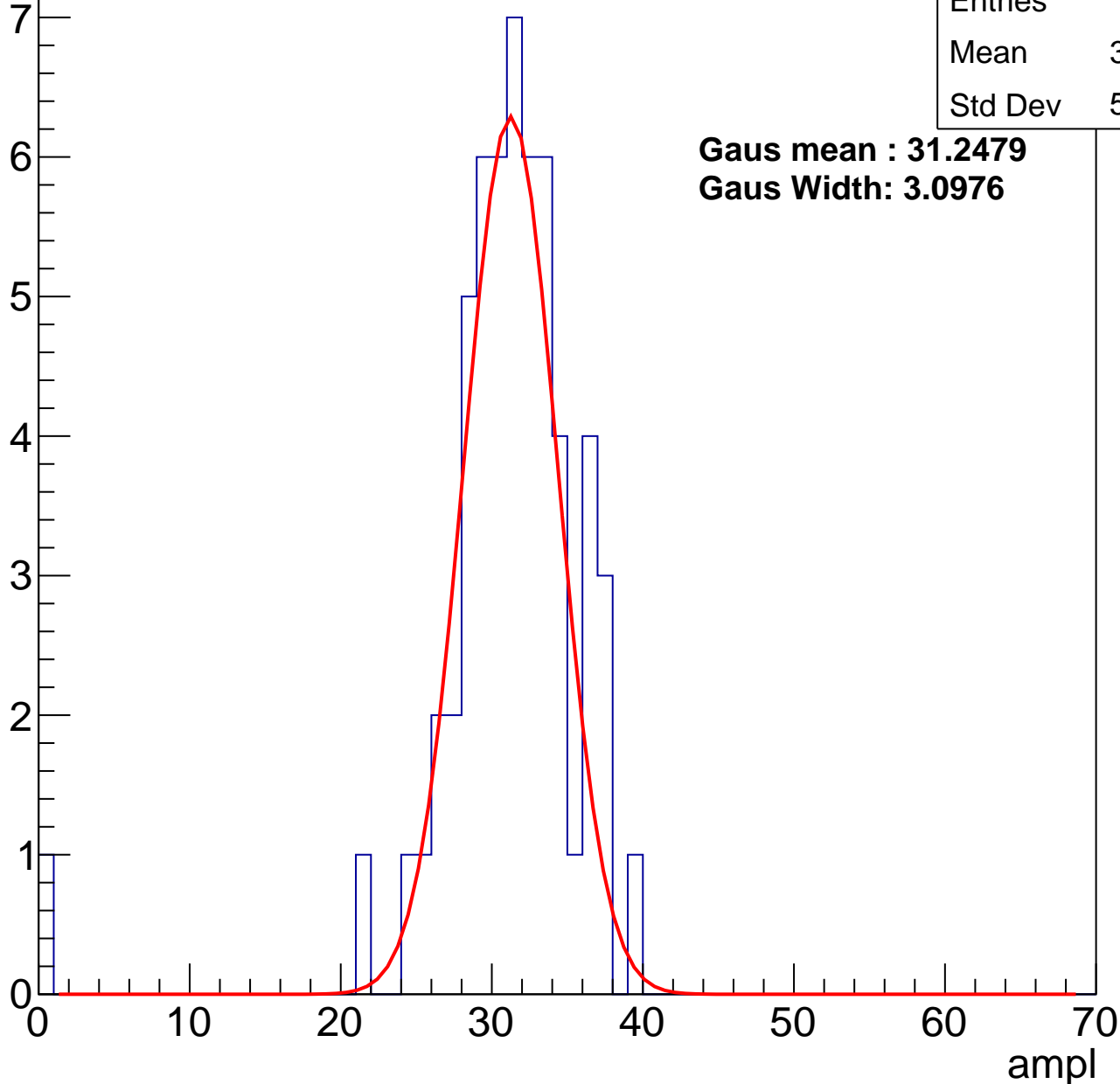
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.56
Std Dev	5.374

Gaus mean : 31.2479

Gaus Width: 3.0976



B0L001S, U25-ch121, adc1

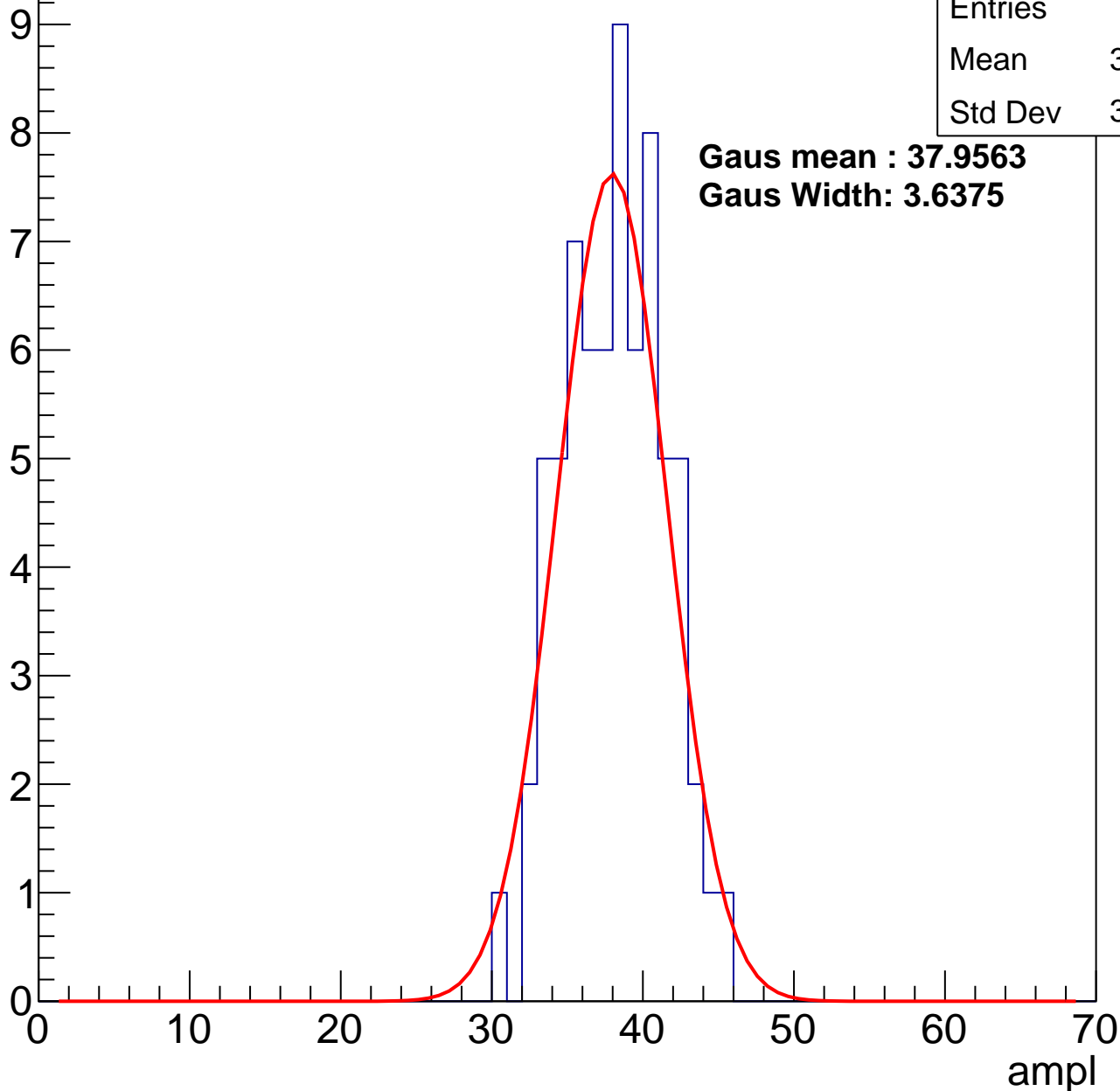
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.65
Std Dev	3.238

Gaus mean : 37.9563

Gaus Width: 3.6375



B0L001S, U25-ch121, adc2

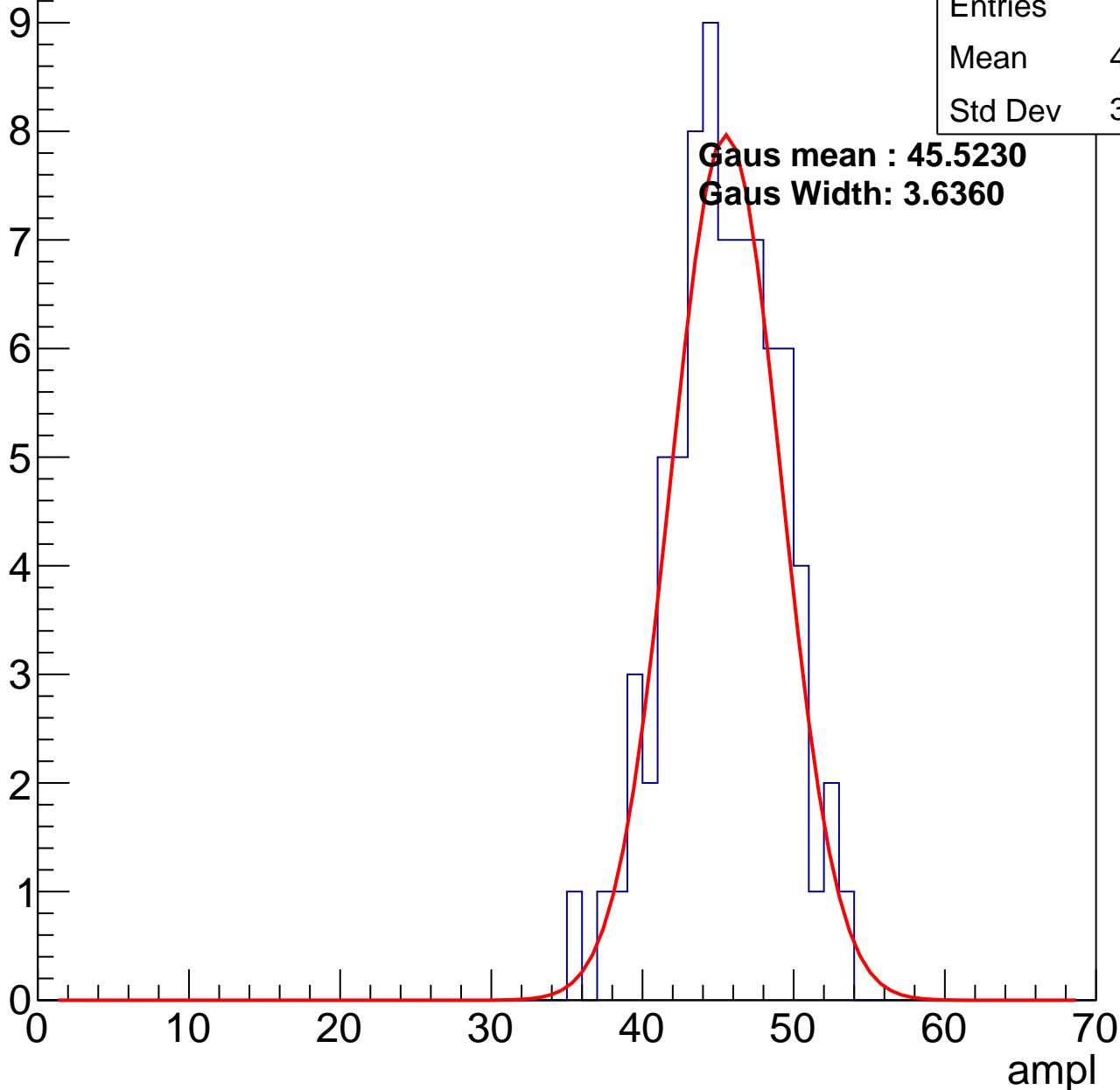
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.97
Std Dev	3.663

Gaus mean : 45.5230

Gaus Width: 3.6360

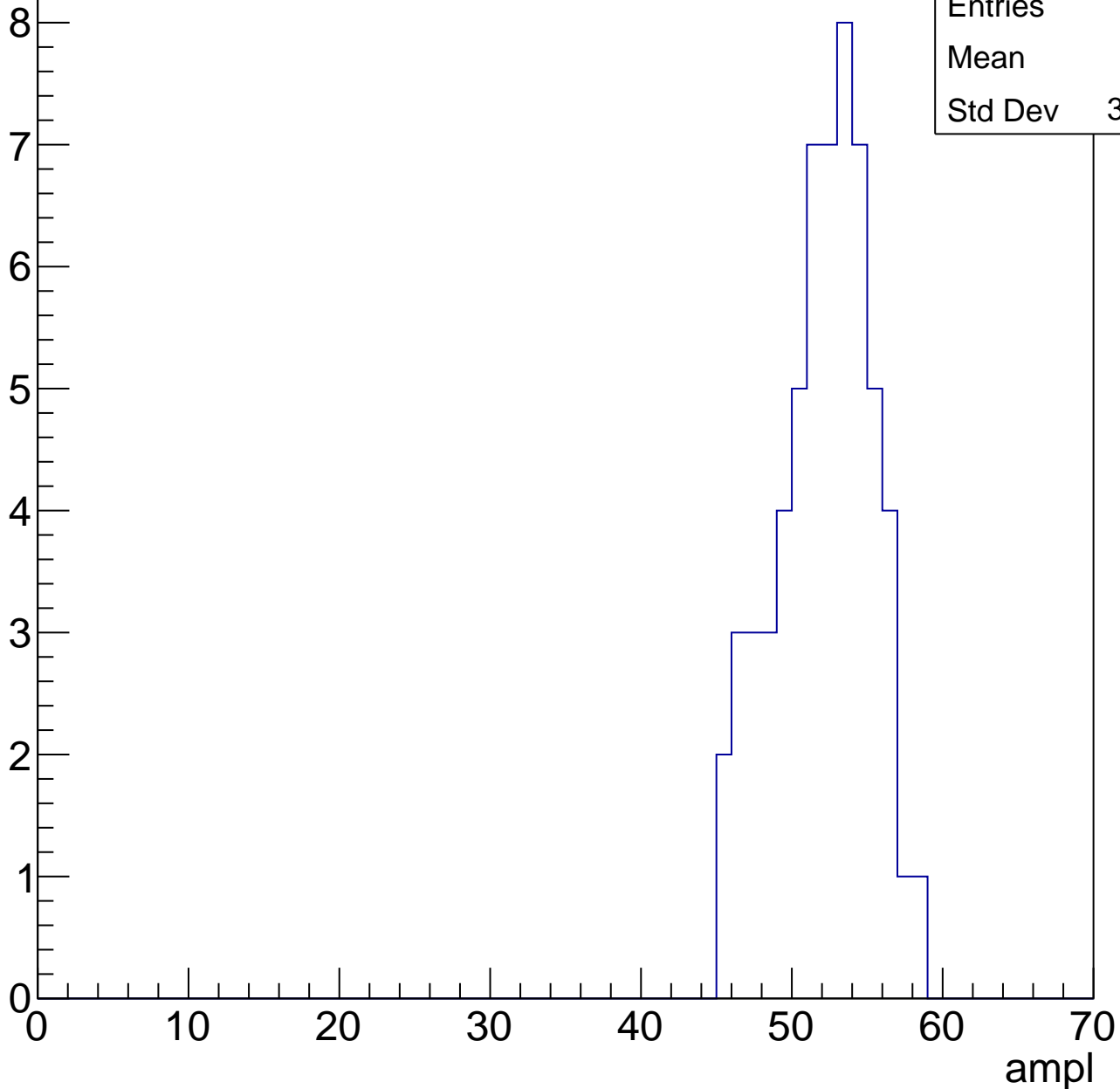


B0L001S, U25-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	51.6
Std Dev	3.132

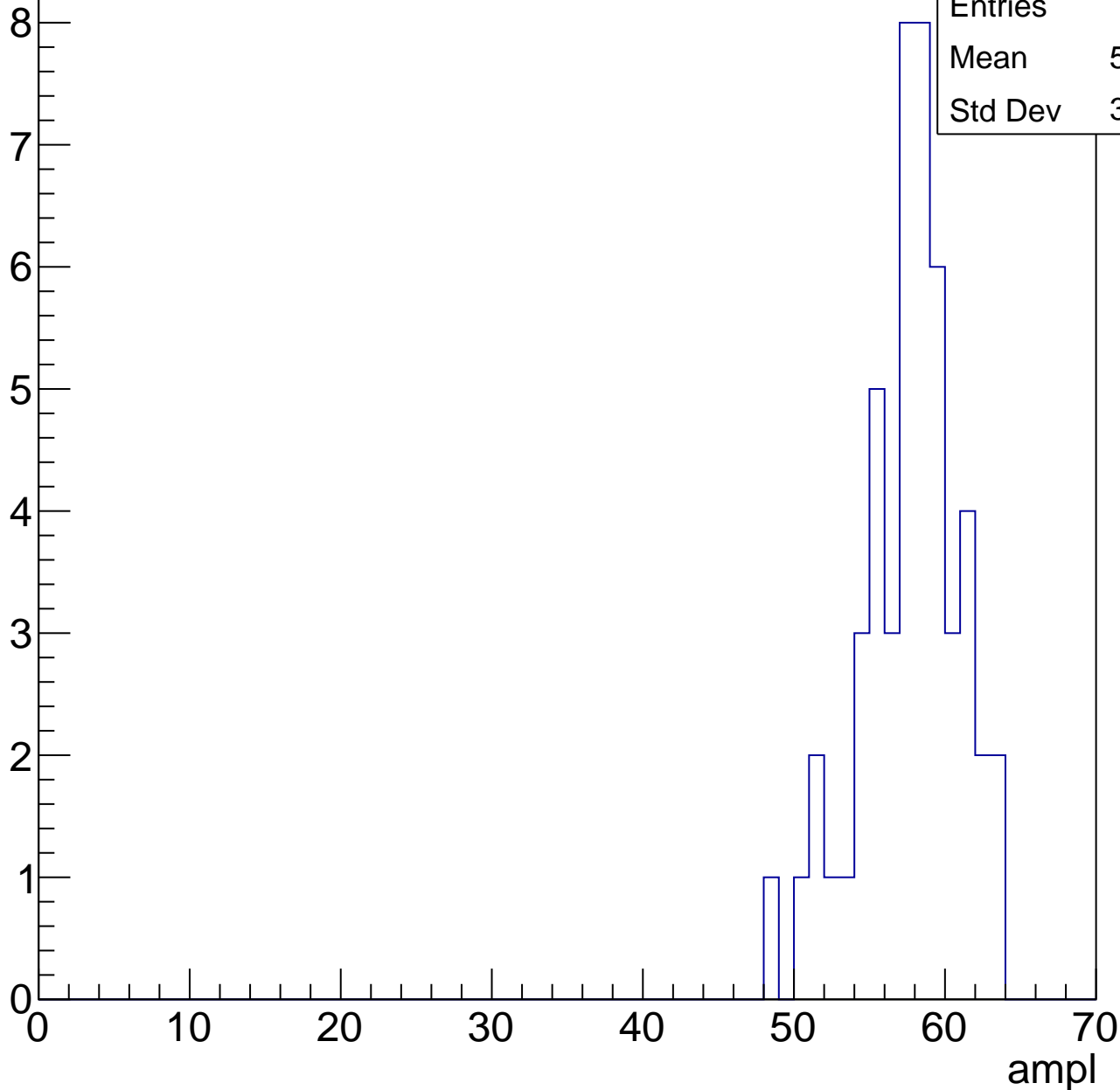


B0L001S, U25-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

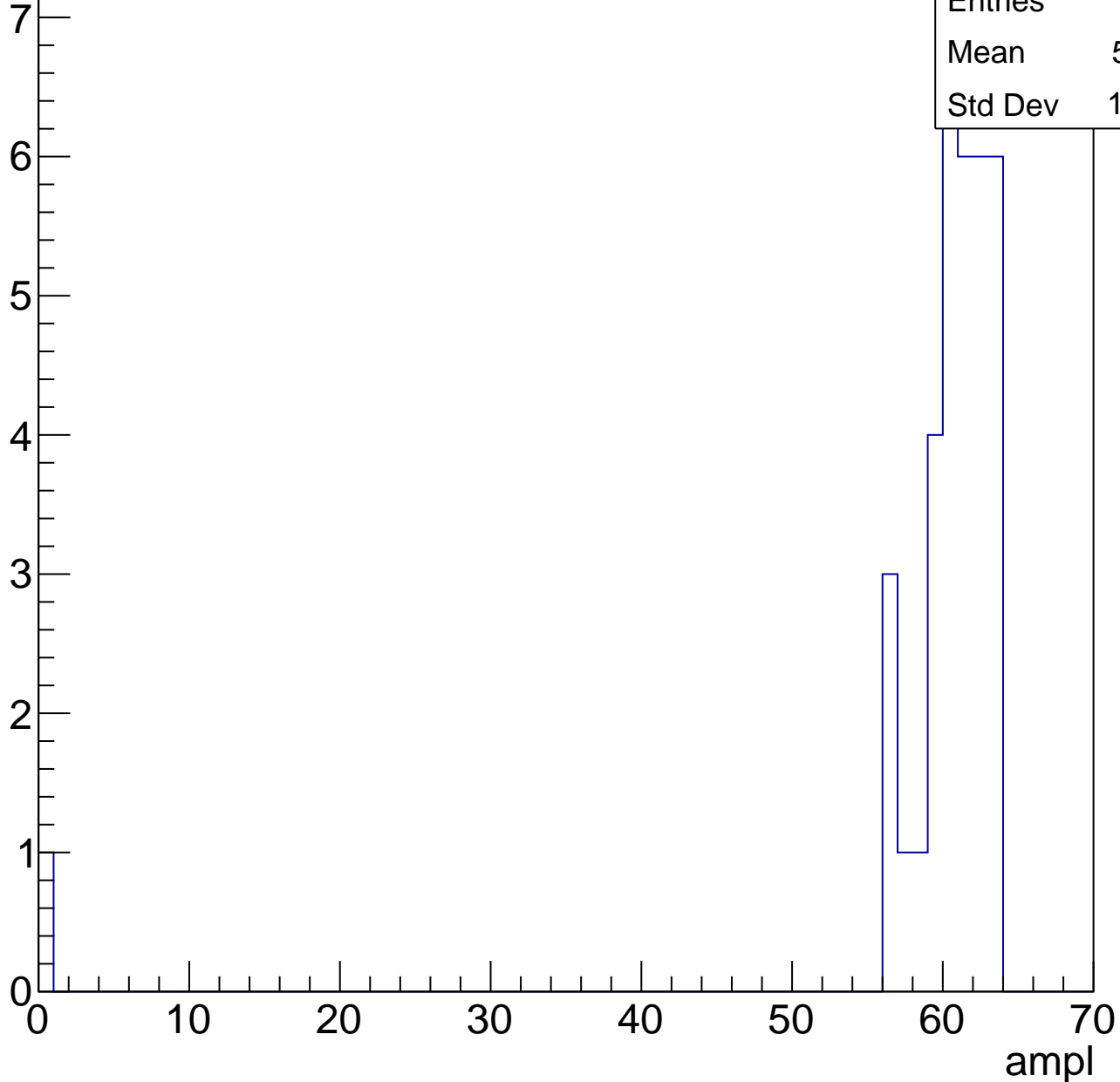
Entries	50
Mean	57.16
Std Dev	3.276



B0L001S, U25-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U25-ch122, adc0

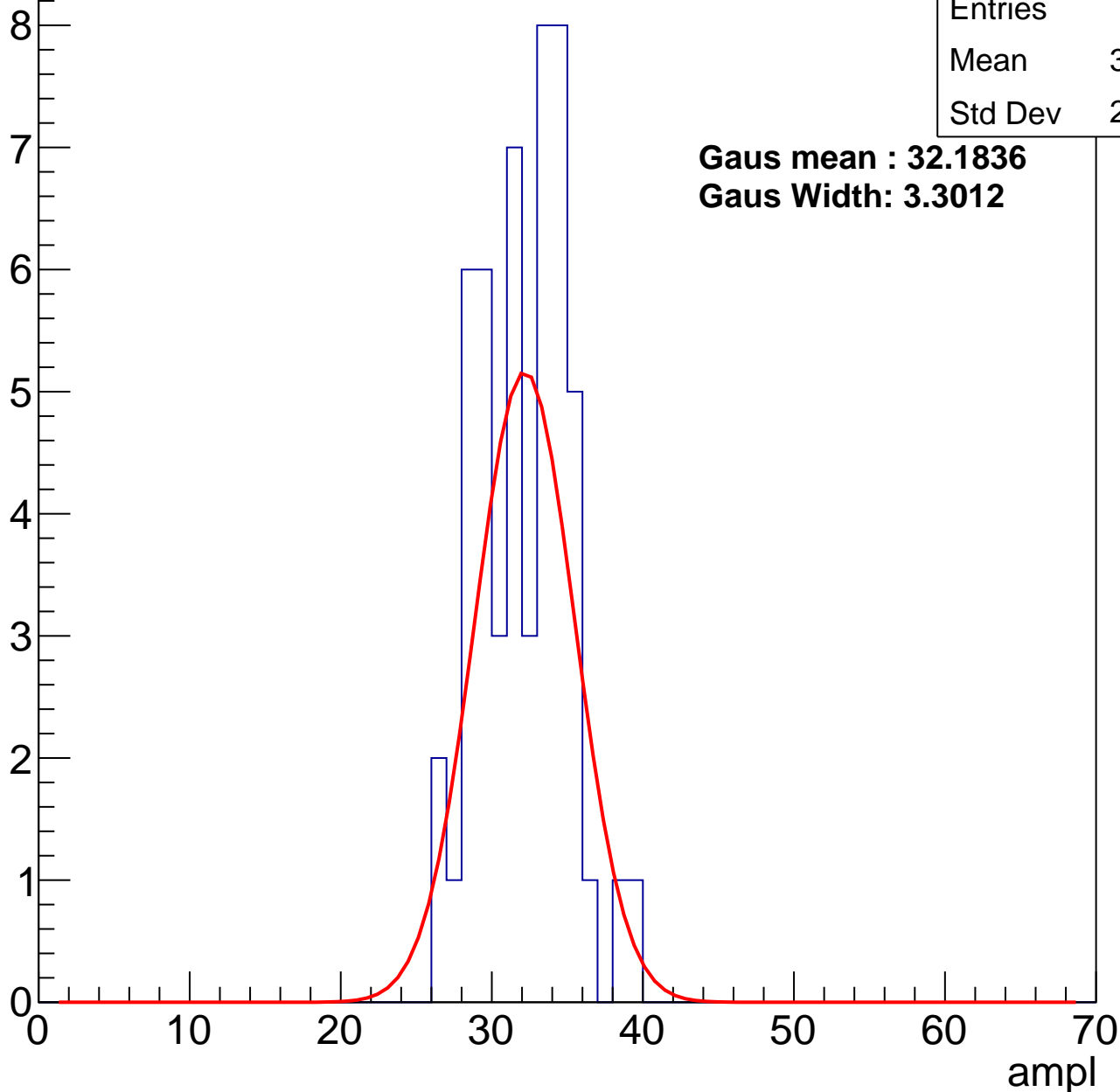
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	31.69
Std Dev	2.932

Gaus mean : 32.1836

Gaus Width: 3.3012



B0L001S, U25-ch122, adc1

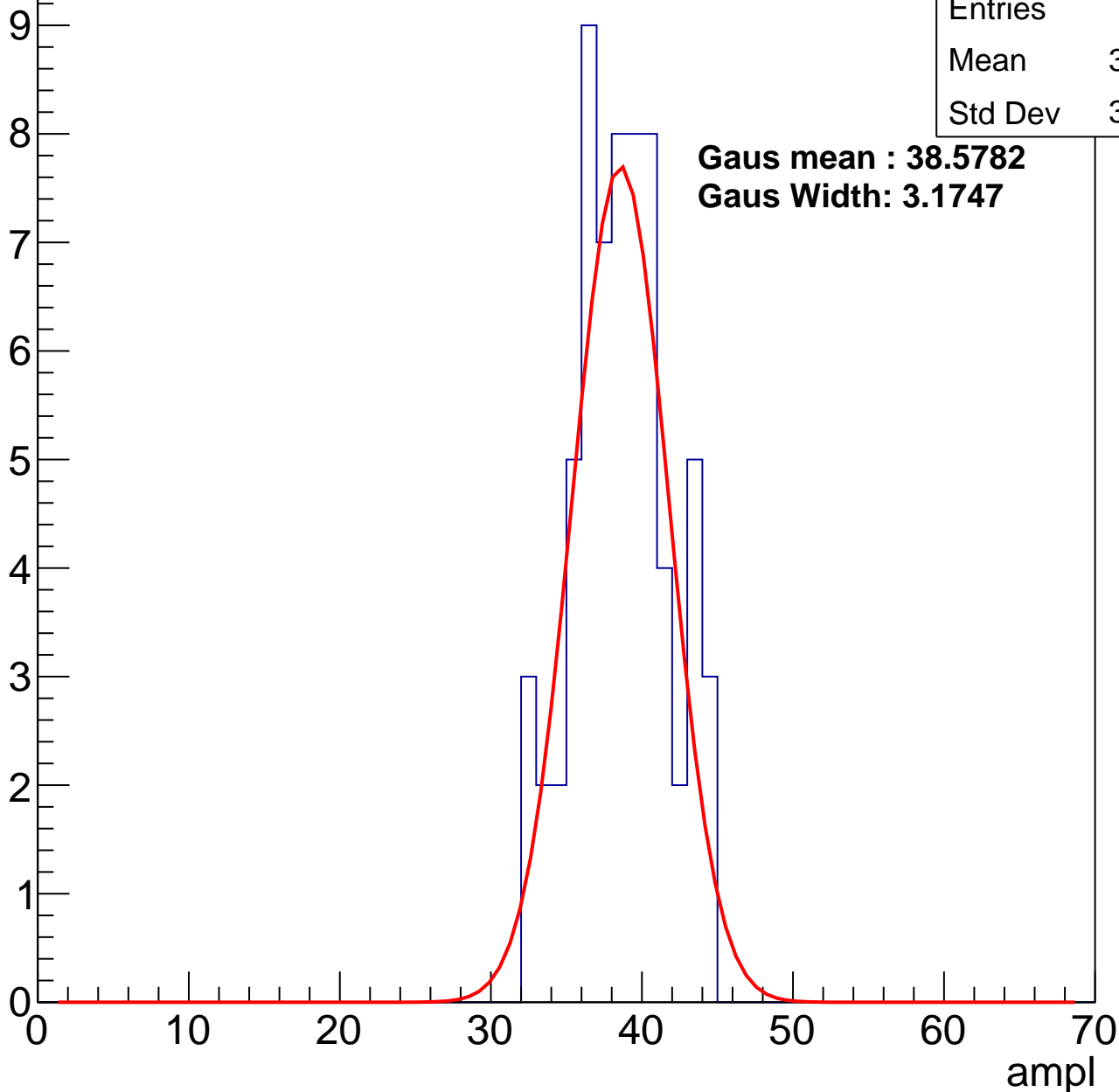
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.17
Std Dev	3.058

Gaus mean : 38.5782

Gaus Width: 3.1747



B0L001S, U25-ch122, adc2

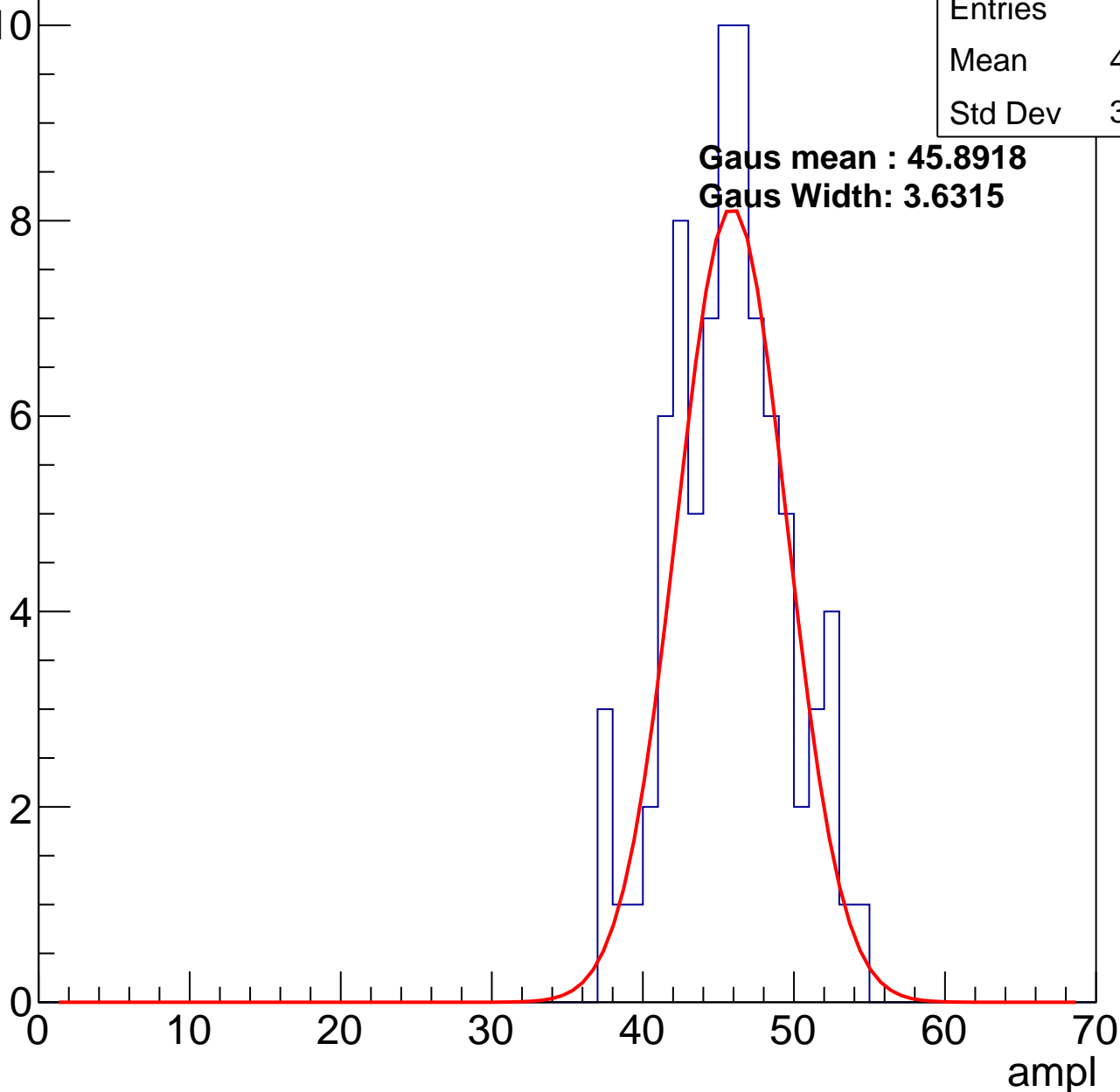
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	45.28
Std Dev	3.804

Gaus mean : 45.8918

Gaus Width: 3.6315

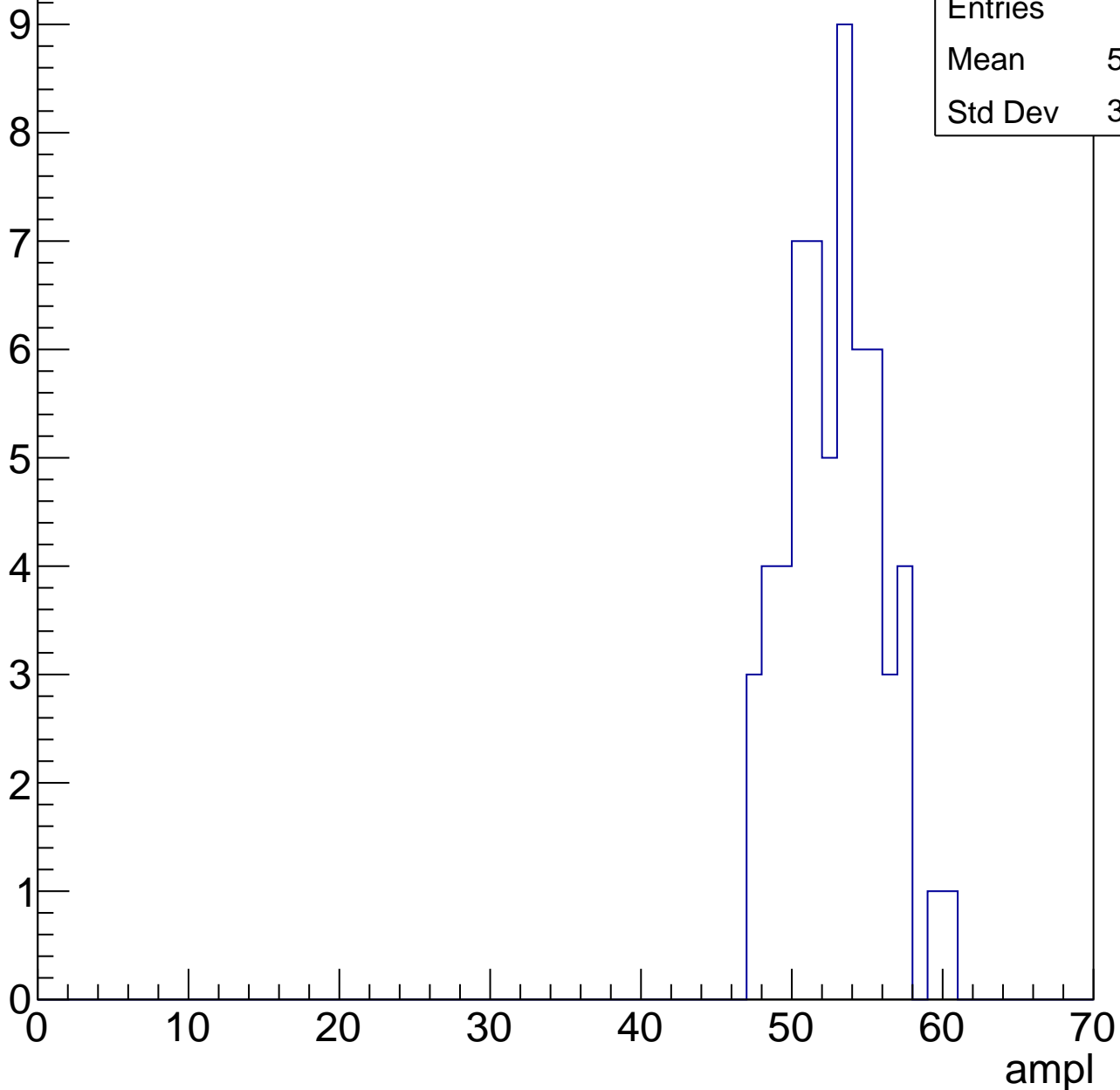


B0L001S, U25-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	52.37
Std Dev	3.027

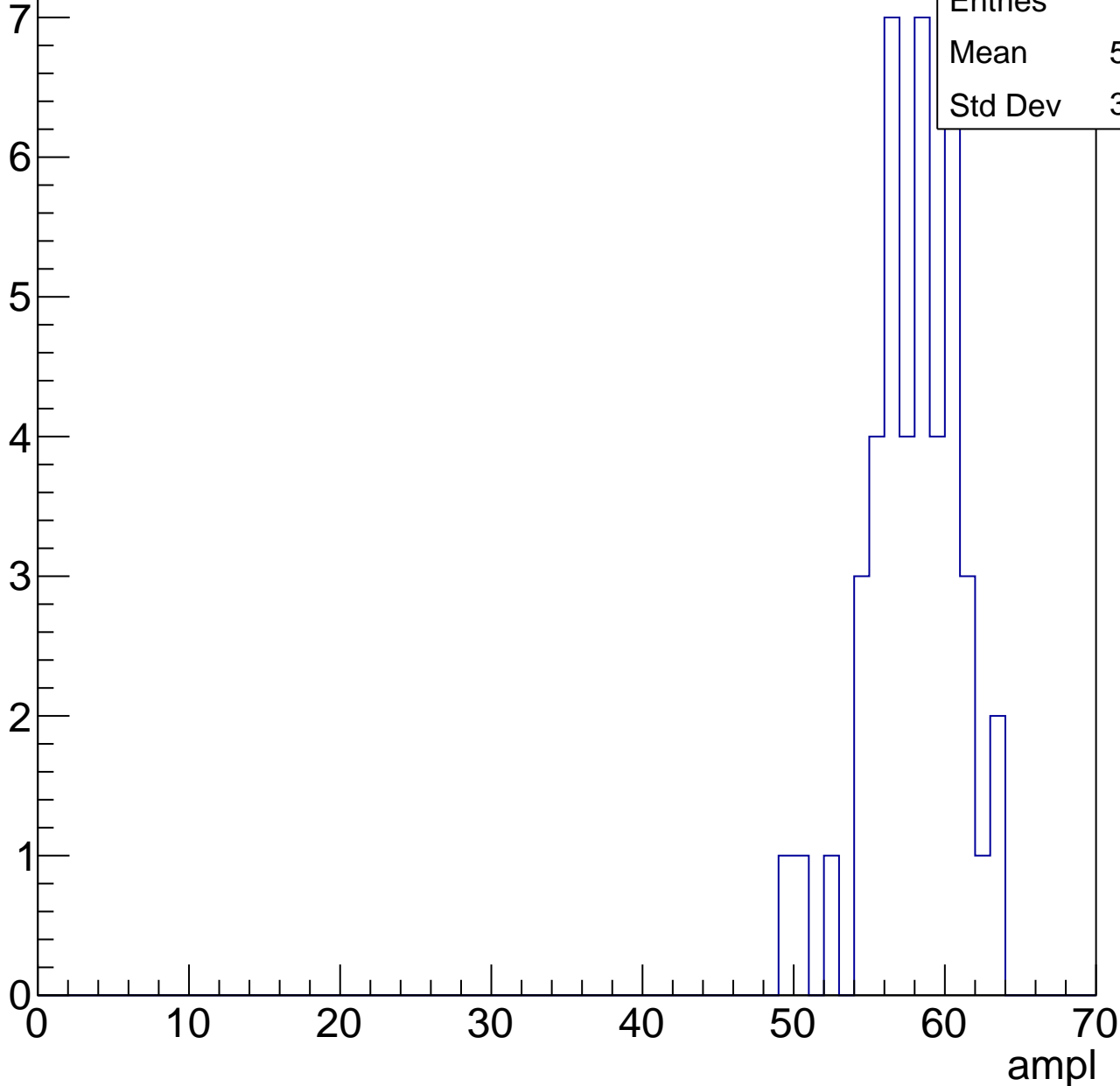


B0L001S, U25-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.47
Std Dev	3.016

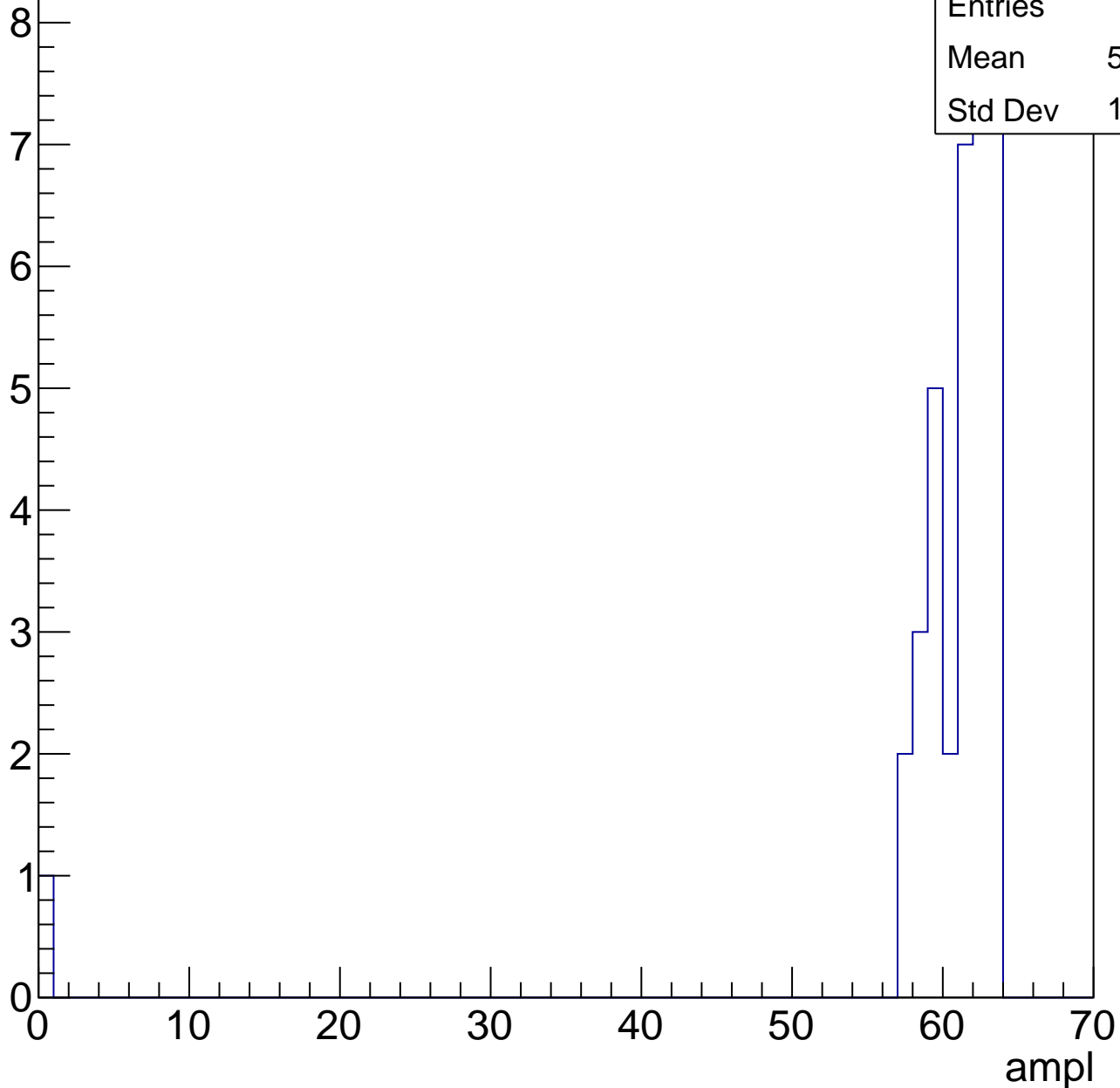


B0L001S, U25-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.17
Std Dev	10.17



B0L001S, U25-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U25-ch123, adc0

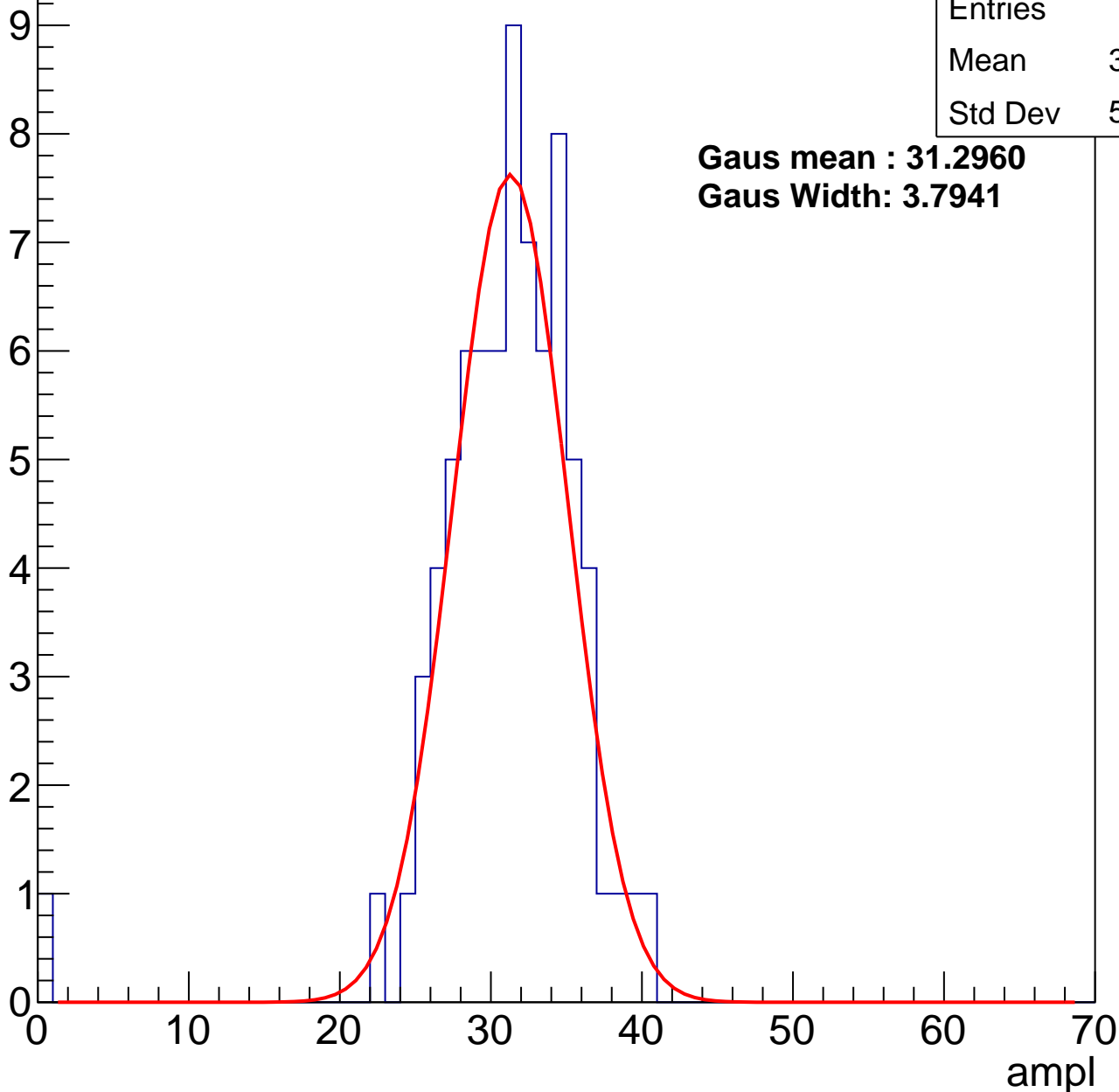
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.63
Std Dev	5.086

Gaus mean : 31.2960

Gaus Width: 3.7941



B0L001S, U25-ch123, adc1

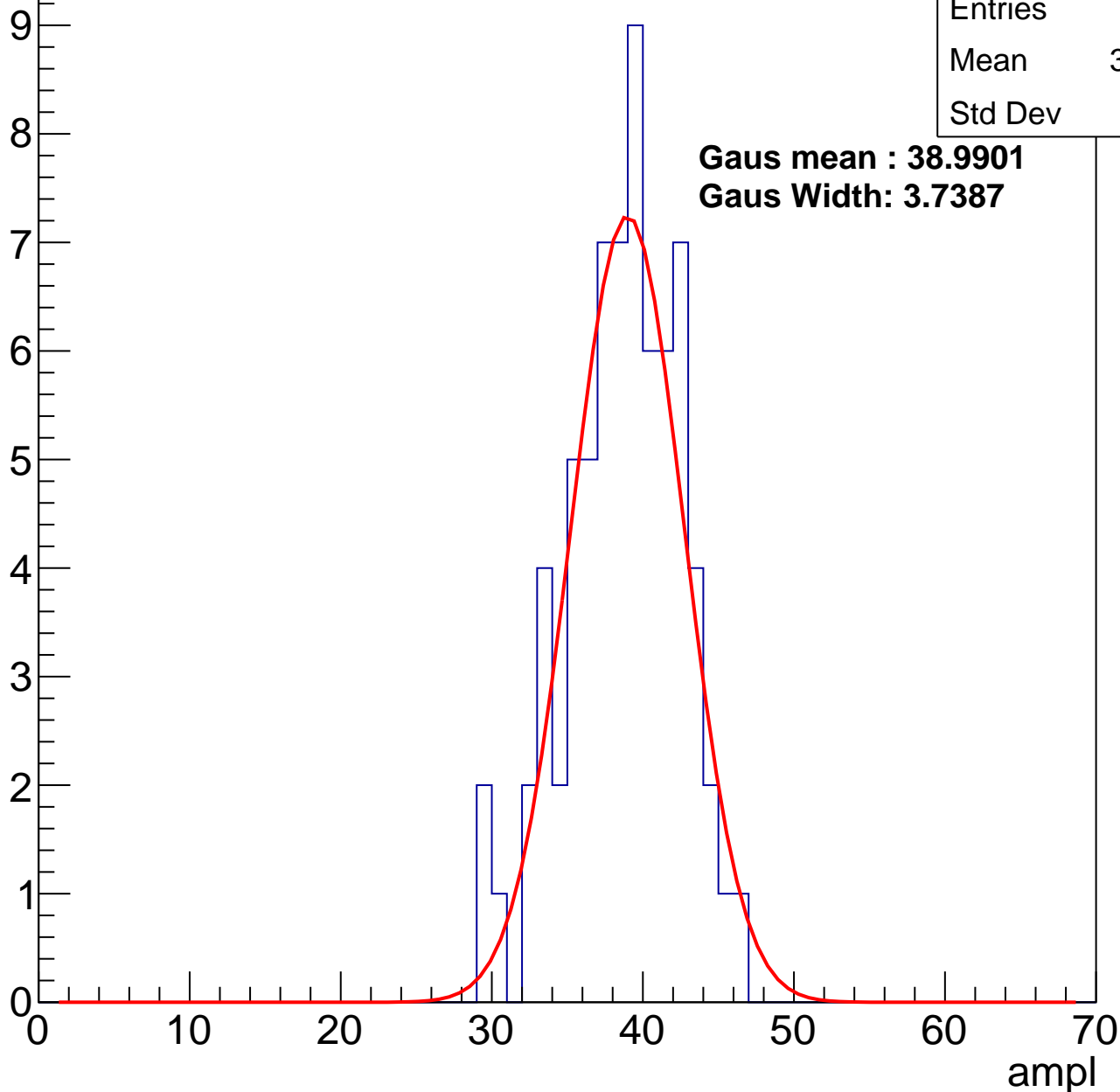
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.23
Std Dev	3.72

Gaus mean : 38.9901

Gaus Width: 3.7387



B0L001S, U25-ch123, adc2

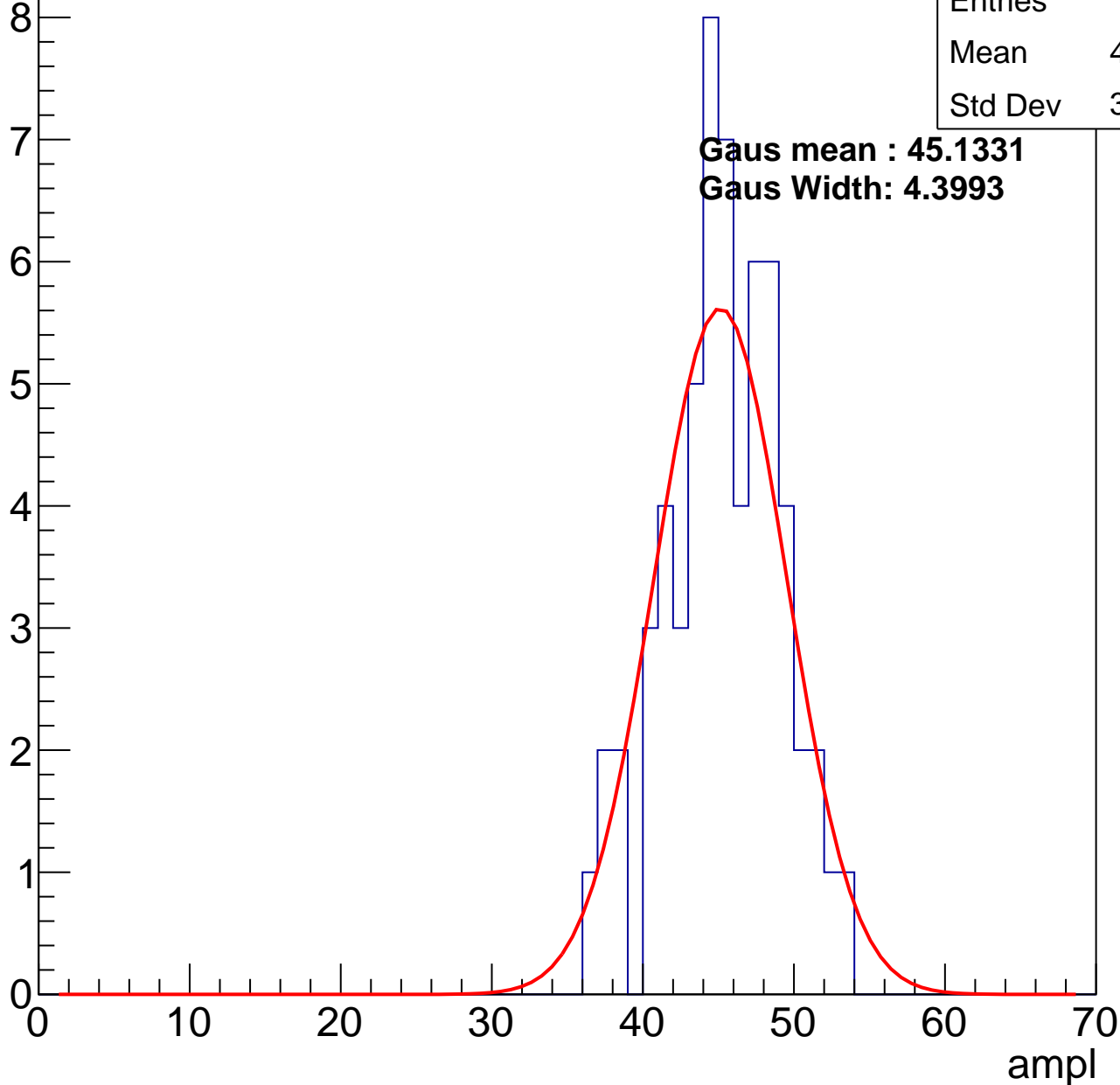
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.84
Std Dev	3.799

Gaus mean : 45.1331

Gaus Width: 4.3993

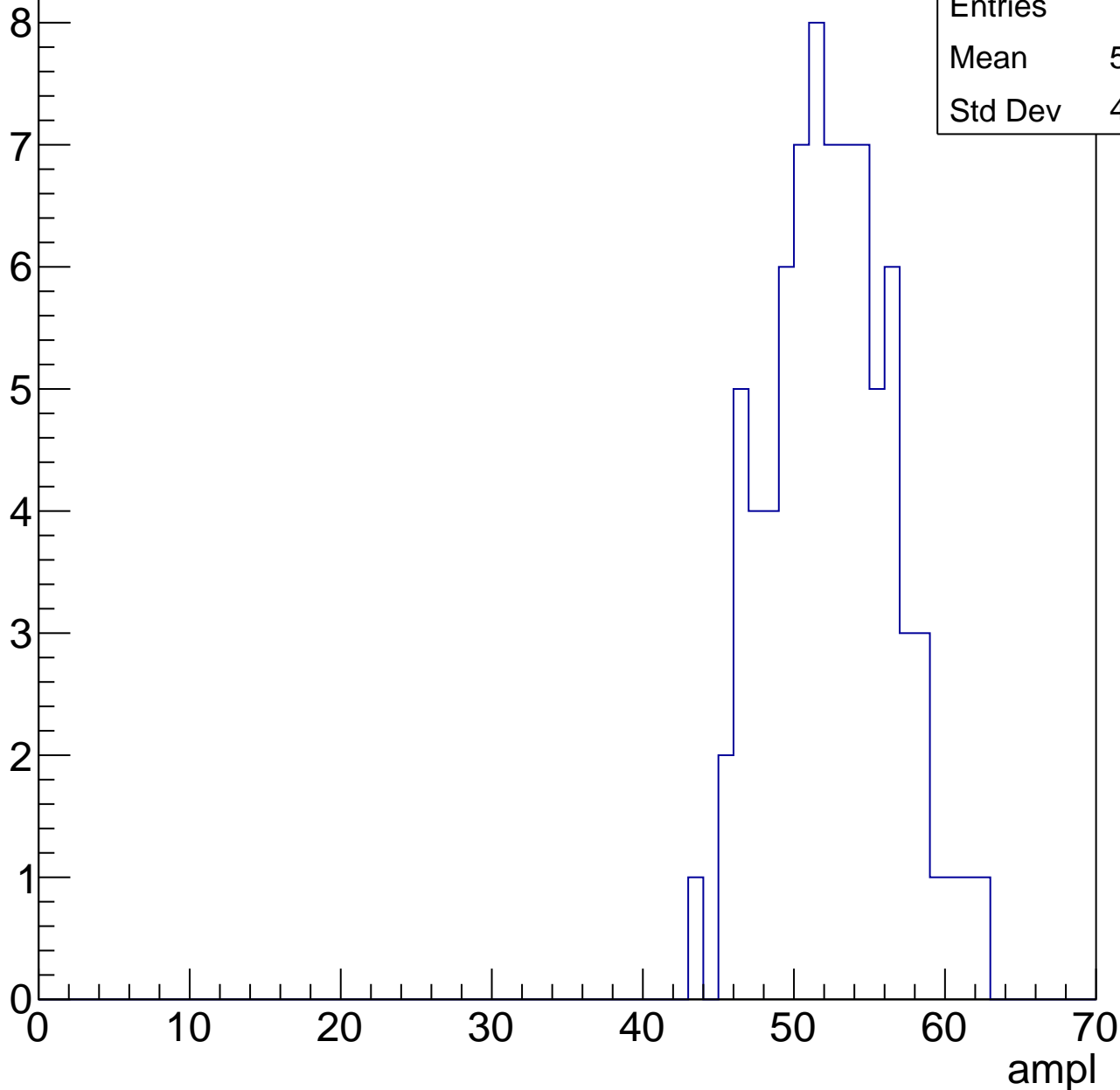


B0L001S, U25-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	51.97
Std Dev	4.003

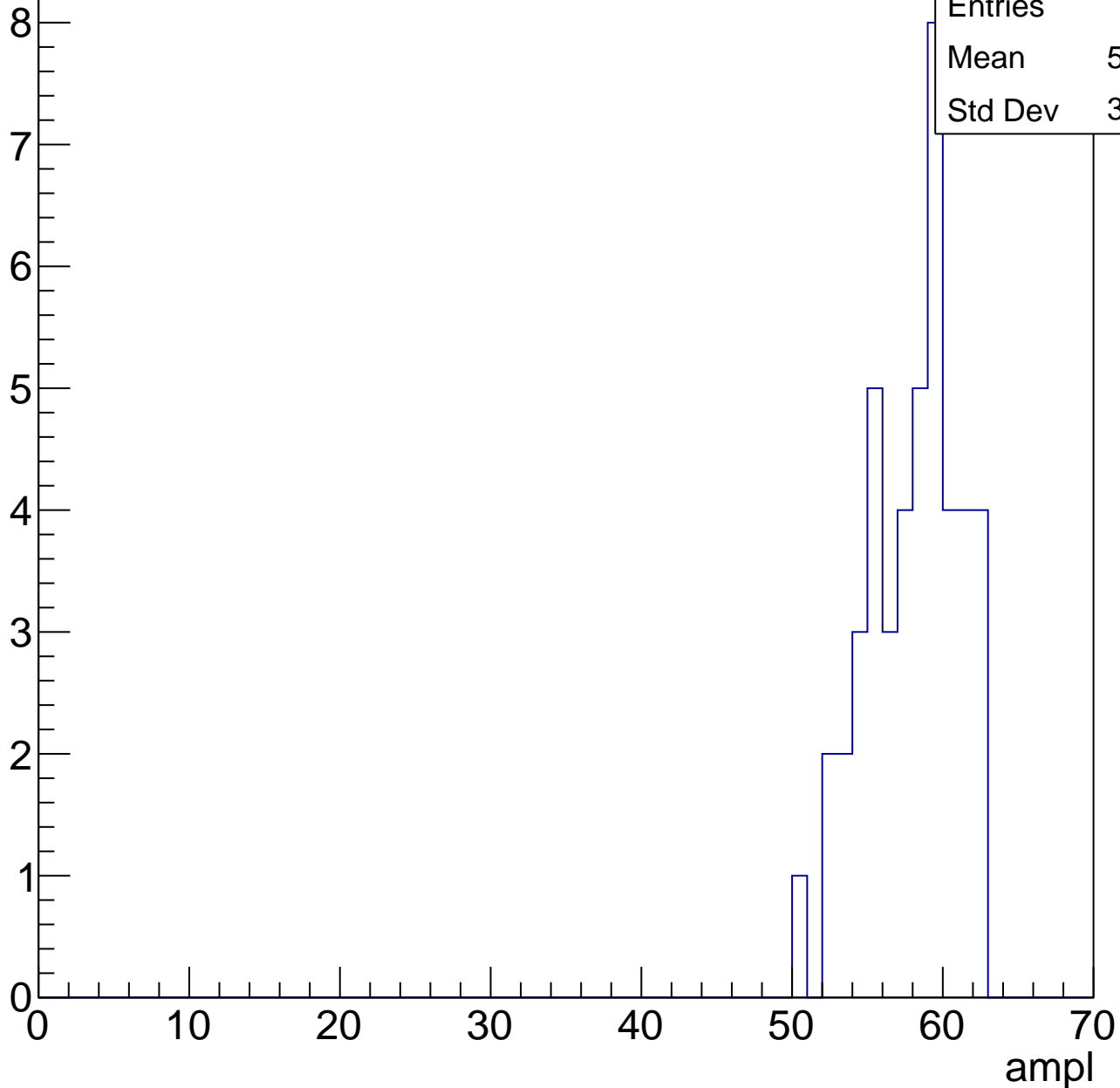


B0L001S, U25-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.49
Std Dev	3.008

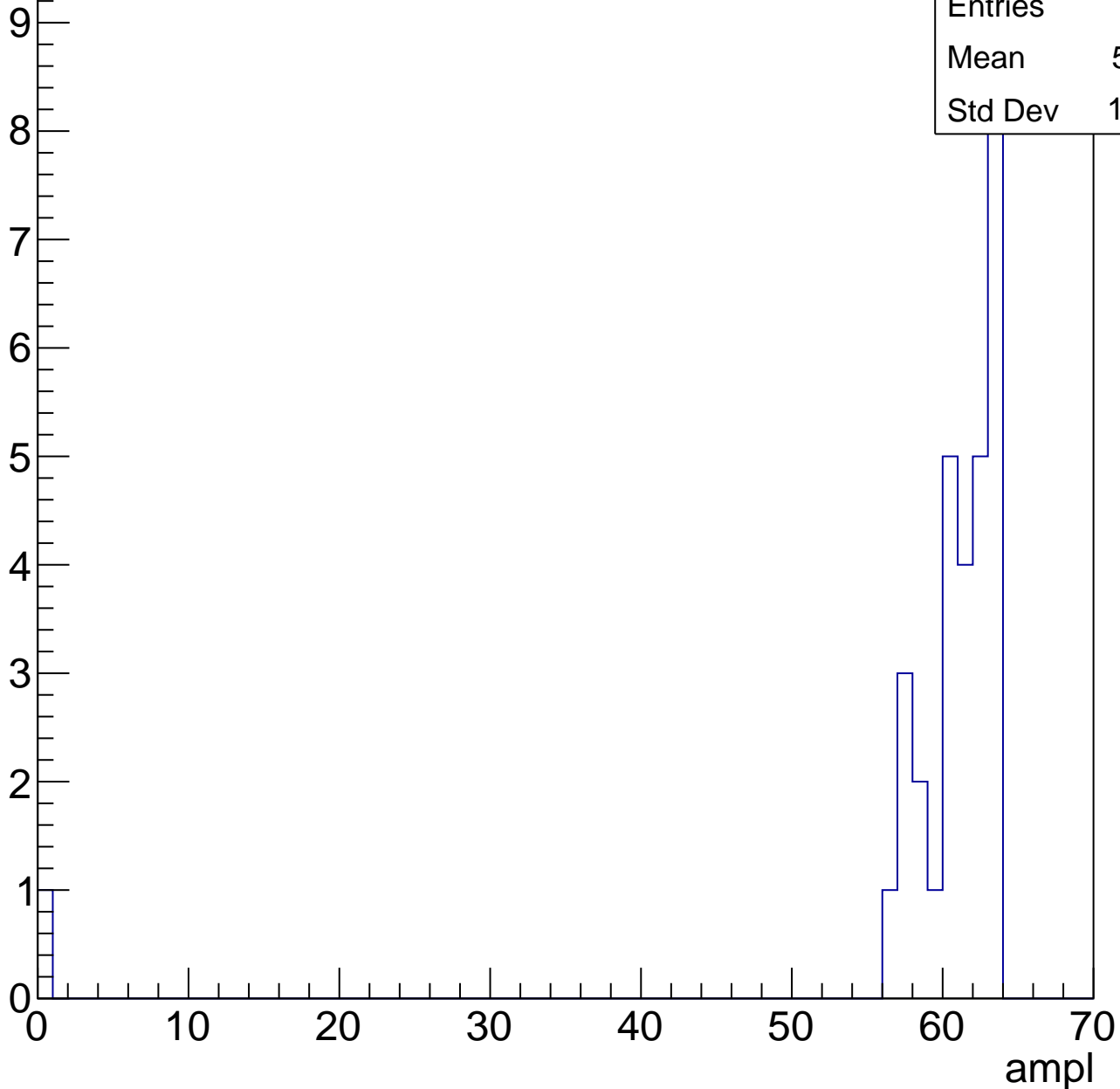


B0L001S, U25-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.81
Std Dev	10.94



B0L001S, U25-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U25-ch124, adc0

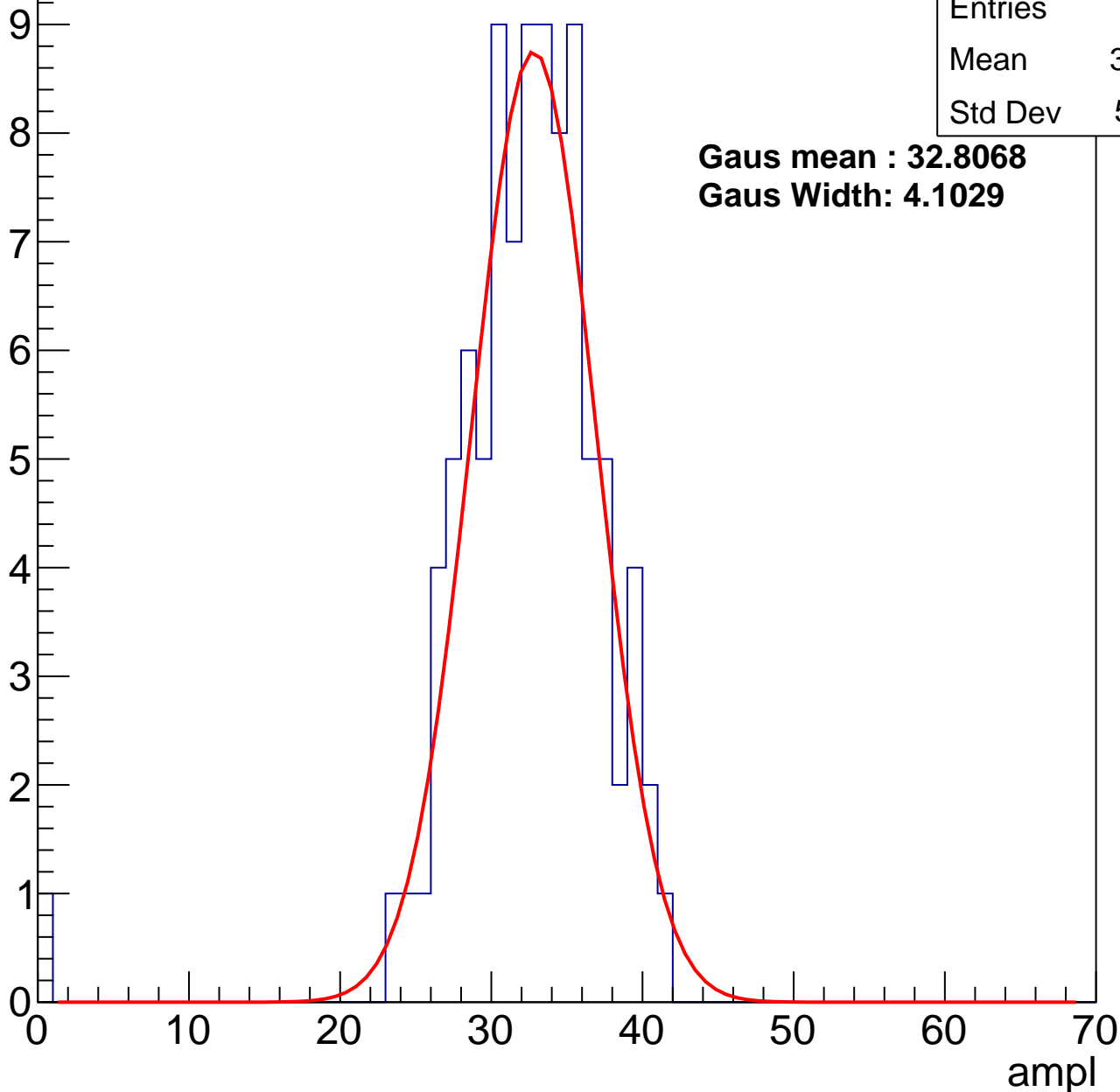
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	94
Mean	31.93
Std Dev	5.131

Gaus mean : 32.8068

Gaus Width: 4.1029



B0L001S, U25-ch124, adc1

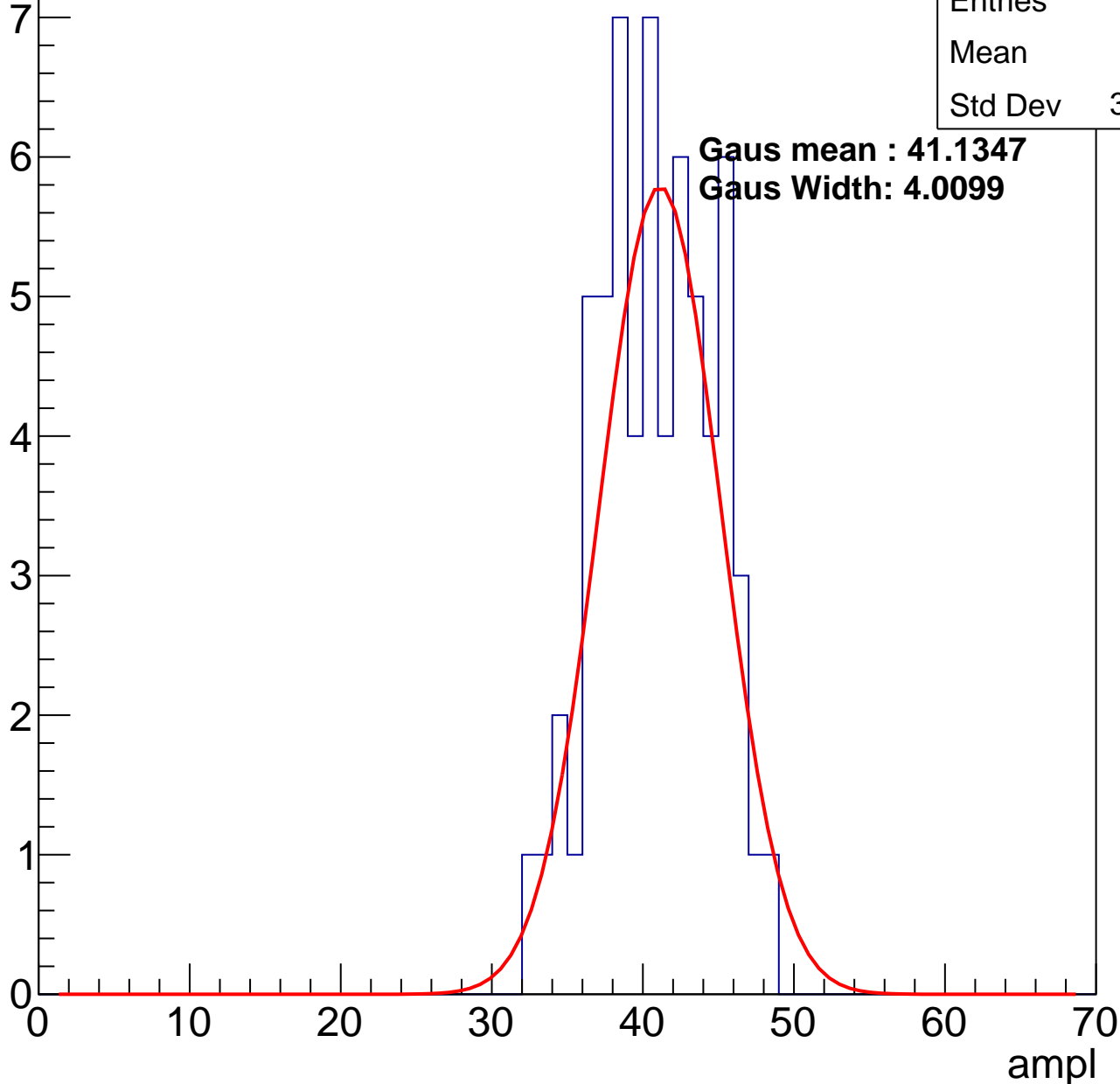
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	40.4
Std Dev	3.706

Gaus mean : 41.1347

Gaus Width: 4.0099



B0L001S, U25-ch124, adc2

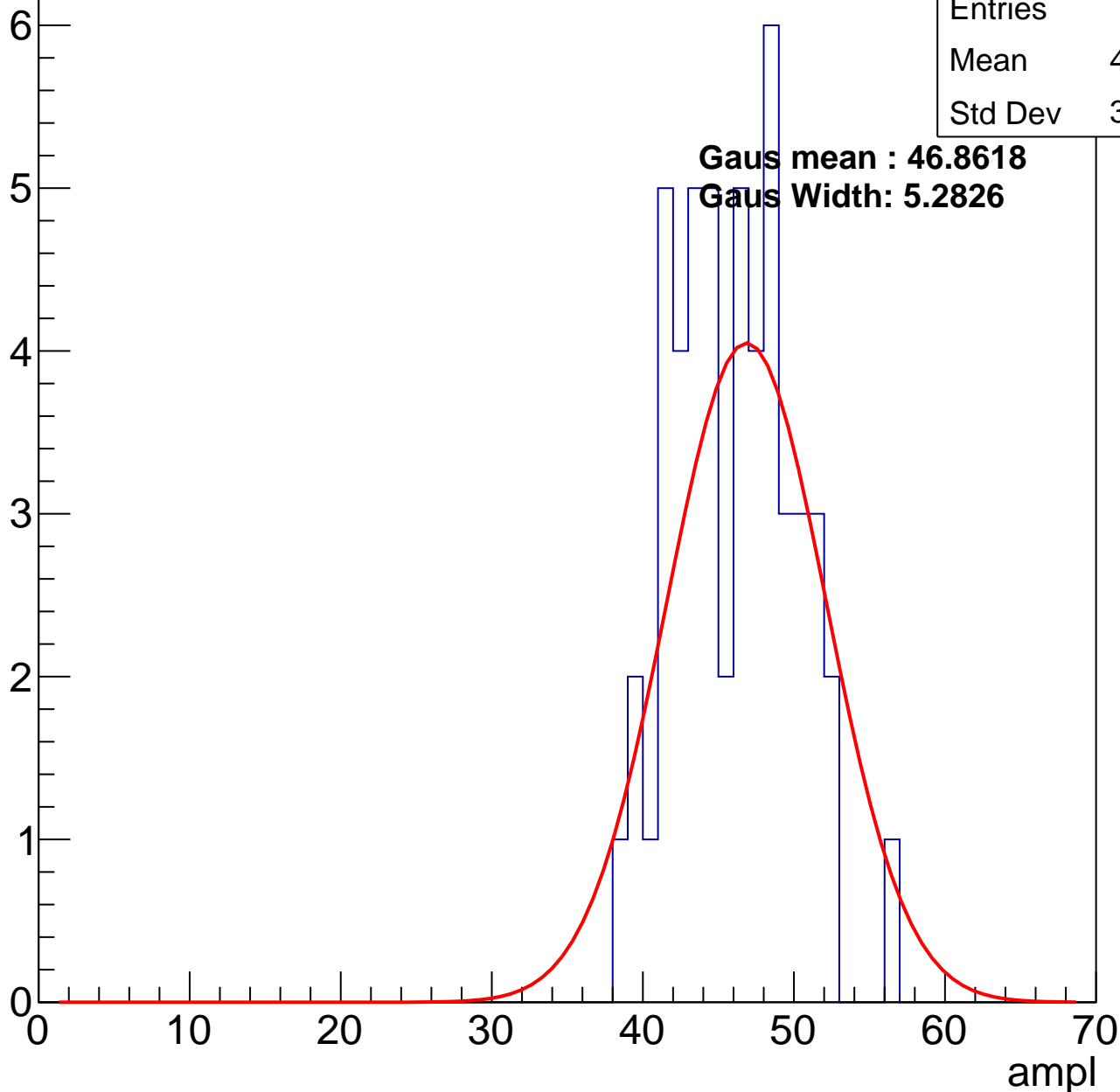
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	45.58
Std Dev	3.909

Gaus mean : 46.8618

Gaus Width: 5.2826

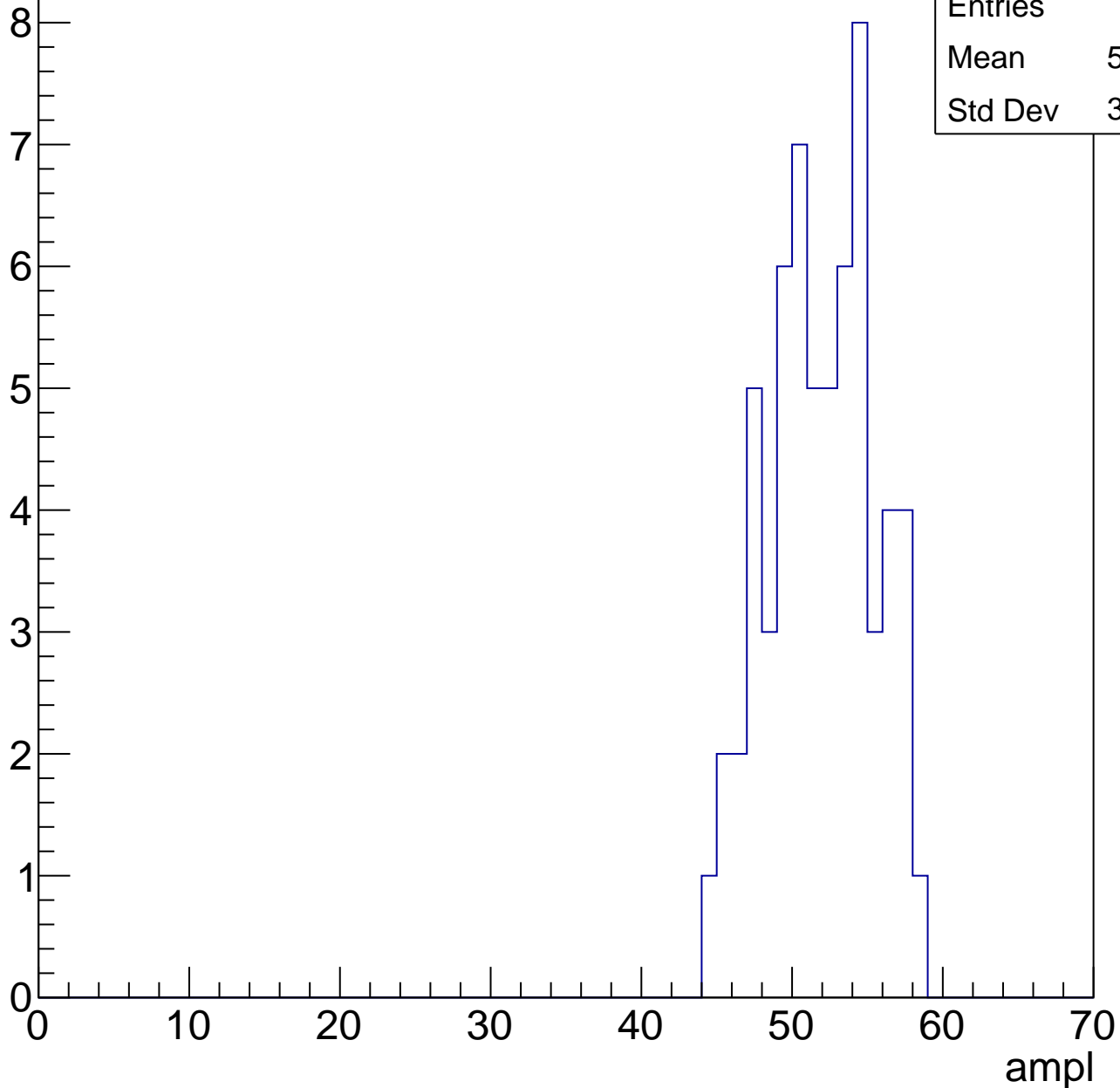


B0L001S, U25-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.44
Std Dev	3.453

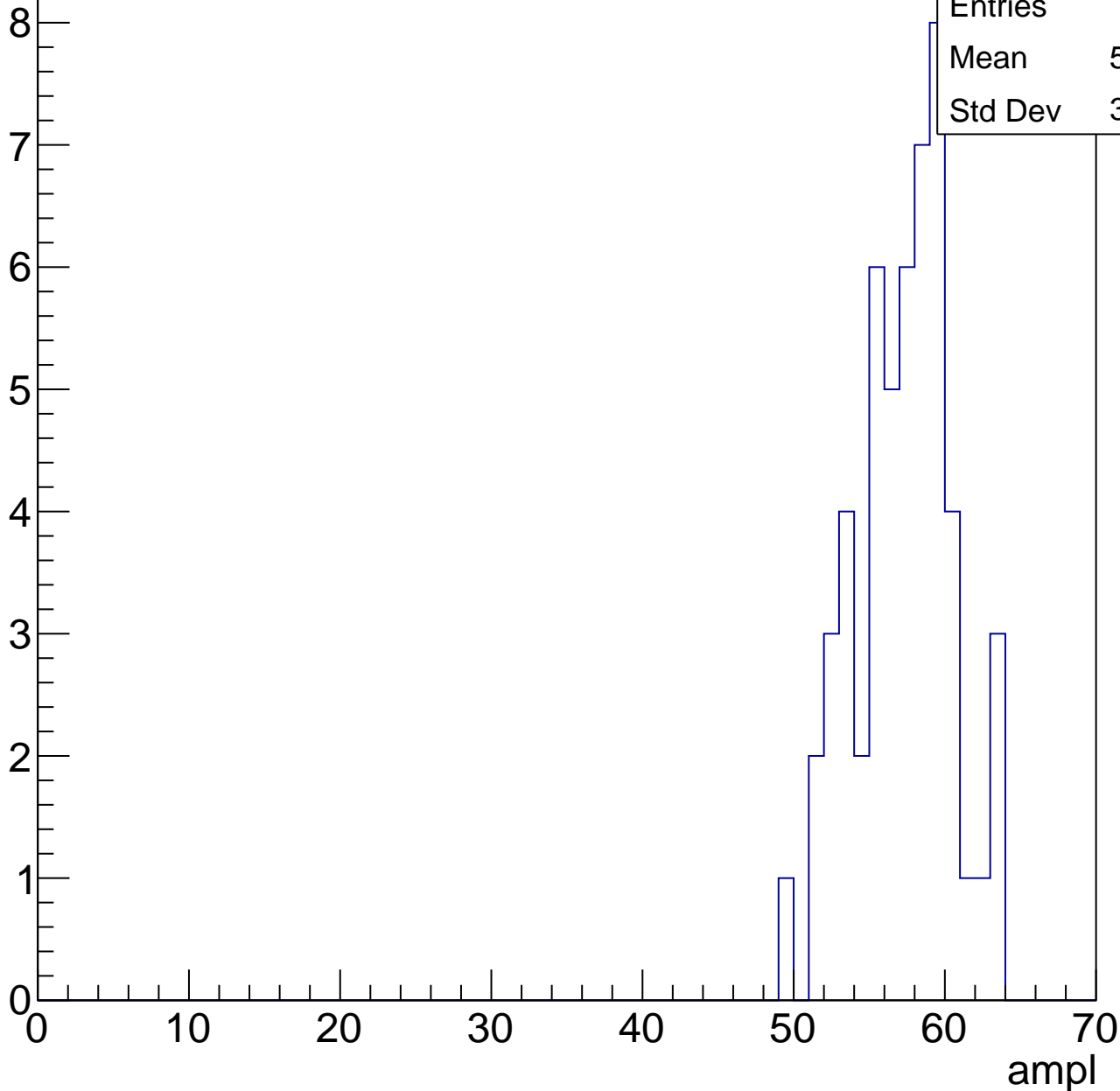


B0L001S, U25-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.77
Std Dev	3.202

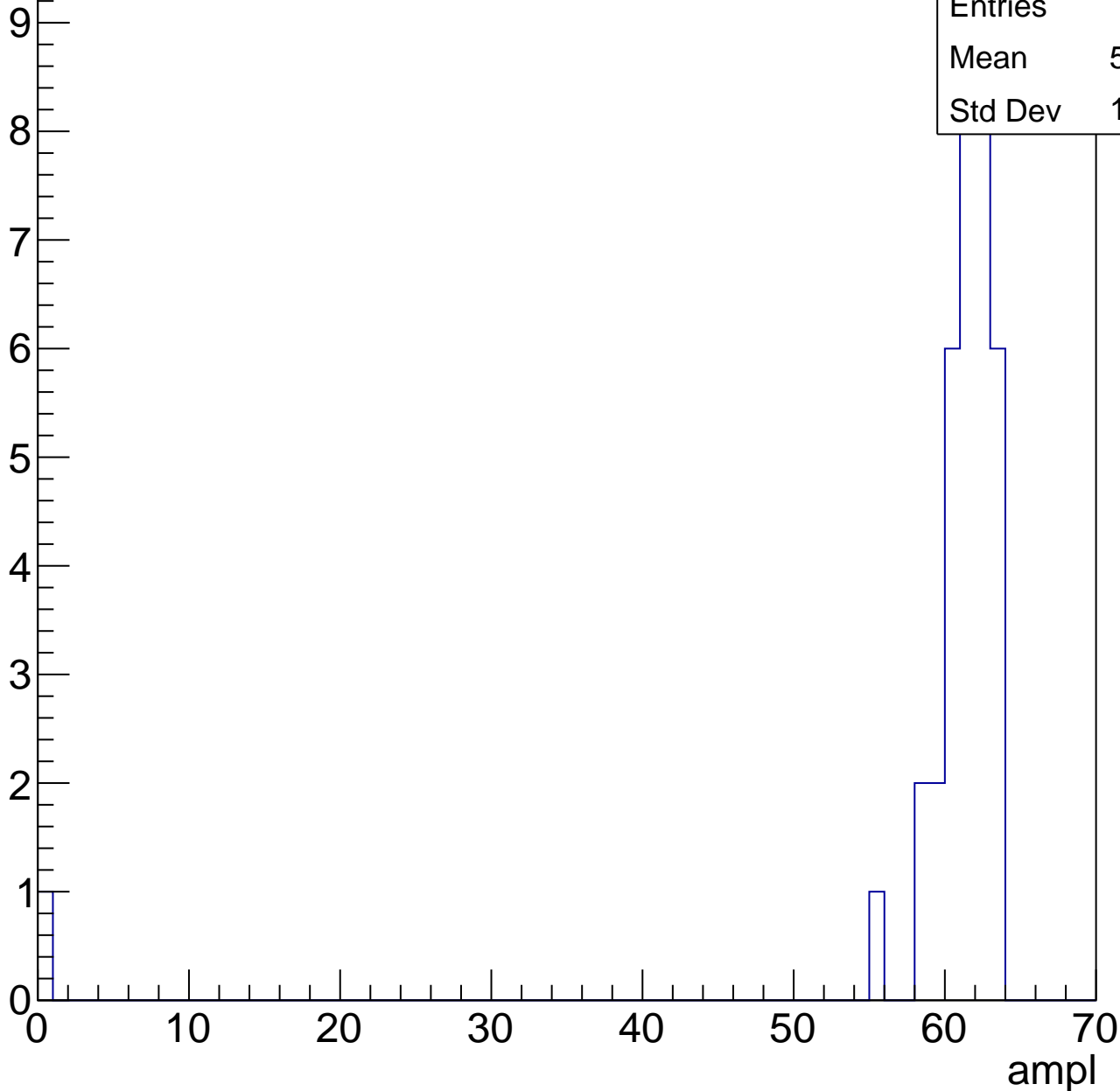


B0L001S, U25-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.28
Std Dev	10.16



B0L001S, U25-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch125, adc0

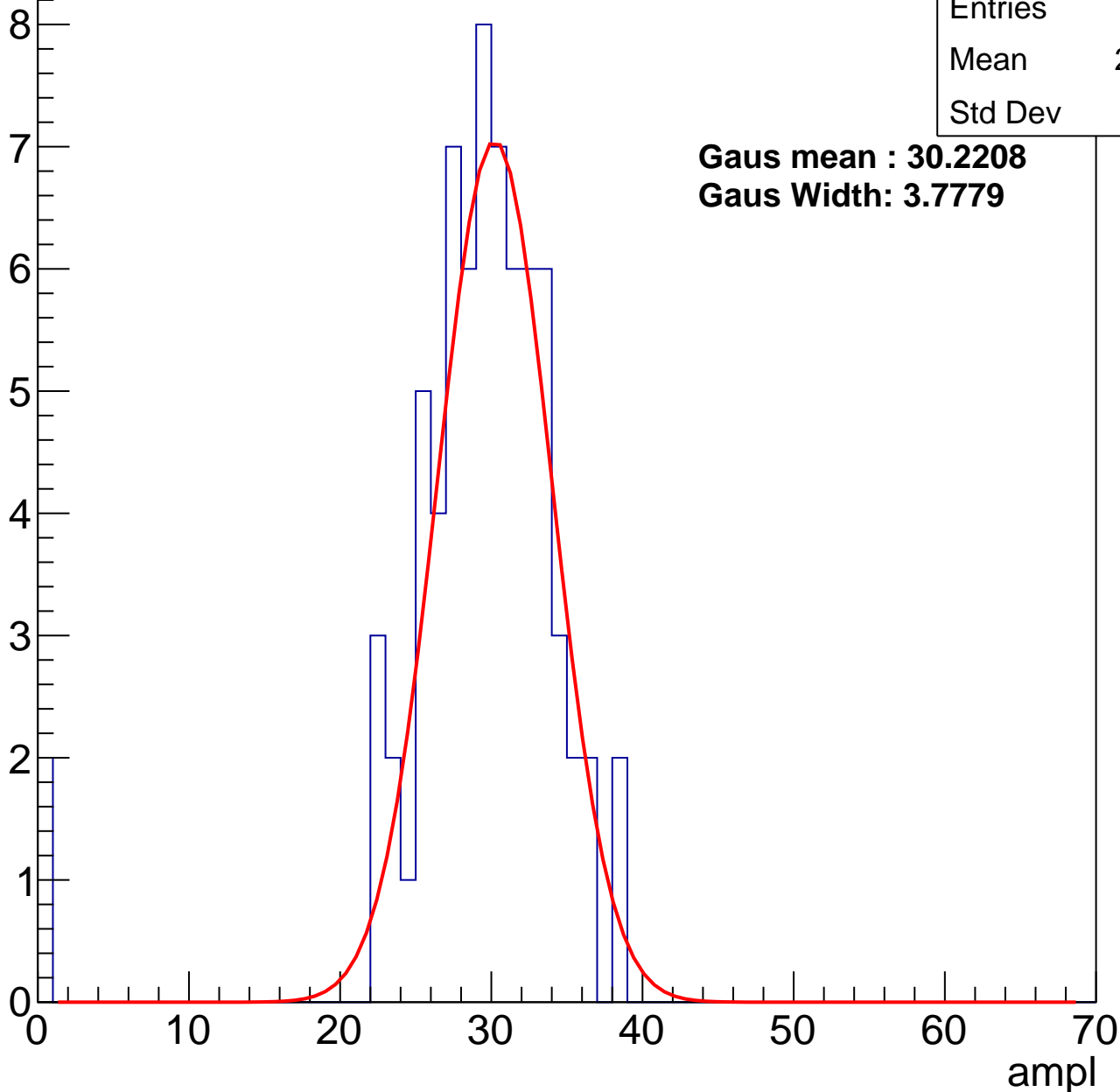
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	28.61
Std Dev	6.07

Gaus mean : 30.2208

Gaus Width: 3.7779



B0L001S, U25-ch125, adc1

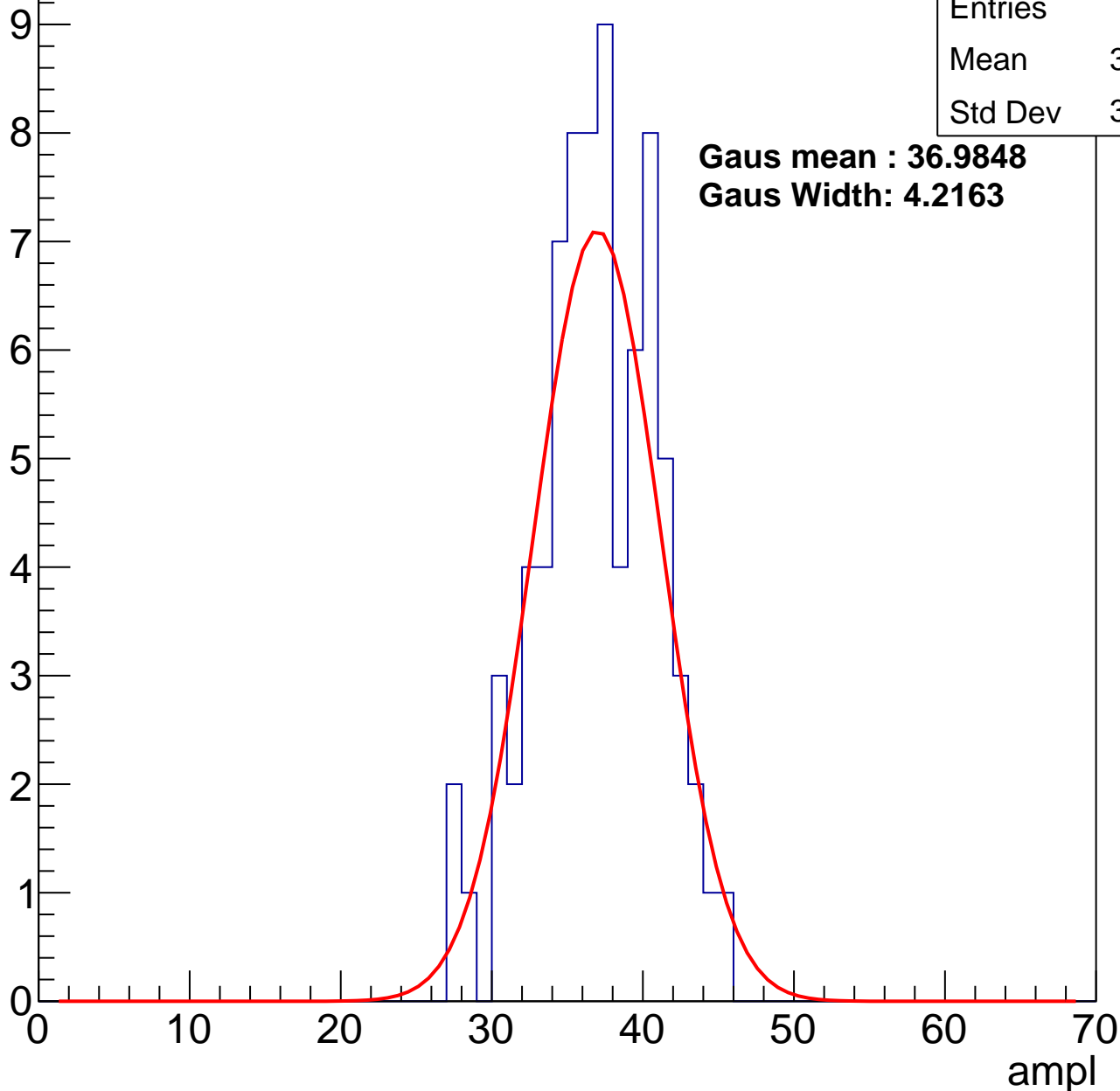
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.47
Std Dev	3.898

Gaus mean : 36.9848

Gaus Width: 4.2163

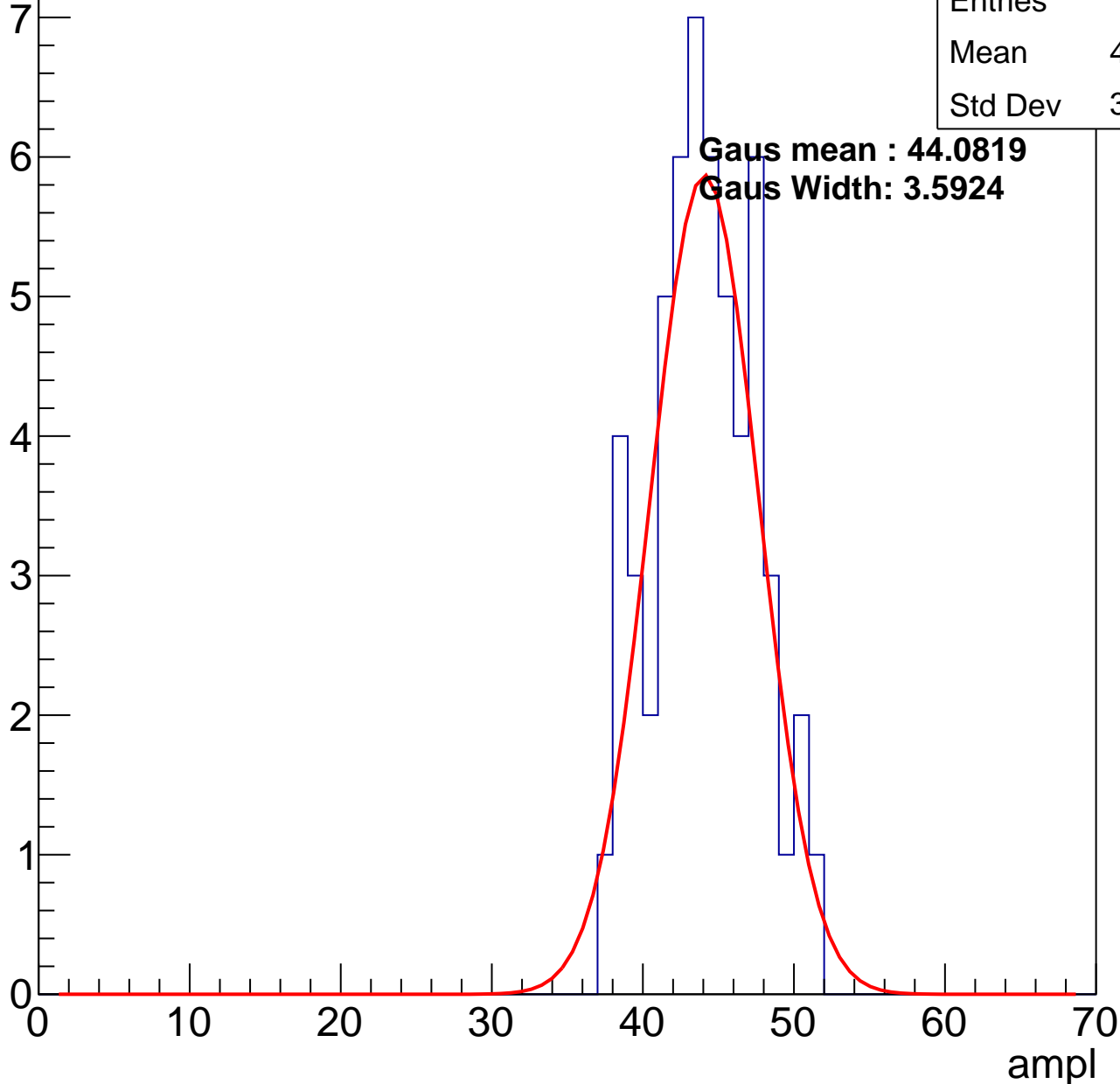


B0L001S, U25-ch125, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.62
Std Dev	3.373

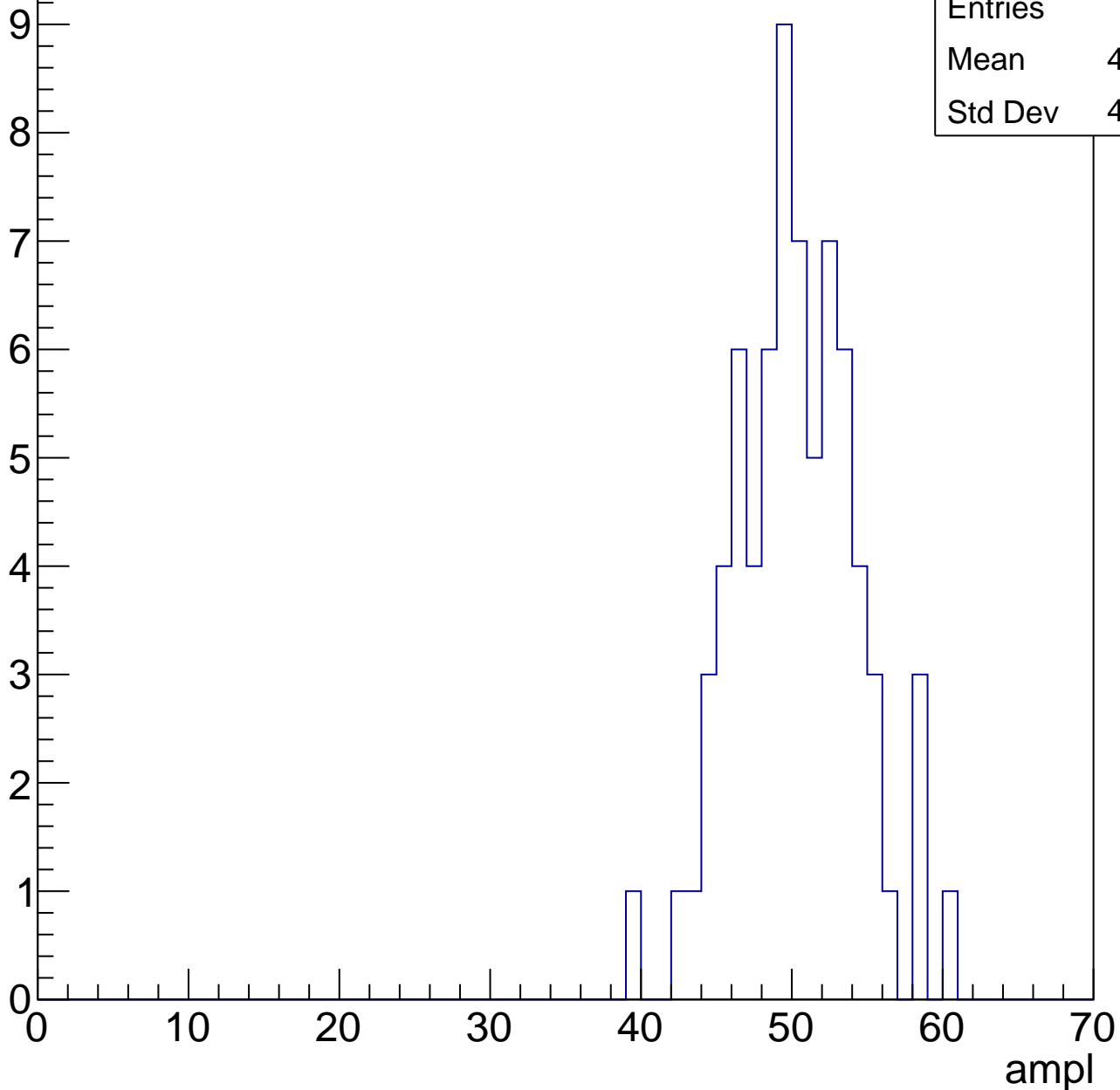


B0L001S, U25-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	49.82
Std Dev	4.012



B0L001S, U25-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

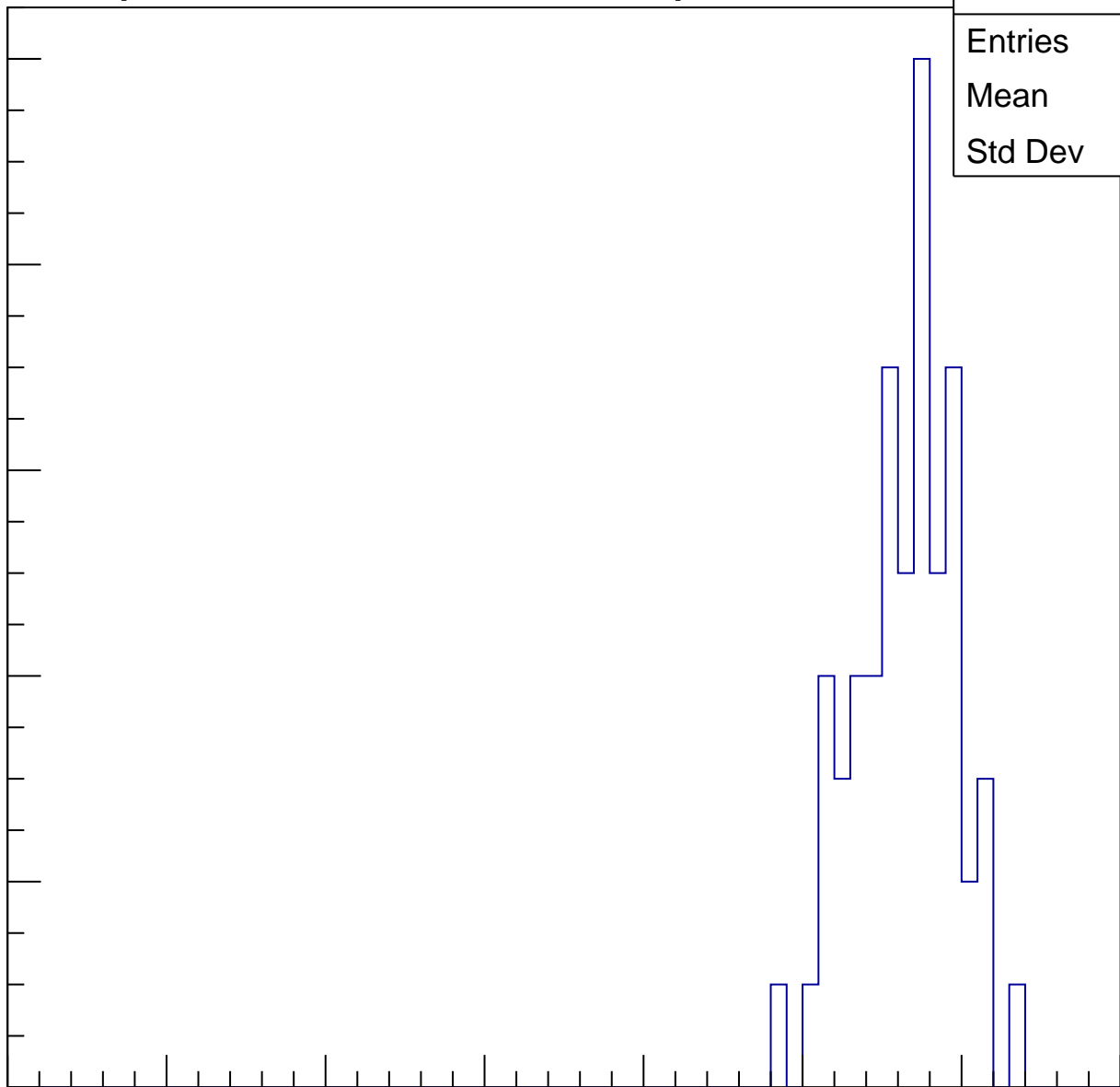
Entries	57
Mean	55.96
Std Dev	3.123

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

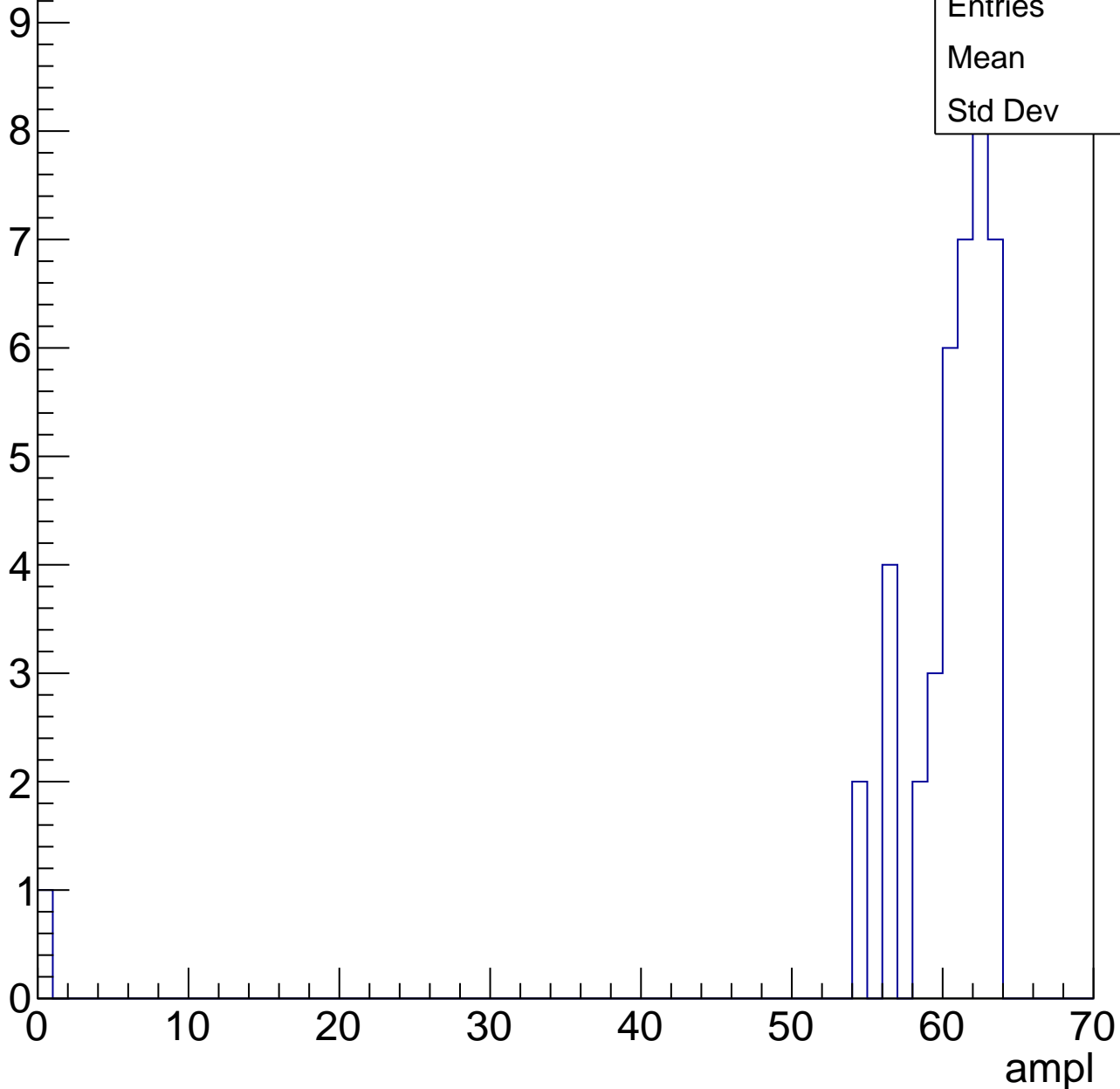


B0L001S, U25-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.8
Std Dev	9.62



B0L001S, U25-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

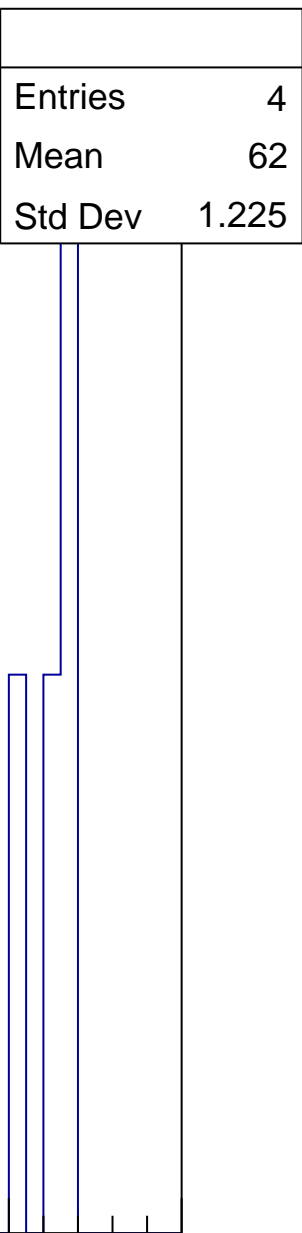
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B0L001S, U25-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch126, adc0

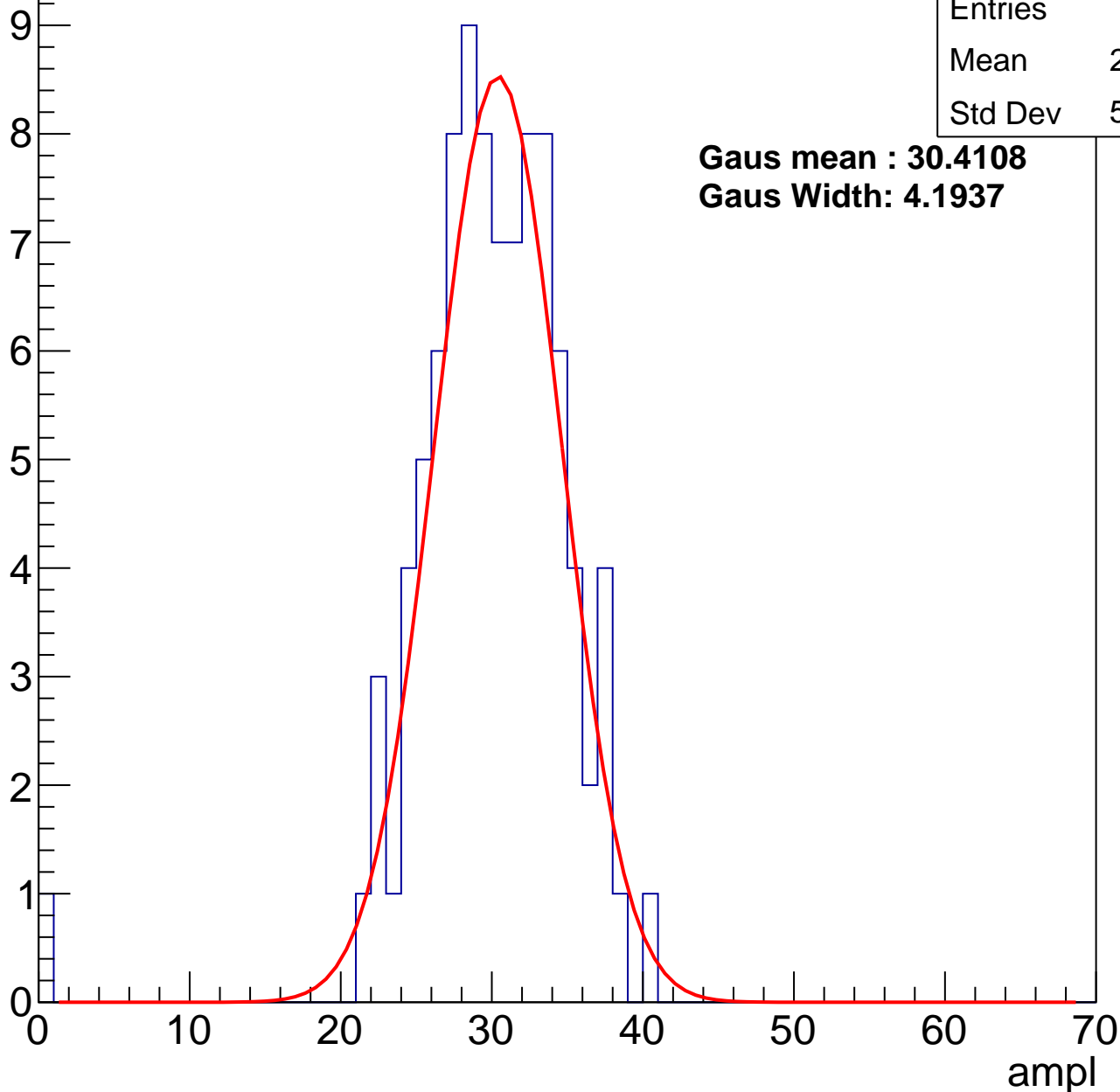
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	94
Mean	29.53
Std Dev	5.075

Gaus mean : 30.4108

Gaus Width: 4.1937



B0L001S, U25-ch126, adc1

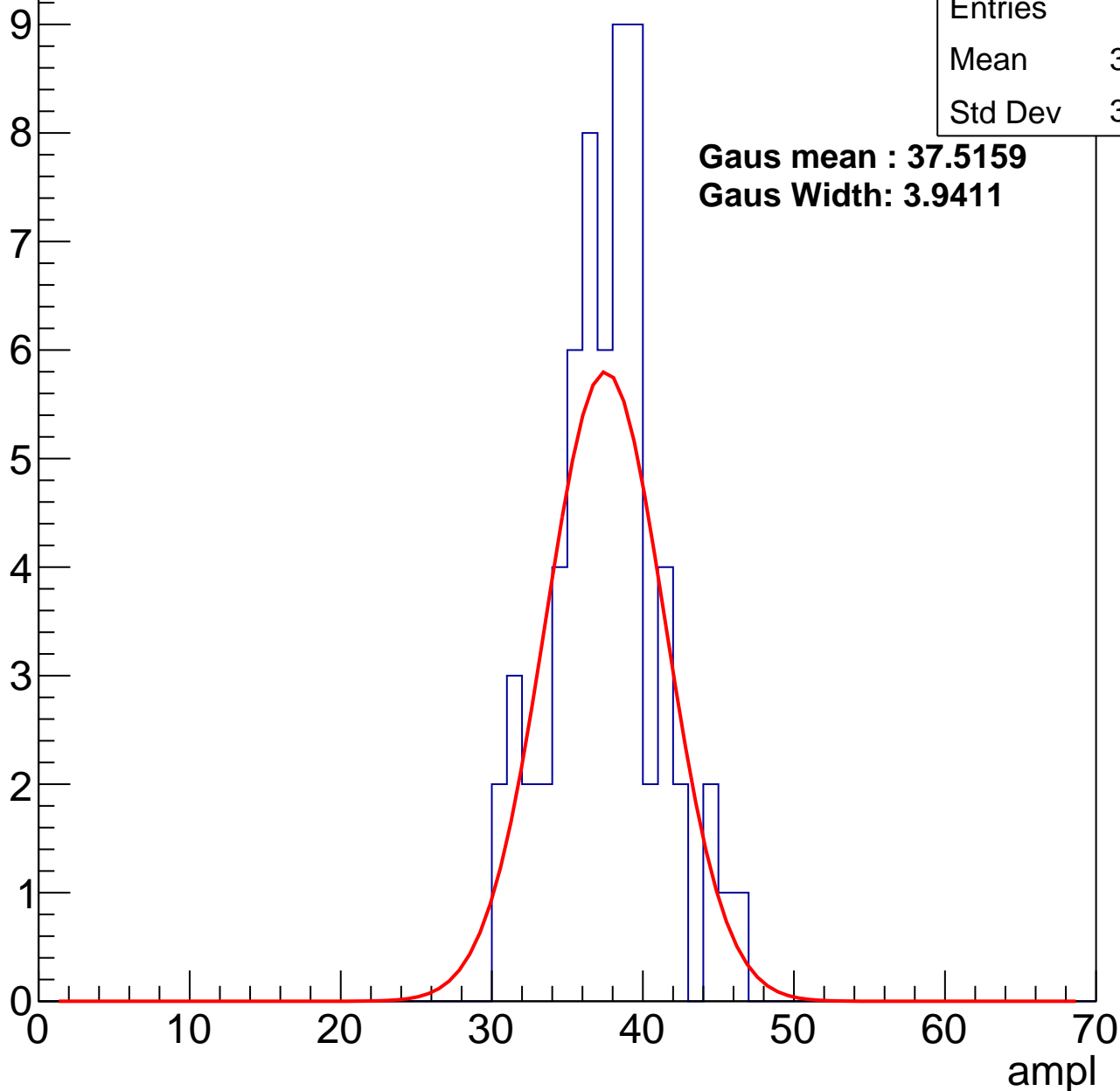
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.13
Std Dev	3.507

Gaus mean : 37.5159

Gaus Width: 3.9411



B0L001S, U25-ch126, adc2

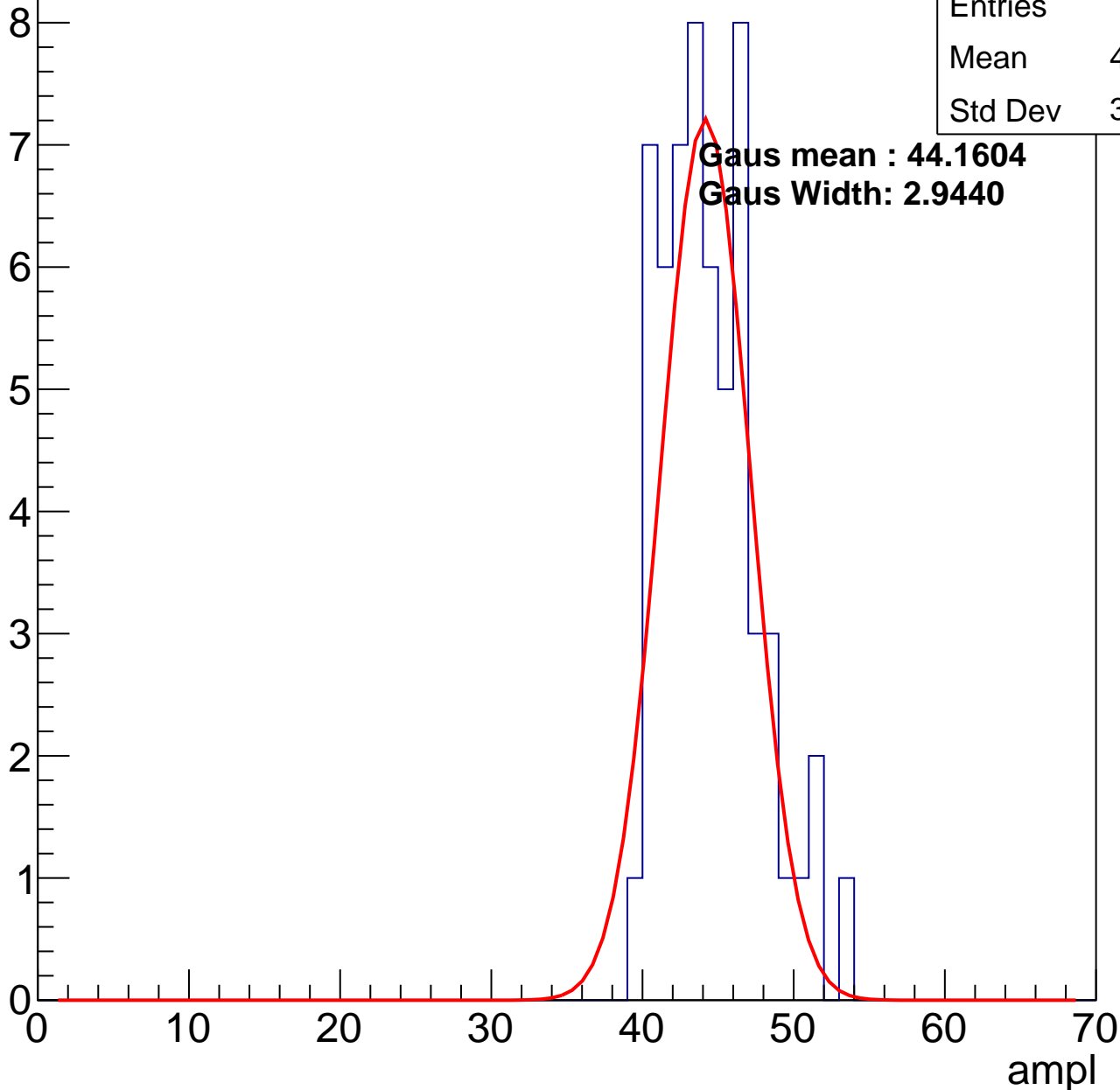
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.05
Std Dev	3.132

Gaus mean : 44.1604

Gaus Width: 2.9440

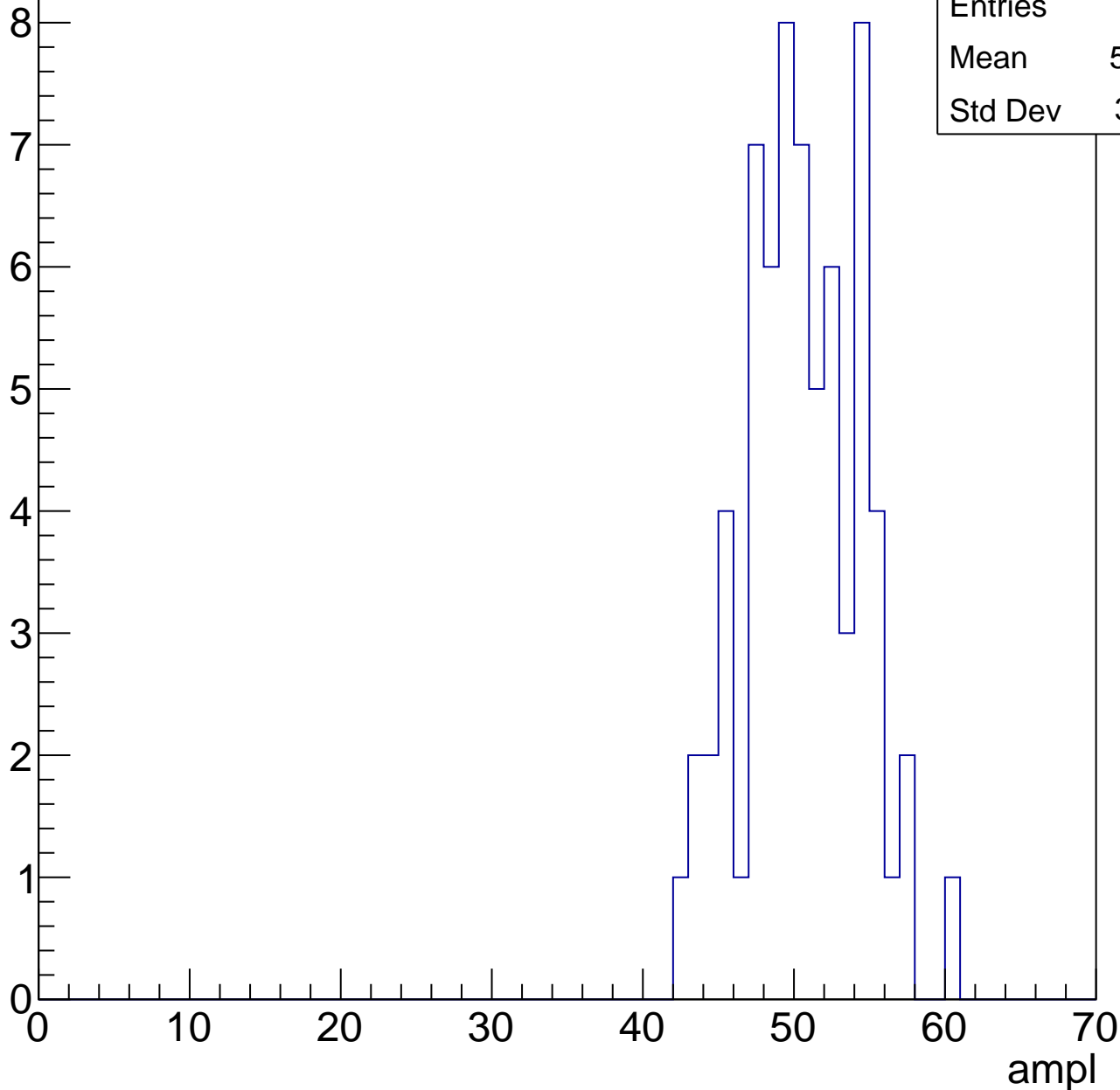


B0L001S, U25-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.13
Std Dev	3.761

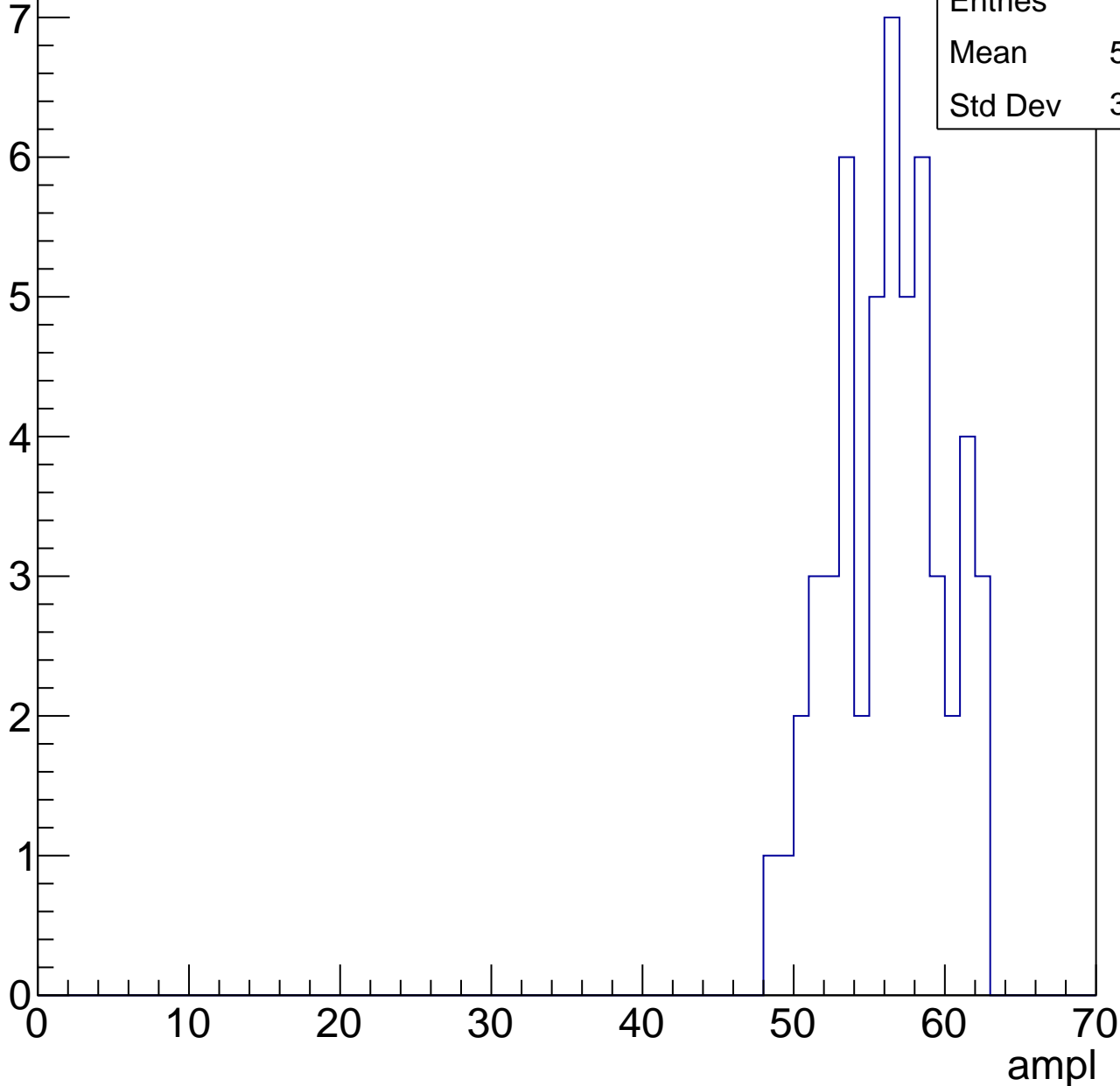


B0L001S, U25-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.83
Std Dev	3.554

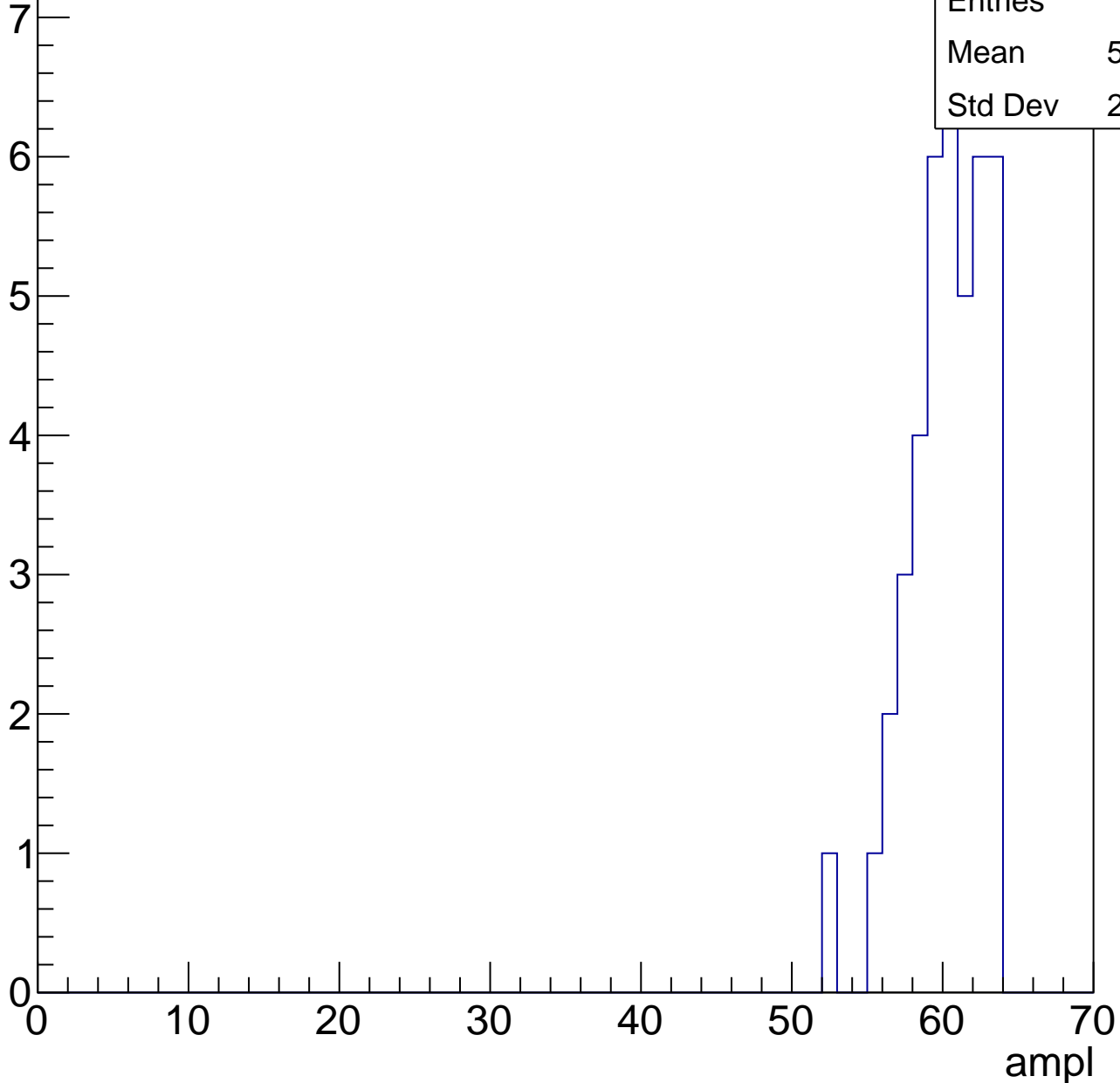


B0L001S, U25-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

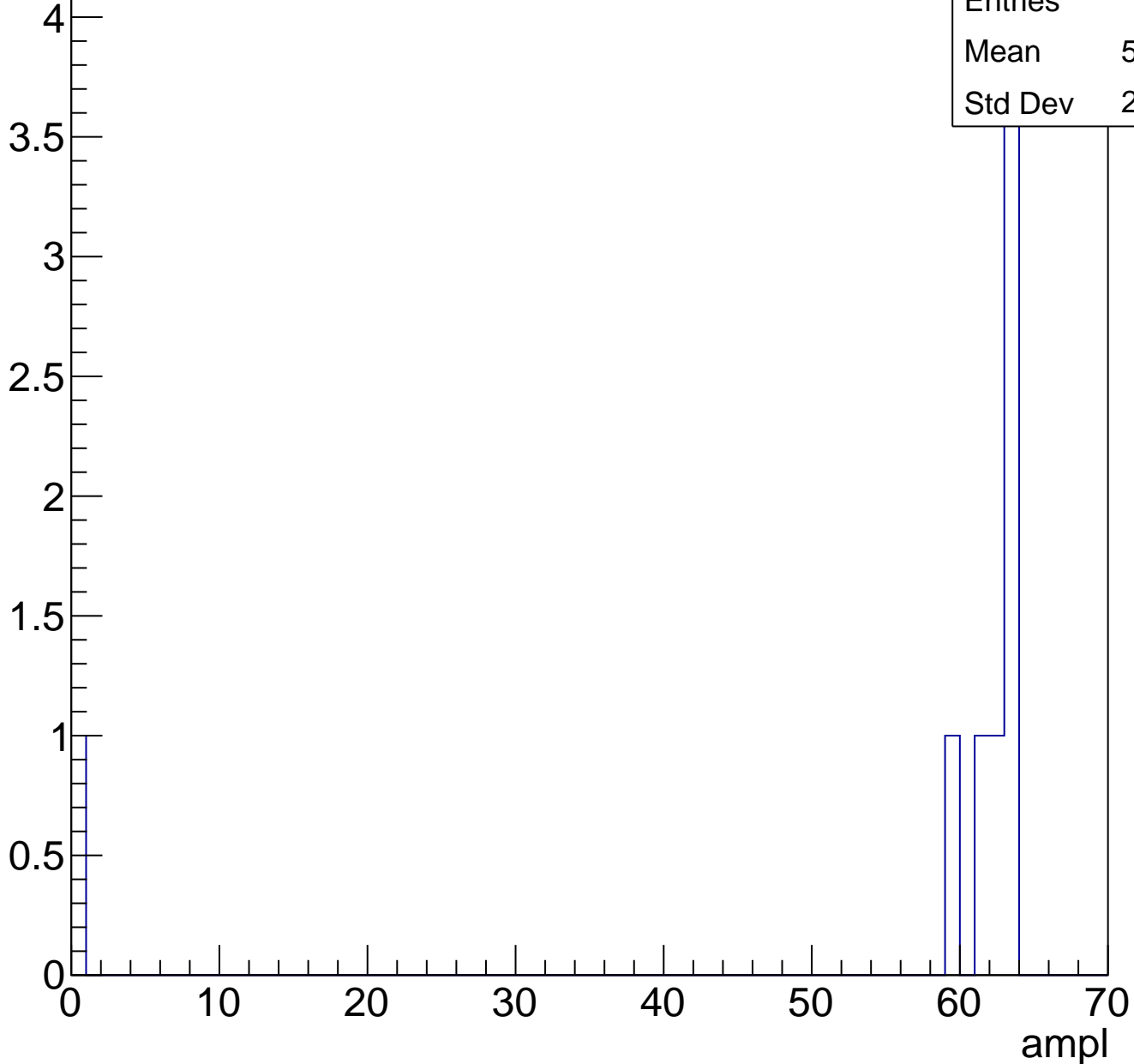
Entries	41
Mean	59.78
Std Dev	2.474



B0L001S, U25-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U25-ch127, adc0

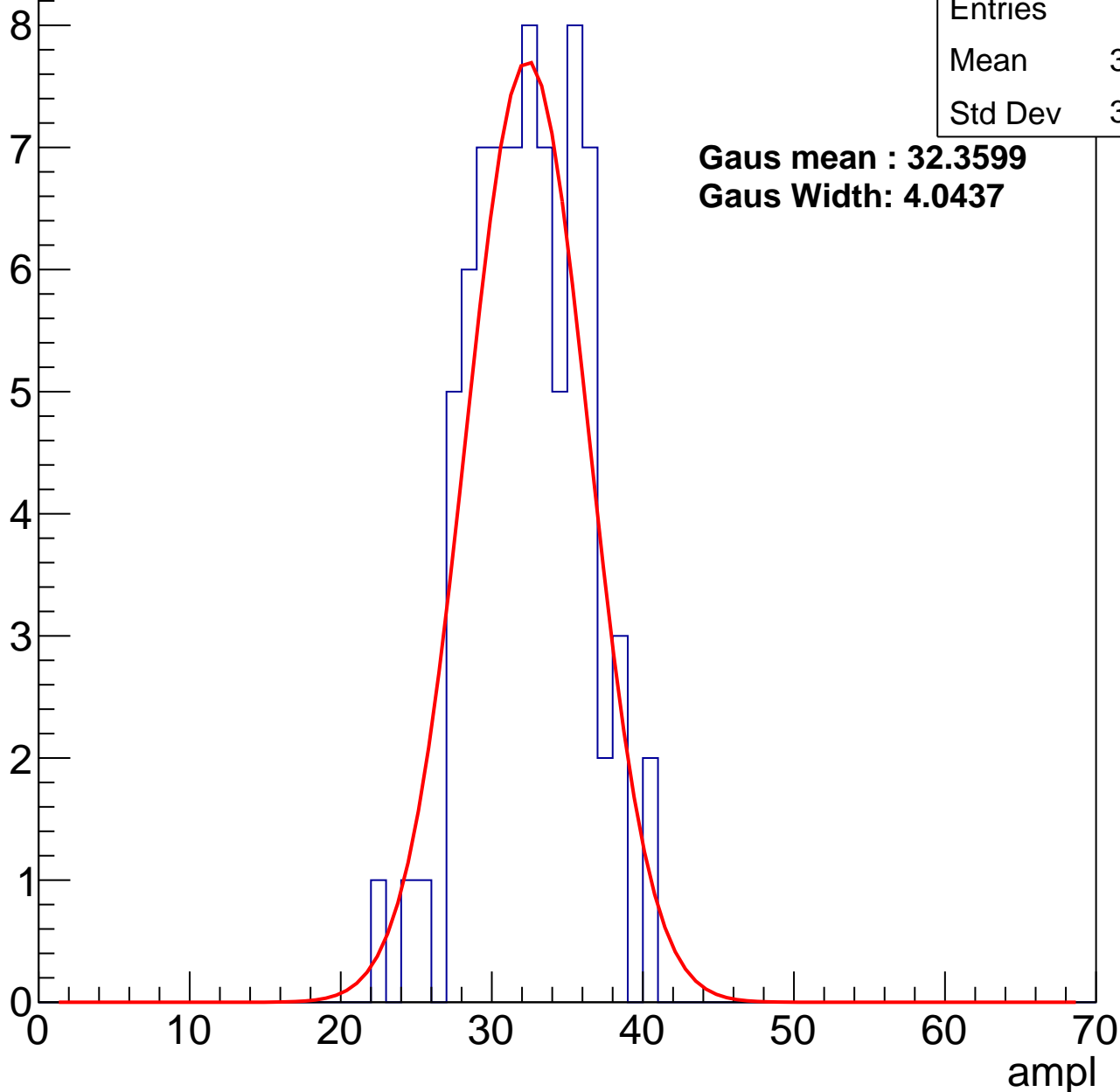
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	31.96
Std Dev	3.655

Gaus mean : 32.3599

Gaus Width: 4.0437



B0L001S, U25-ch127, adc1

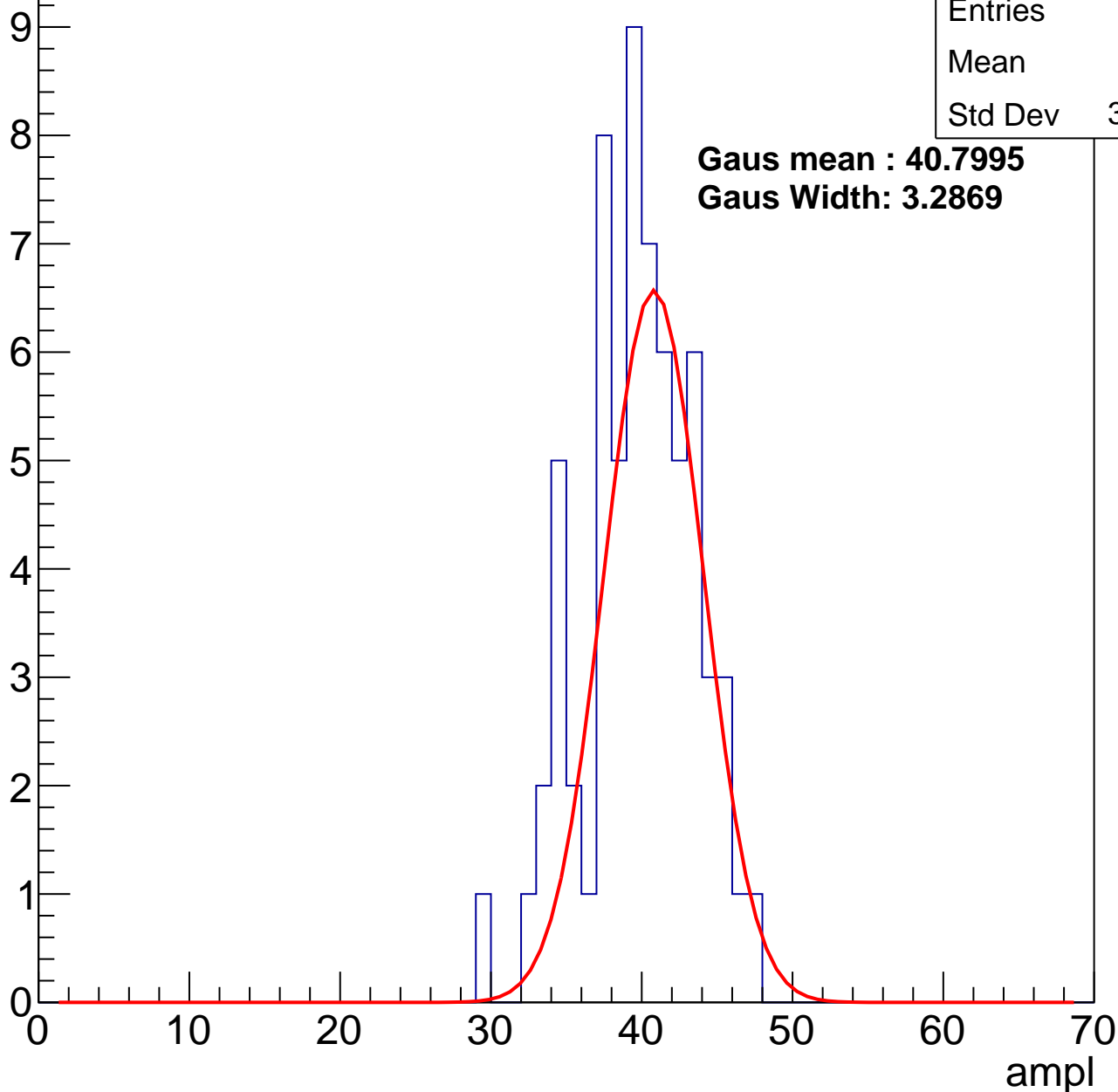
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	39.3
Std Dev	3.664

Gaus mean : 40.7995

Gaus Width: 3.2869



B0L001S, U25-ch127, adc2

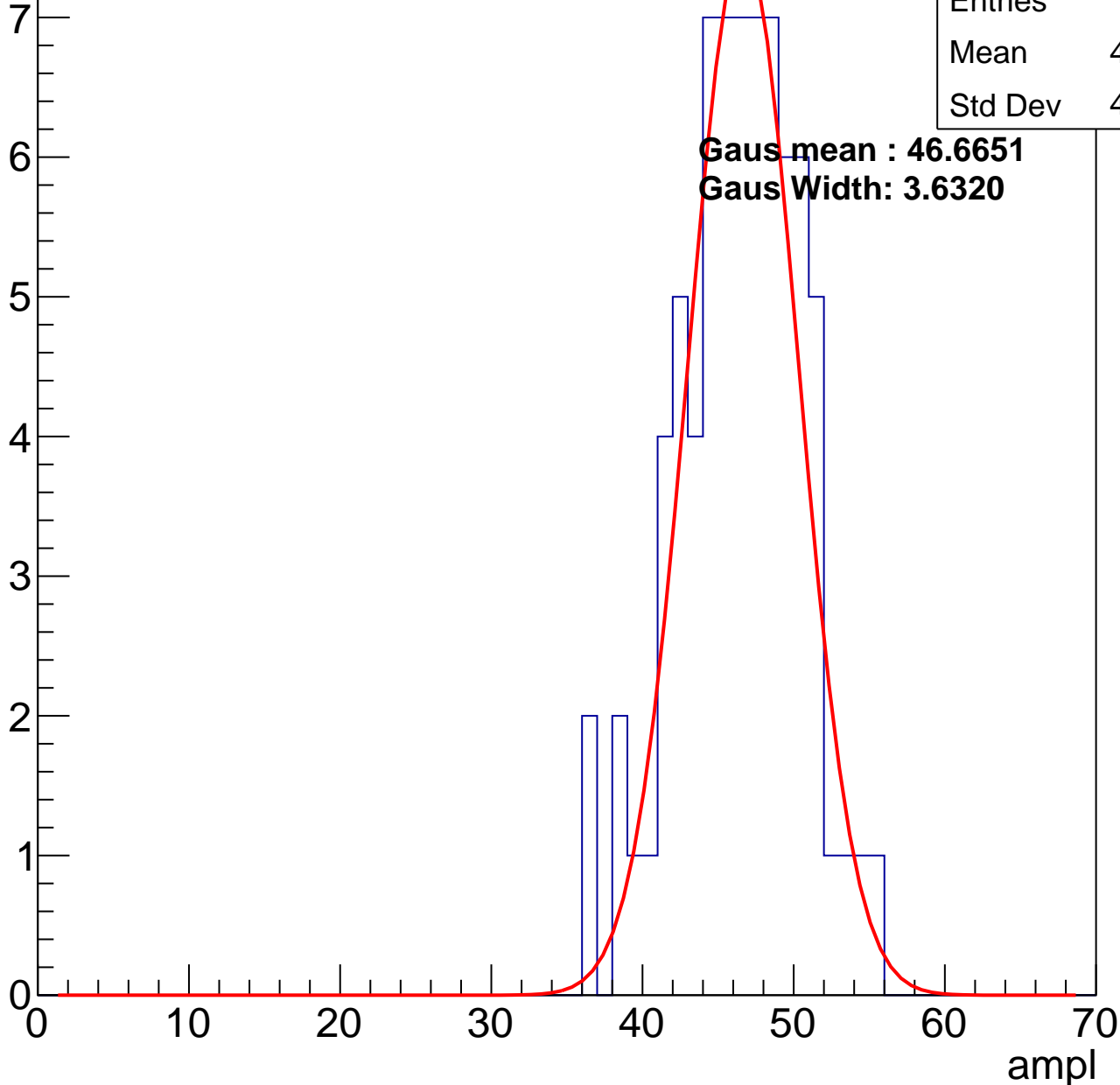
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	45.95
Std Dev	4.006

Gaus mean : 46.6651

Gaus Width: 3.6320

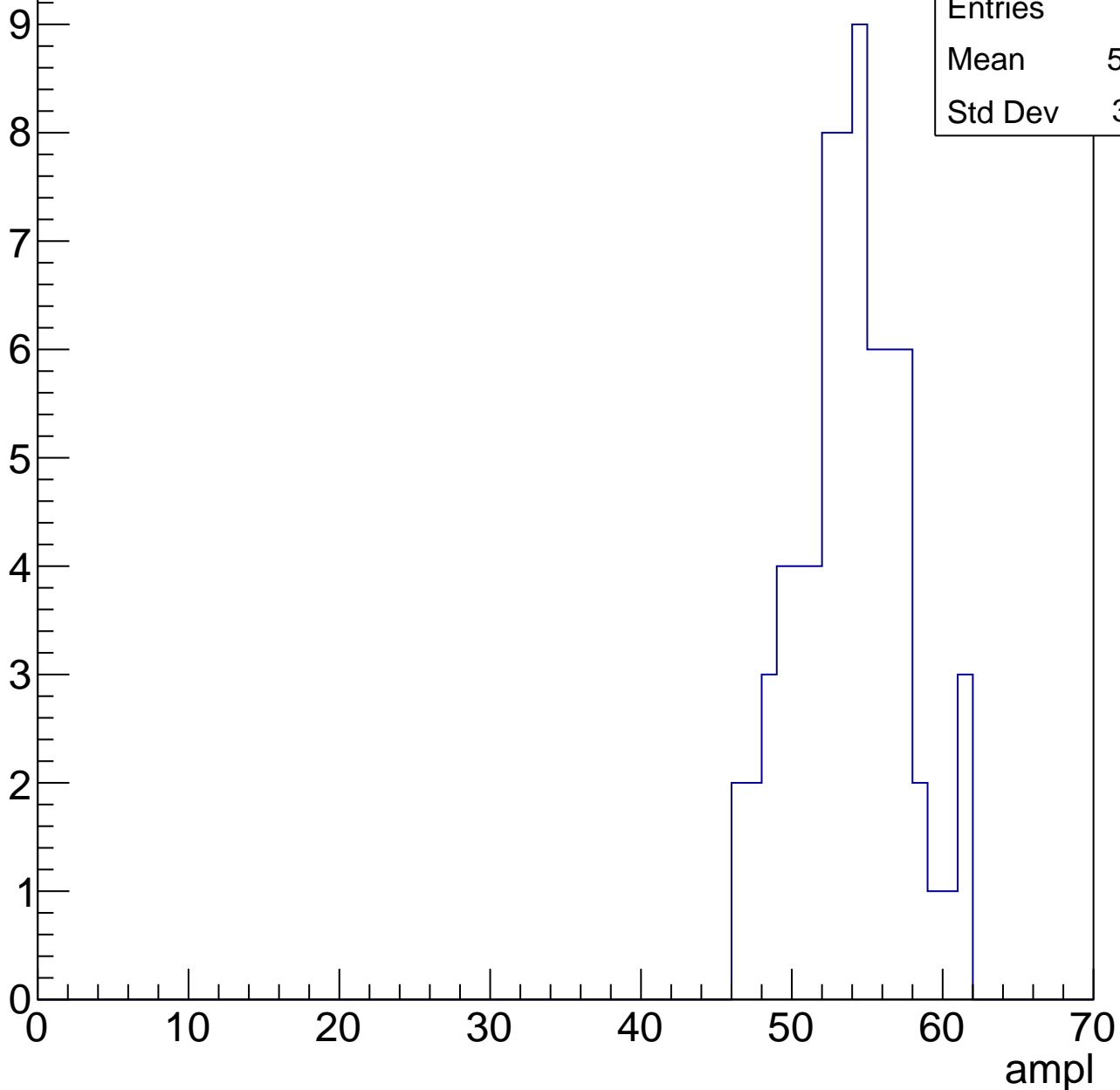


B0L001S, U25-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	53.36
Std Dev	3.551

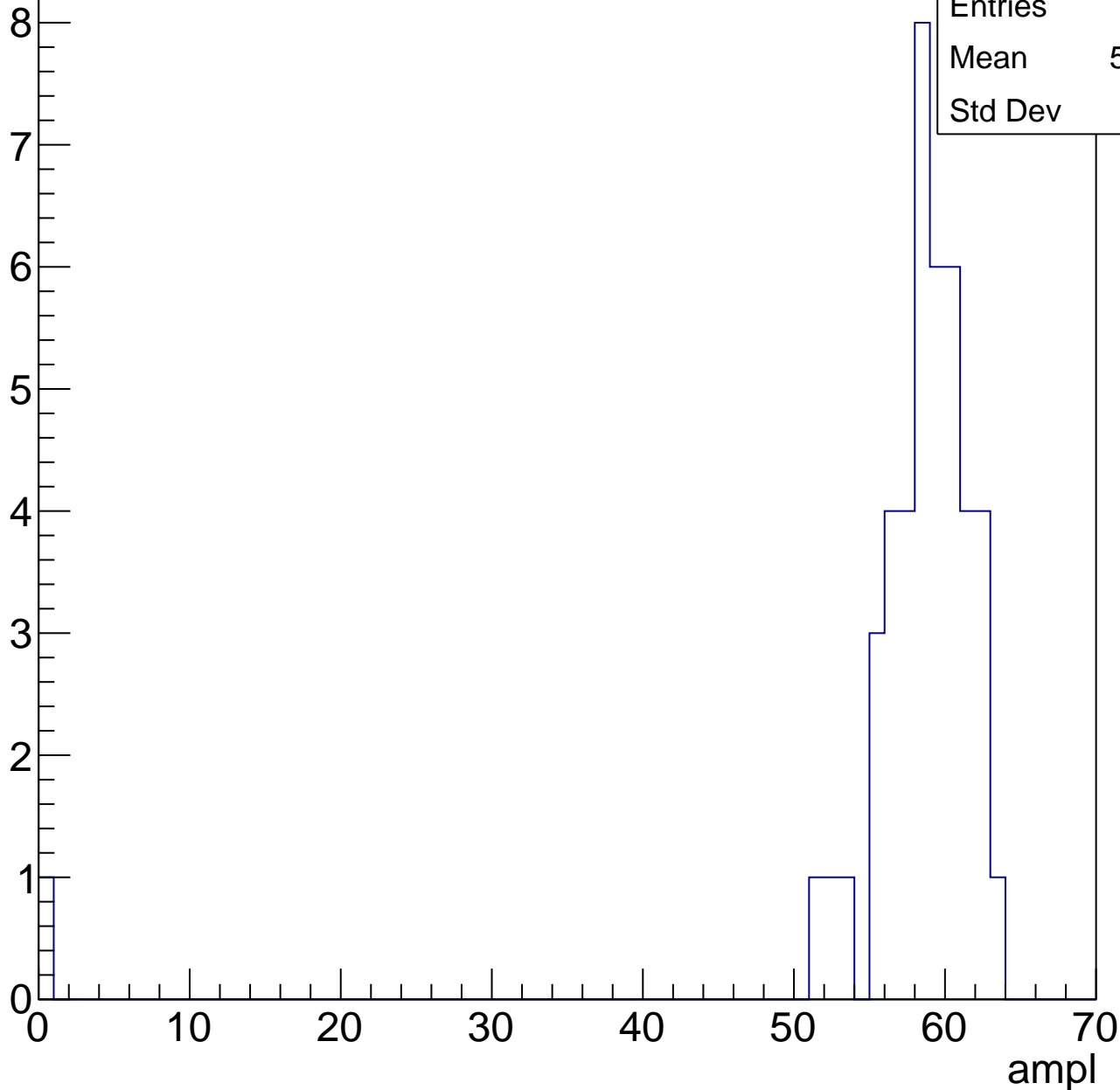


B0L001S, U25-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	56.95
Std Dev	9.08

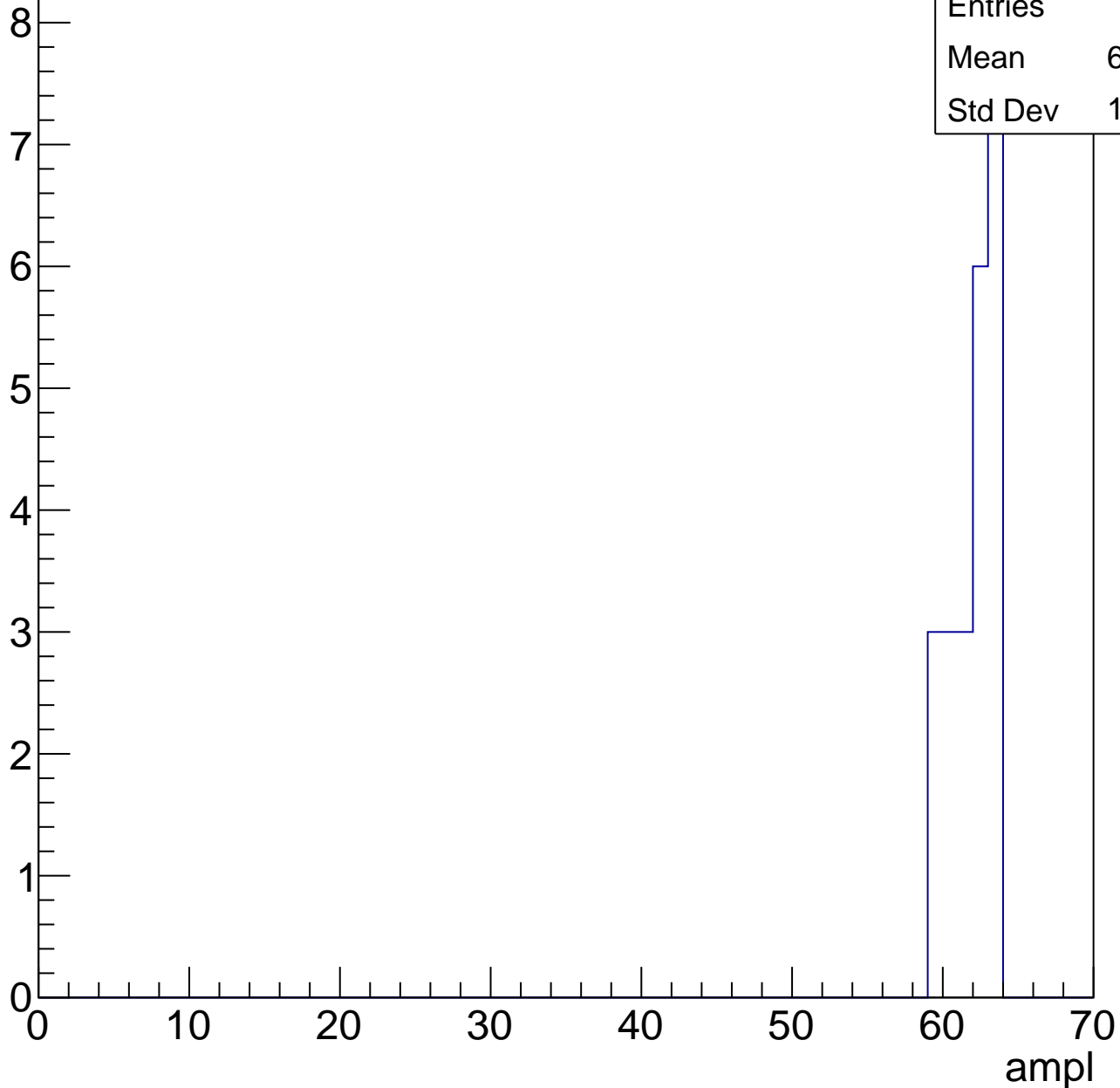


B0L001S, U25-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	61.57
Std Dev	1.409



B0L001S, U25-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0