

B1L103S, U10-ch0, adc0

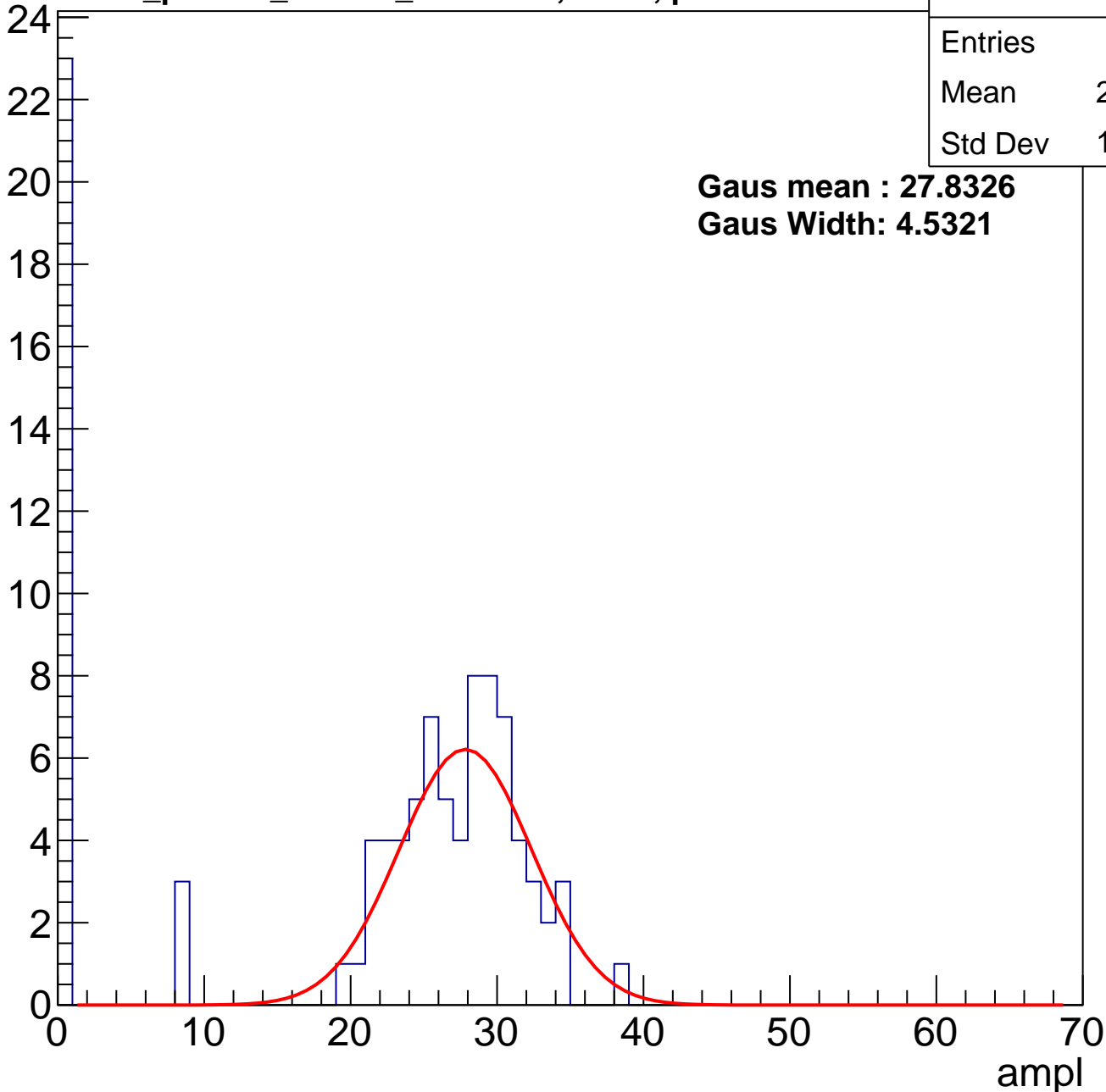
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	20.12
Std Dev	12.16

Gaus mean : 27.8326

Gaus Width: 4.5321

Entry



B1L103S, U10-ch0, adc1

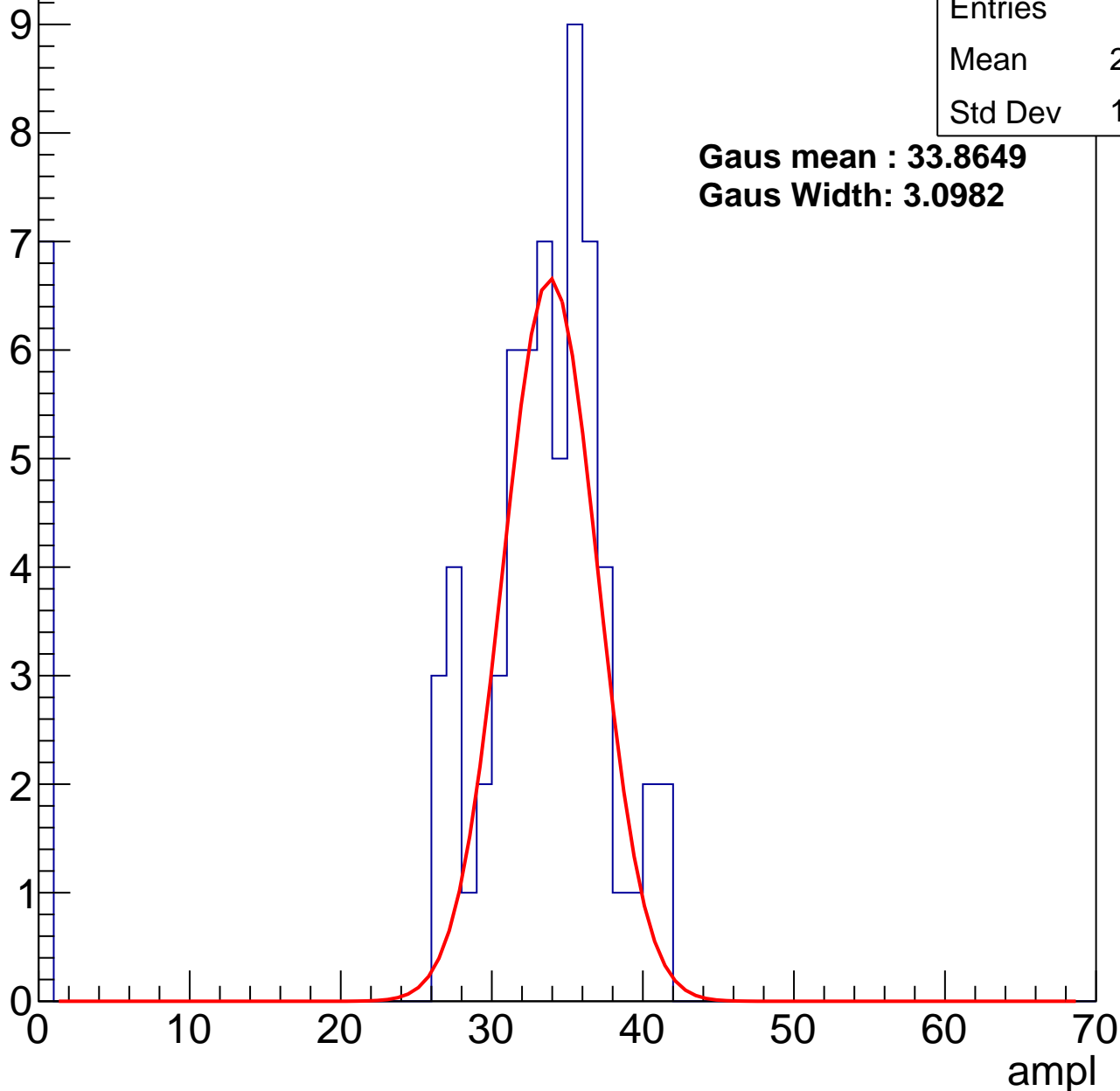
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	29.93
Std Dev	10.57

Gaus mean : 33.8649

Gaus Width: 3.0982



B1L103S, U10-ch0, adc2

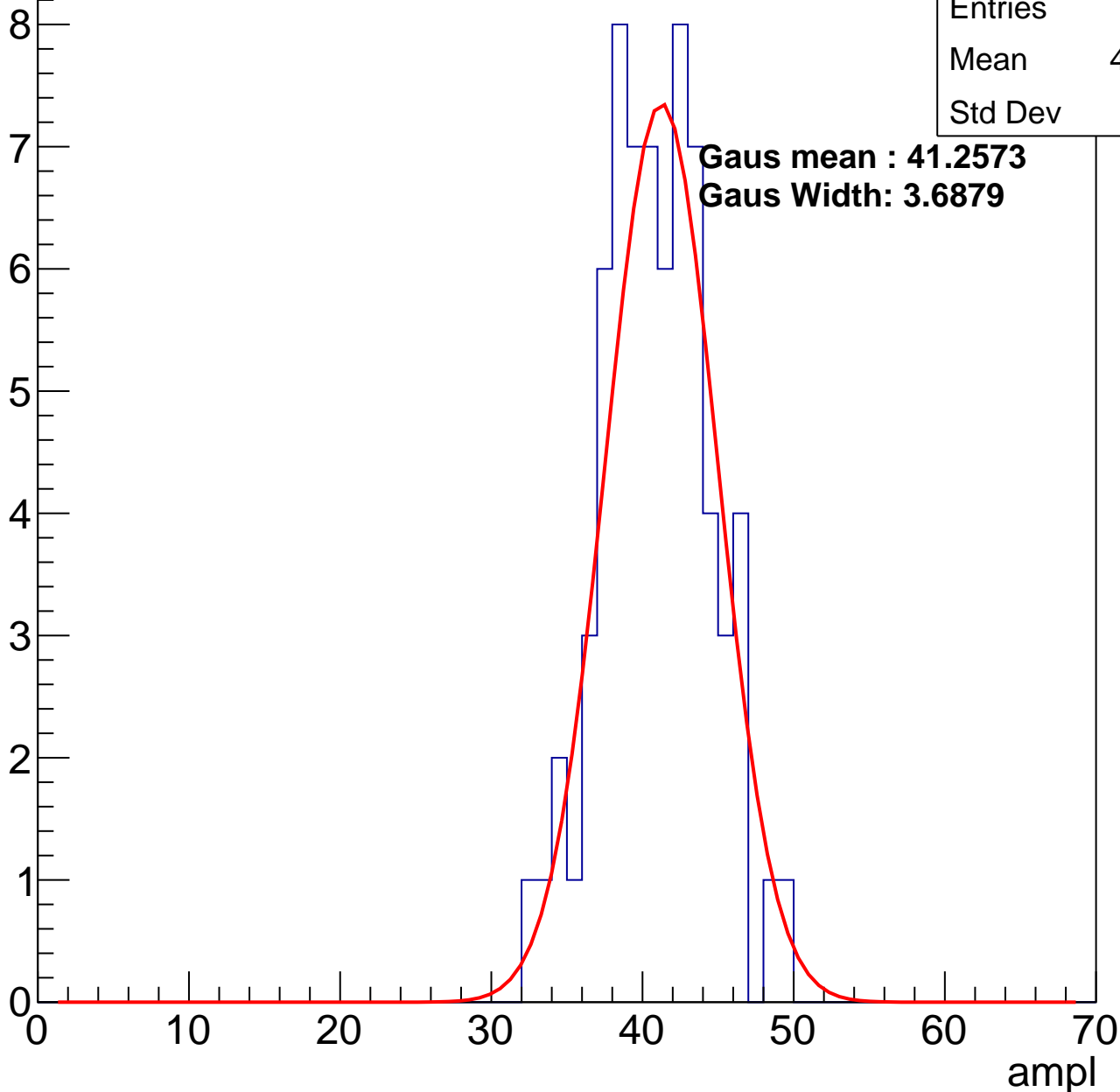
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	40.43
Std Dev	3.52

Gaus mean : 41.2573

Gaus Width: 3.6879

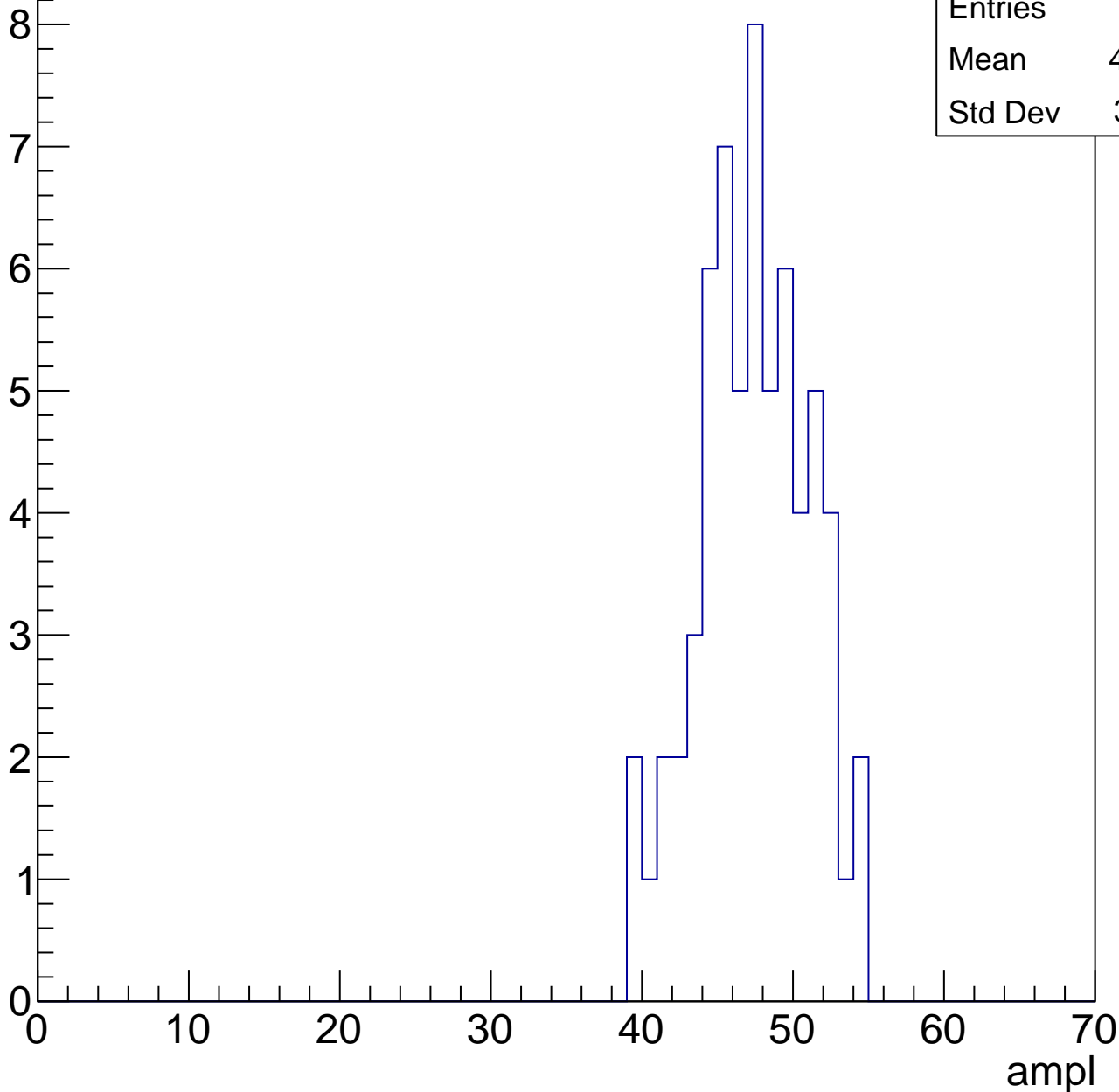


B1L103S, U10-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.92
Std Dev	3.591

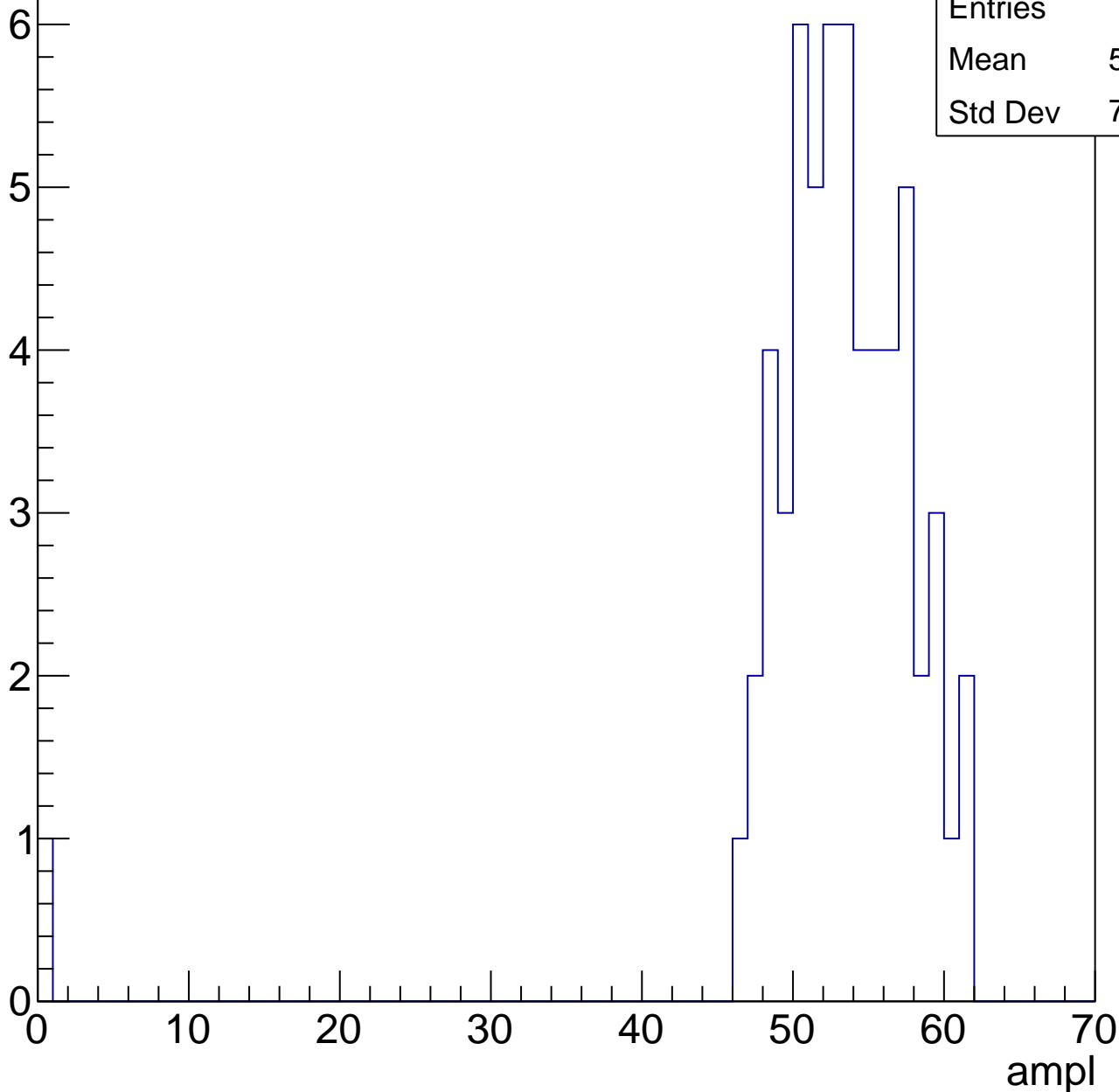


B1L103S, U10-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.27
Std Dev	7.809

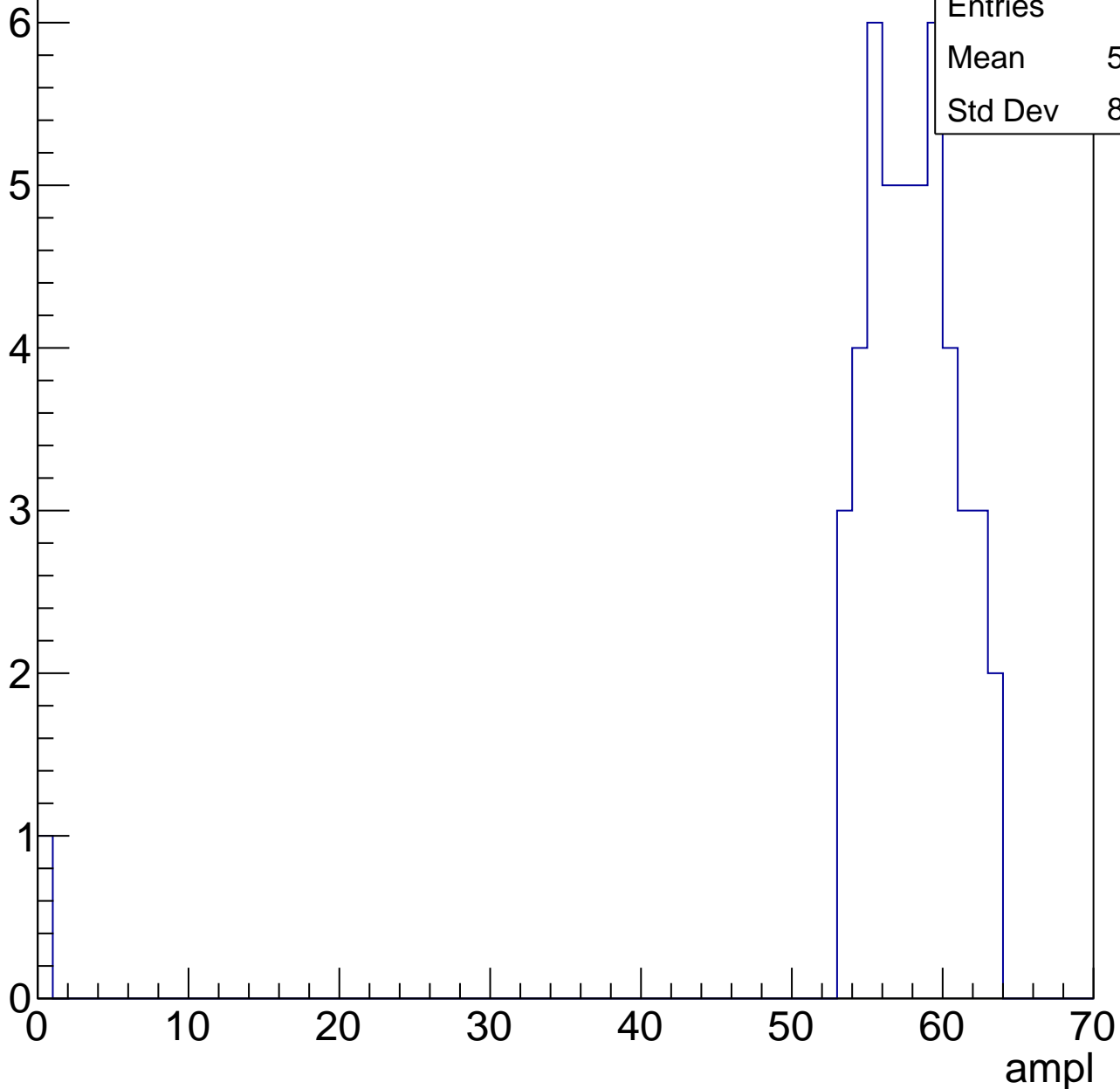


B1L103S, U10-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	56.36
Std Dev	8.755

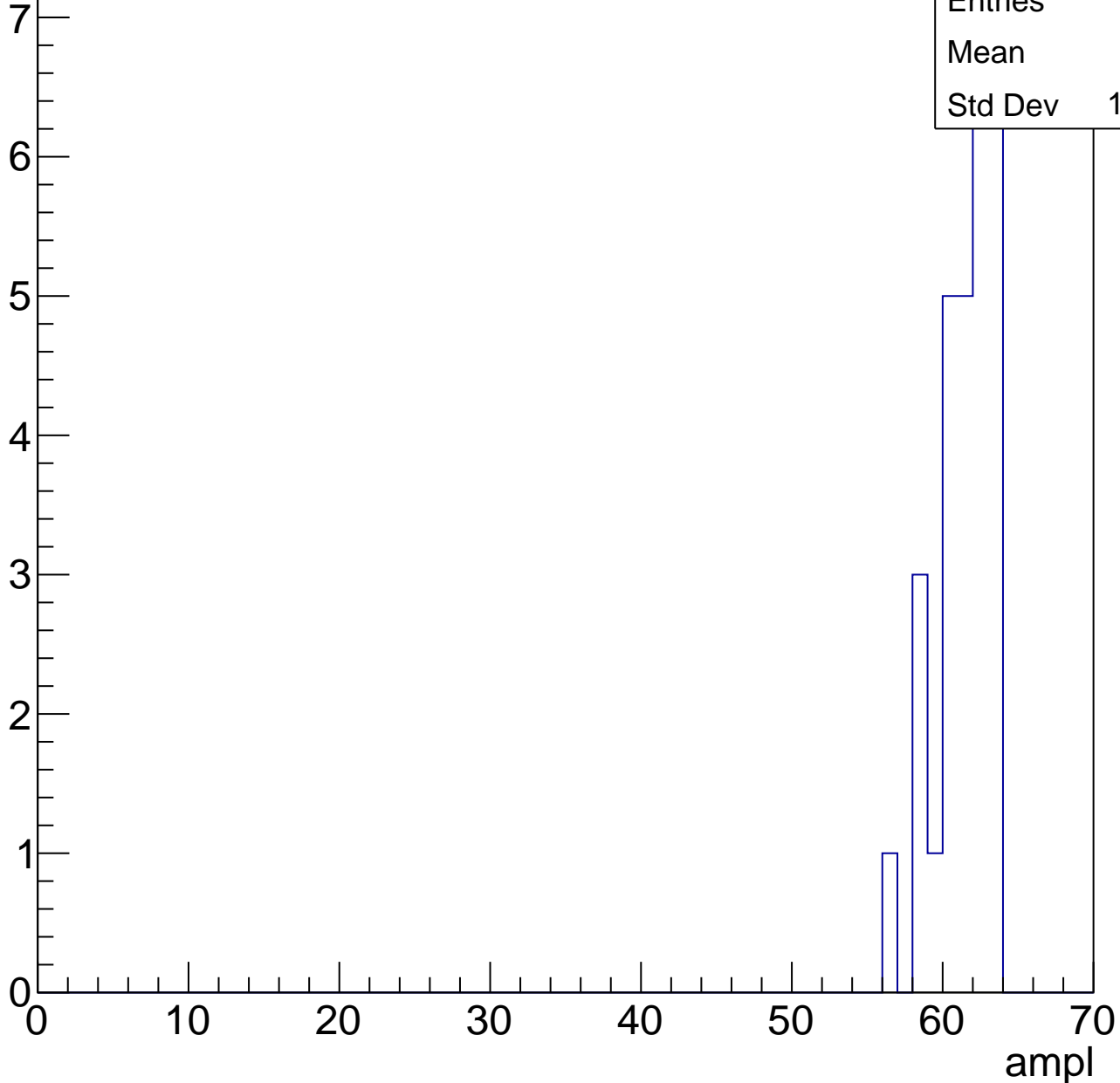


B1L103S, U10-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

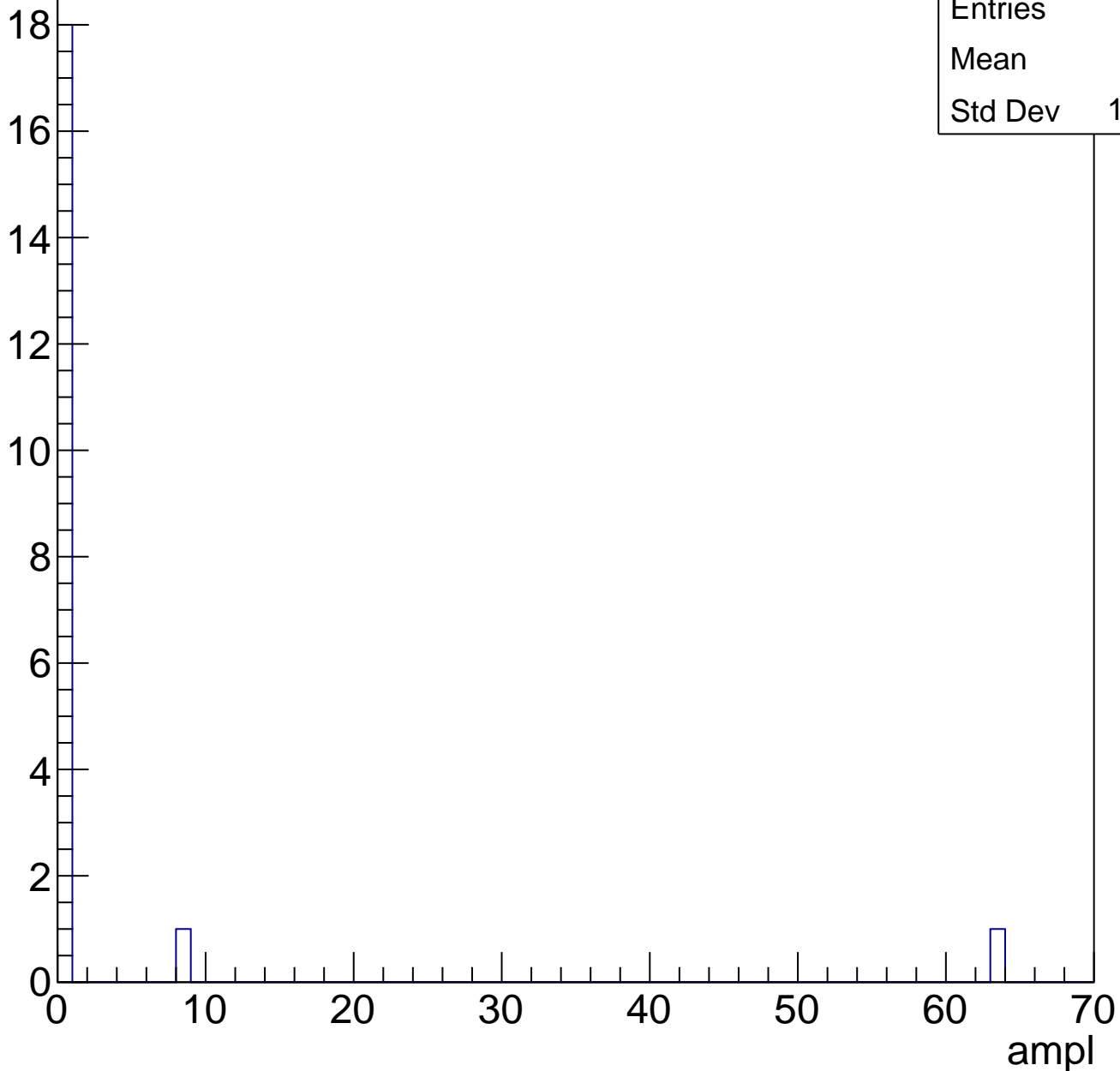
Entries	29
Mean	61
Std Dev	1.819



B1L103S, U10-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.55
Std Dev	13.75

B1L103S, U10-ch1, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	25.13
Std Dev	9.248

Gaus mean : 28.7044

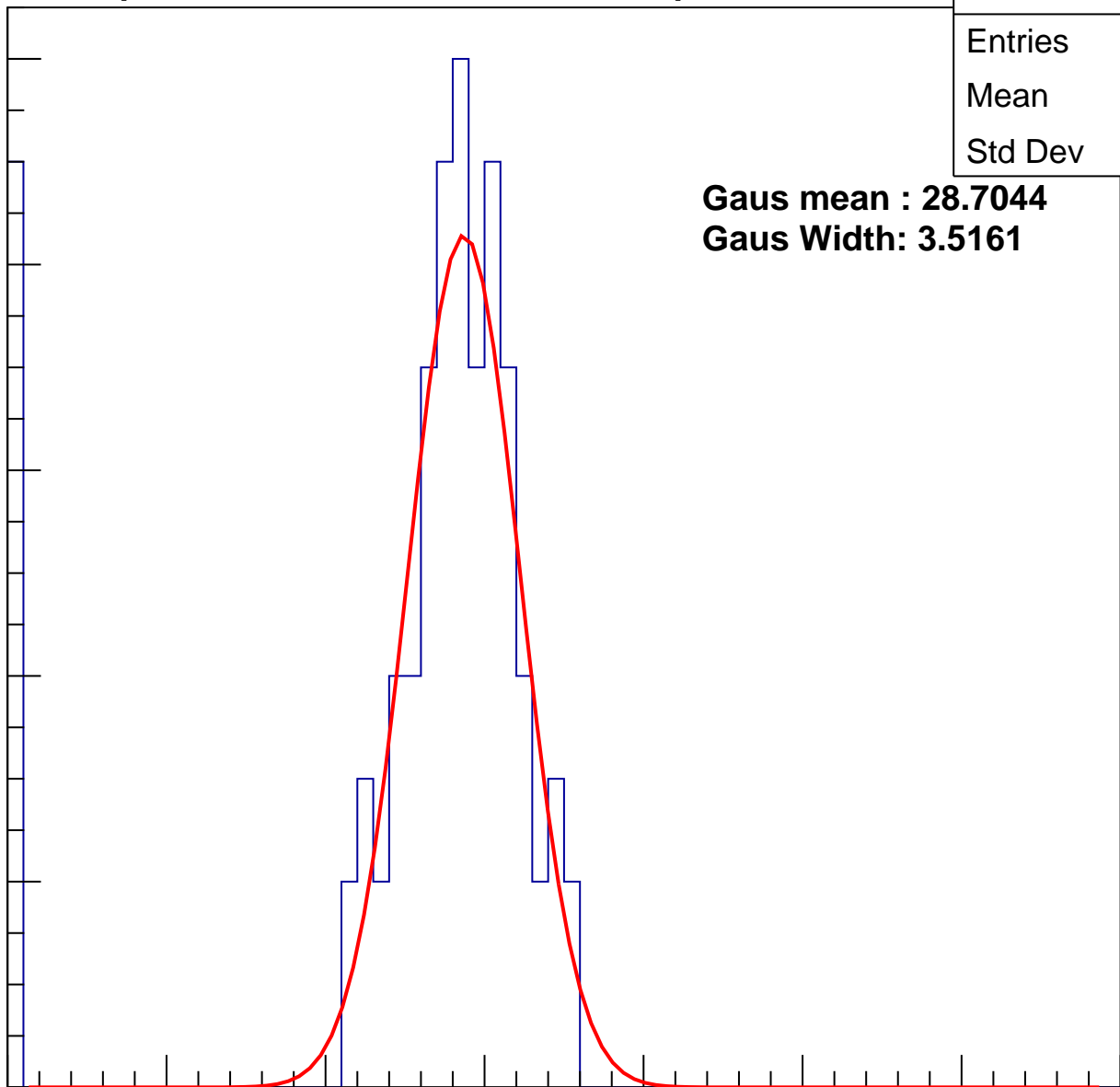
Gaus Width: 3.5161

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch1, adc1

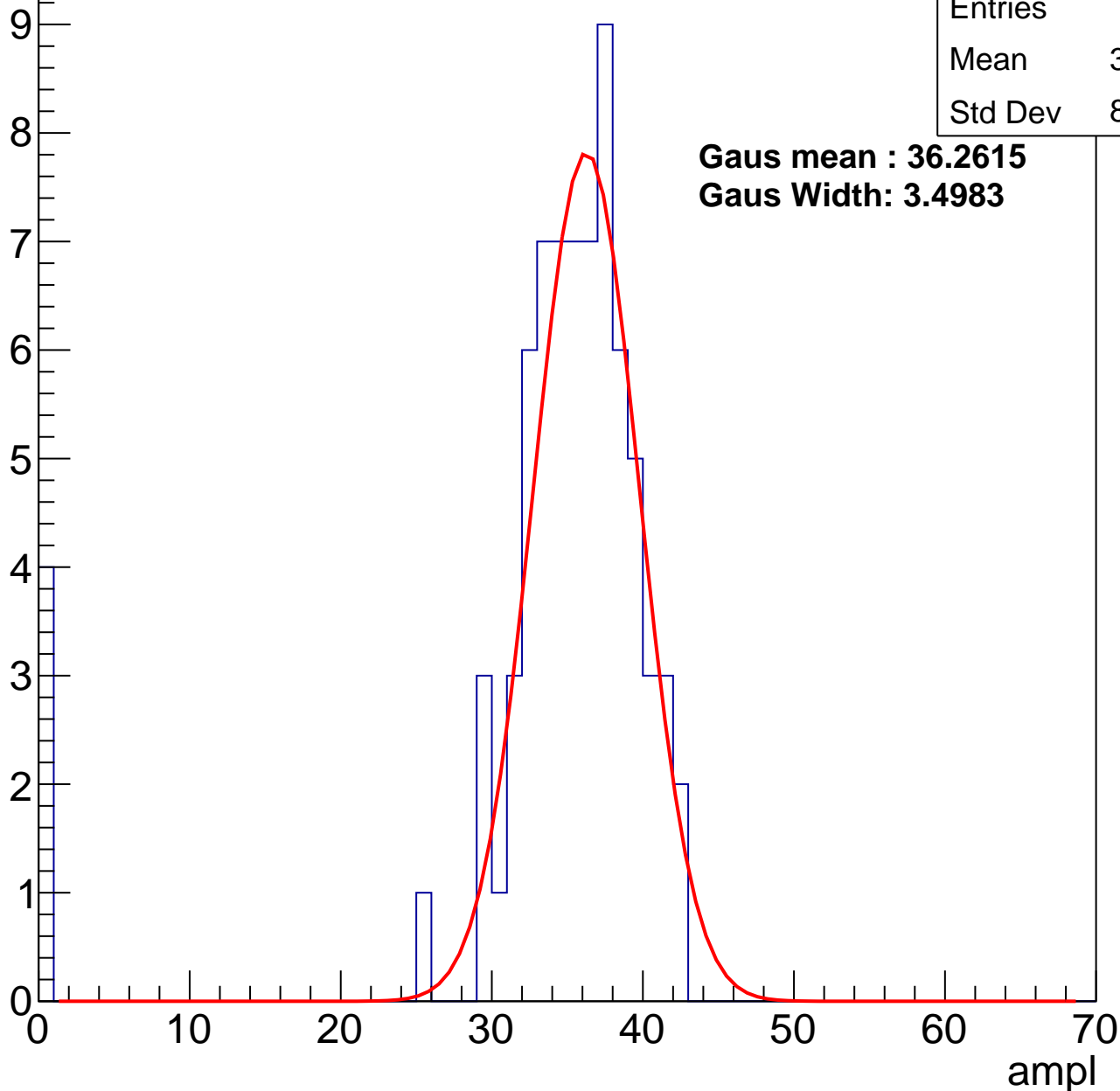
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	33.46
Std Dev	8.662

Gaus mean : 36.2615

Gaus Width: 3.4983



B1L103S, U10-ch1, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	38.82
Std Dev	11.73

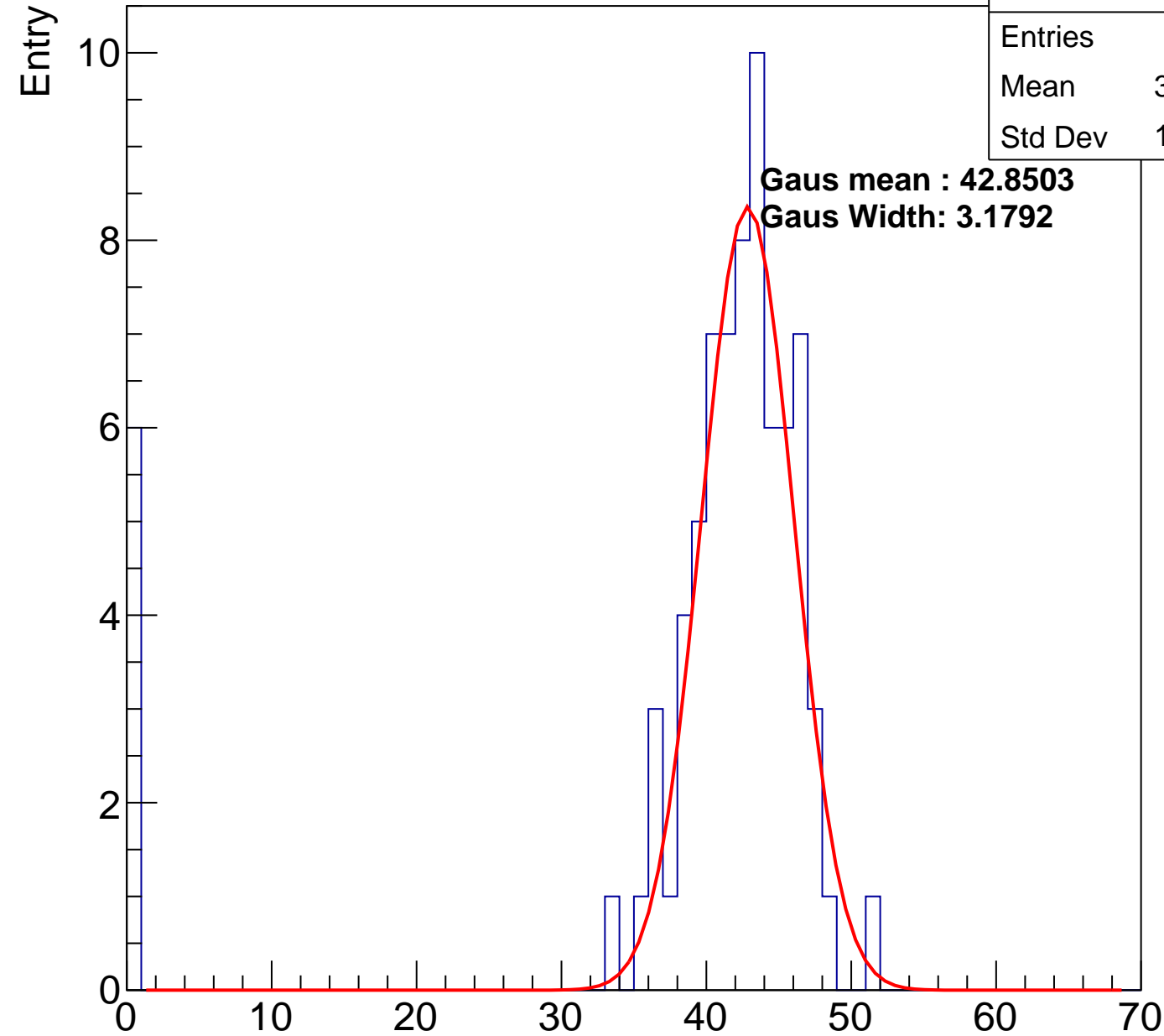
Gaus mean : 42.8503

Gaus Width: 3.1792

Entry

10
8
6
4
2
0

ampl

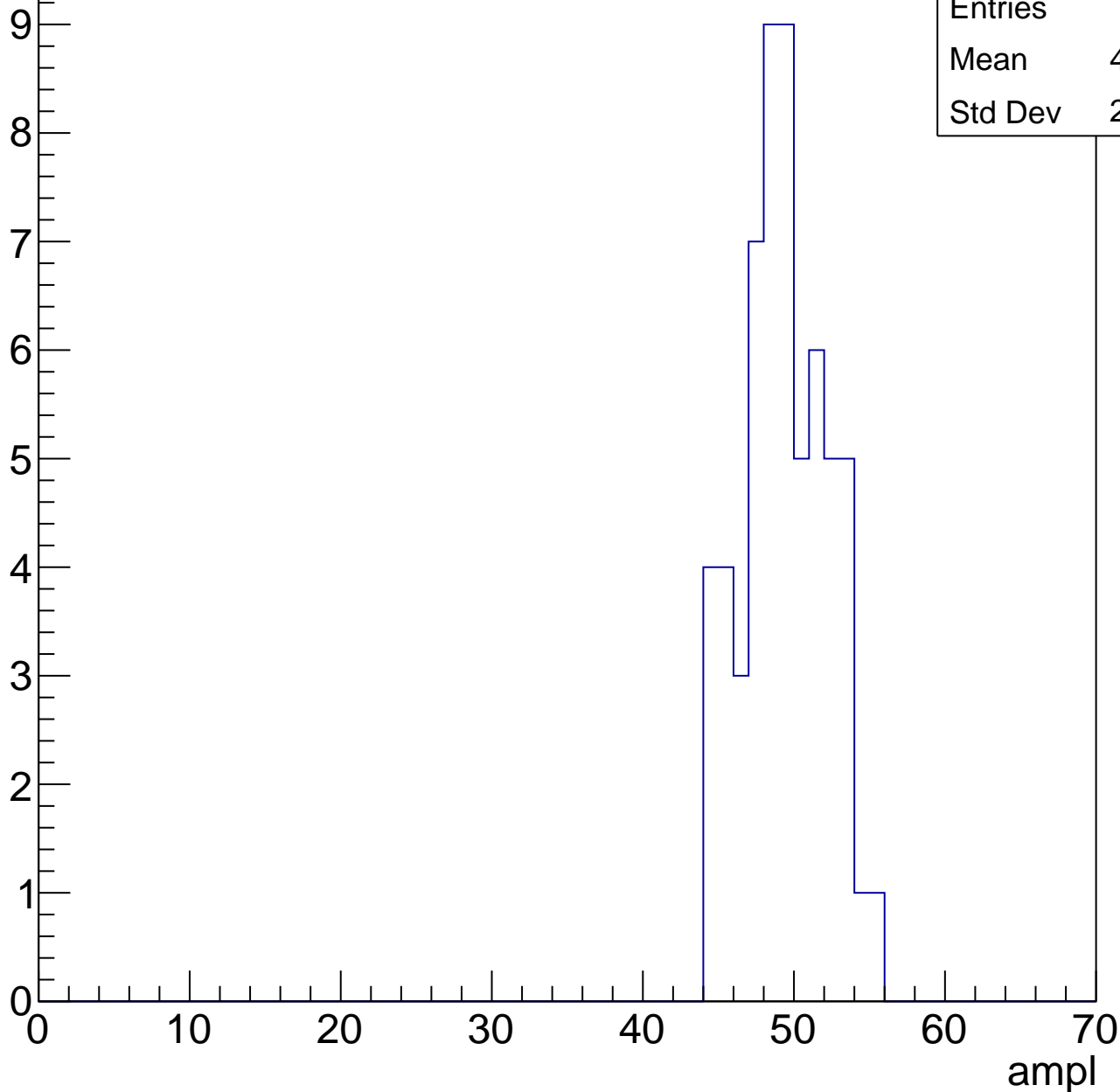


B1L103S, U10-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	48.92
Std Dev	2.739

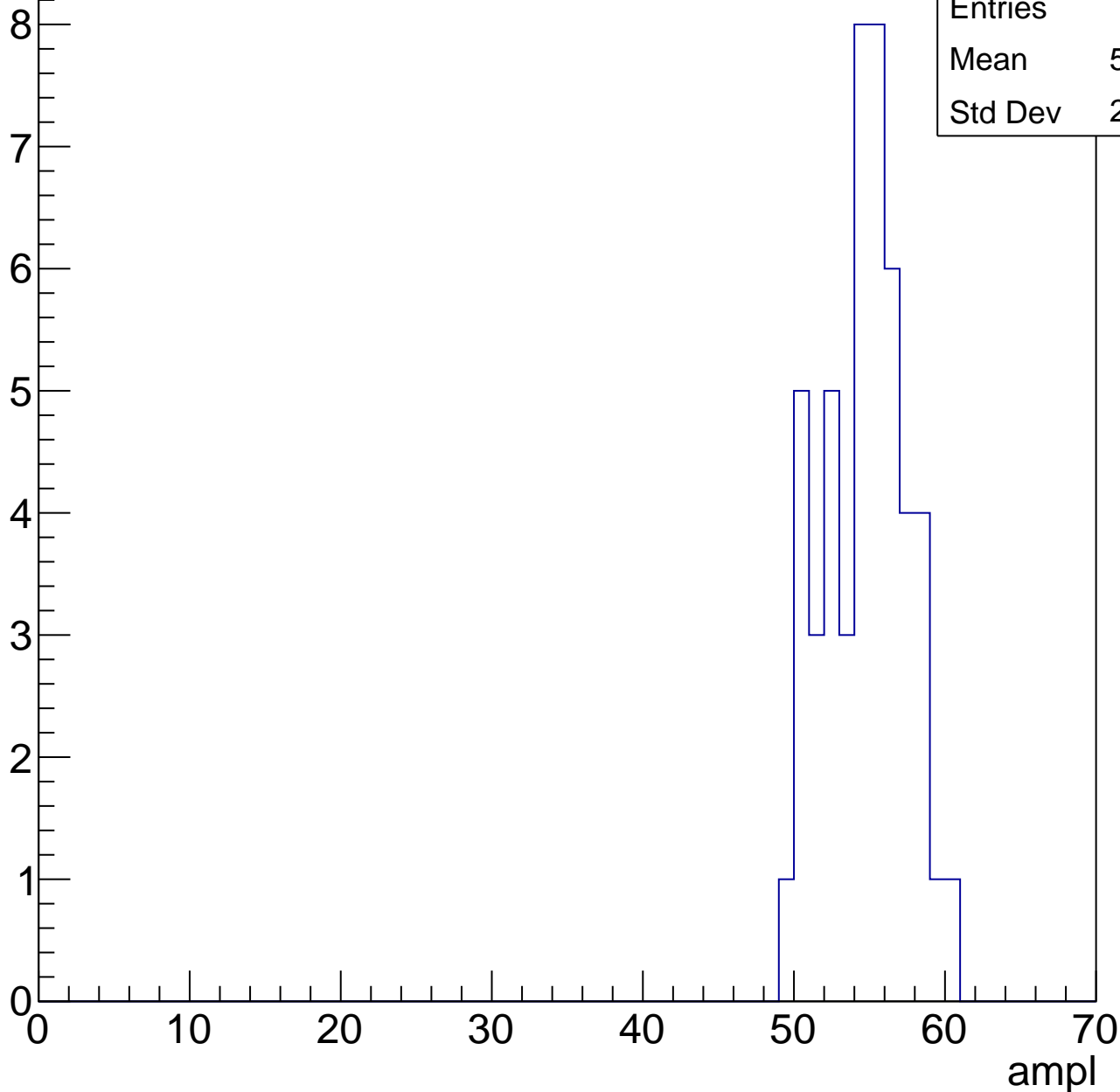


B1L103S, U10-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.24
Std Dev	2.654



B1L103S, U10-ch1, adc5

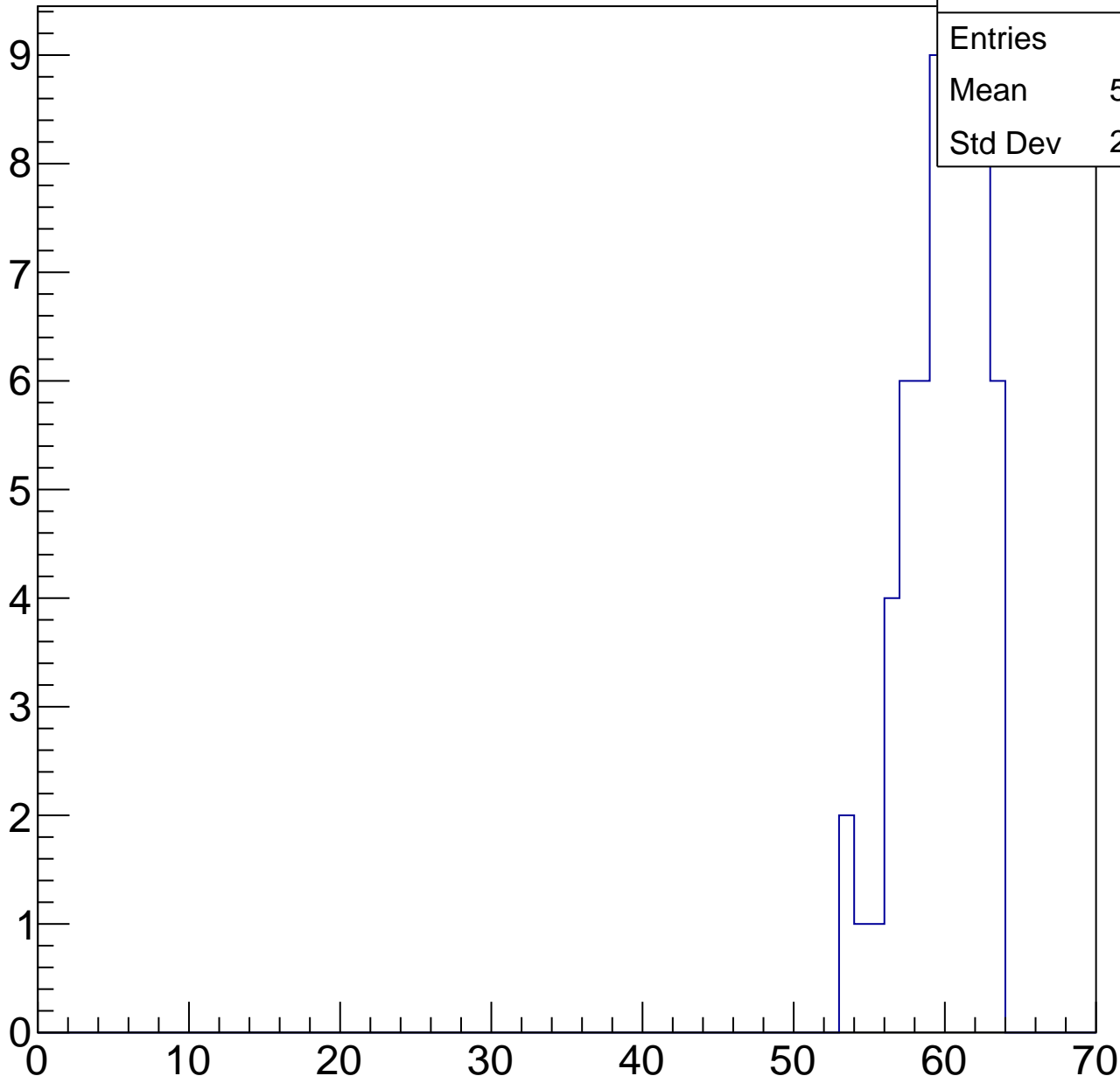
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	59.38
Std Dev	2.524

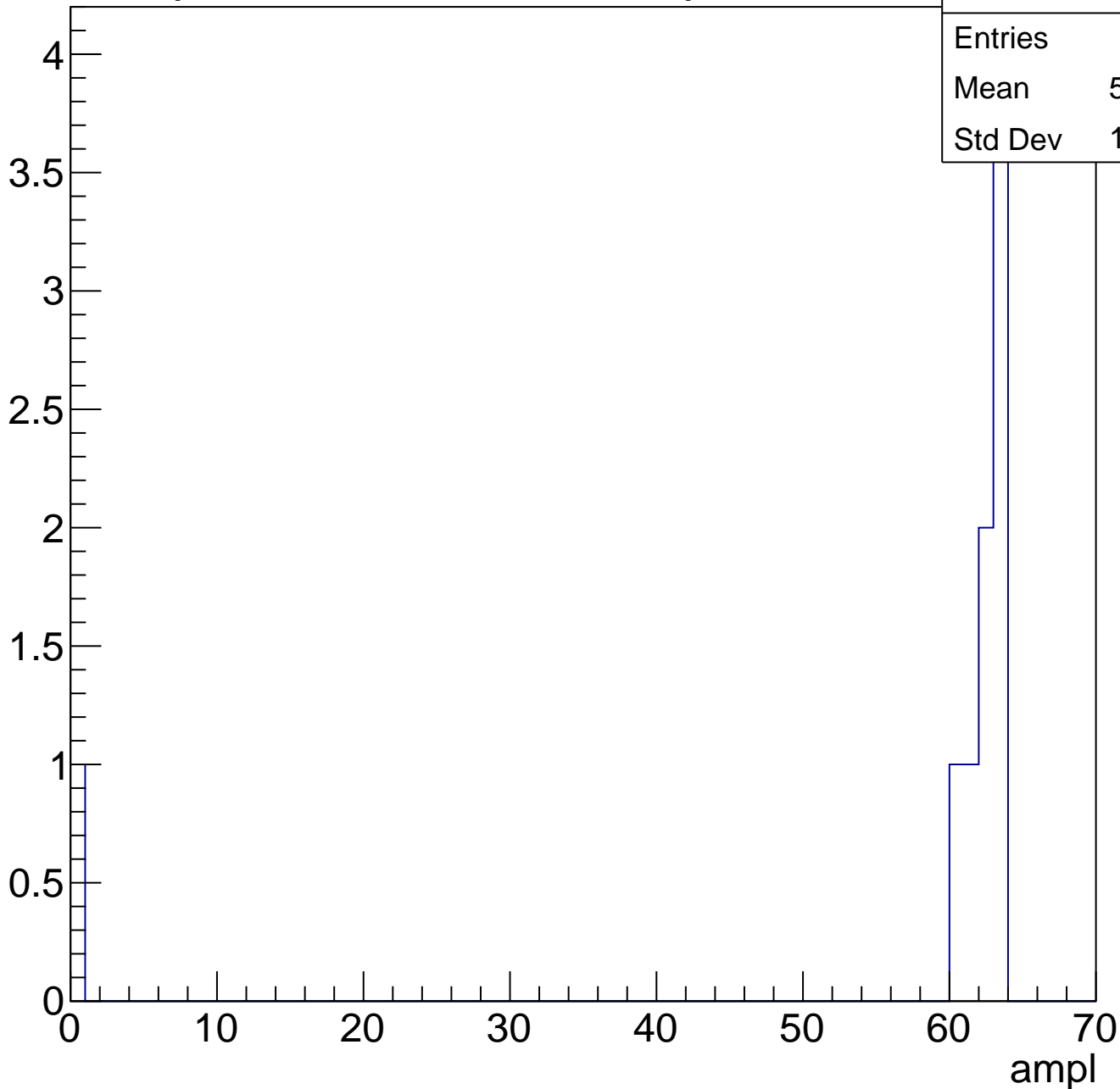
ampl



B1L103S, U10-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

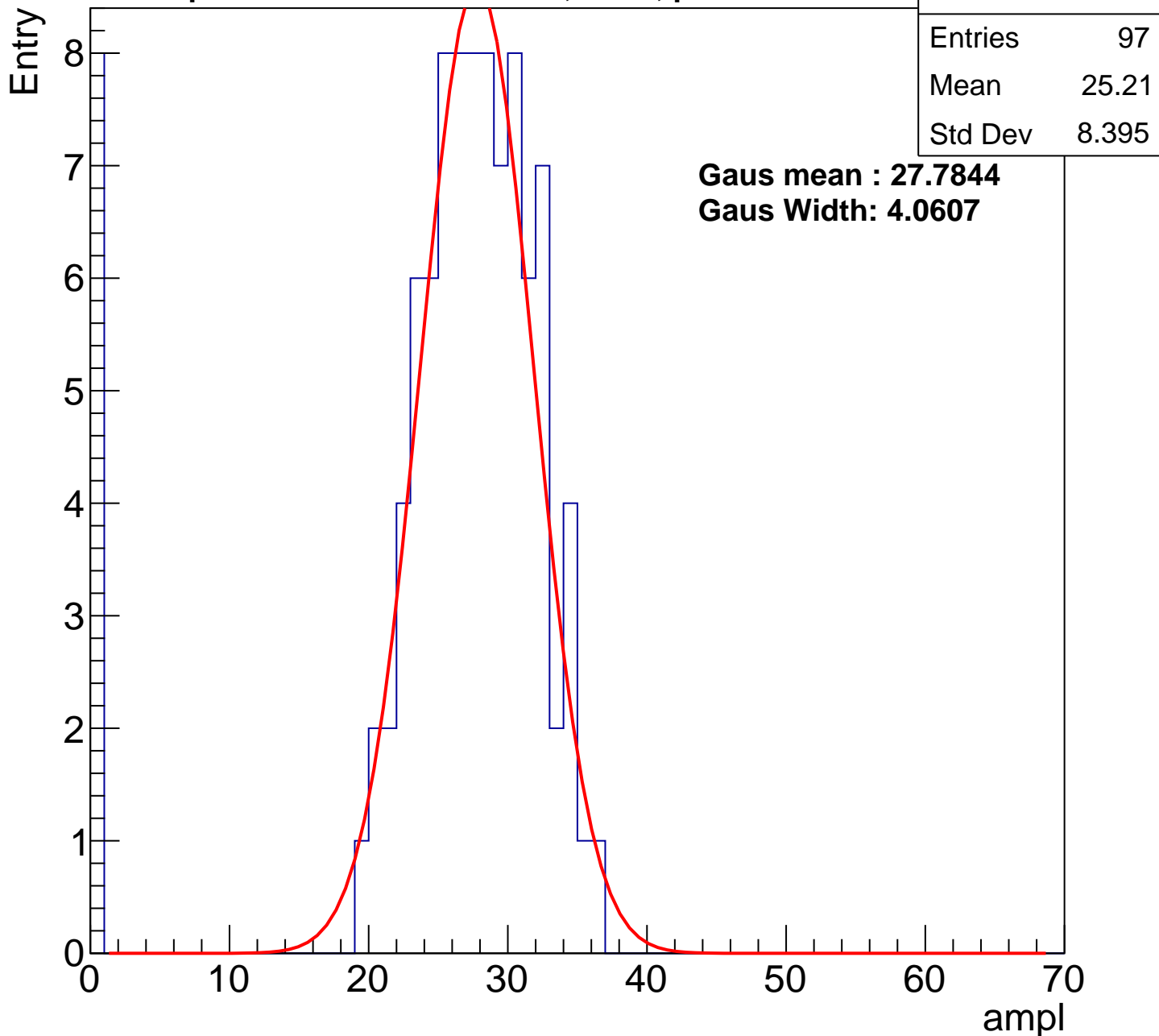
18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch2, adc0

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch2, adc1

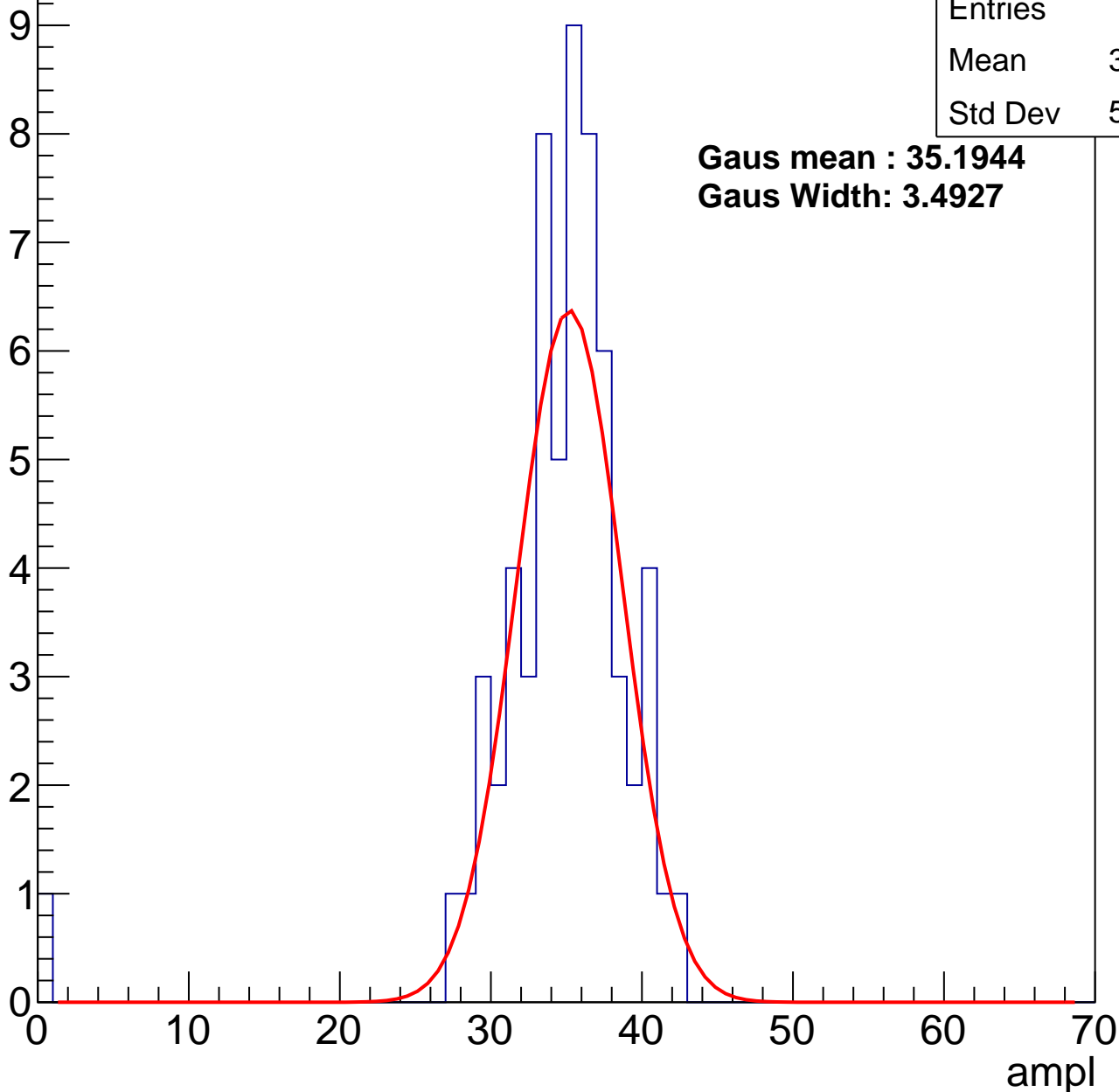
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.13
Std Dev	5.467

Gaus mean : 35.1944

Gaus Width: 3.4927



B1L103S, U10-ch2, adc2

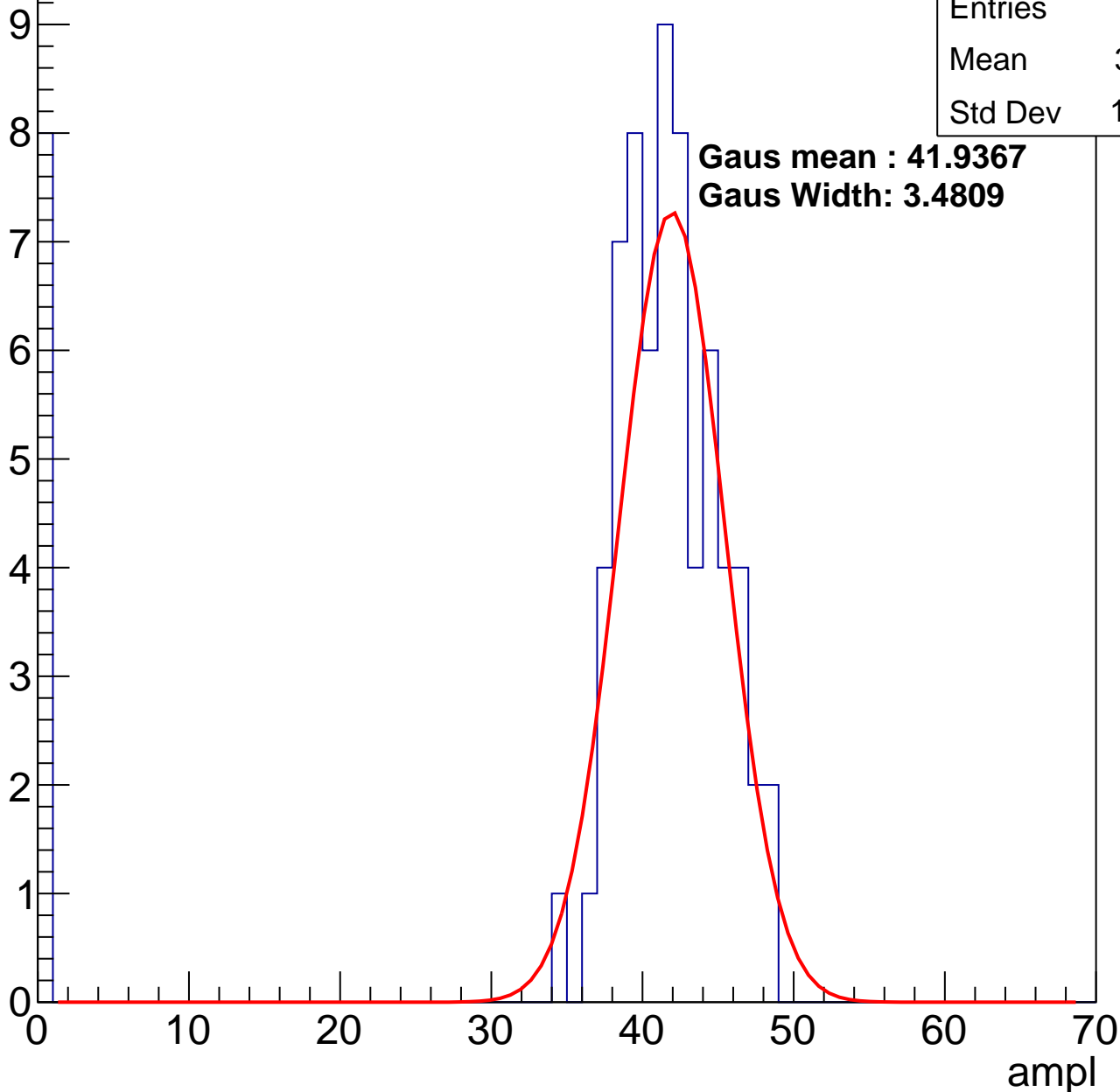
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.91
Std Dev	13.18

Gaus mean : 41.9367

Gaus Width: 3.4809

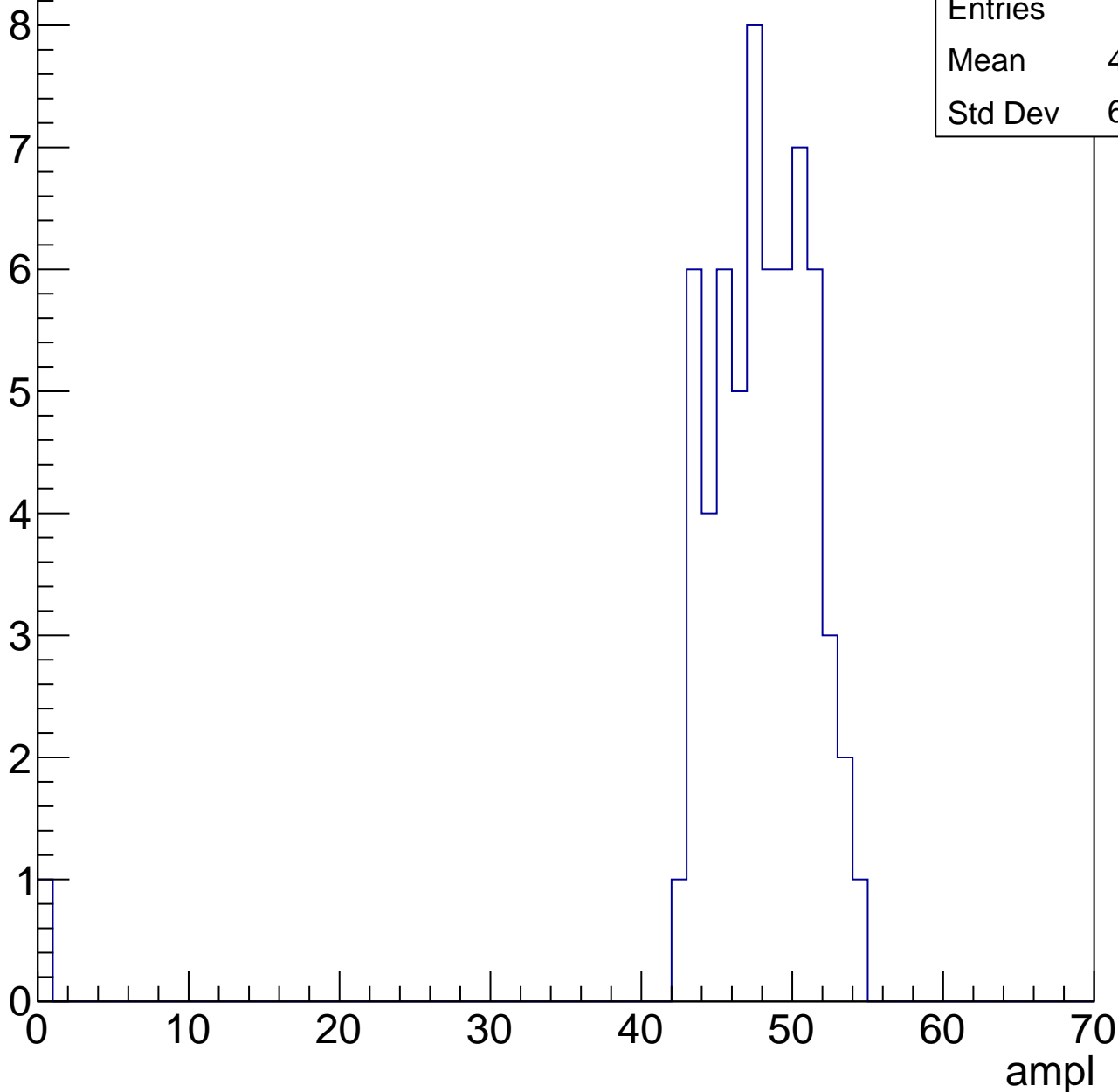


B1L103S, U10-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	46.87
Std Dev	6.695

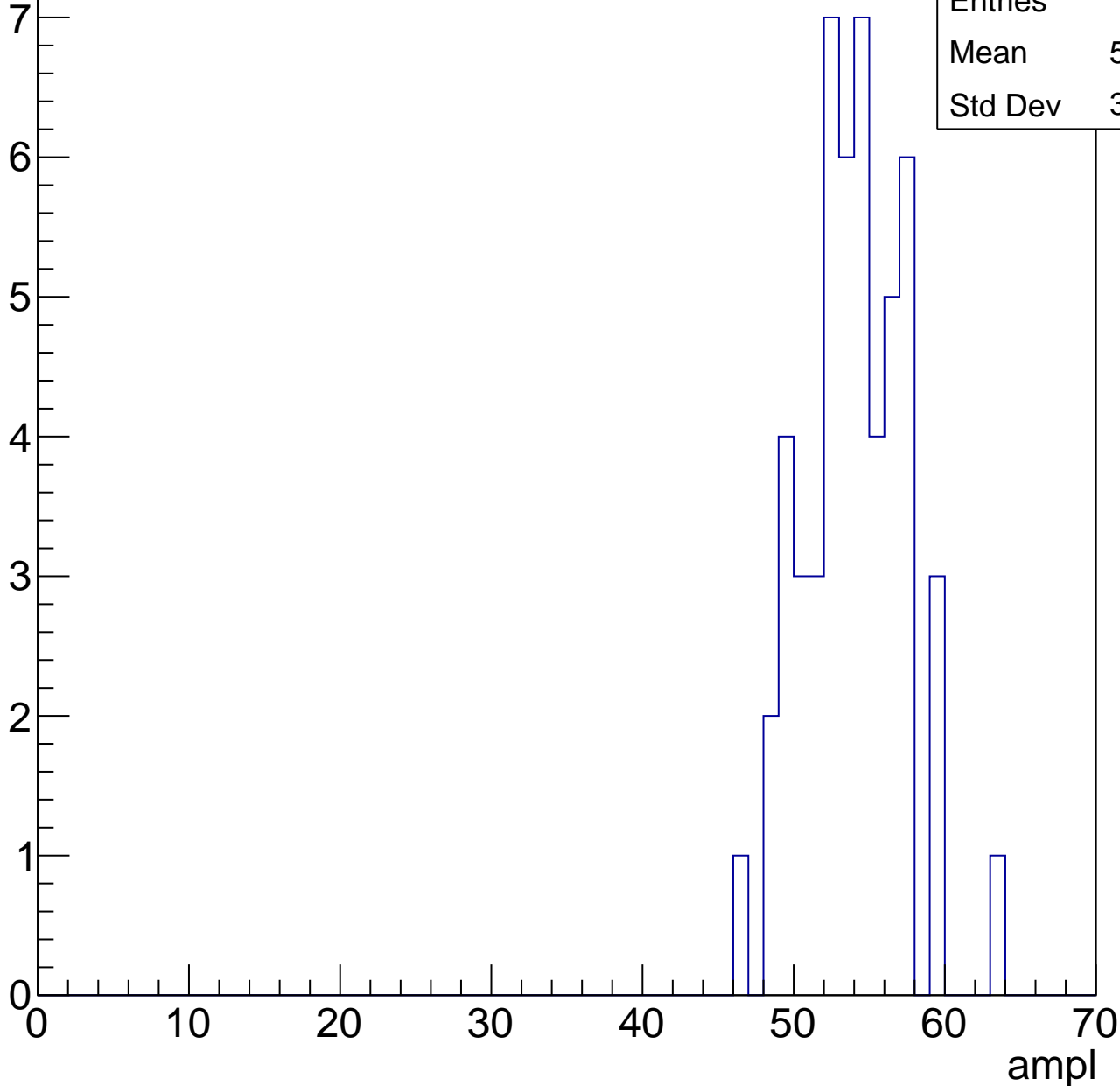


B1L103S, U10-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.52
Std Dev	3.302

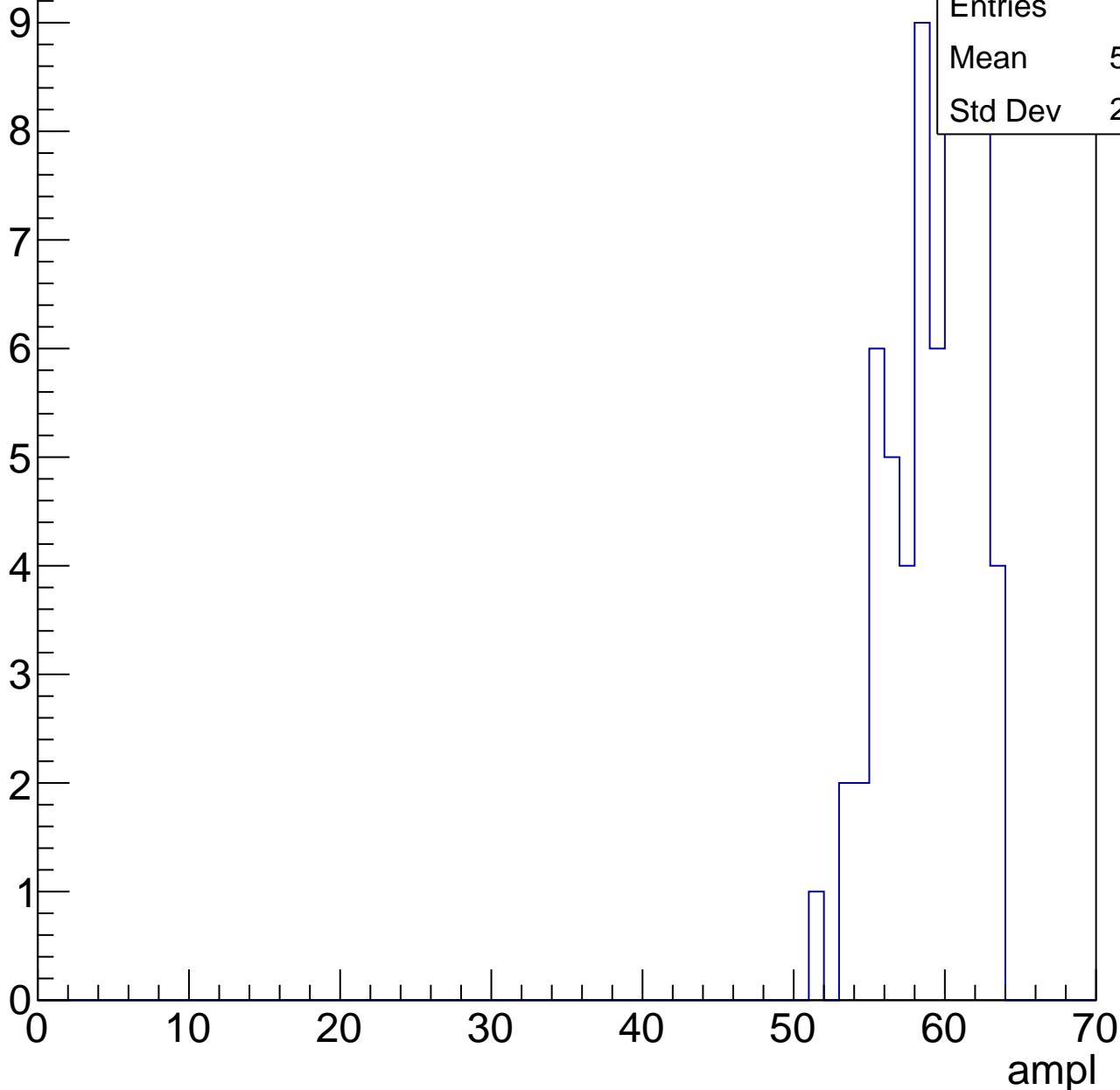


B1L103S, U10-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	58.72
Std Dev	2.864

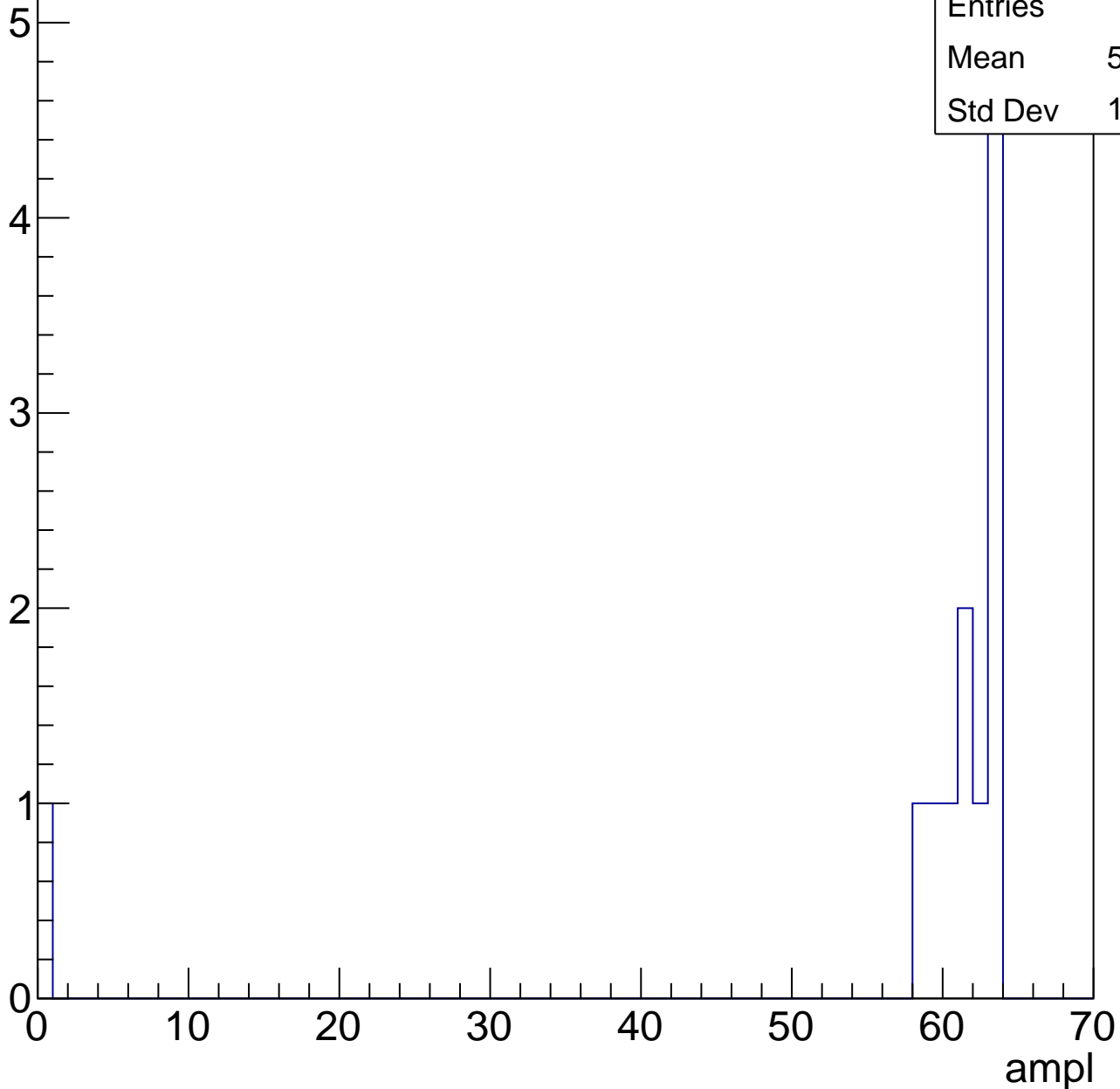


B1L103S, U10-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

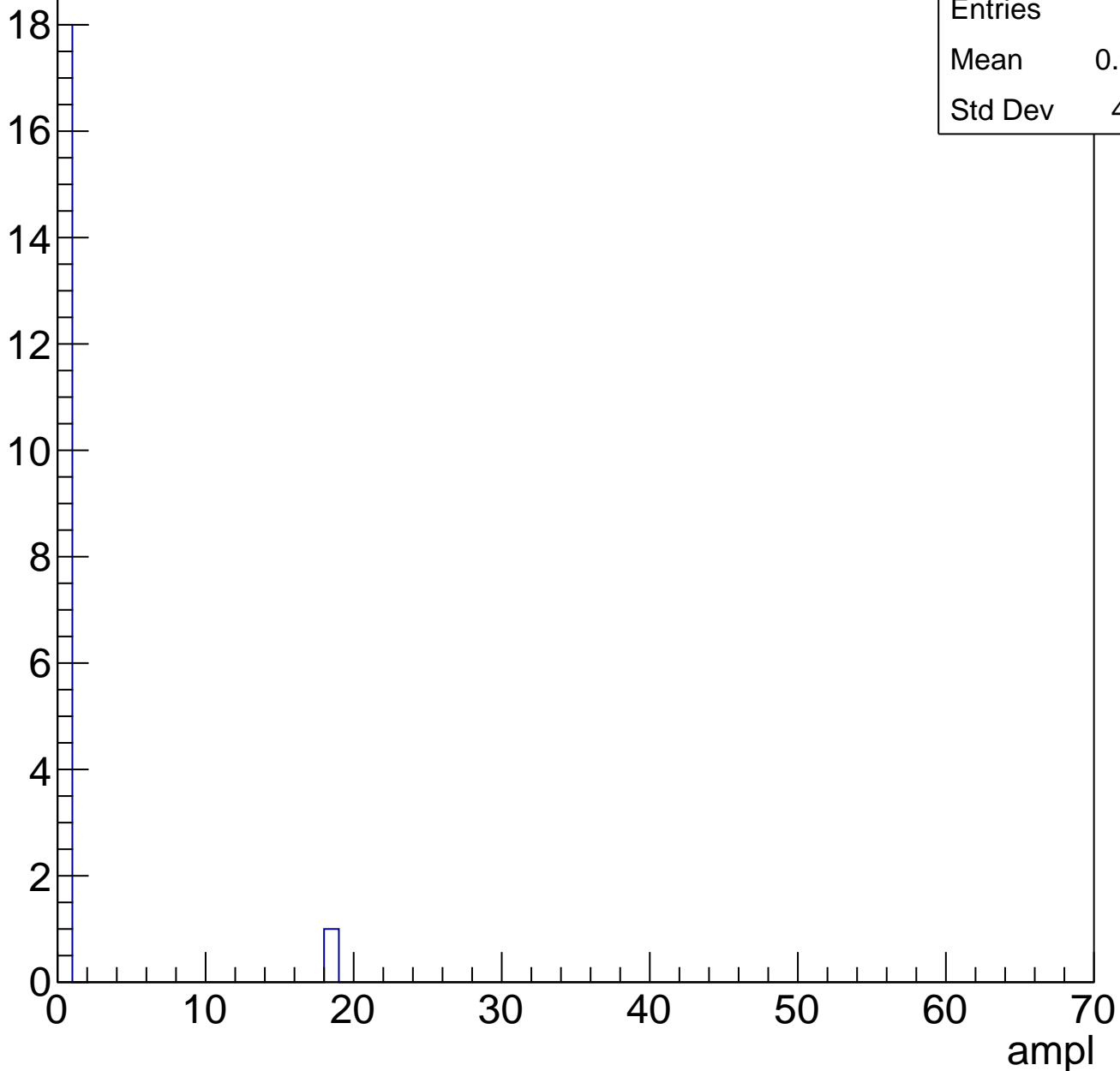
Entries	12
Mean	56.33
Std Dev	17.07



B1L103S, U10-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0.9474
Std Dev	4.019

B1L103S, U10-ch3, adc0

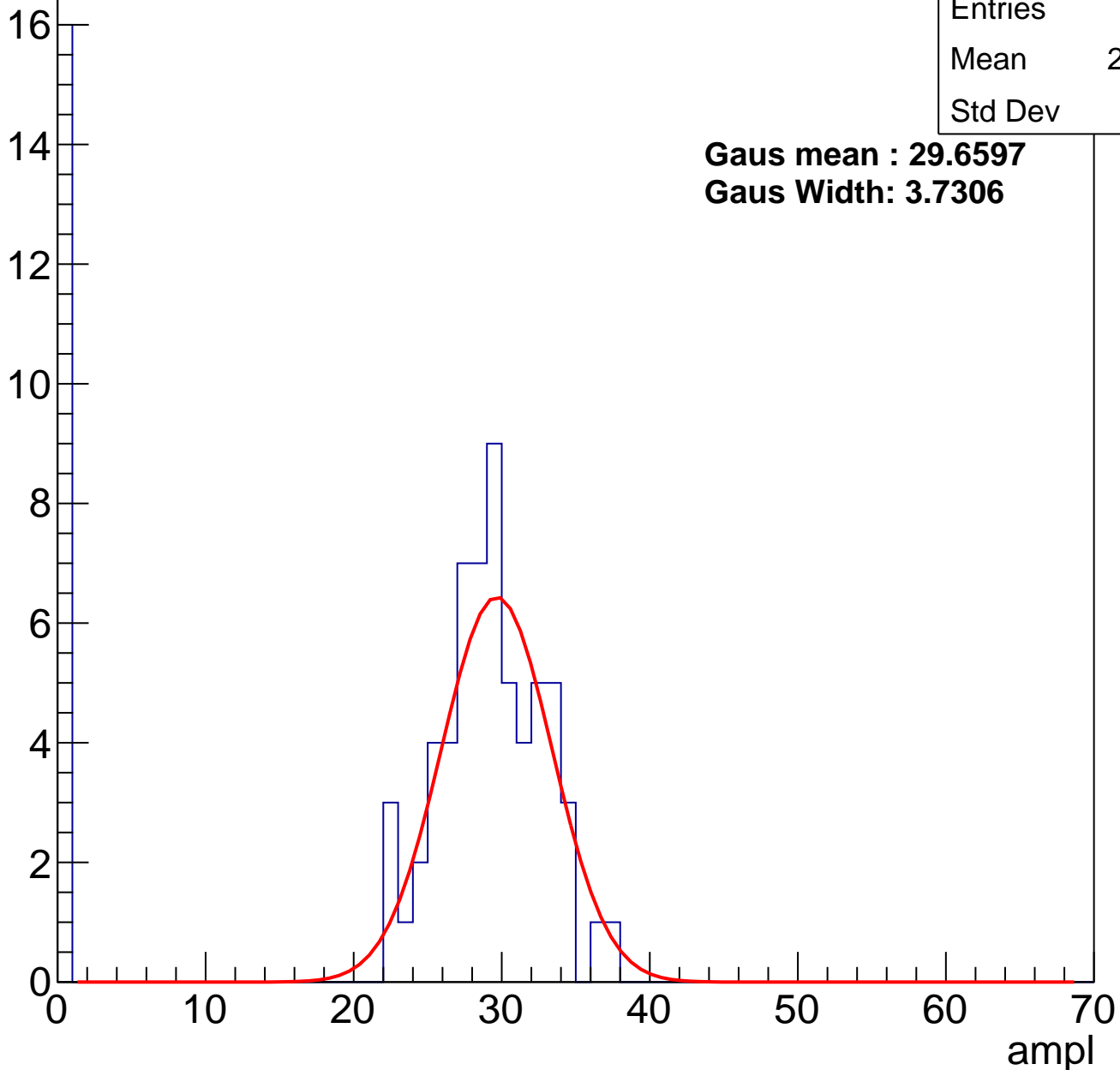
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	22.87
Std Dev	12.1

Gaus mean : 29.6597

Gaus Width: 3.7306

Entry



B1L103S, U10-ch3, adc1

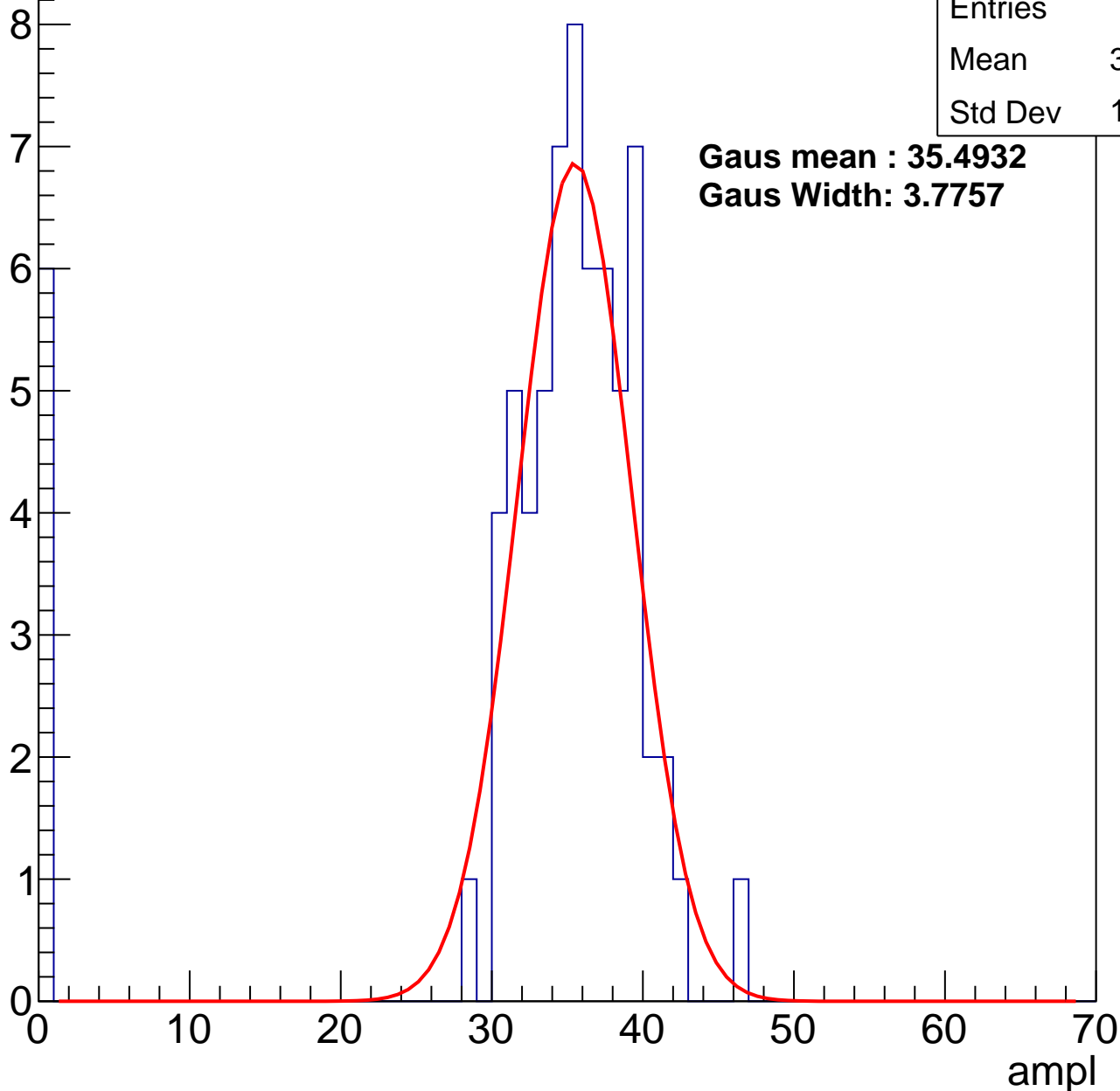
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.36
Std Dev	10.44

Gaus mean : 35.4932

Gaus Width: 3.7757



B1L103S, U10-ch3, adc2

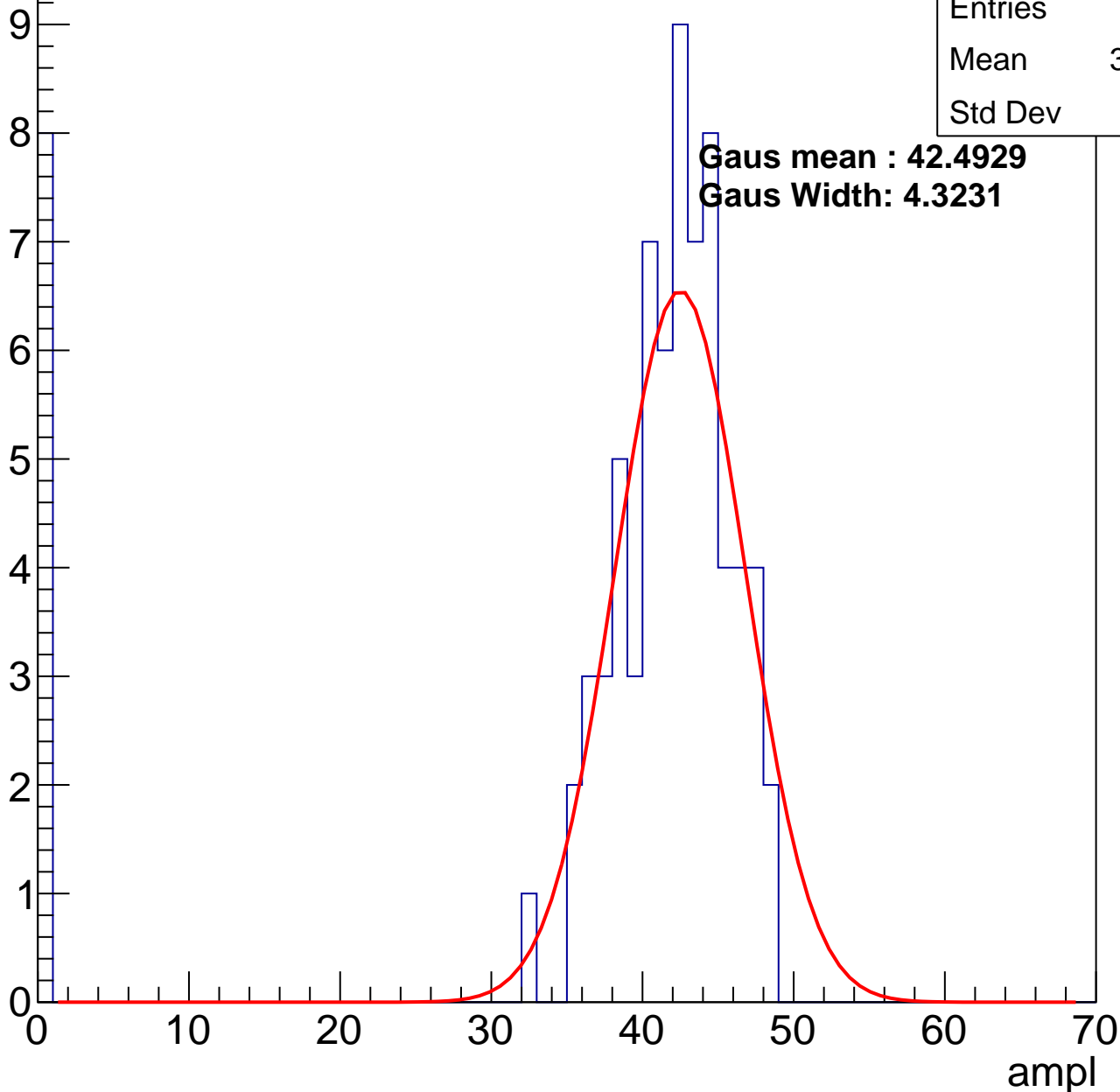
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	37.28
Std Dev	13.2

Gaus mean : 42.4929

Gaus Width: 4.3231

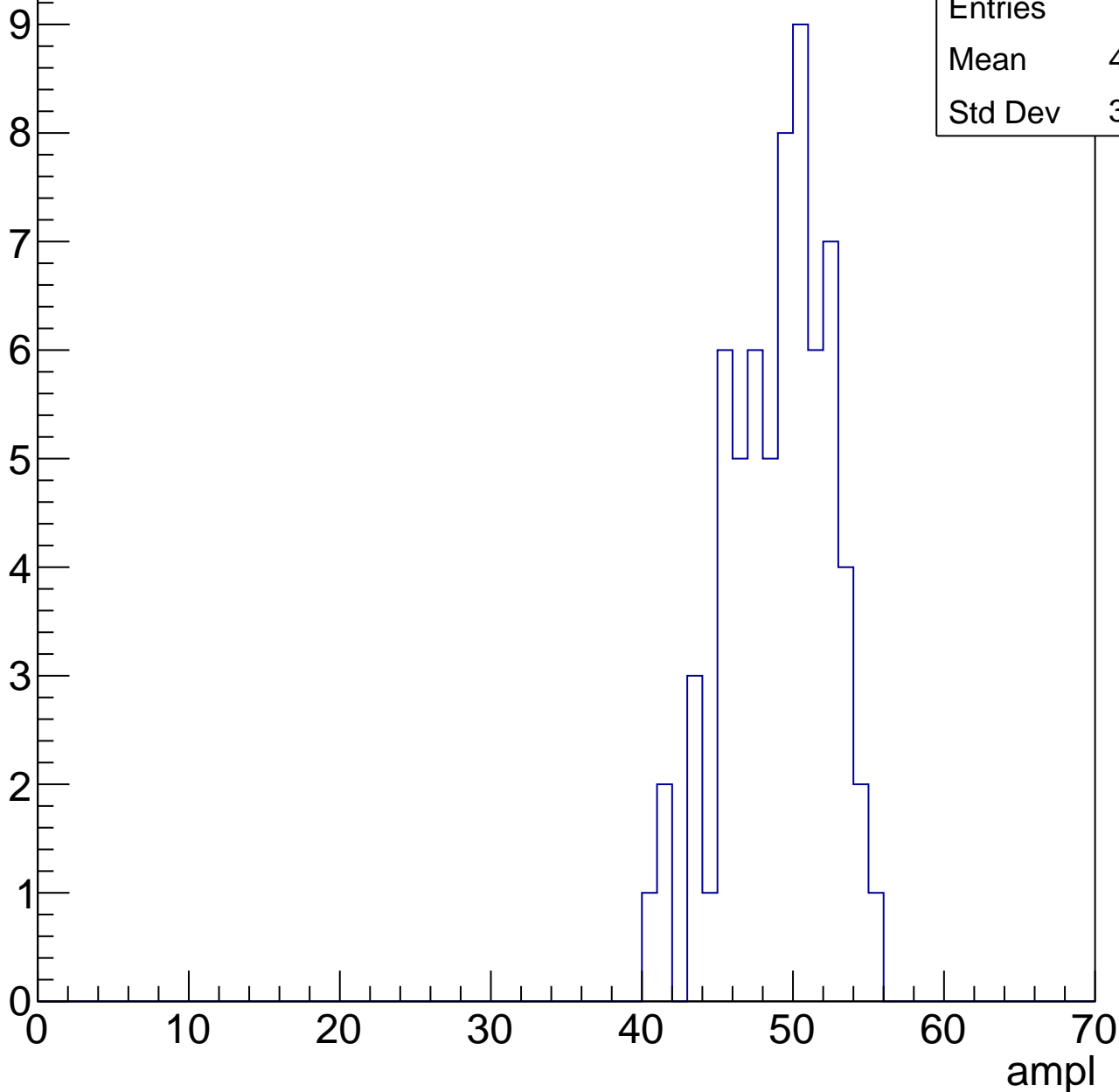


B1L103S, U10-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.55
Std Dev	3.358

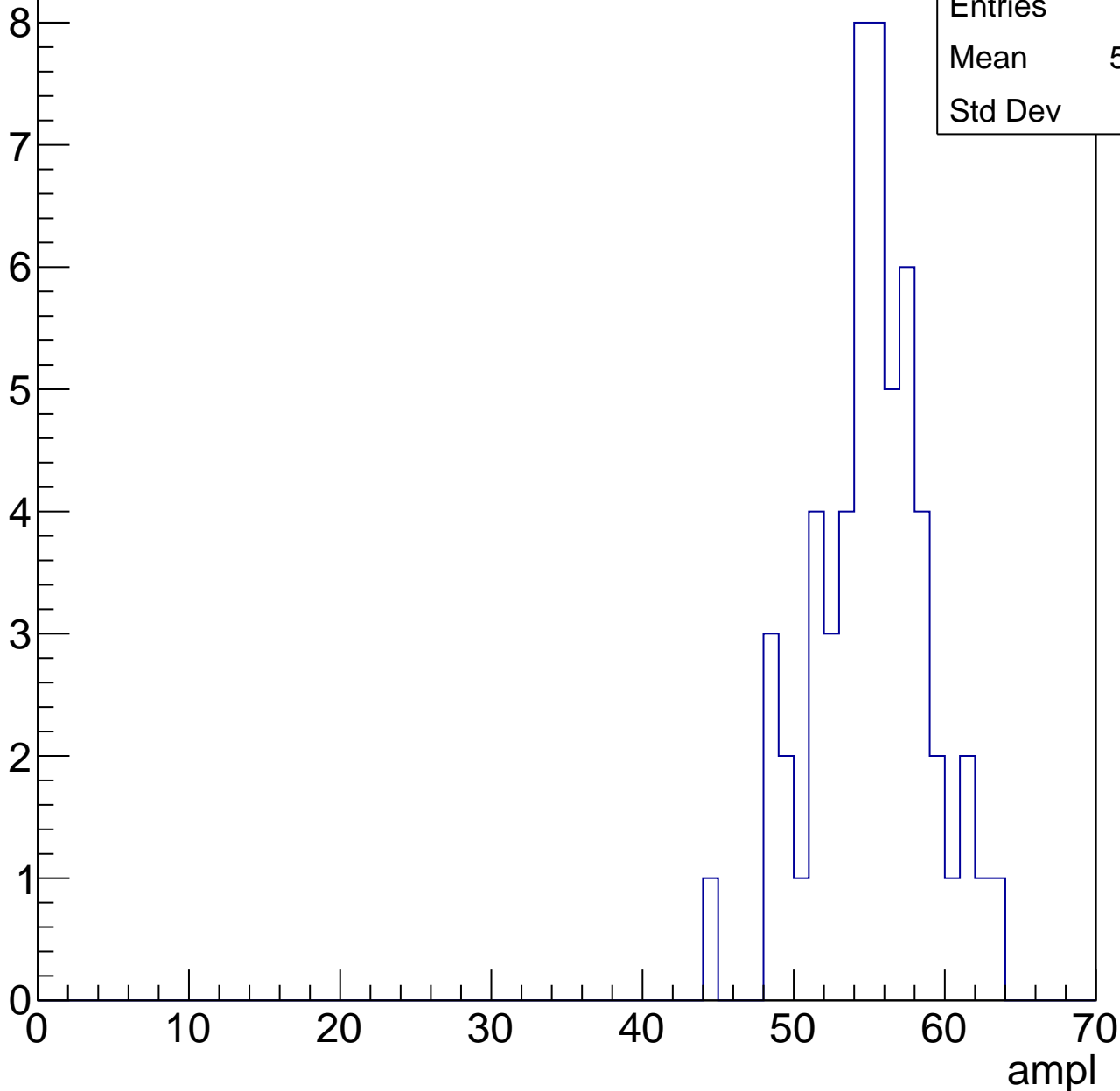


B1L103S, U10-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

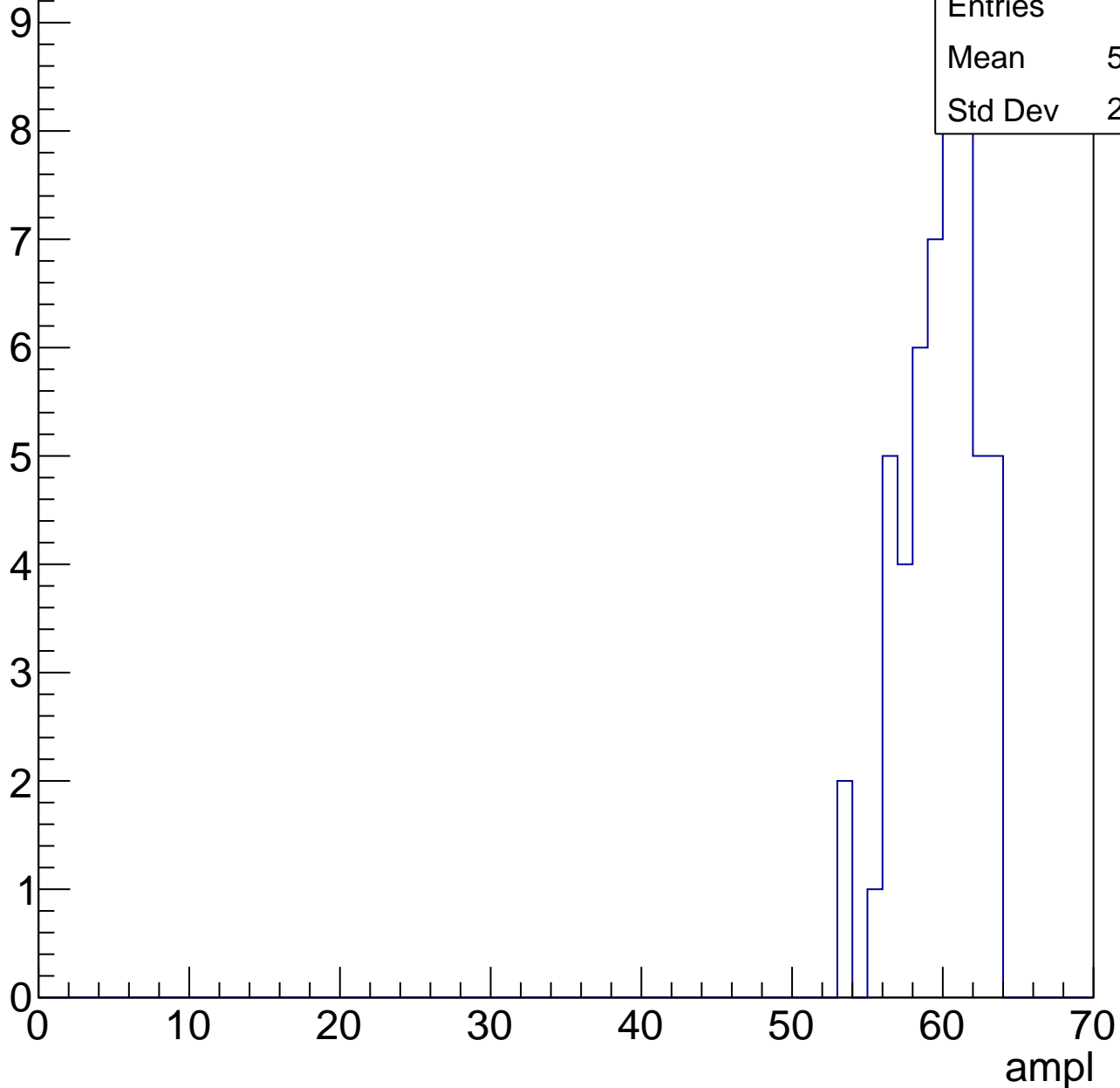
Entries	56
Mean	54.62
Std Dev	3.73



B1L103S, U10-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

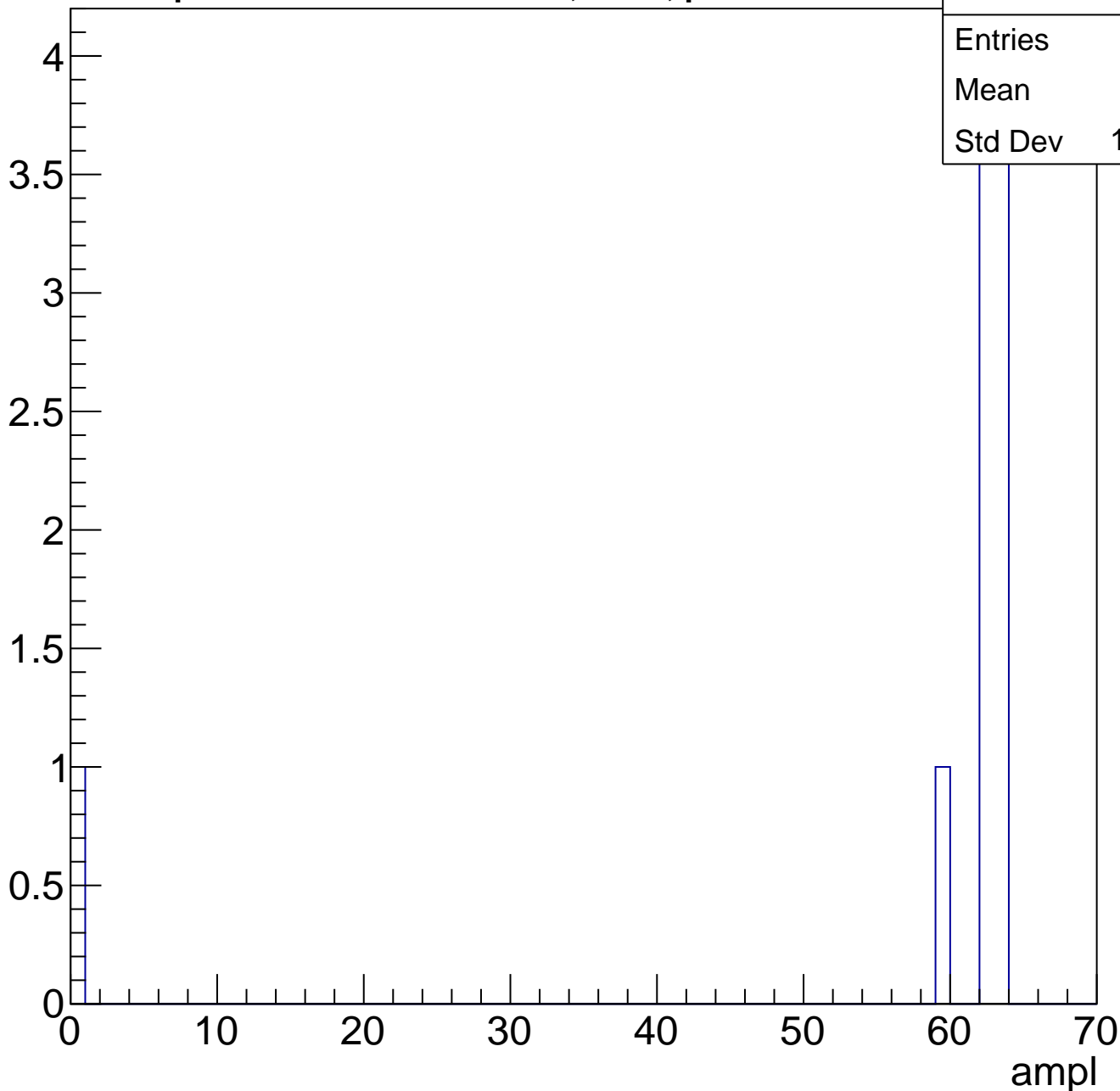
Entry



B1L103S, U10-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch4, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	95
Mean	26.22
Std Dev	10.93

Gaus mean : 31.1134

Gaus Width: 3.5256

0

10

20

30

40

50

60

70

ampl

B1L103S, U10-ch4, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	35.03
Std Dev	10.11

Gaus mean : 38.4794

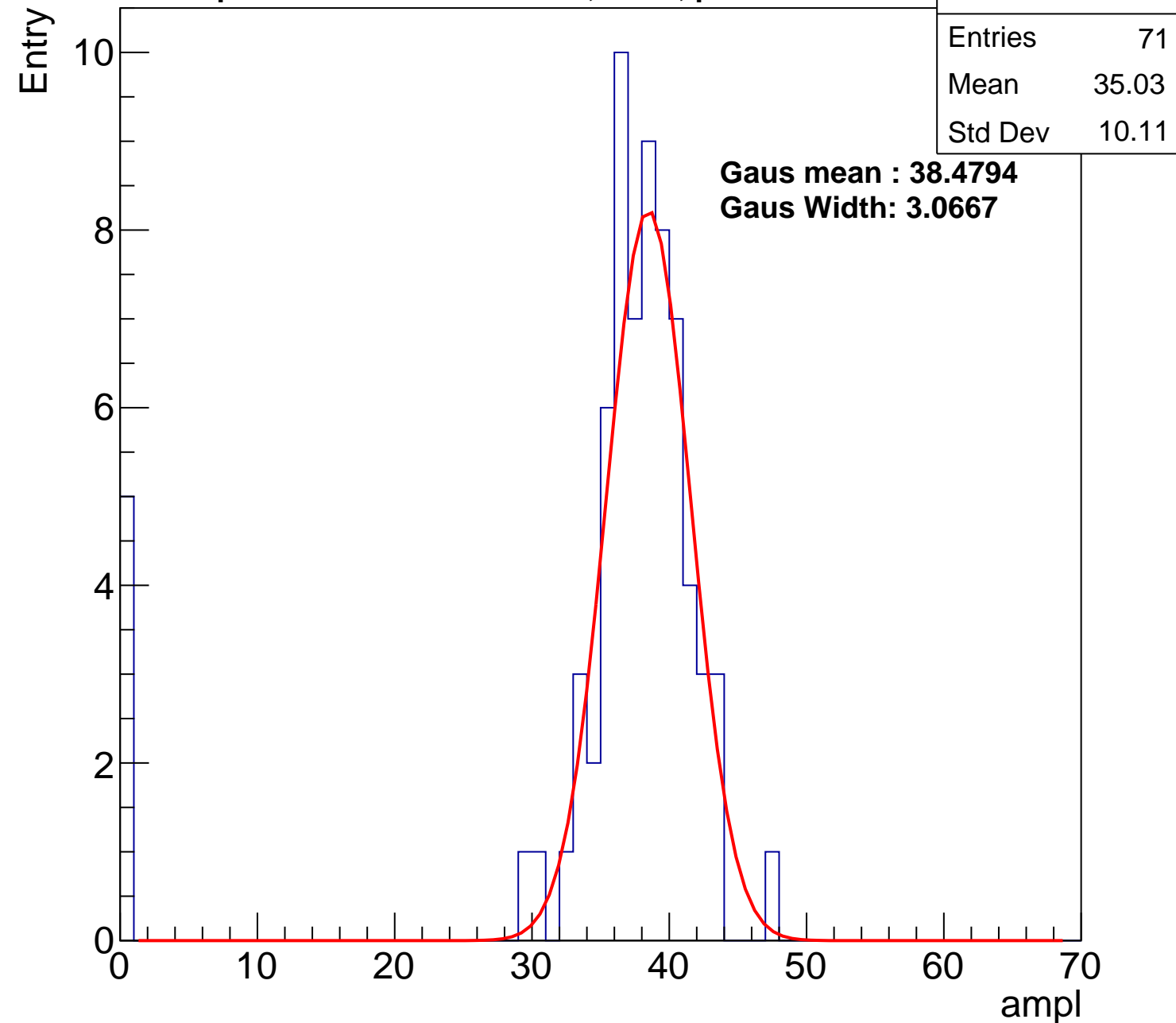
Gaus Width: 3.0667

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

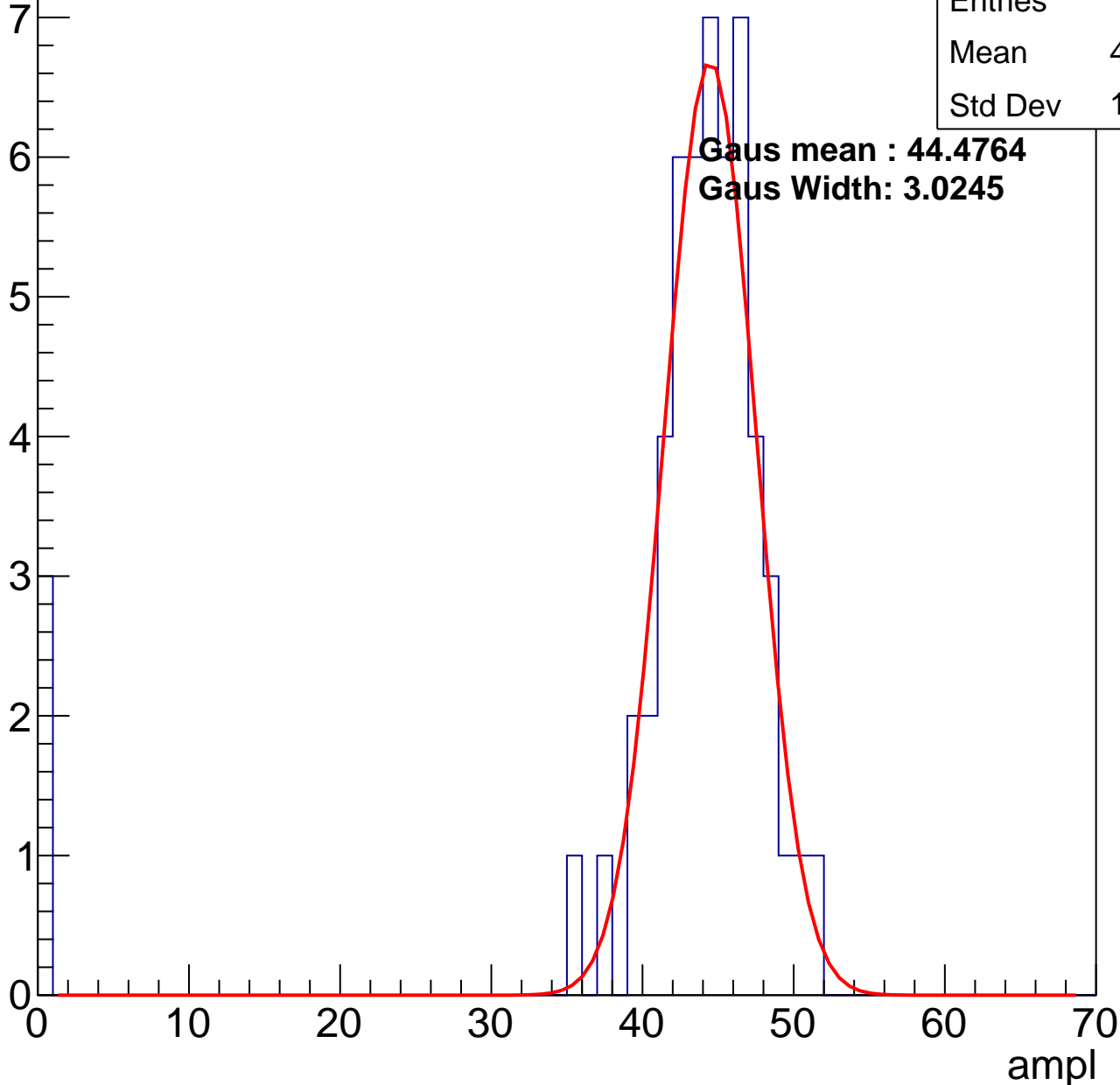


B1L103S, U10-ch4, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	41.56
Std Dev	10.43

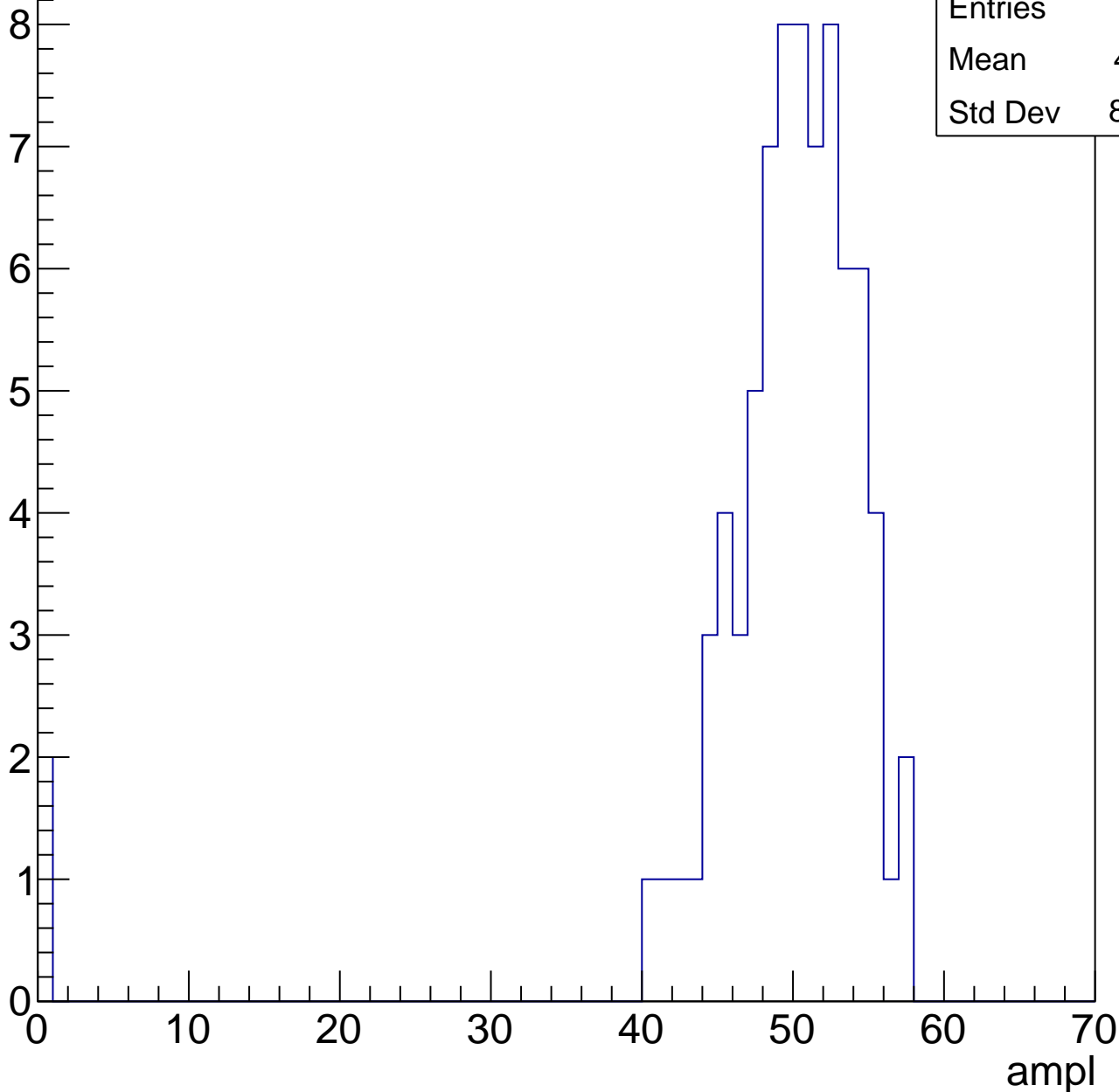


B1L103S, U10-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	48.51
Std Dev	8.684

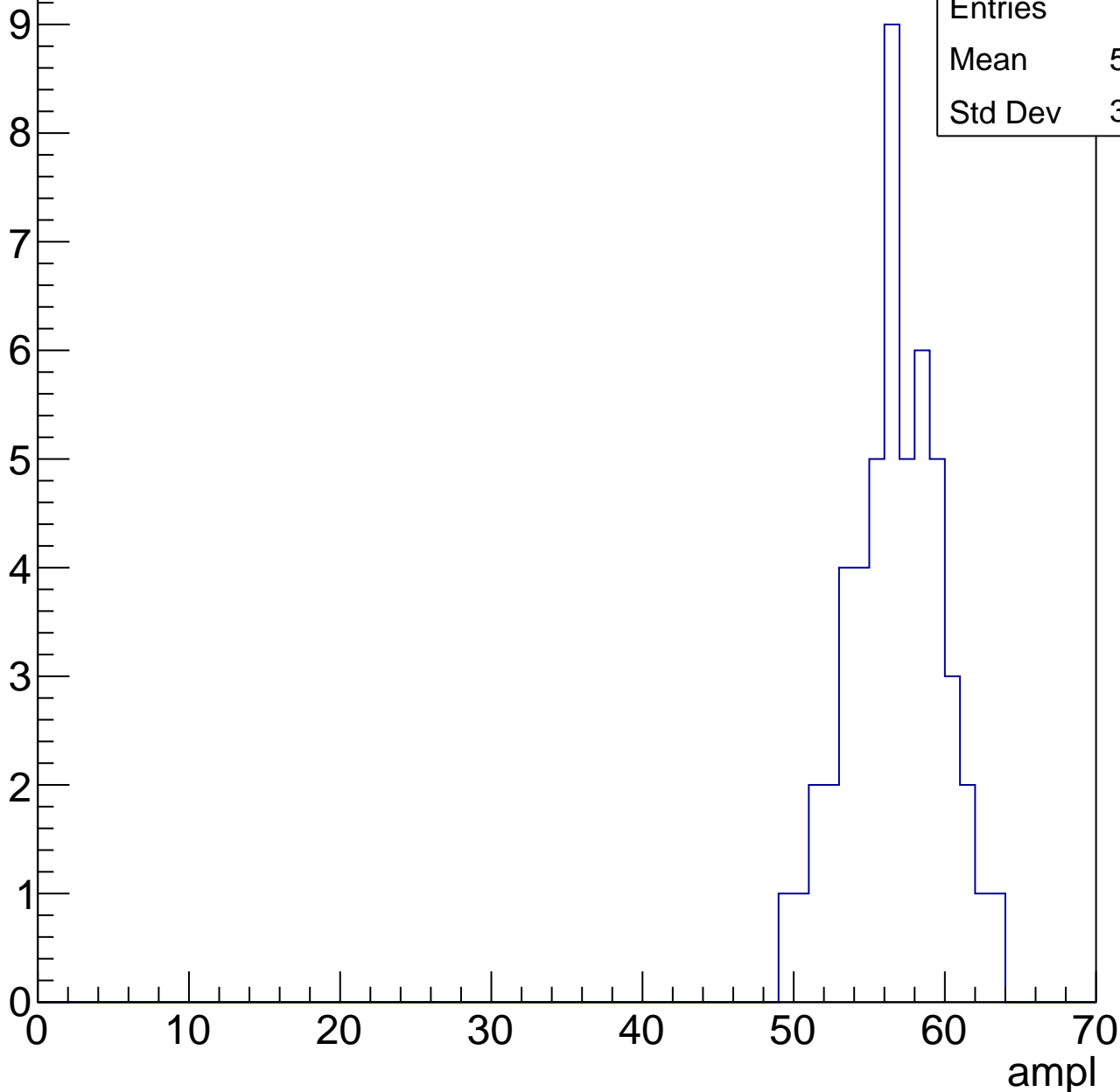


B1L103S, U10-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	56.22
Std Dev	3.063

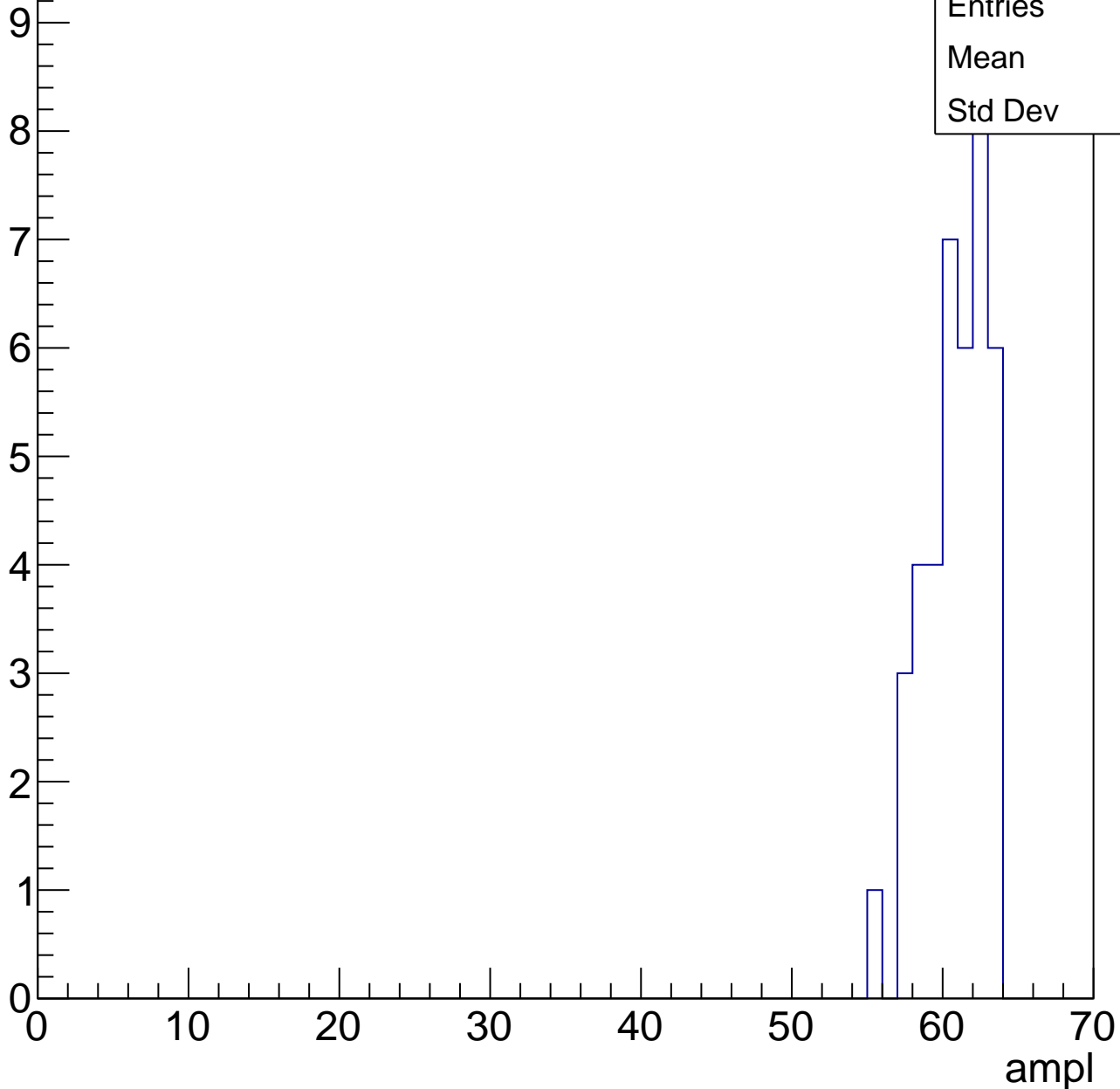


B1L103S, U10-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

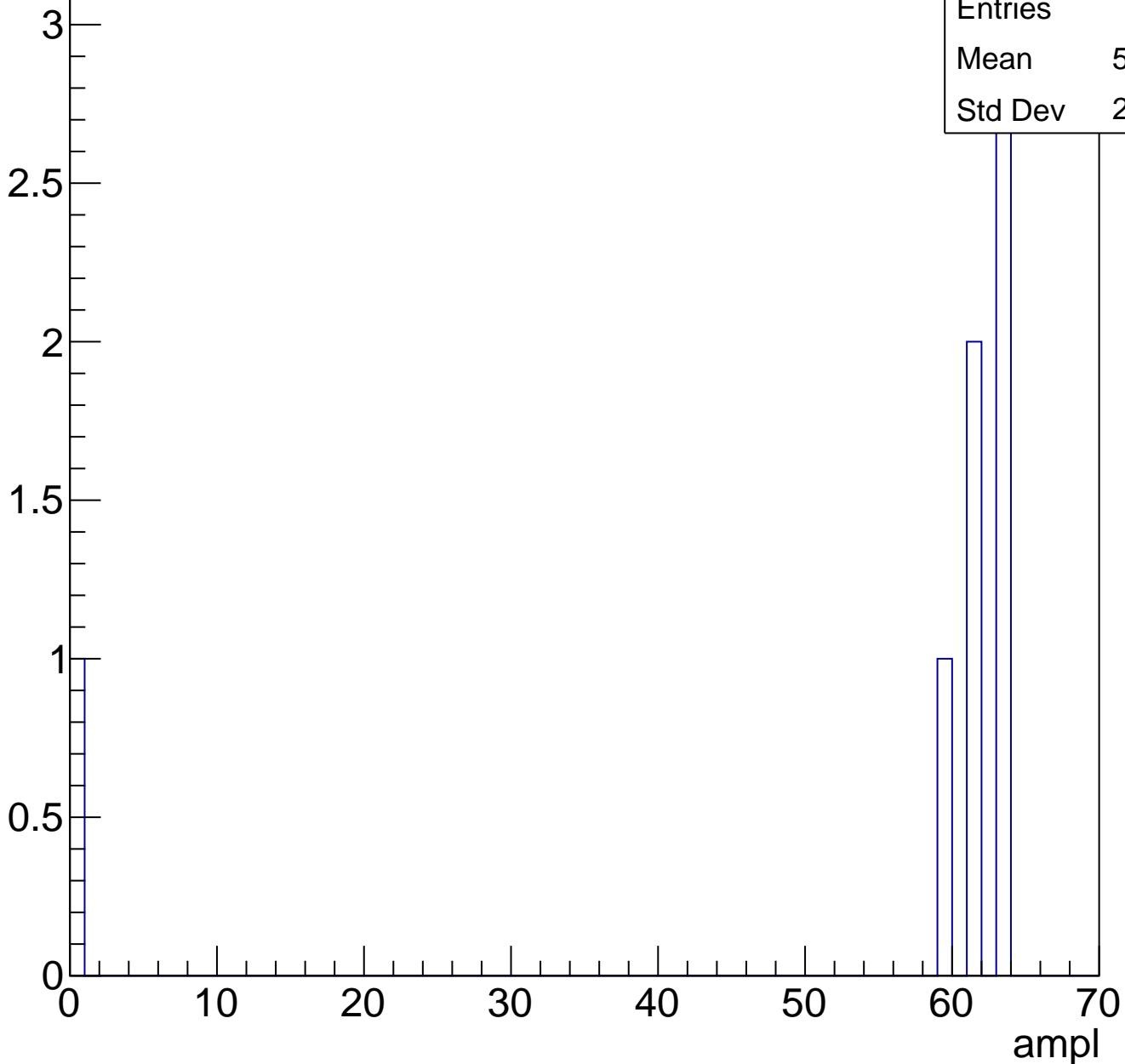
Entries	40
Mean	60.4
Std Dev	2.01



B1L103S, U10-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch5, adc0

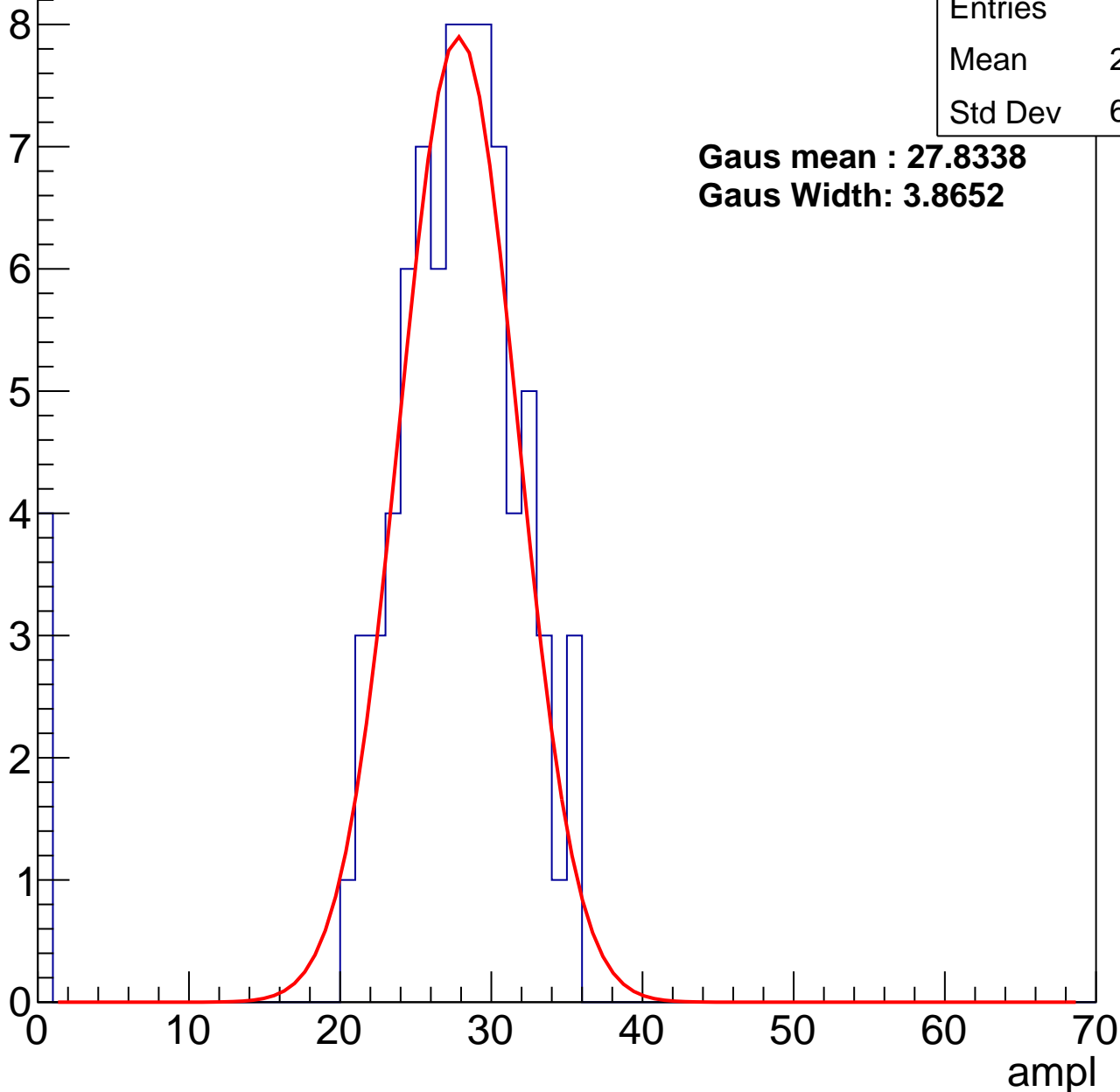
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	26.17
Std Dev	6.926

Gaus mean : 27.8338

Gaus Width: 3.8652



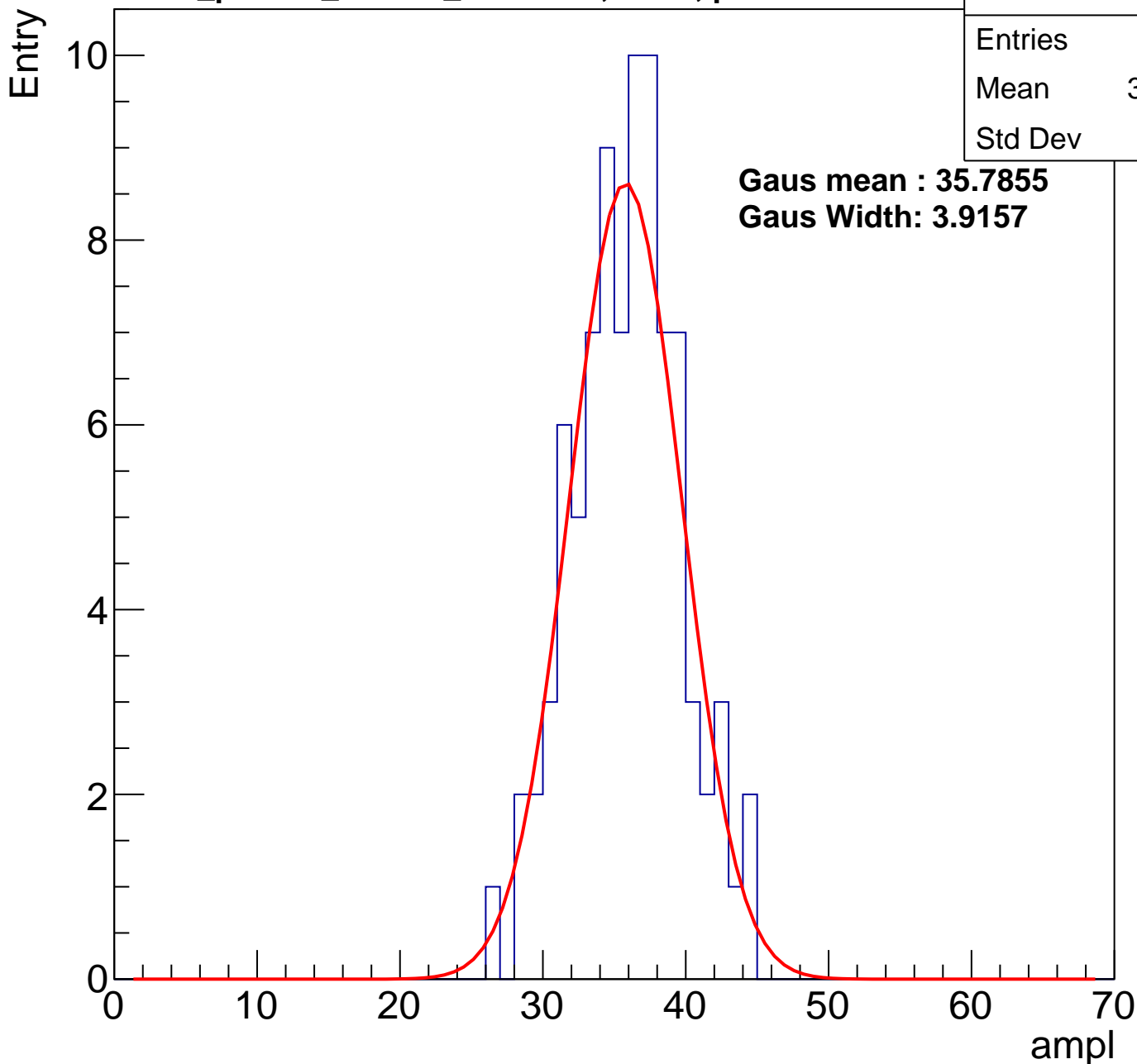
B1L103S, U10-ch5, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	35.47
Std Dev	3.76

Gaus mean : 35.7855

Gaus Width: 3.9157



B1L103S, U10-ch5, adc2

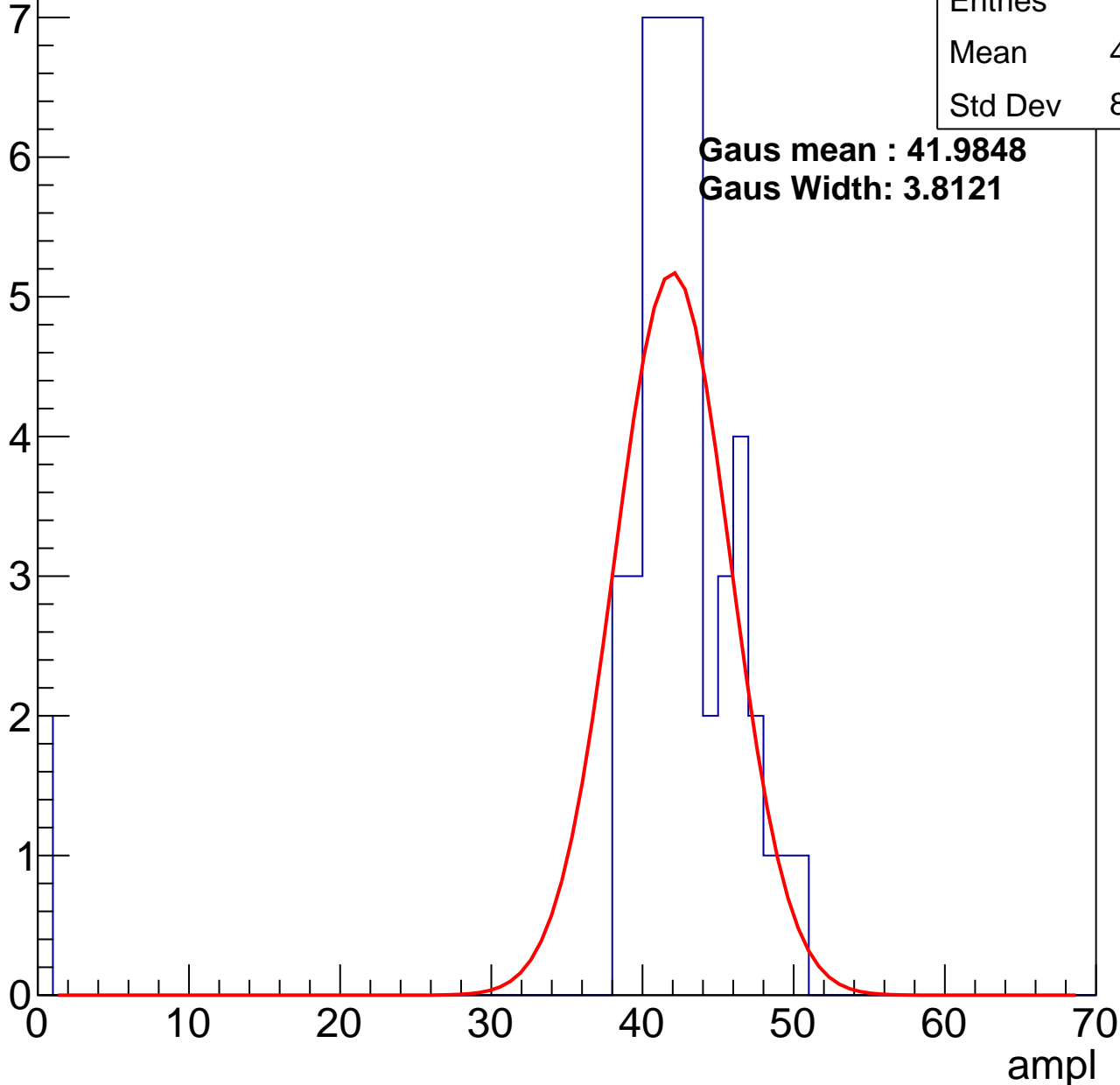
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	40.82
Std Dev	8.802

Gaus mean : 41.9848

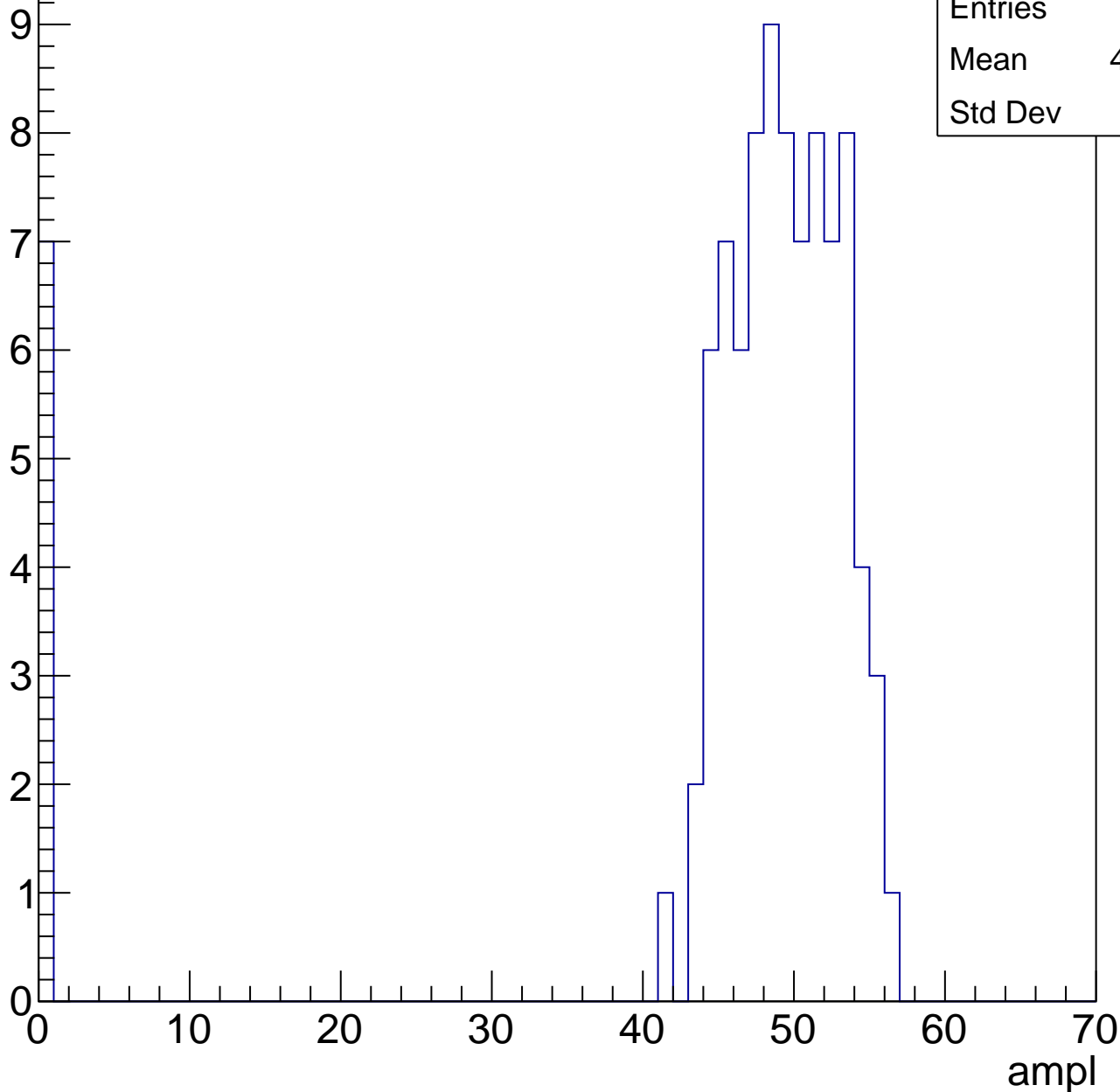
Gaus Width: 3.8121



B1L103S, U10-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

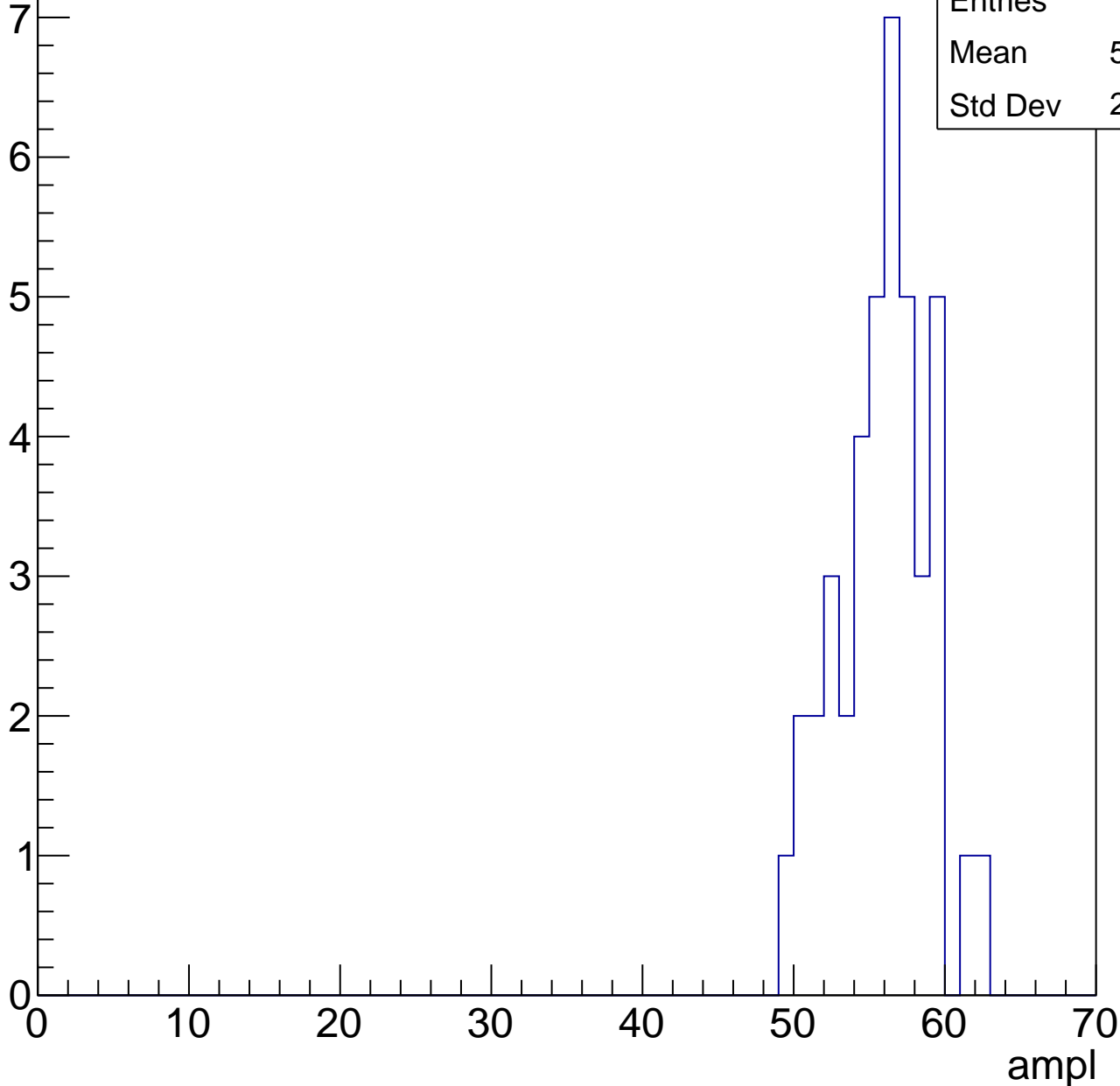


B1L103S, U10-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	55.44
Std Dev	2.996



B1L103S, U10-ch5, adc5

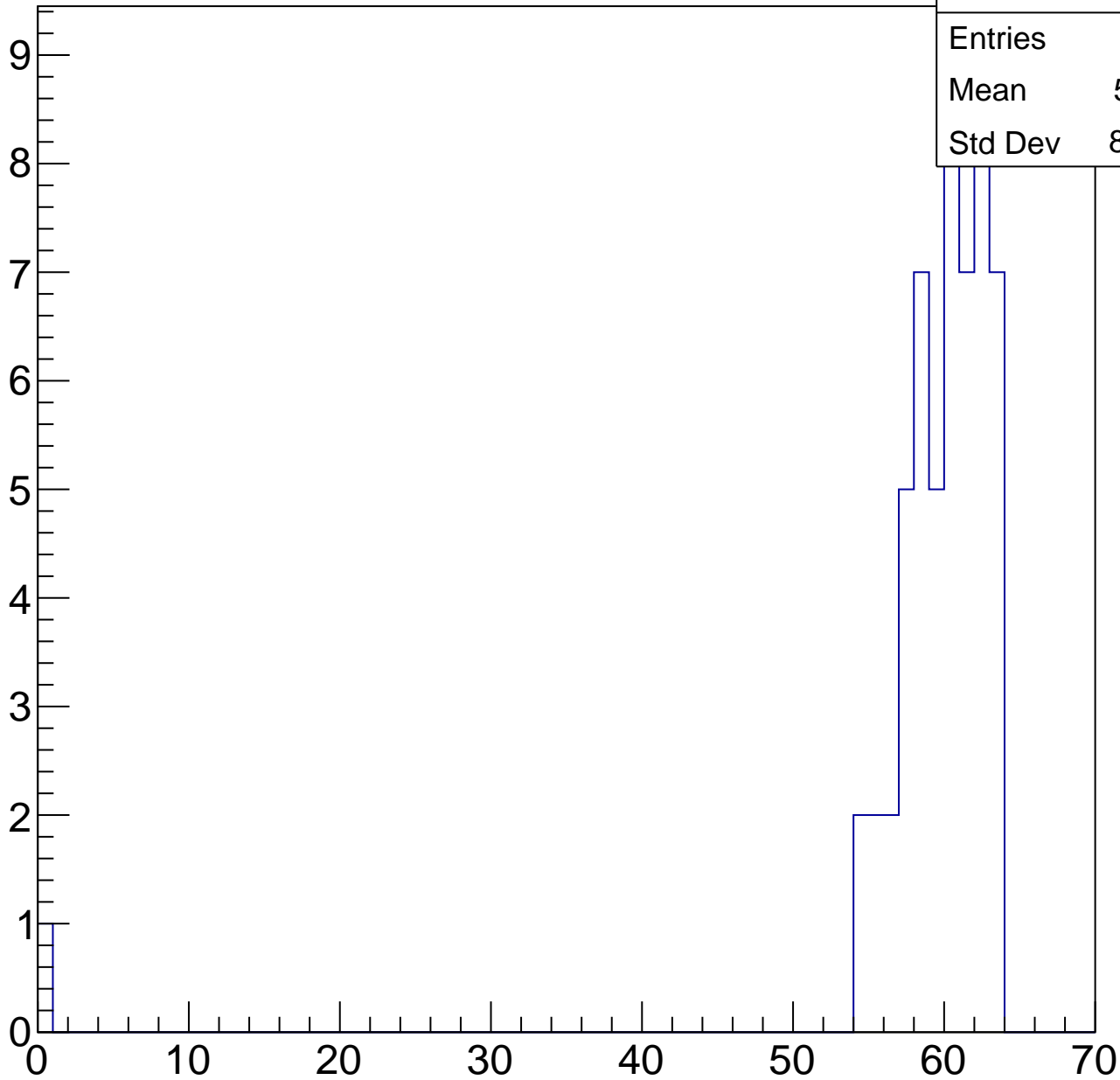
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.61
Std Dev	8.269

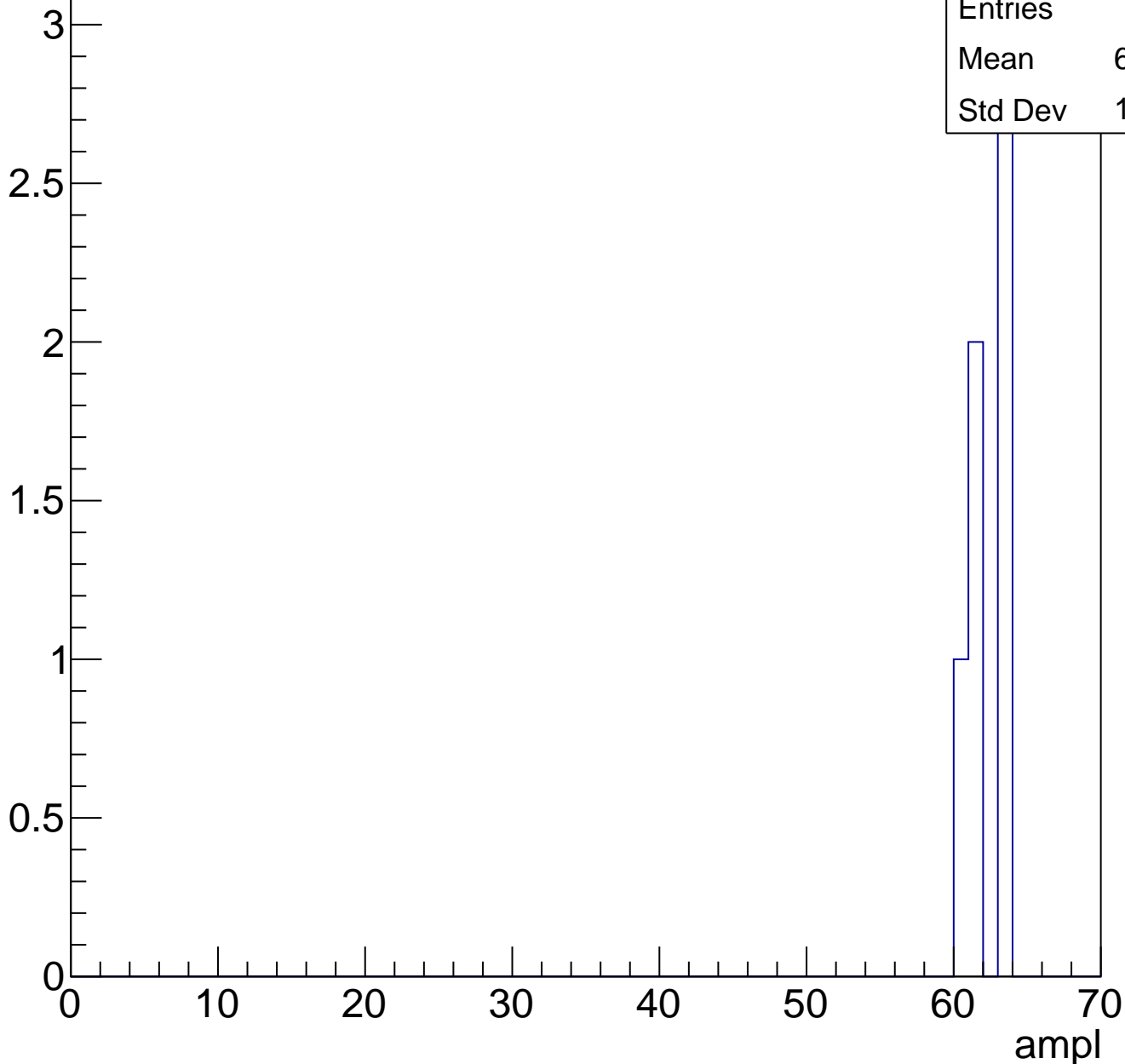
ampl



B1L103S, U10-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch6, adc0

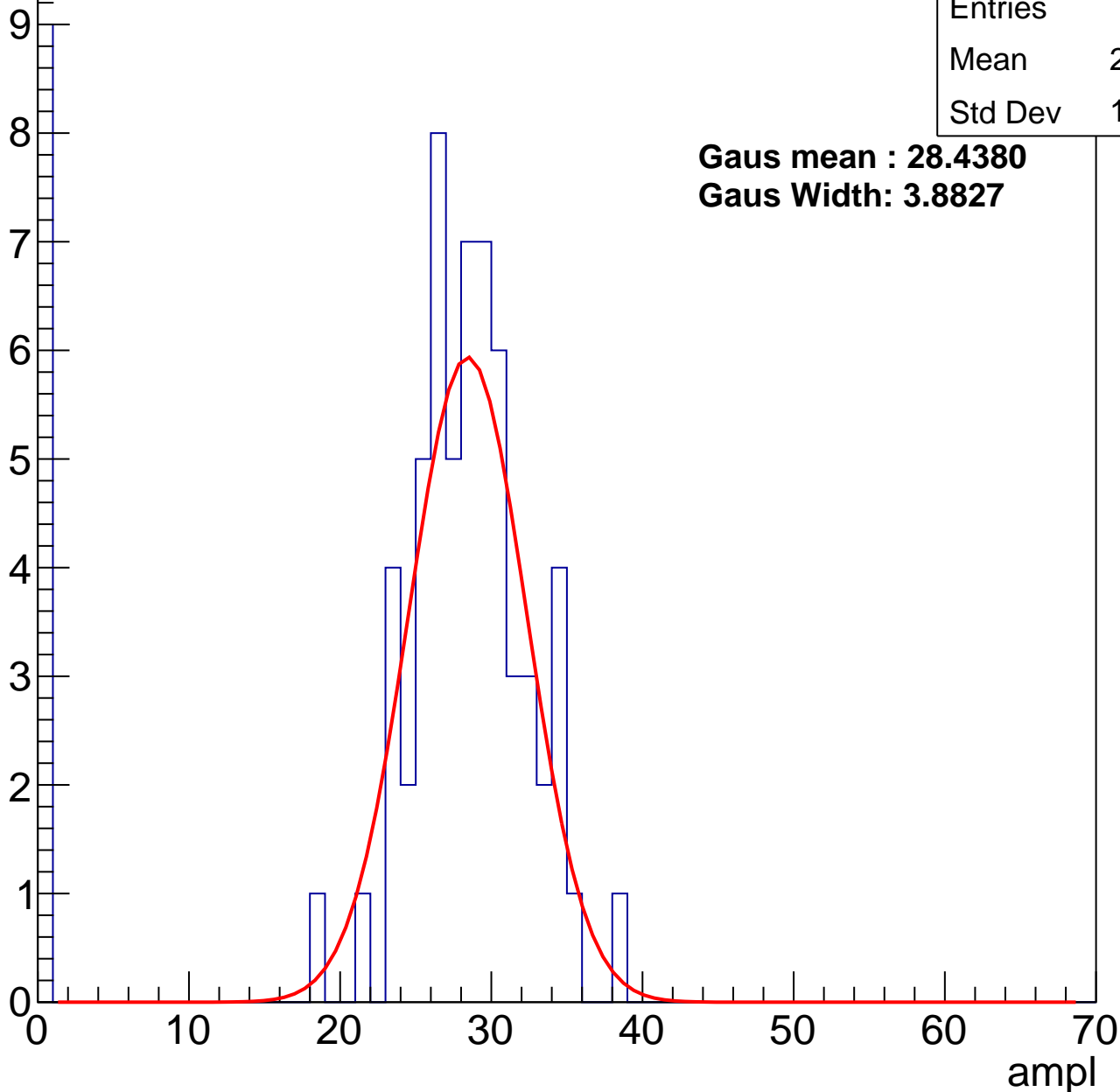
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	24.49
Std Dev	10.09

Gaus mean : 28.4380

Gaus Width: 3.8827



B1L103S, U10-ch6, adc1

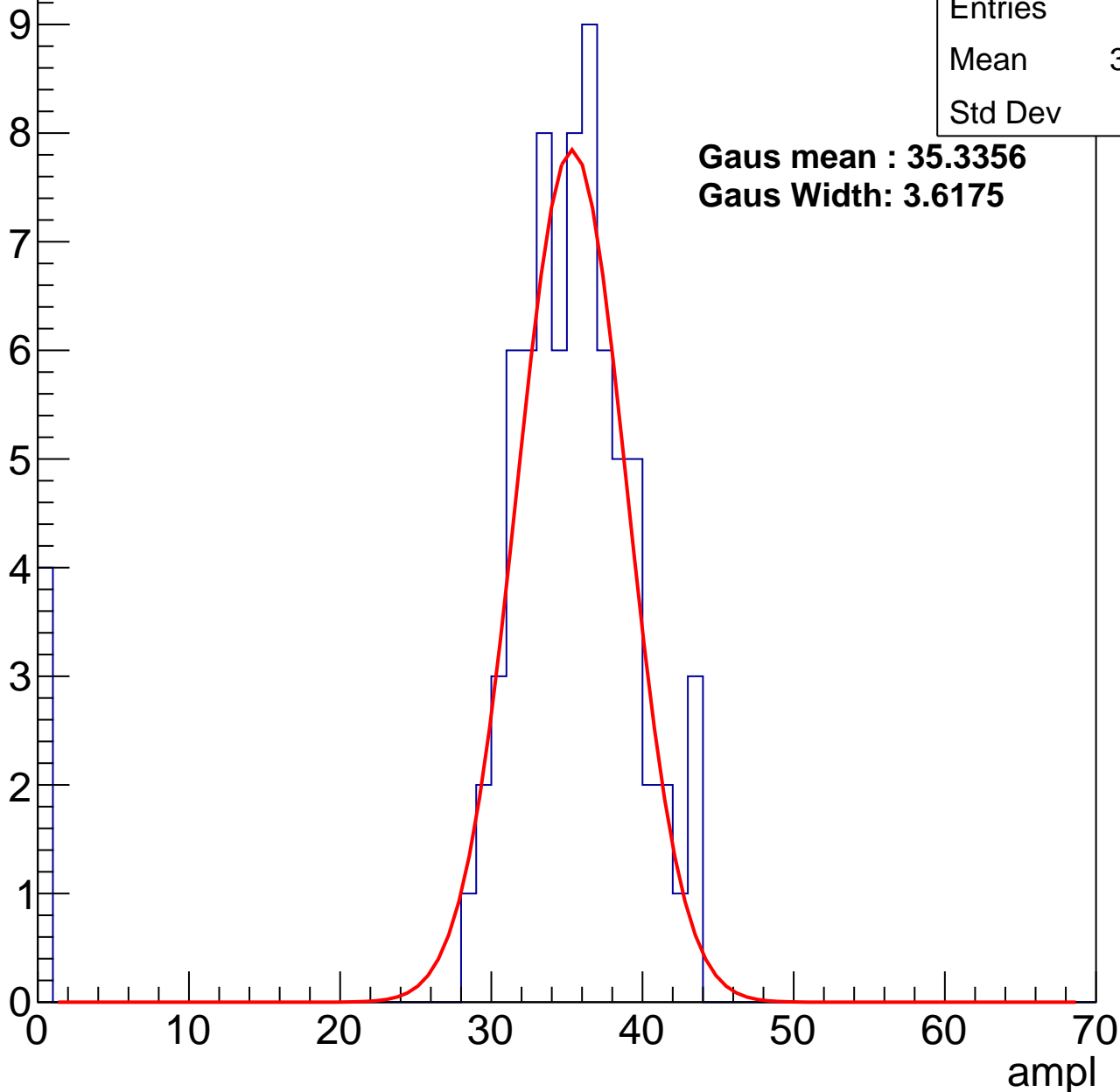
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	33.32
Std Dev	8.52

Gaus mean : 35.3356

Gaus Width: 3.6175



B1L103S, U10-ch6, adc2

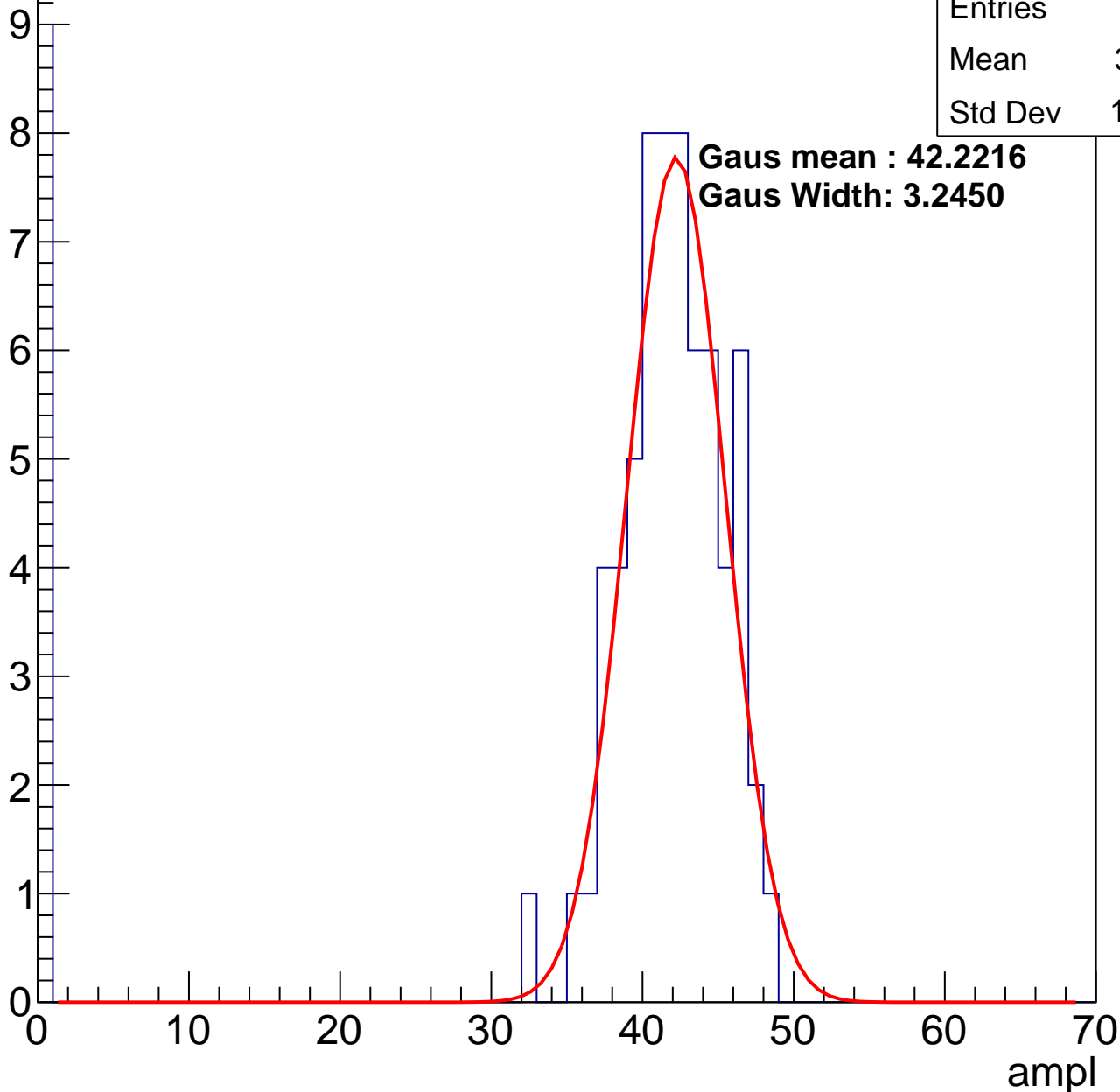
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.51
Std Dev	13.92

Gaus mean : 42.2216

Gaus Width: 3.2450

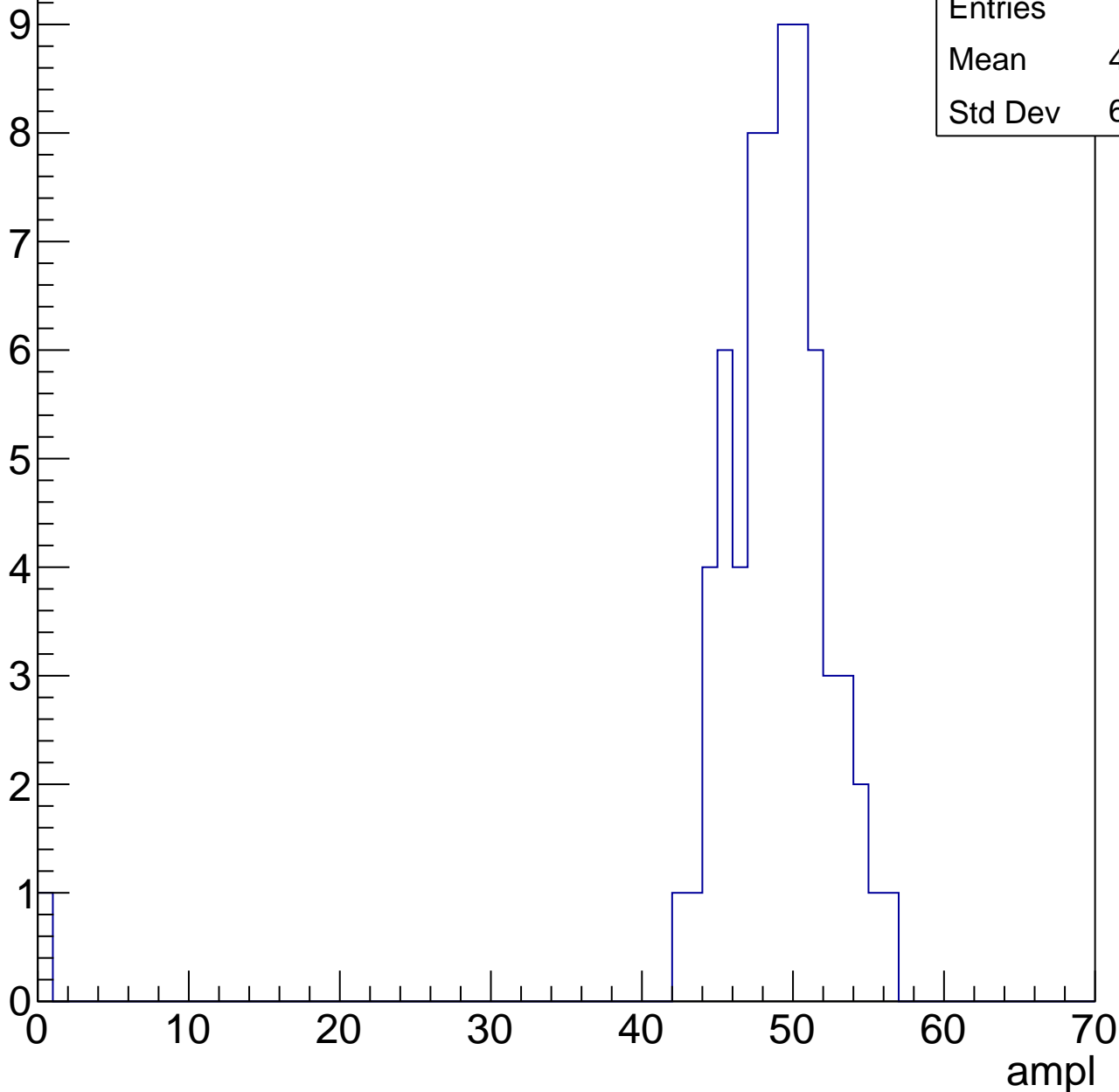


B1L103S, U10-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.85
Std Dev	6.593

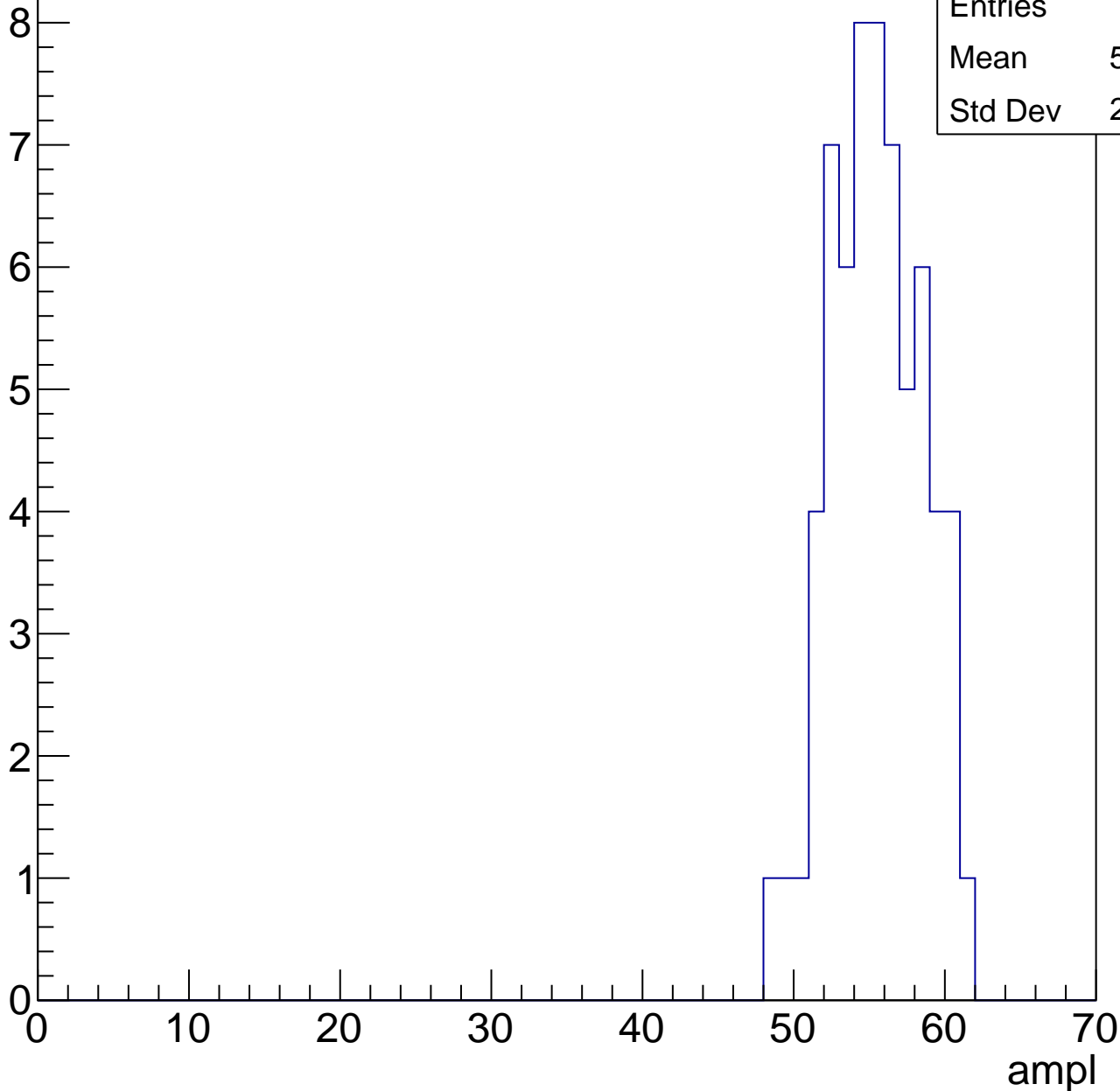


B1L103S, U10-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	55.03
Std Dev	2.955

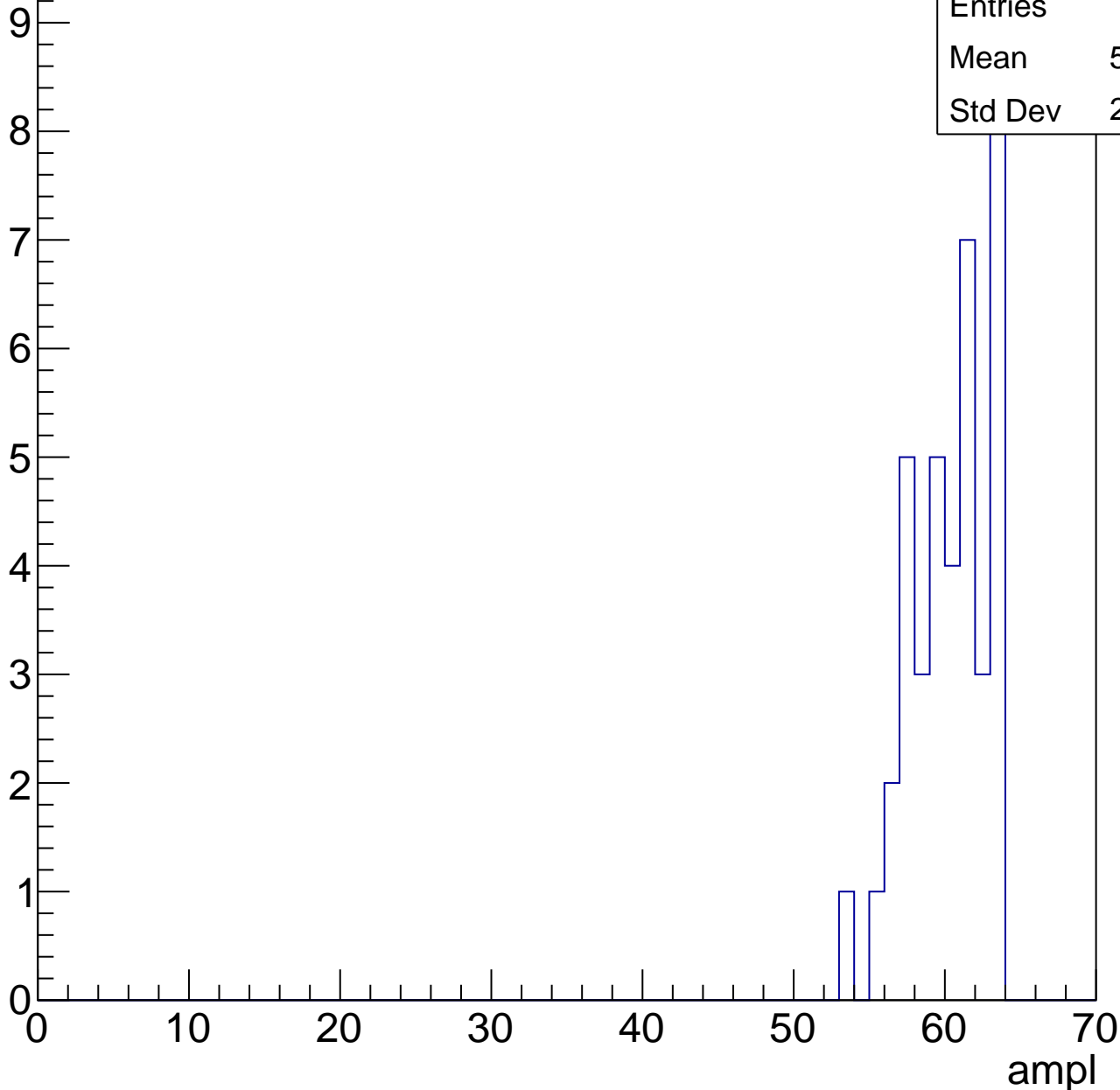


B1L103S, U10-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.85
Std Dev	2.584

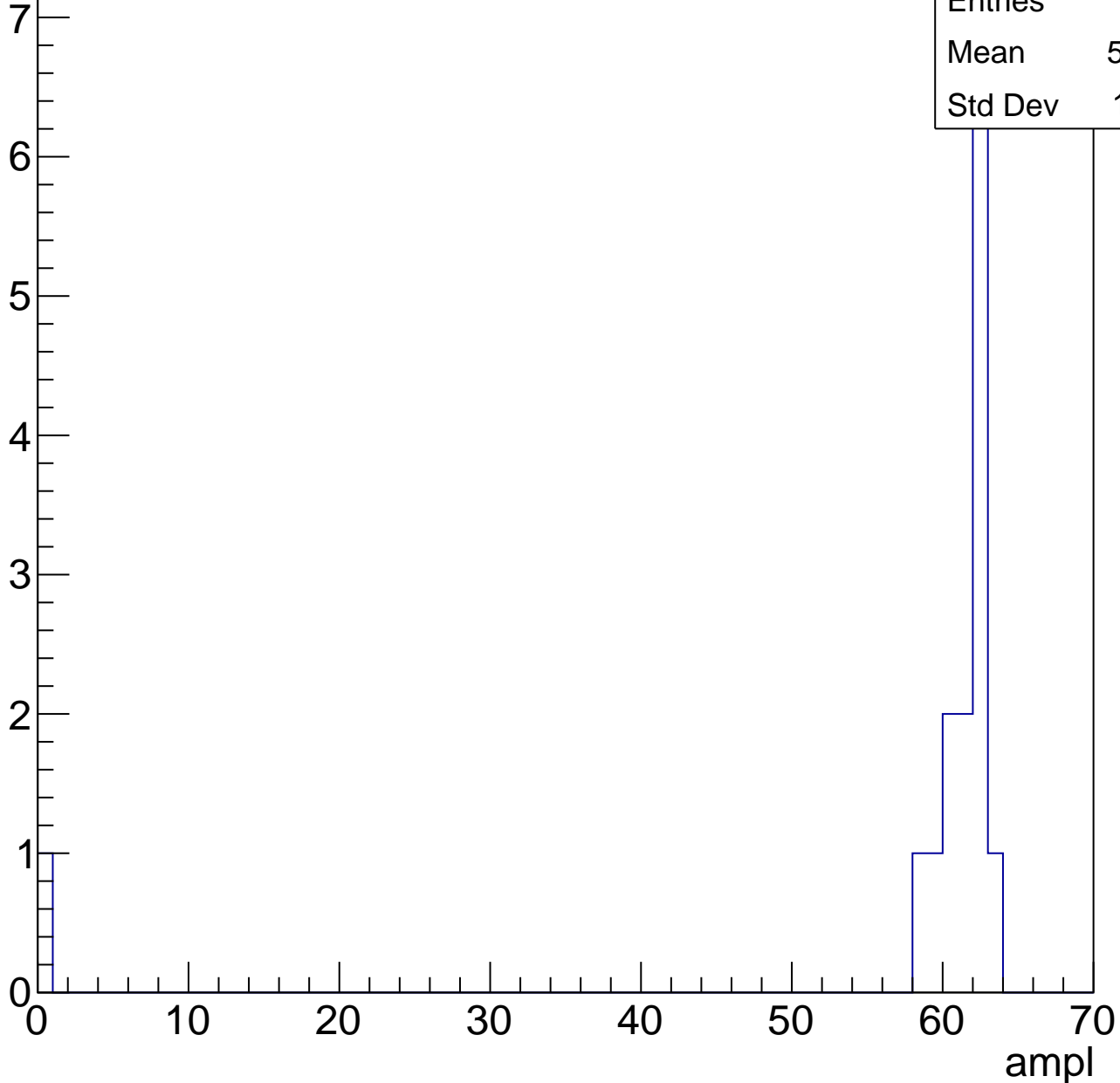


B1L103S, U10-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.07
Std Dev	15.31



B1L103S, U10-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch7, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	22.04
Std Dev	14.14

Gaus mean : 31.3661

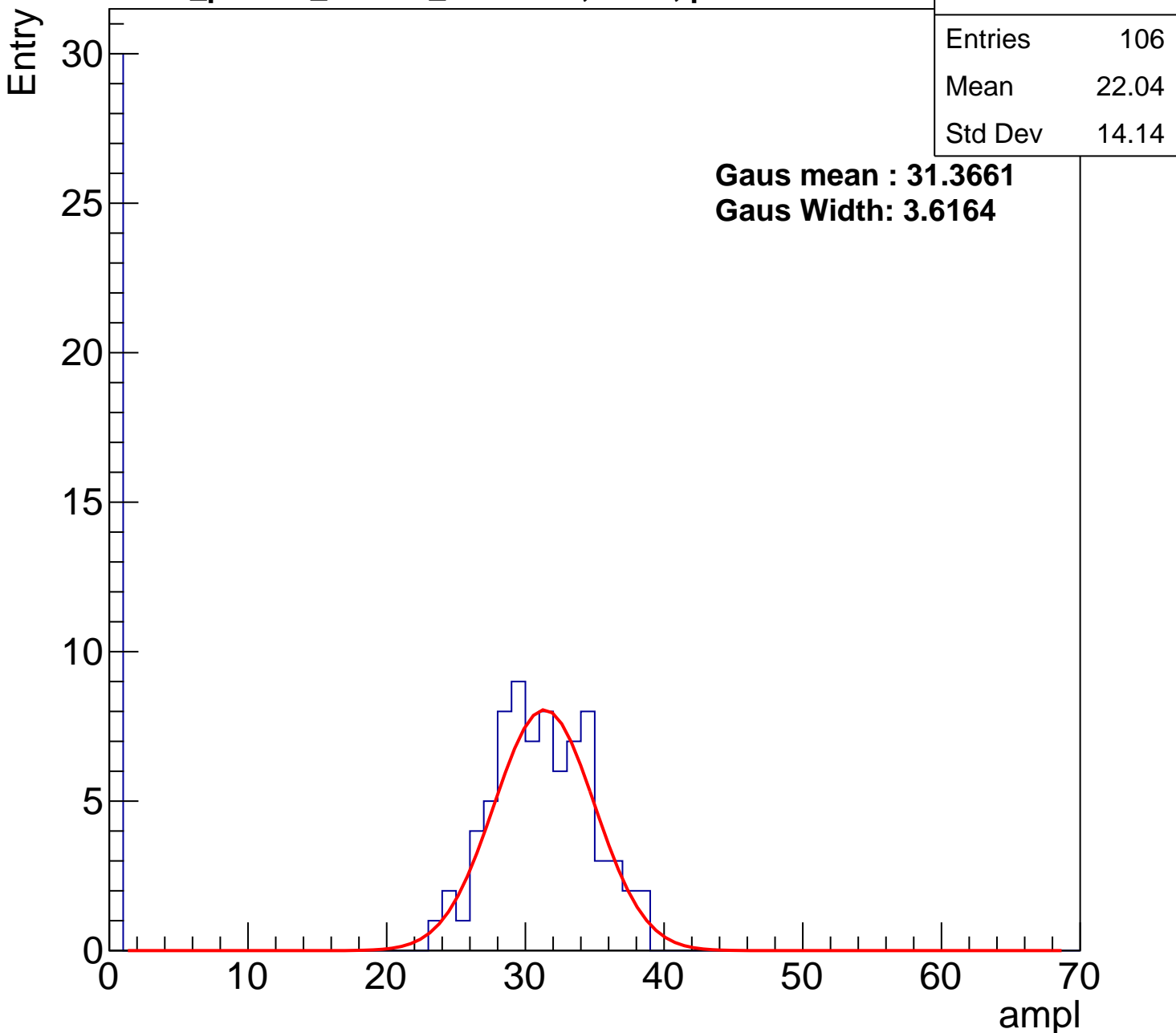
Gaus Width: 3.6164

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



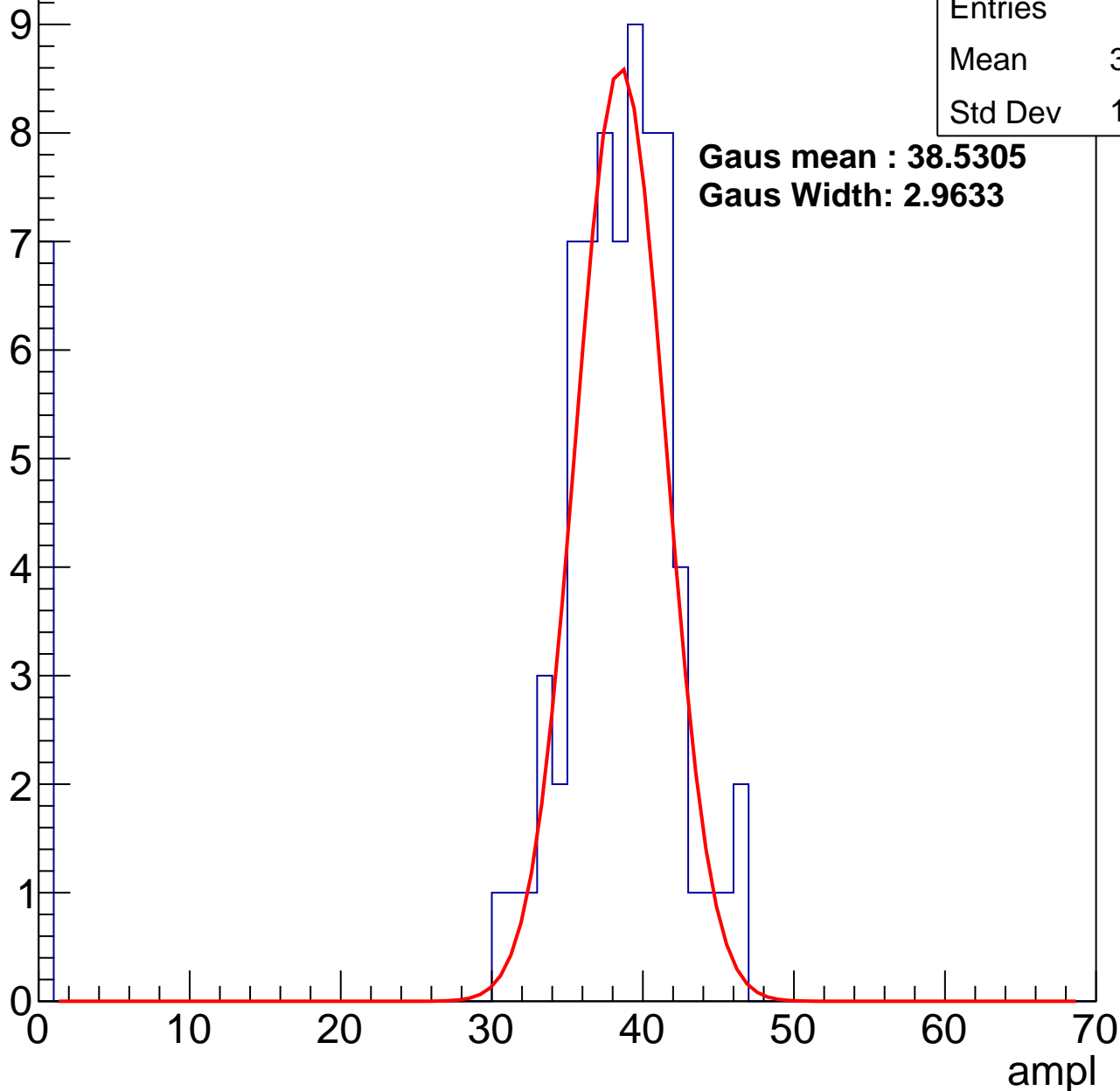
B1L103S, U10-ch7, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.74
Std Dev	11.35

Gaus mean : 38.5305
Gaus Width: 2.9633



B1L103S, U10-ch7, adc2

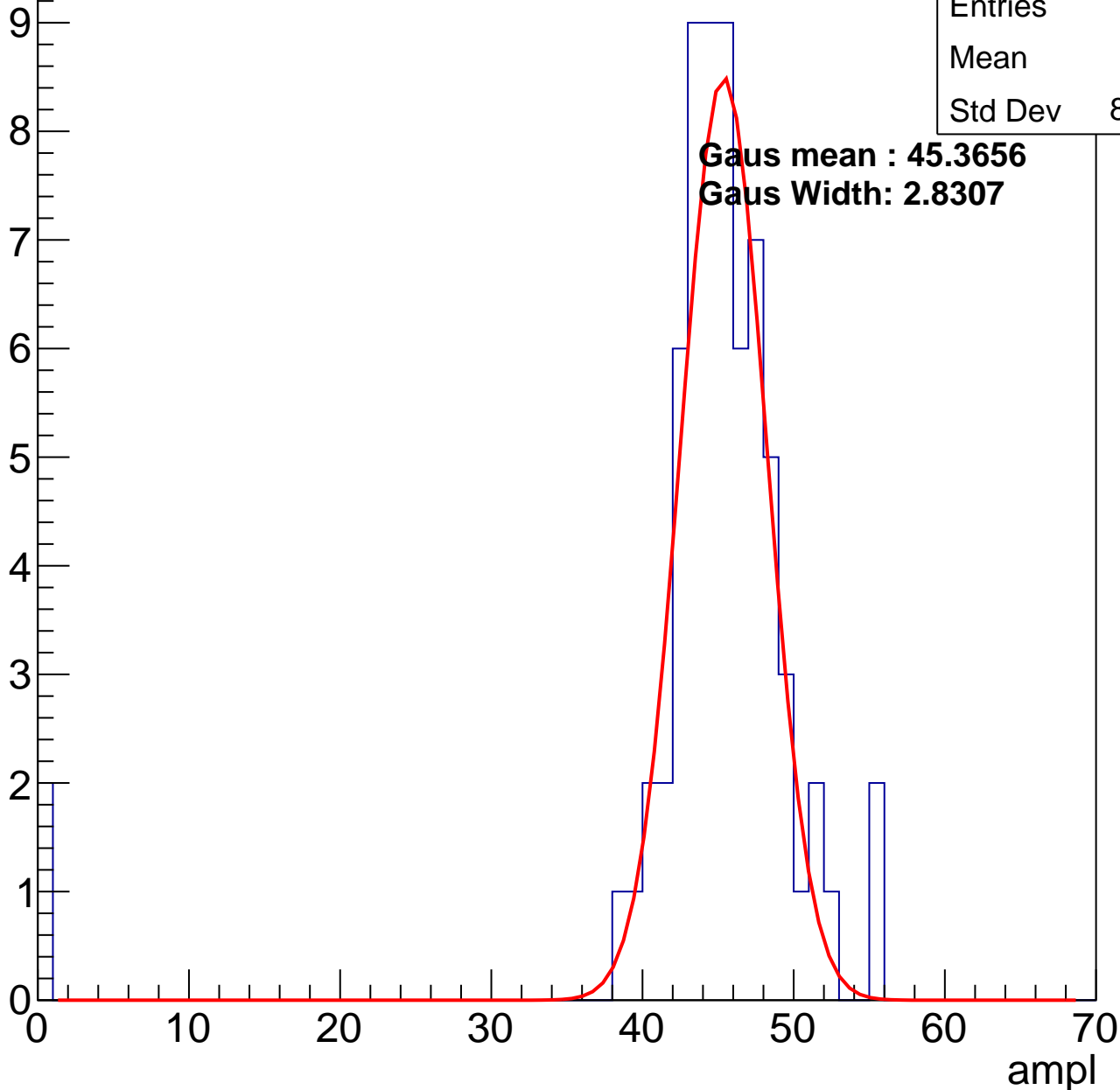
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.9
Std Dev	8.319

Gaus mean : 45.3656

Gaus Width: 2.8307

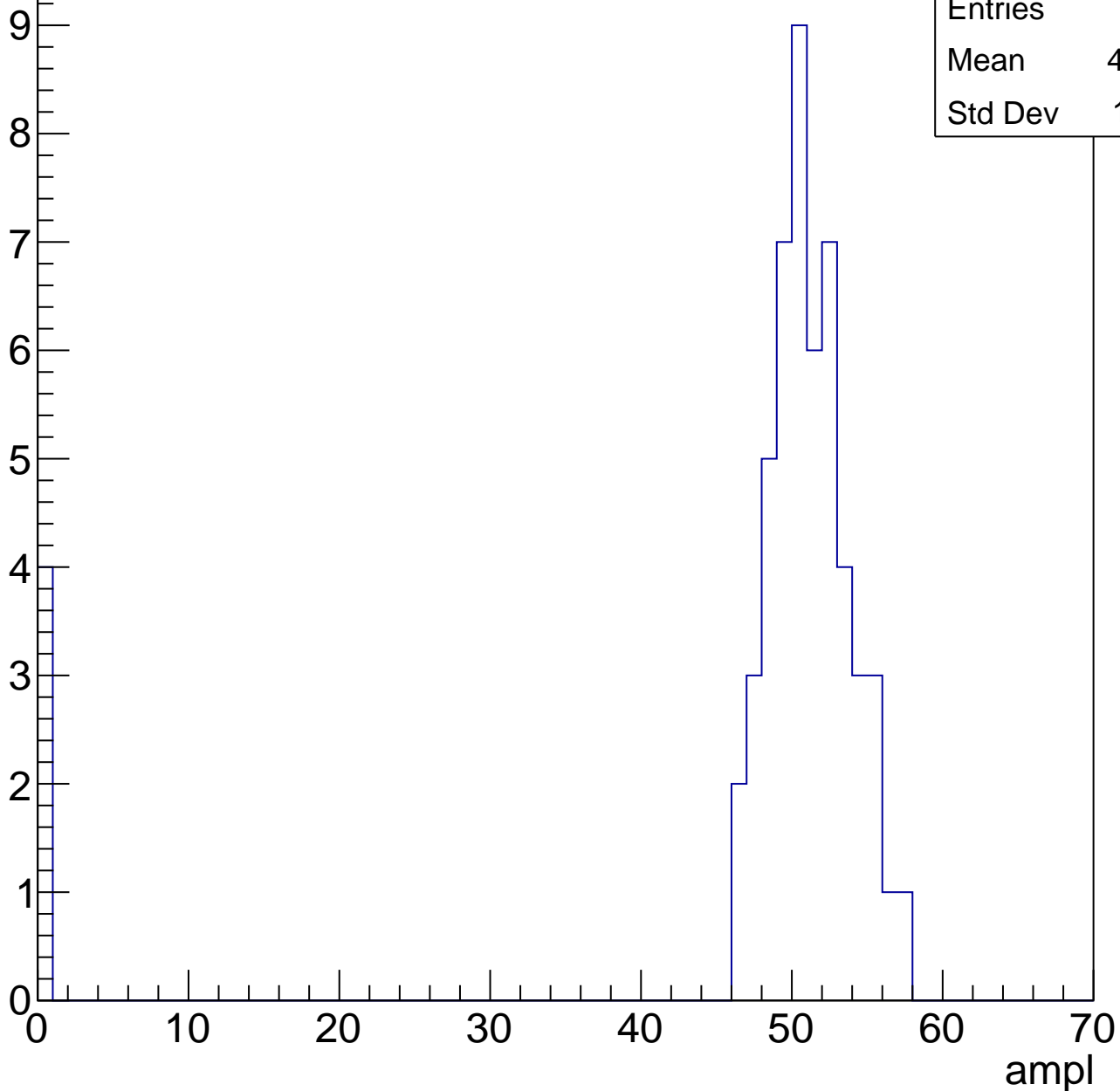


B1L103S, U10-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

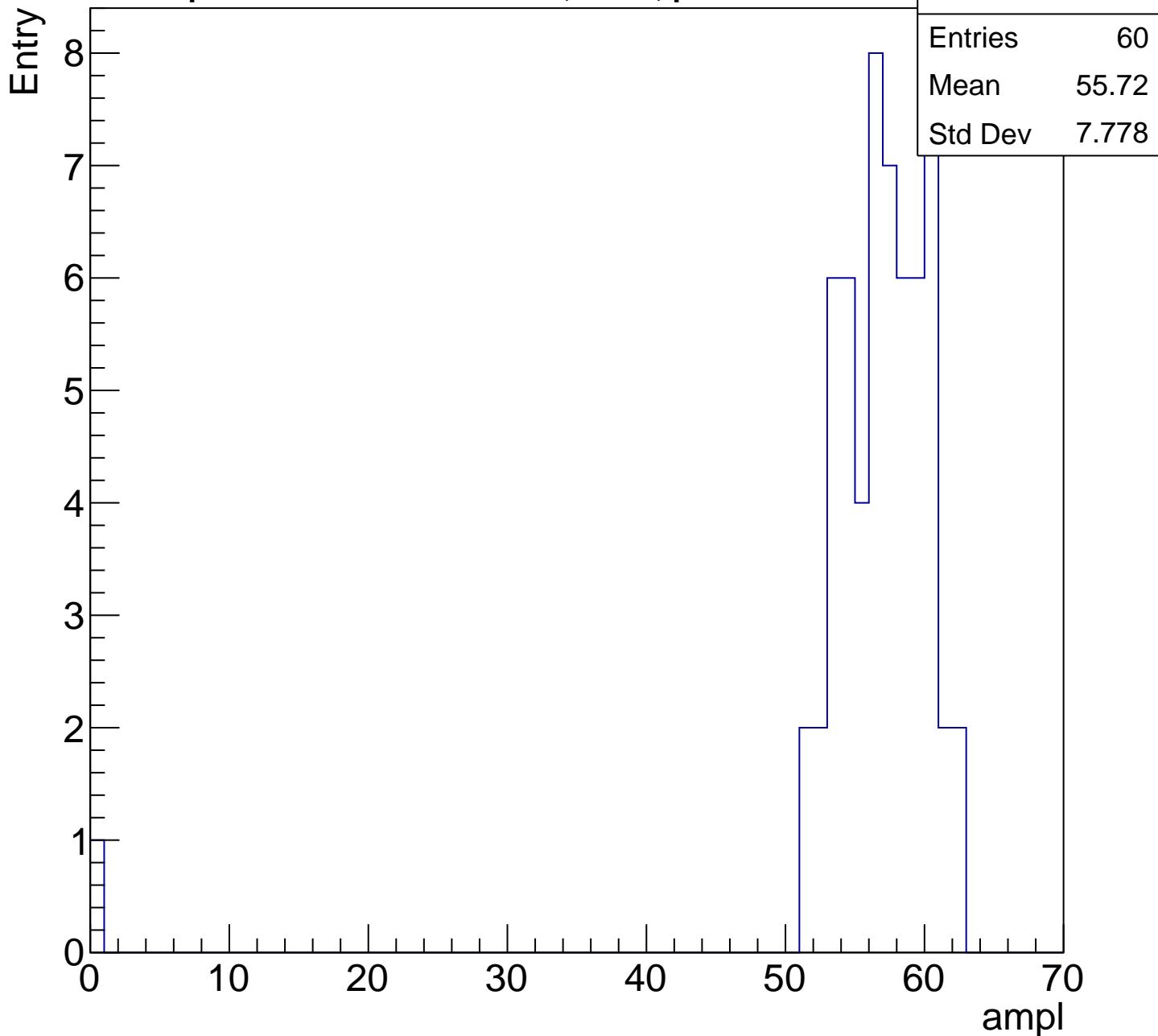
Entry

Entries	55
Mean	47.05
Std Dev	13.41



B1L103S, U10-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

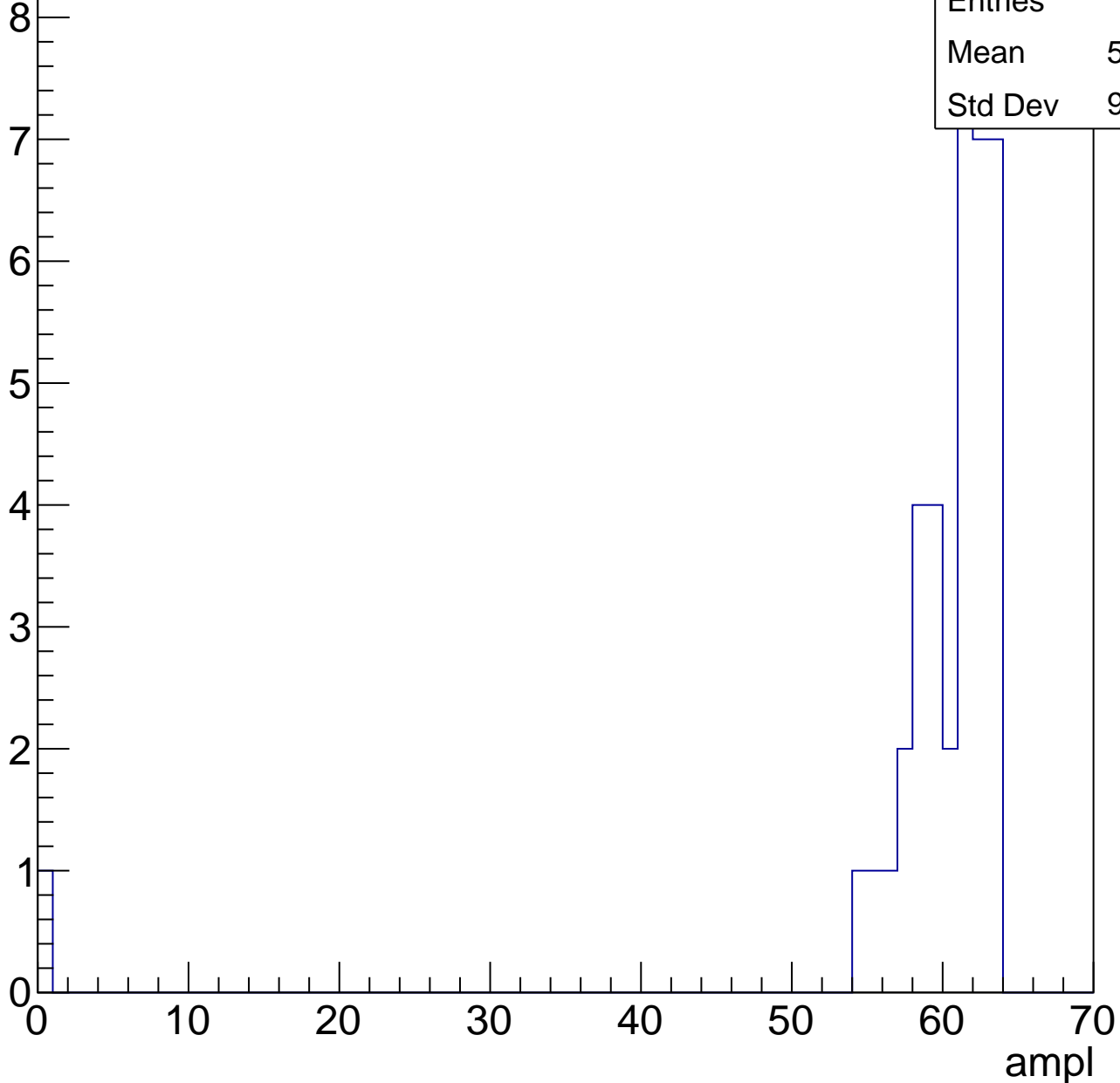


B1L103S, U10-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

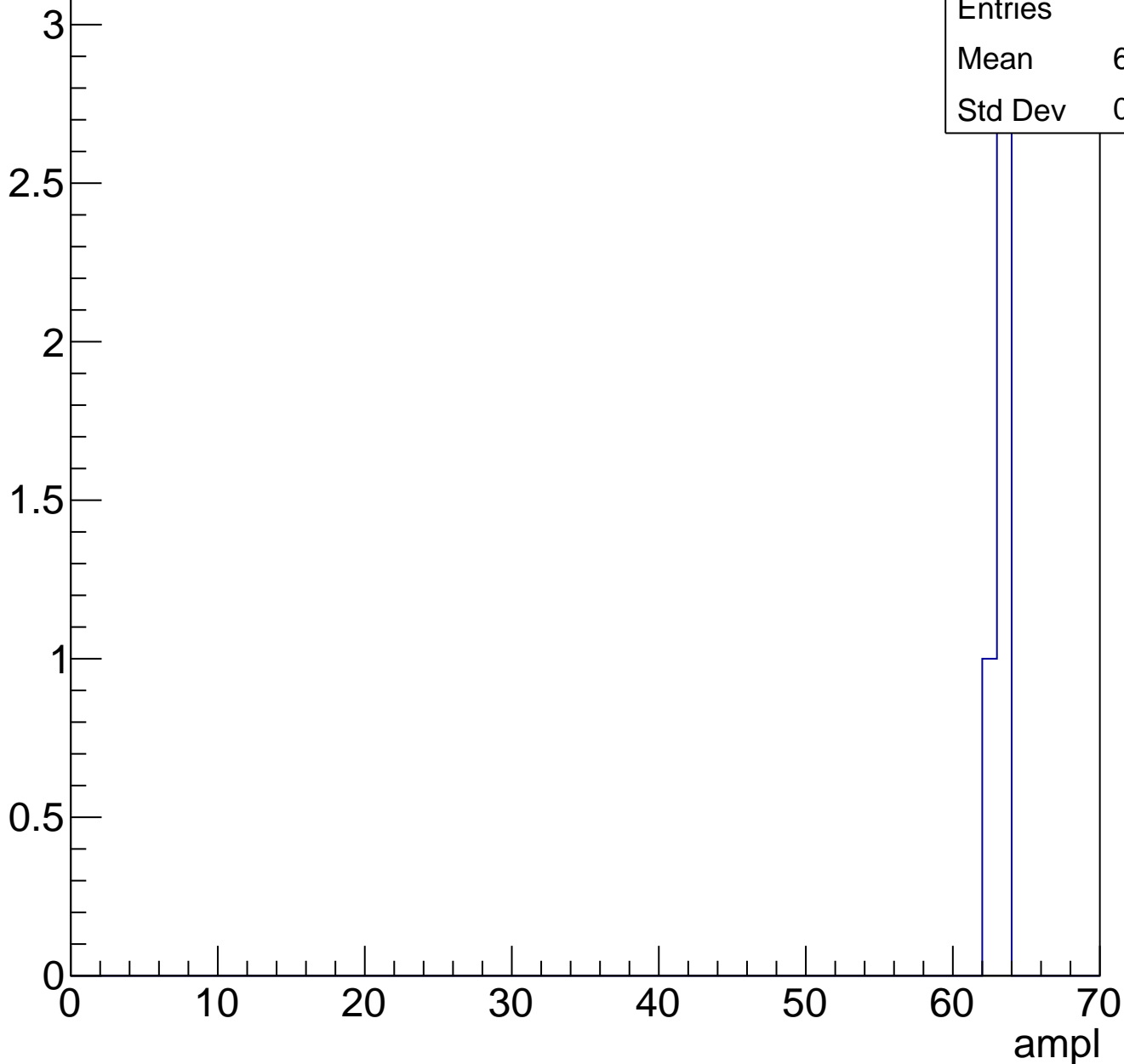
Entries	38
Mean	58.68
Std Dev	9.932



B1L103S, U10-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch8, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	27.6
Std Dev	8.253

Gaus mean : 29.6842

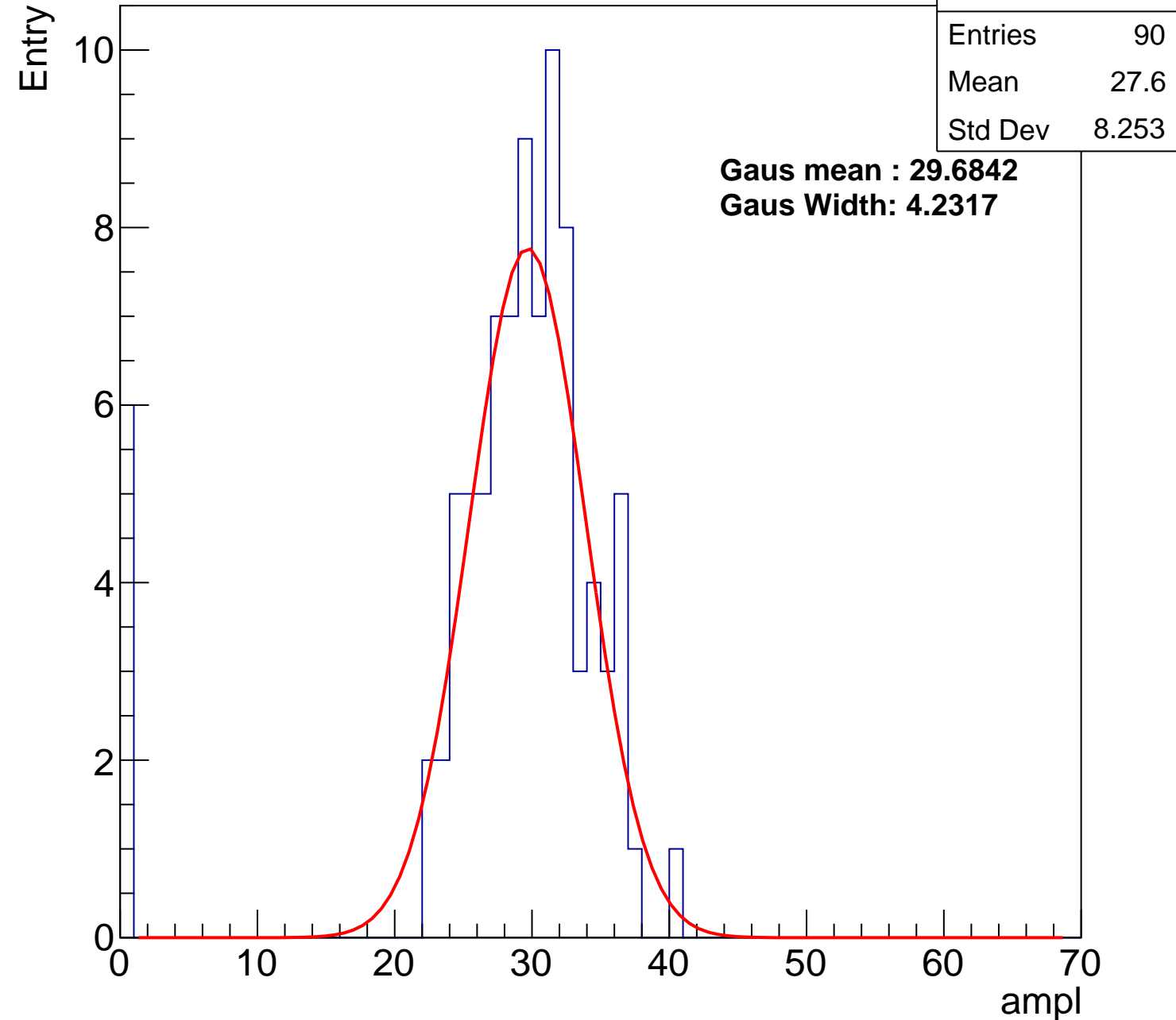
Gaus Width: 4.2317

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch8, adc1

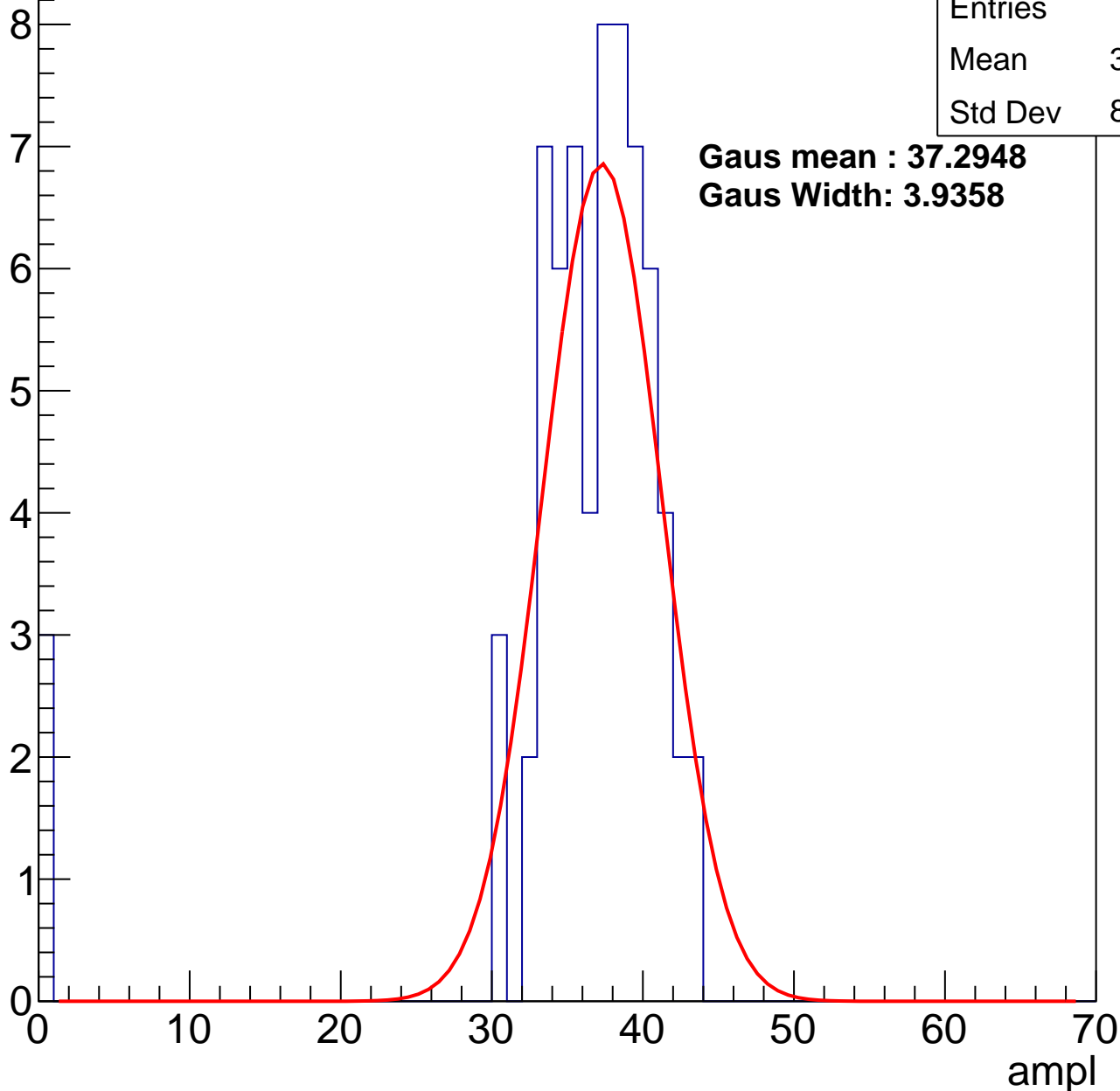
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.14
Std Dev	8.112

Gaus mean : 37.2948

Gaus Width: 3.9358



B1L103S, U10-ch8, adc2

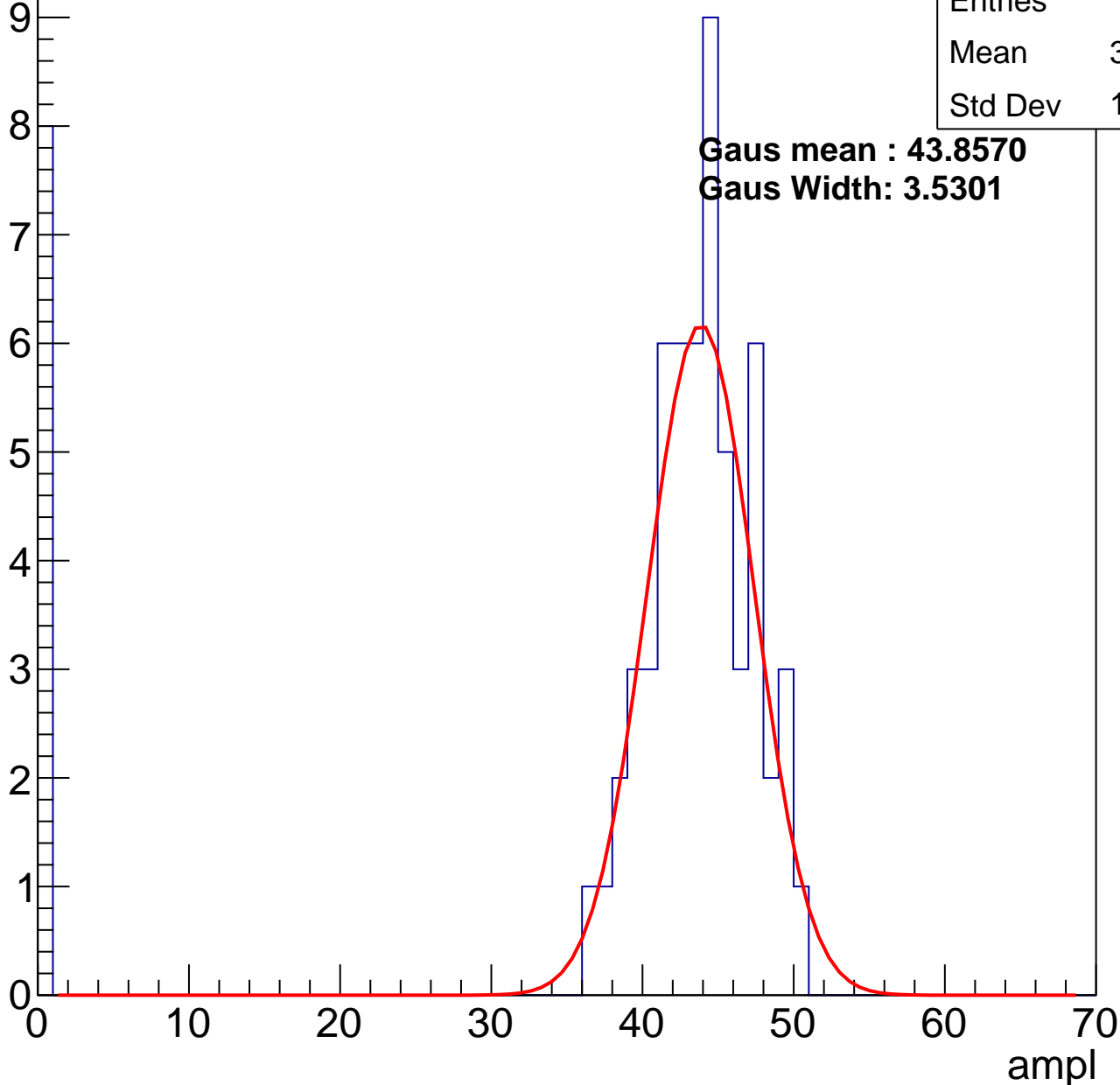
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.09
Std Dev	14.58

Gaus mean : 43.8570

Gaus Width: 3.5301

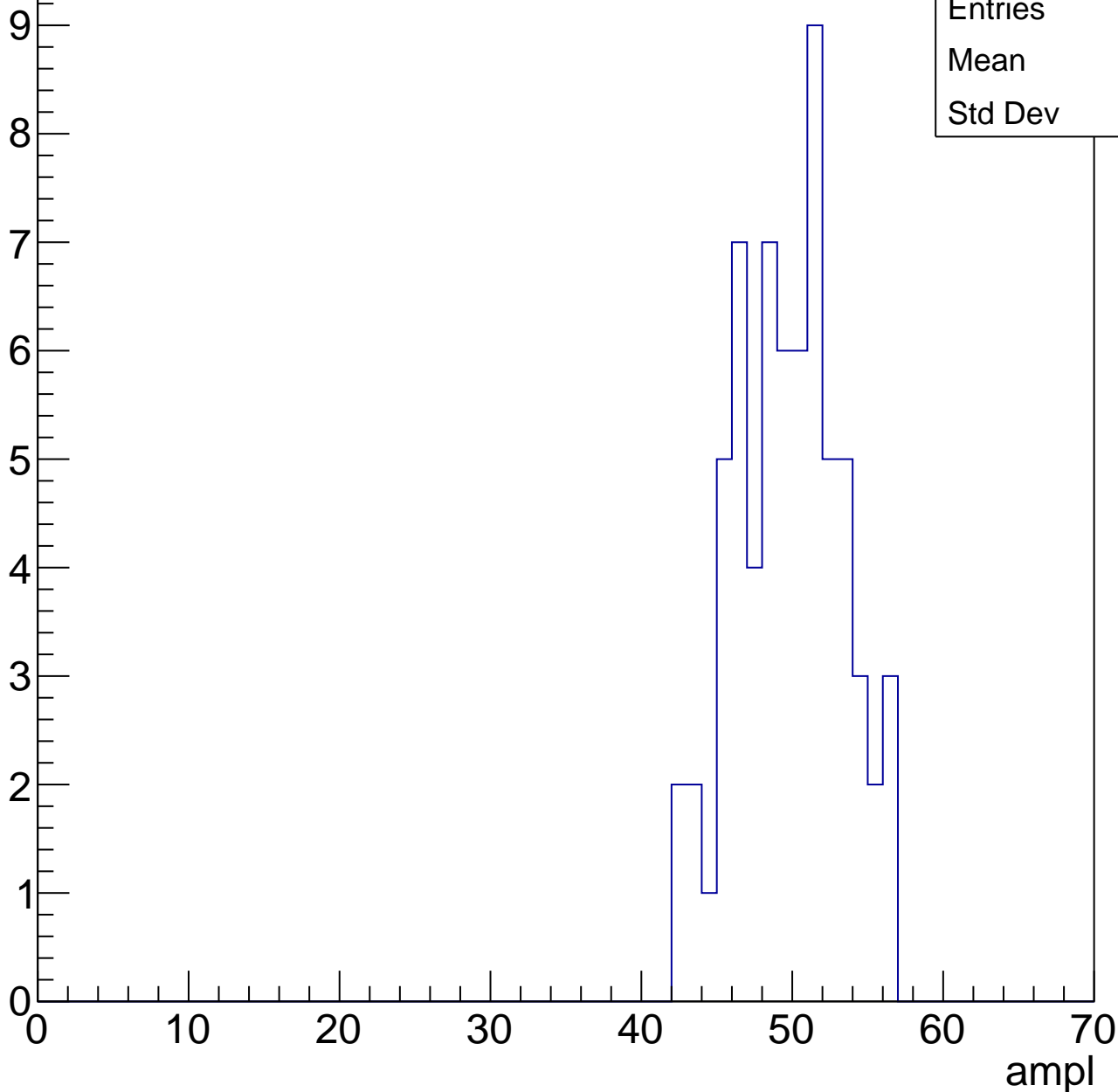


B1L103S, U10-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

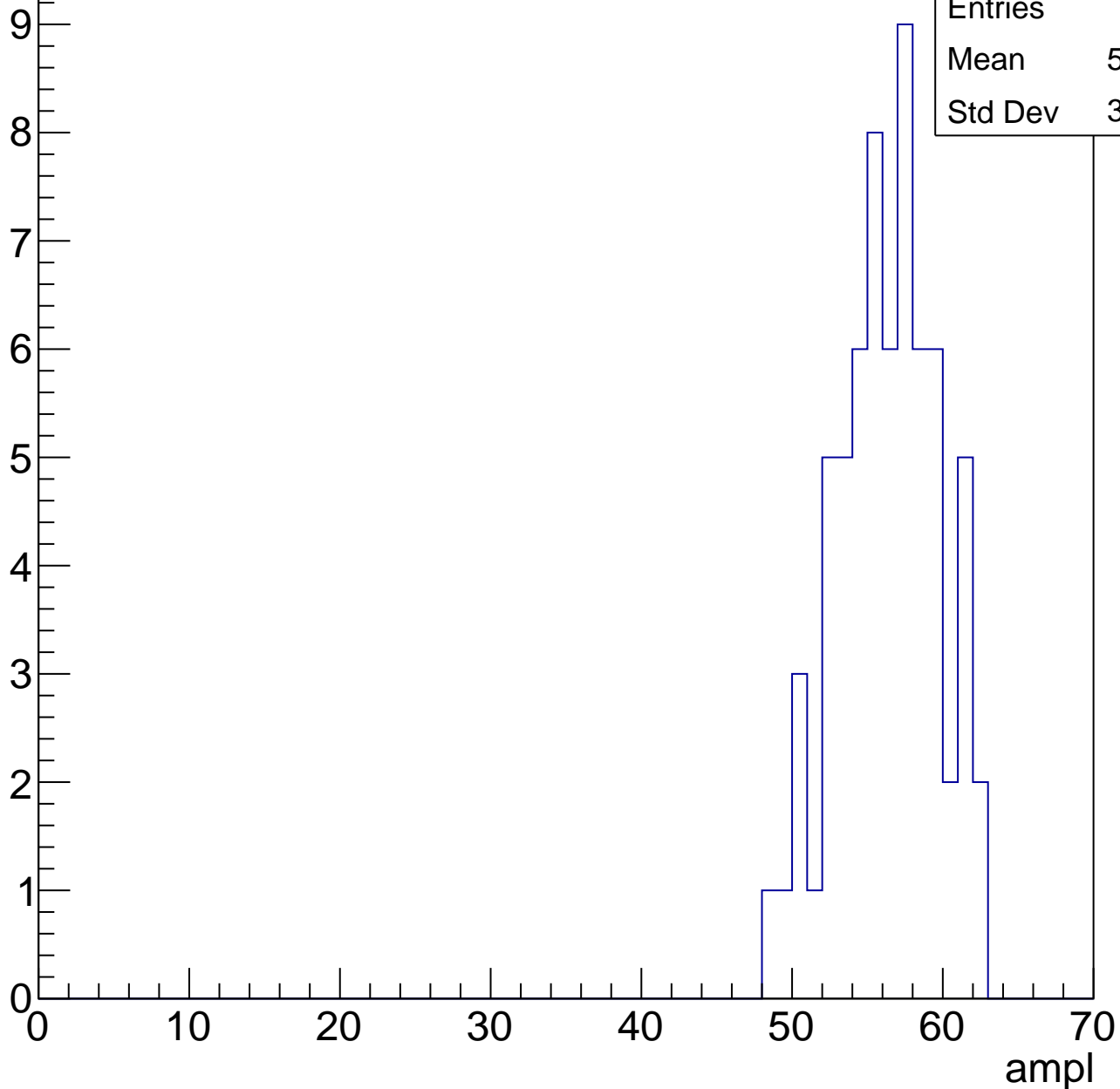
Entries	67
Mean	49.3
Std Dev	3.49



B1L103S, U10-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch8, adc5

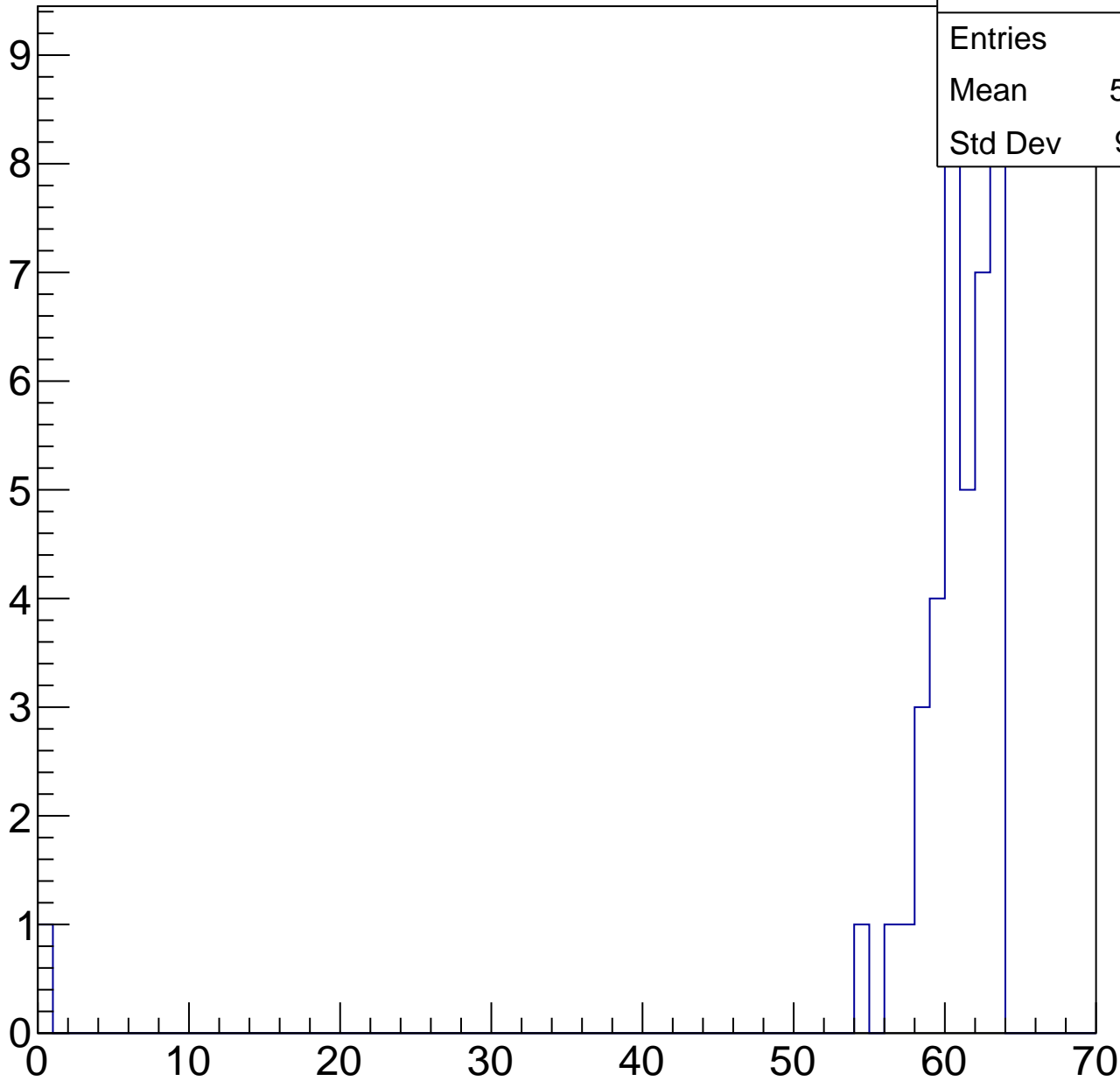
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.08
Std Dev	9.691

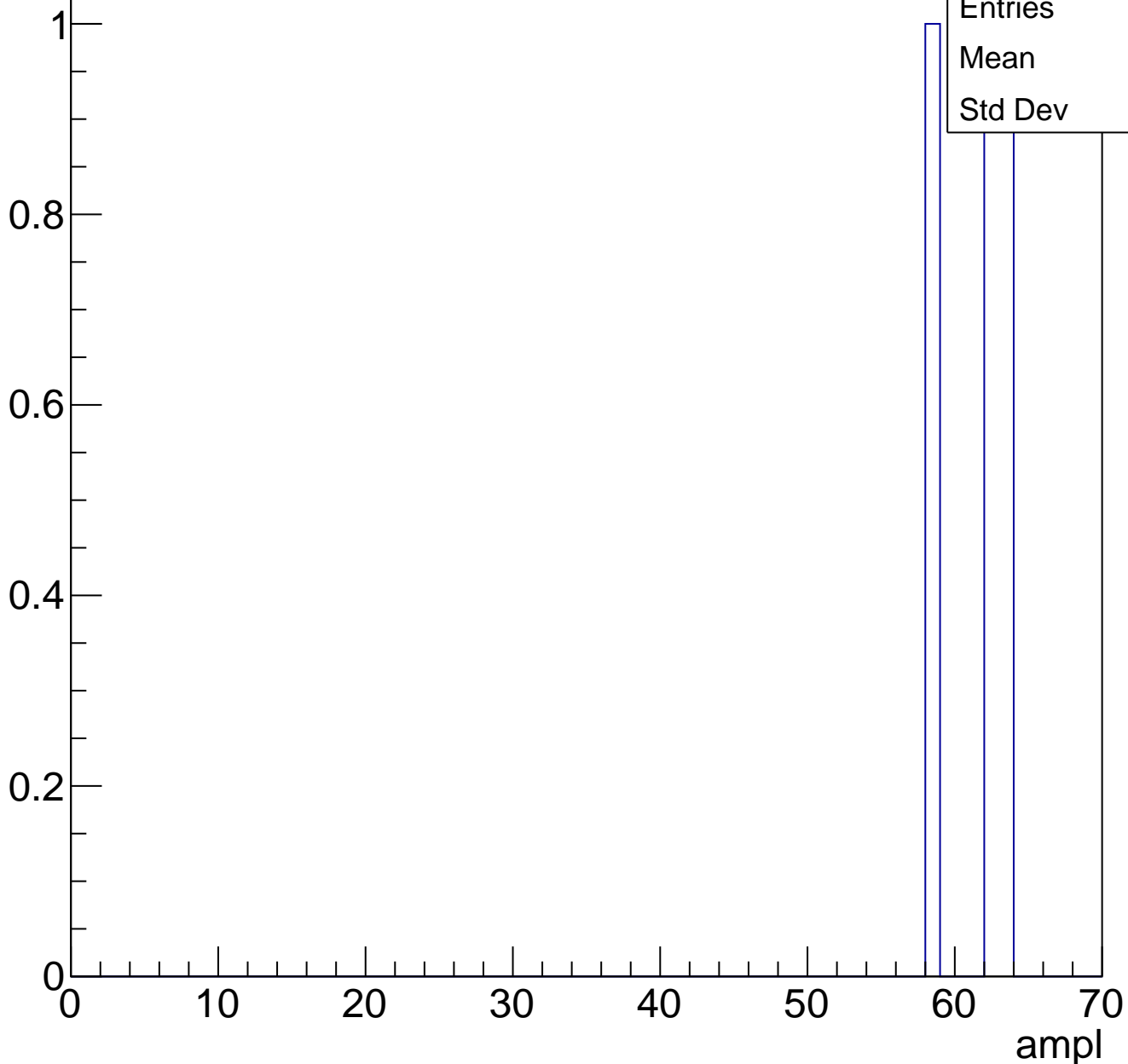
ampl



B1L103S, U10-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	3
Mean	61
Std Dev	2.16

B1L103S, U10-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch9, adc0

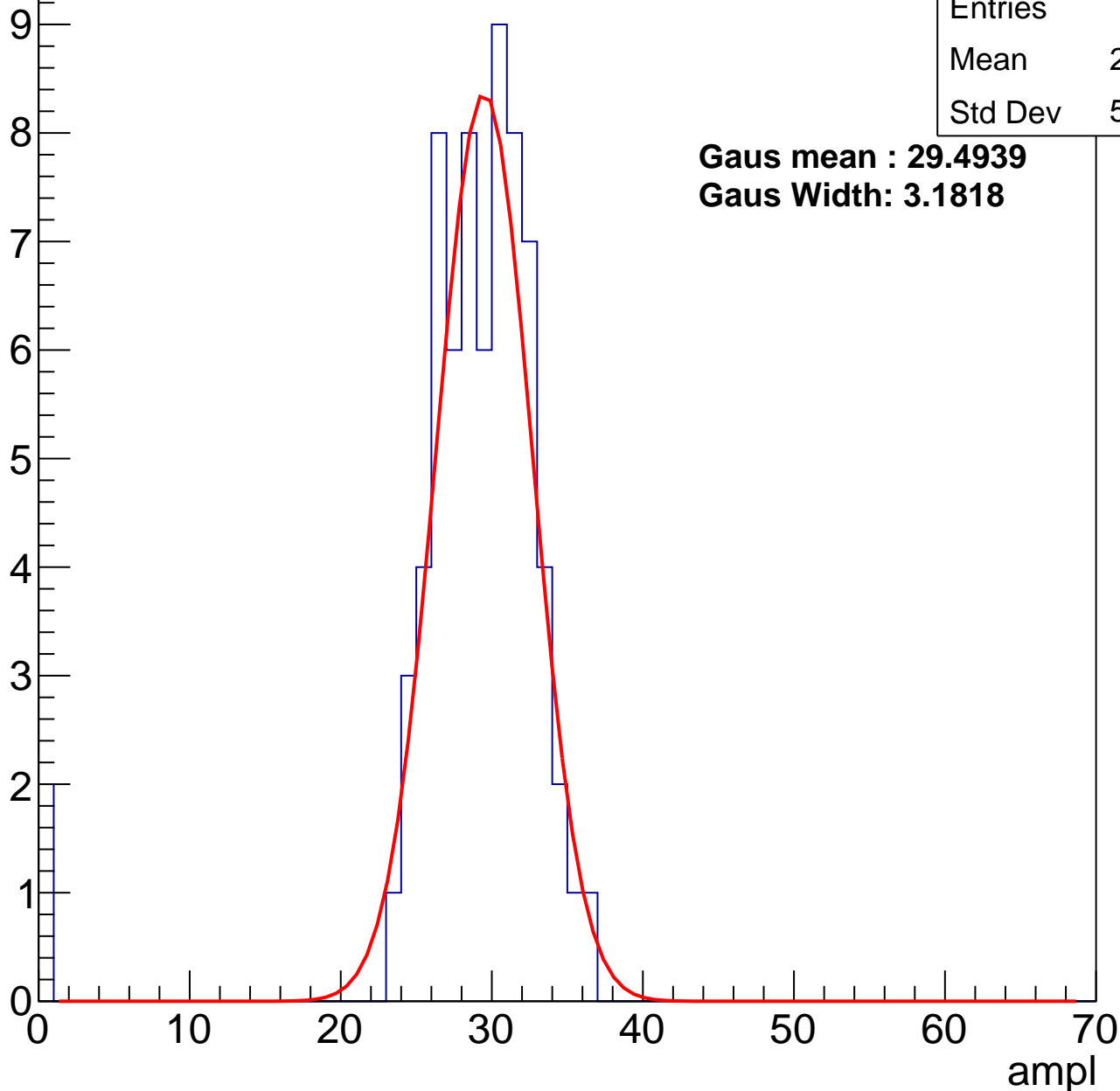
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	28.23
Std Dev	5.635

Gaus mean : 29.4939

Gaus Width: 3.1818



B1L103S, U10-ch9, adc1

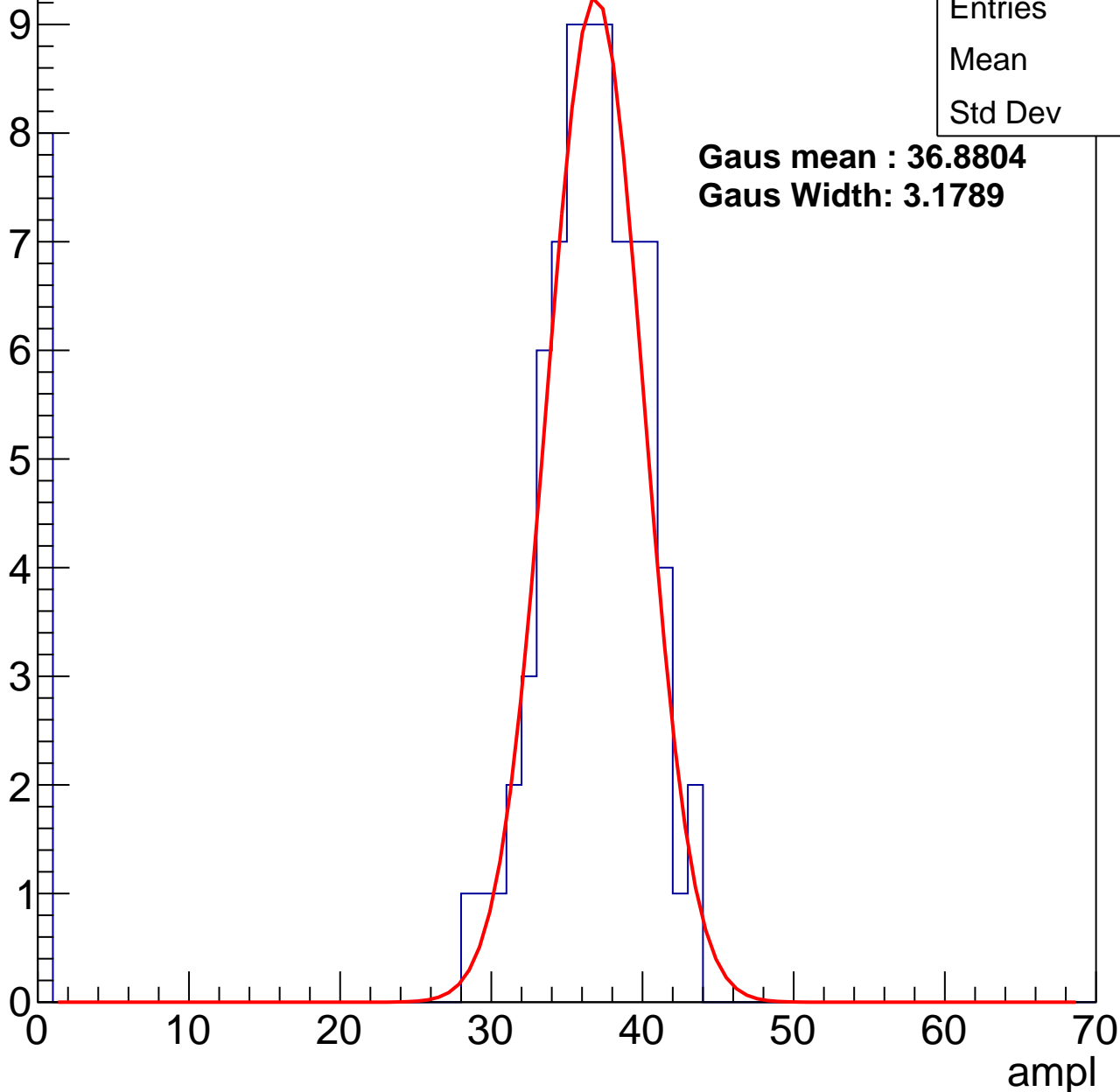
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	32.9
Std Dev	11.1

Gaus mean : 36.8804

Gaus Width: 3.1789



B1L103S, U10-ch9, adc2

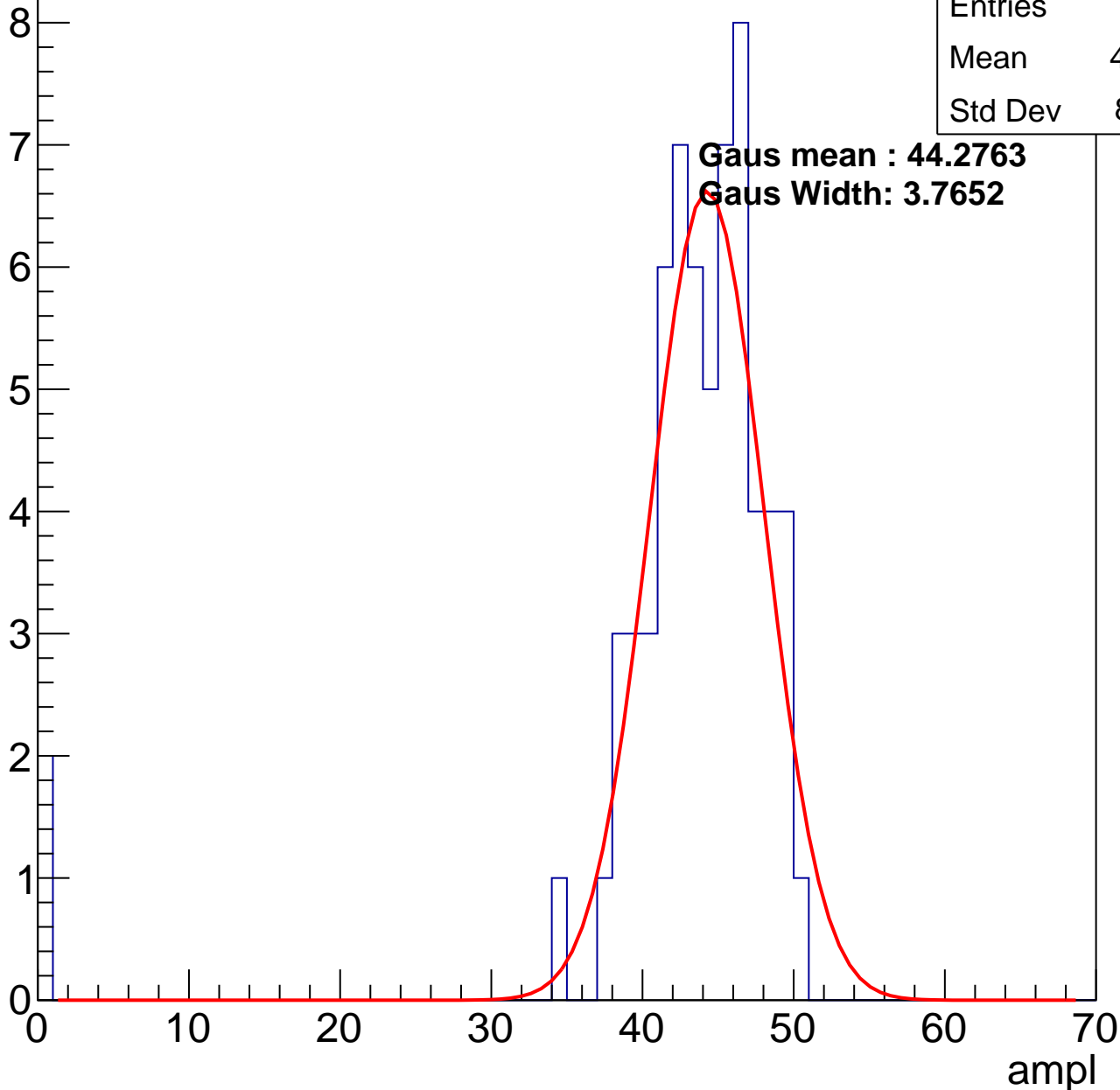
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	42.29
Std Dev	8.251

Gaus mean : 44.2763

Gaus Width: 3.7652

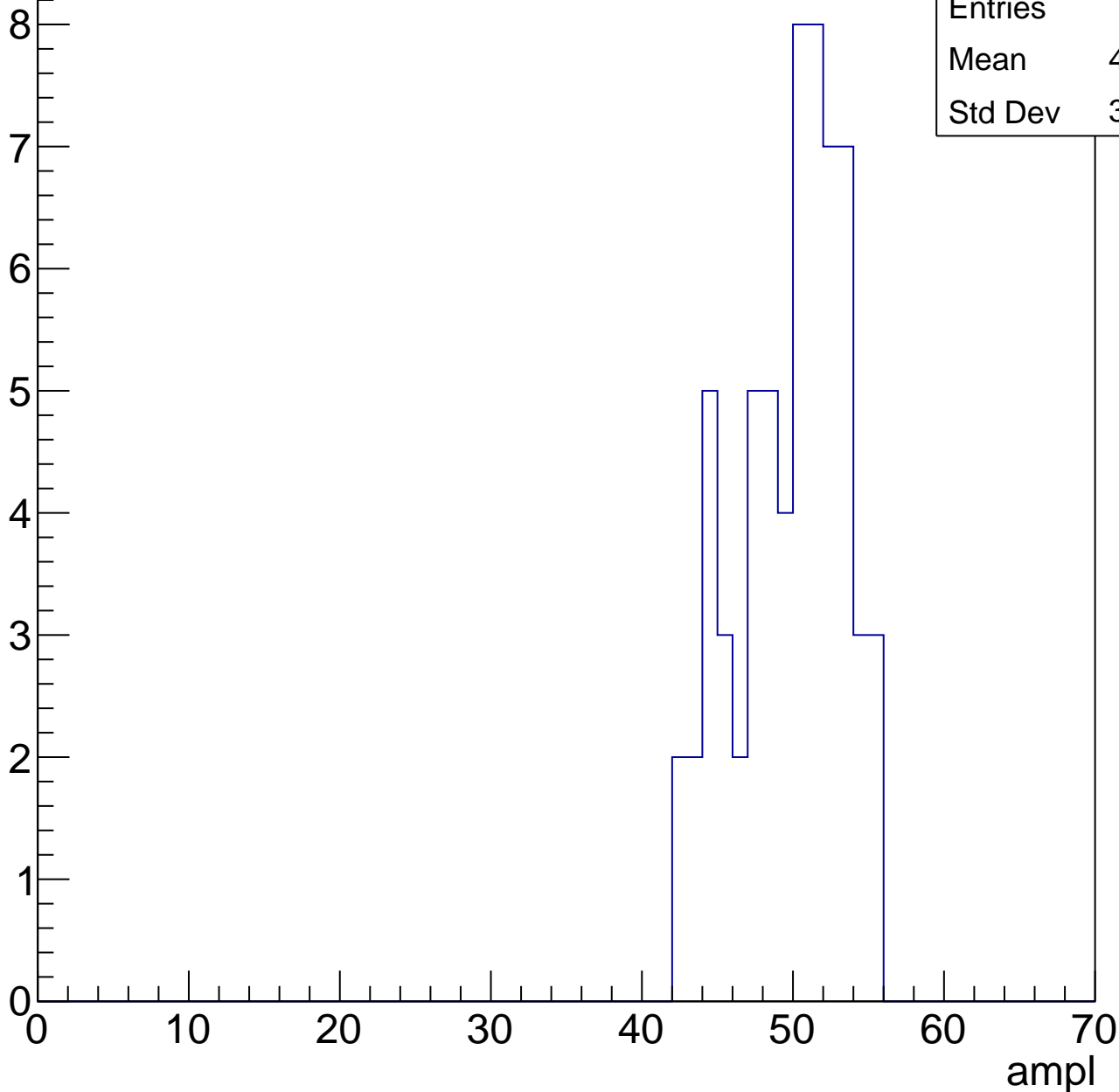


B1L103S, U10-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.34
Std Dev	3.483

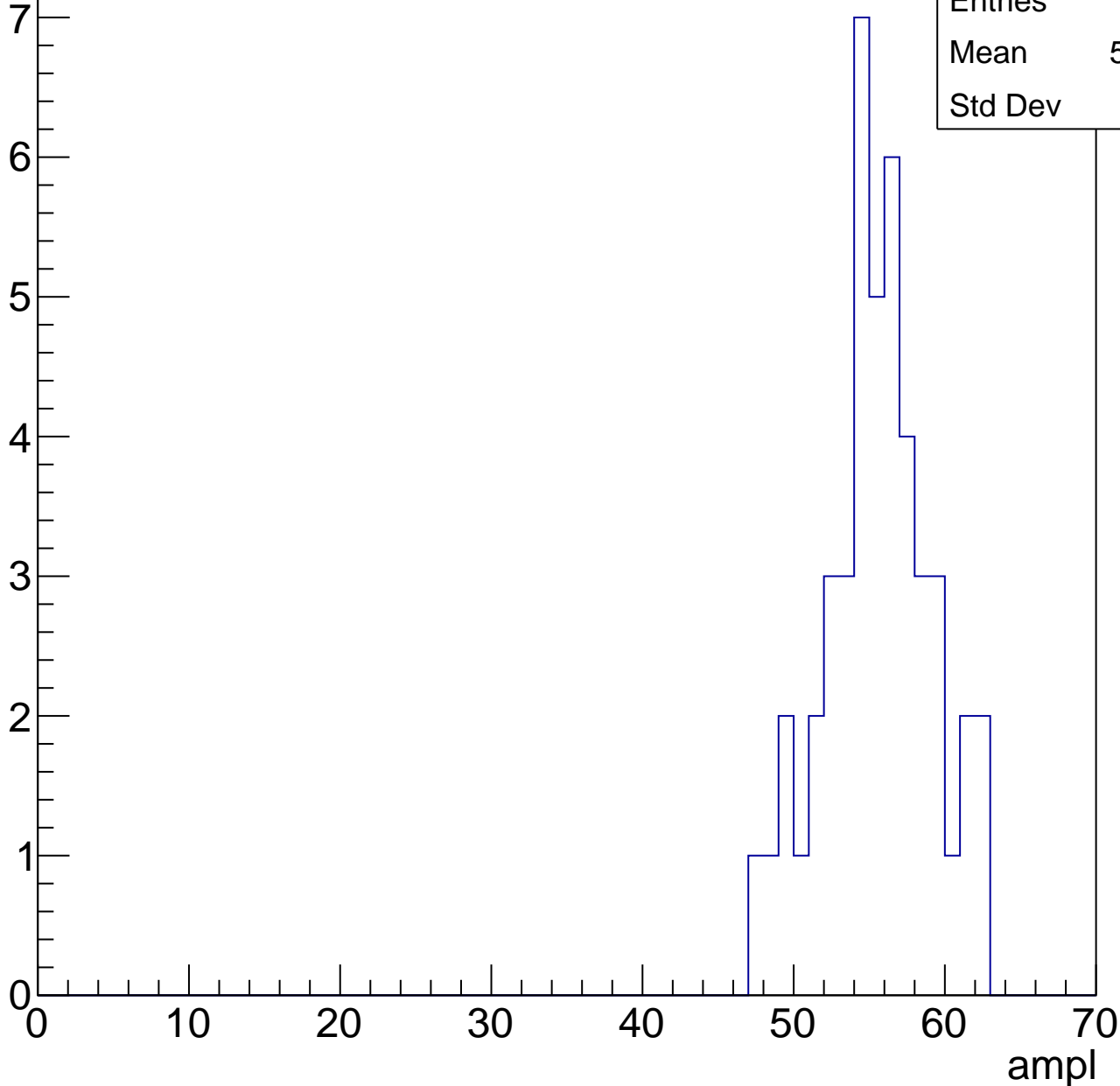


B1L103S, U10-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	55.09
Std Dev	3.55

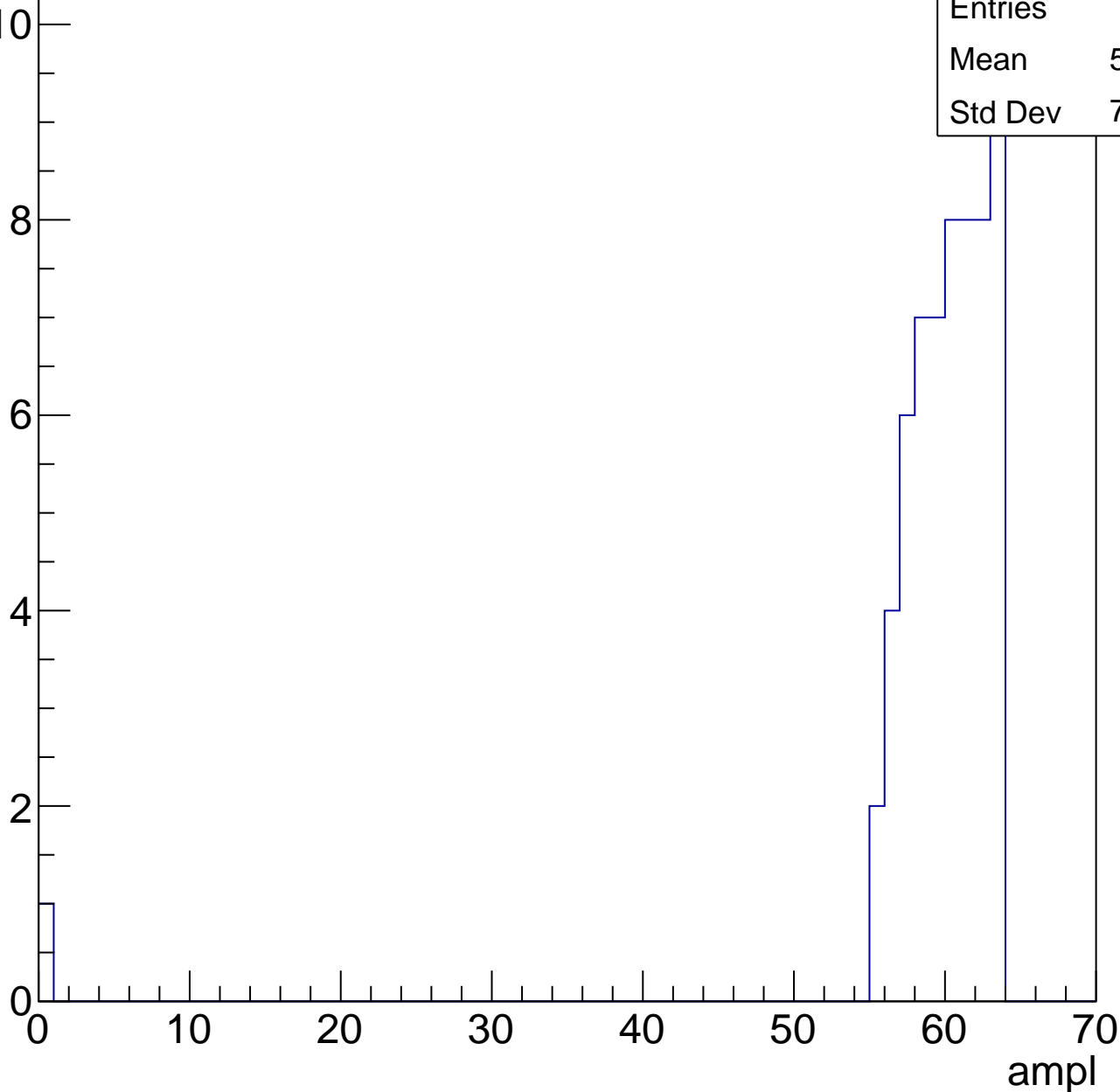


B1L103S, U10-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.84
Std Dev	7.945



B1L103S, U10-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

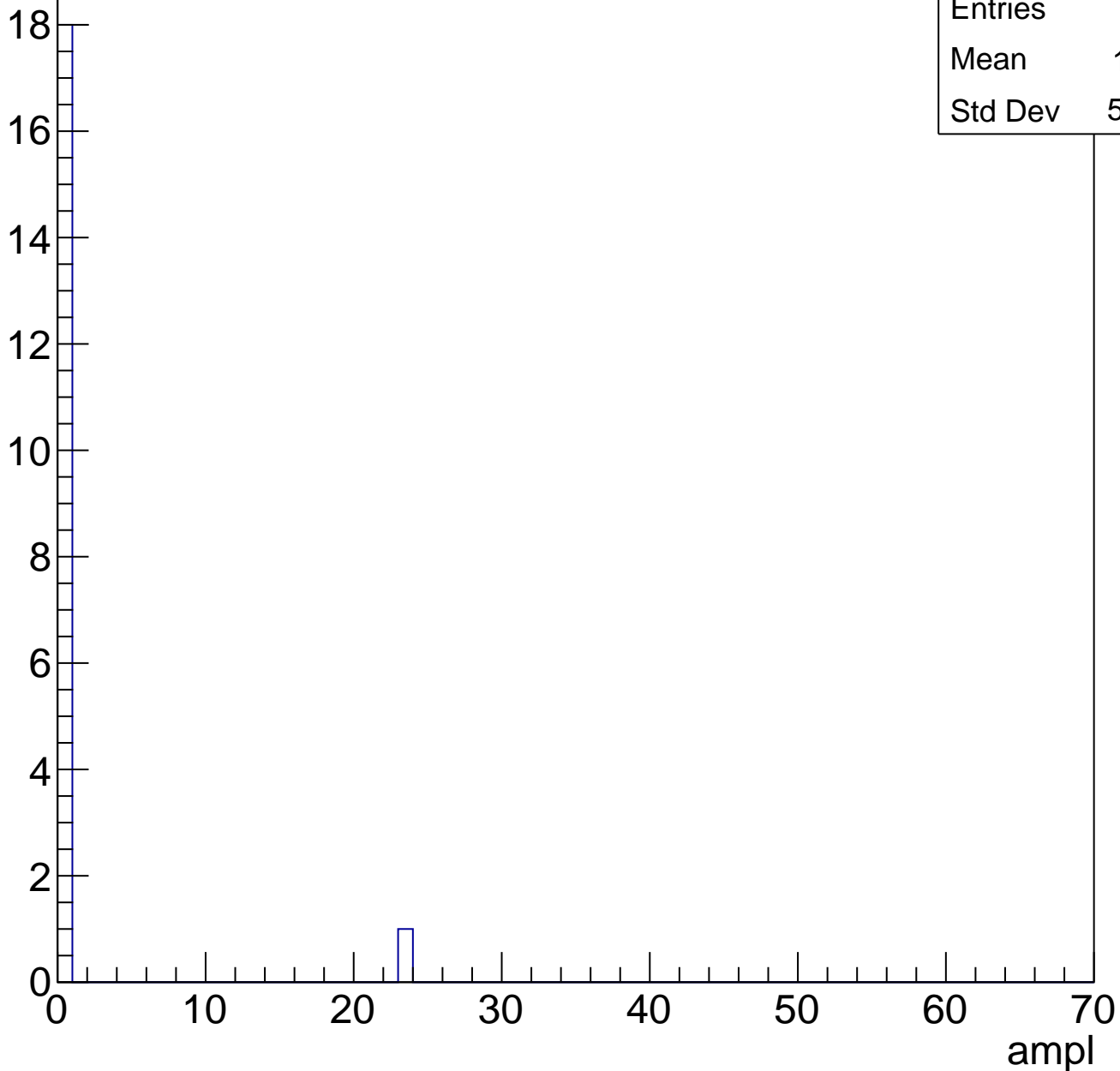
Entry



B1L103S, U10-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch10, adc0

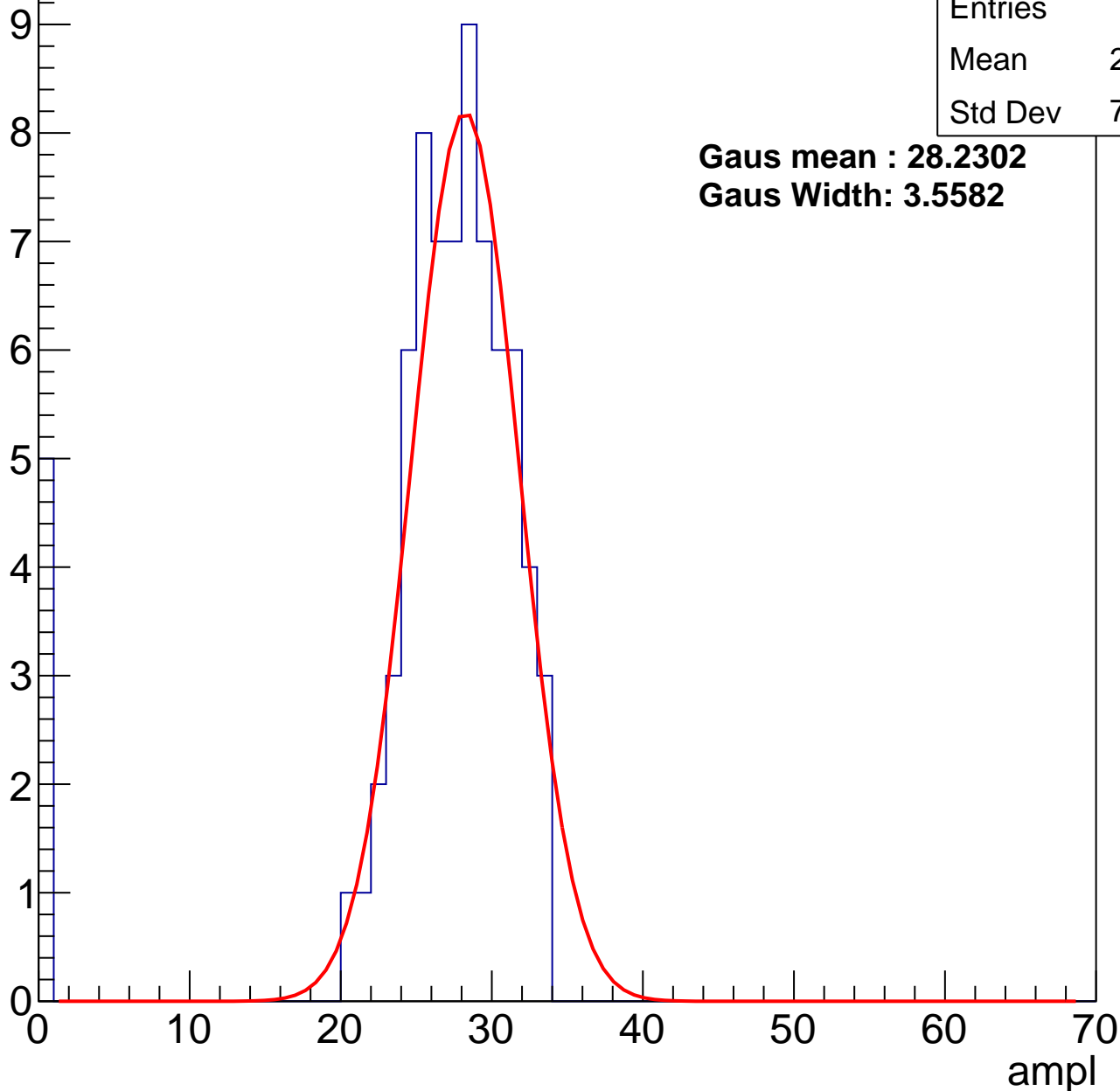
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.56
Std Dev	7.448

Gaus mean : 28.2302

Gaus Width: 3.5582



B1L103S, U10-ch10, adc1

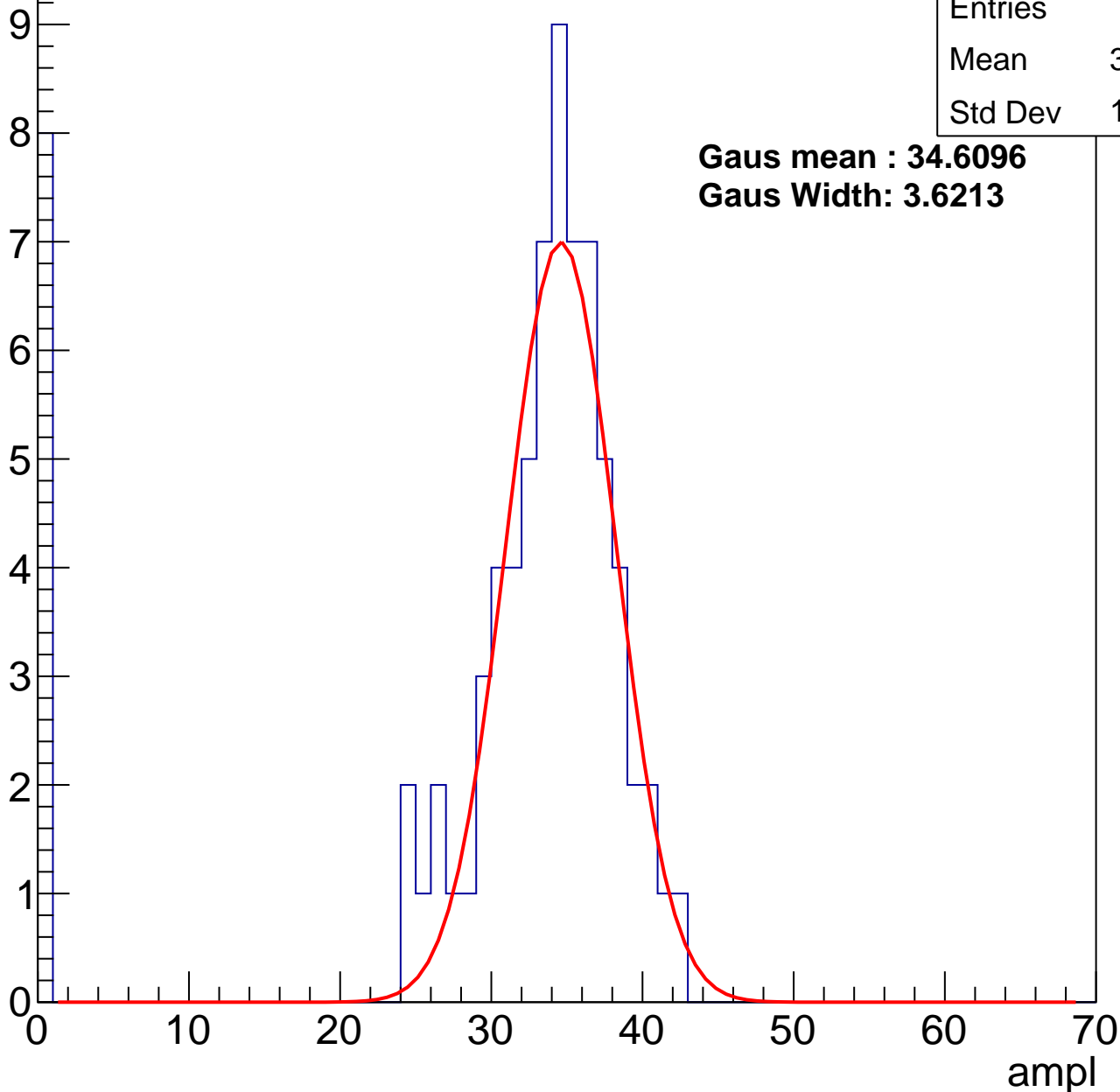
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	30.04
Std Dev	10.96

Gaus mean : 34.6096

Gaus Width: 3.6213



B1L103S, U10-ch10, adc2

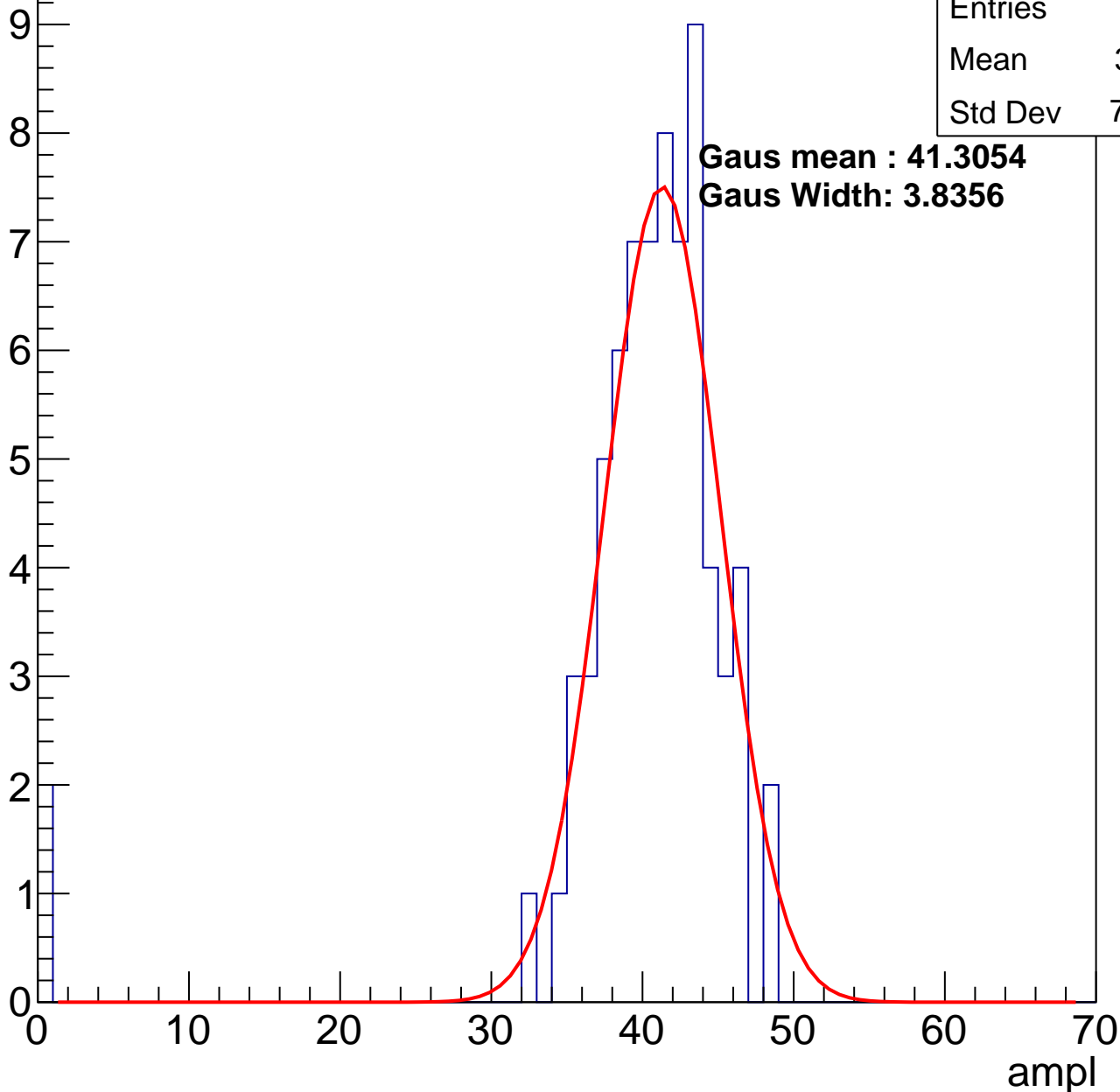
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	39.51
Std Dev	7.463

Gaus mean : 41.3054

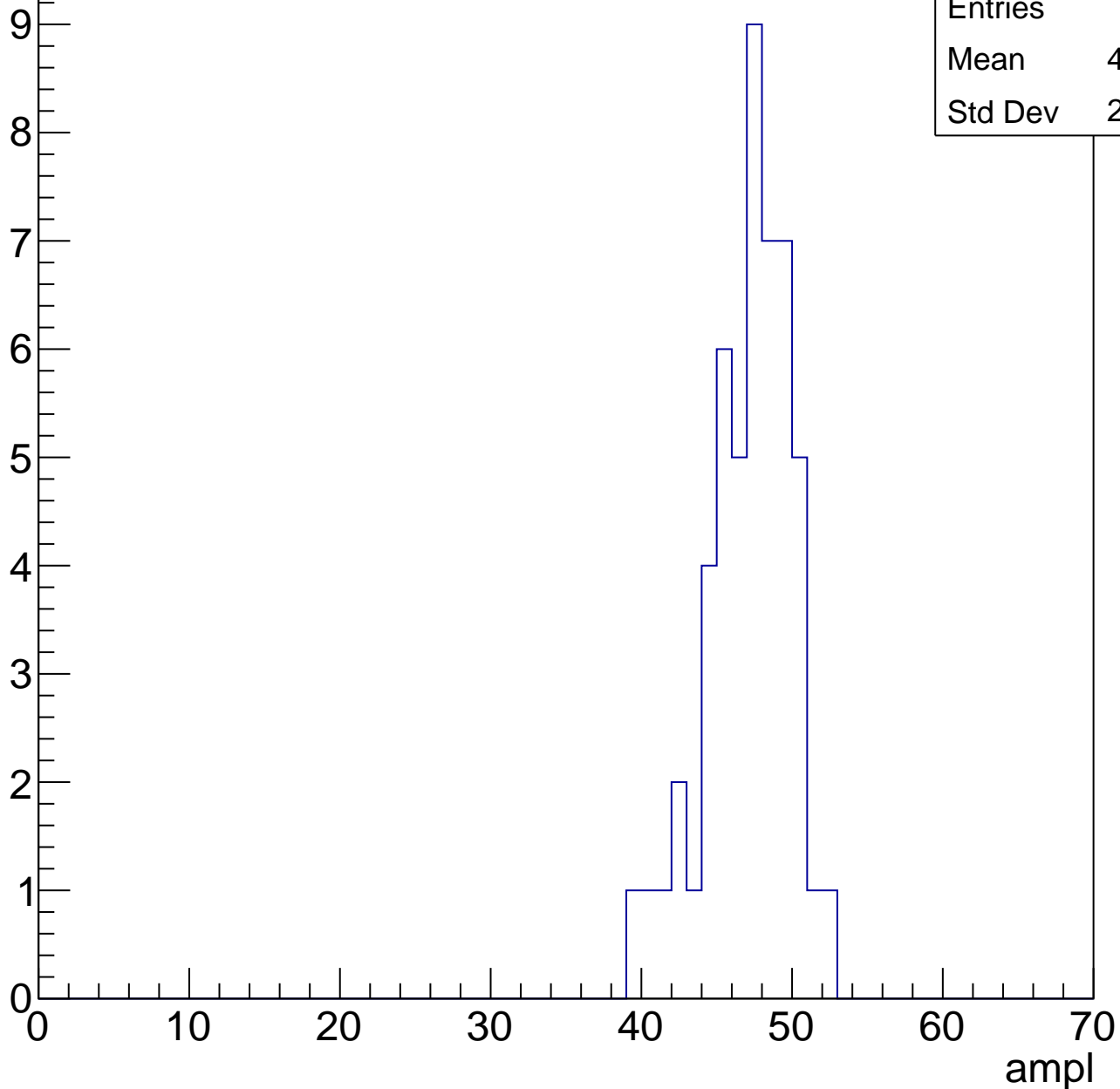
Gaus Width: 3.8356



B1L103S, U10-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

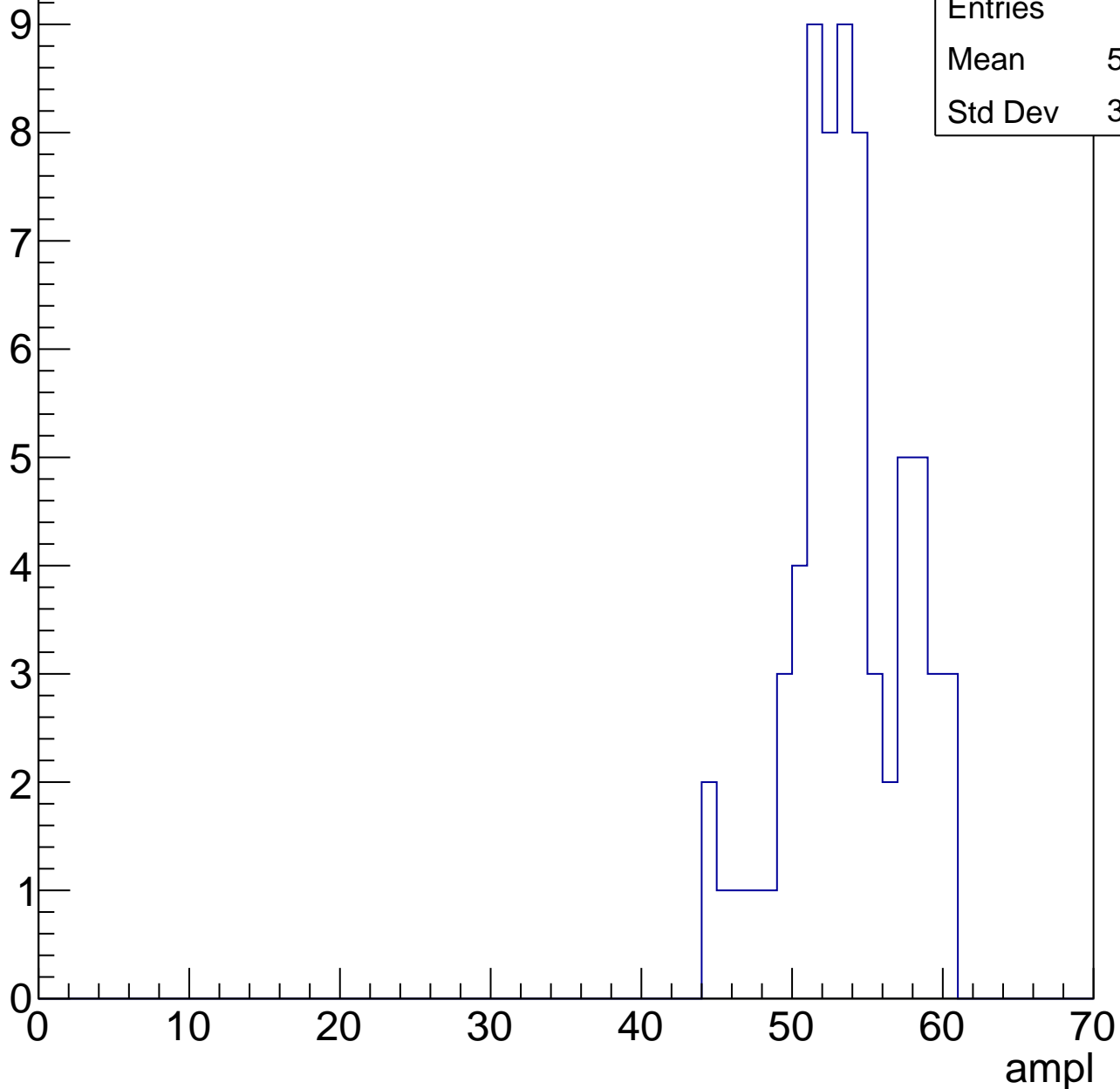
Entry



B1L103S, U10-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



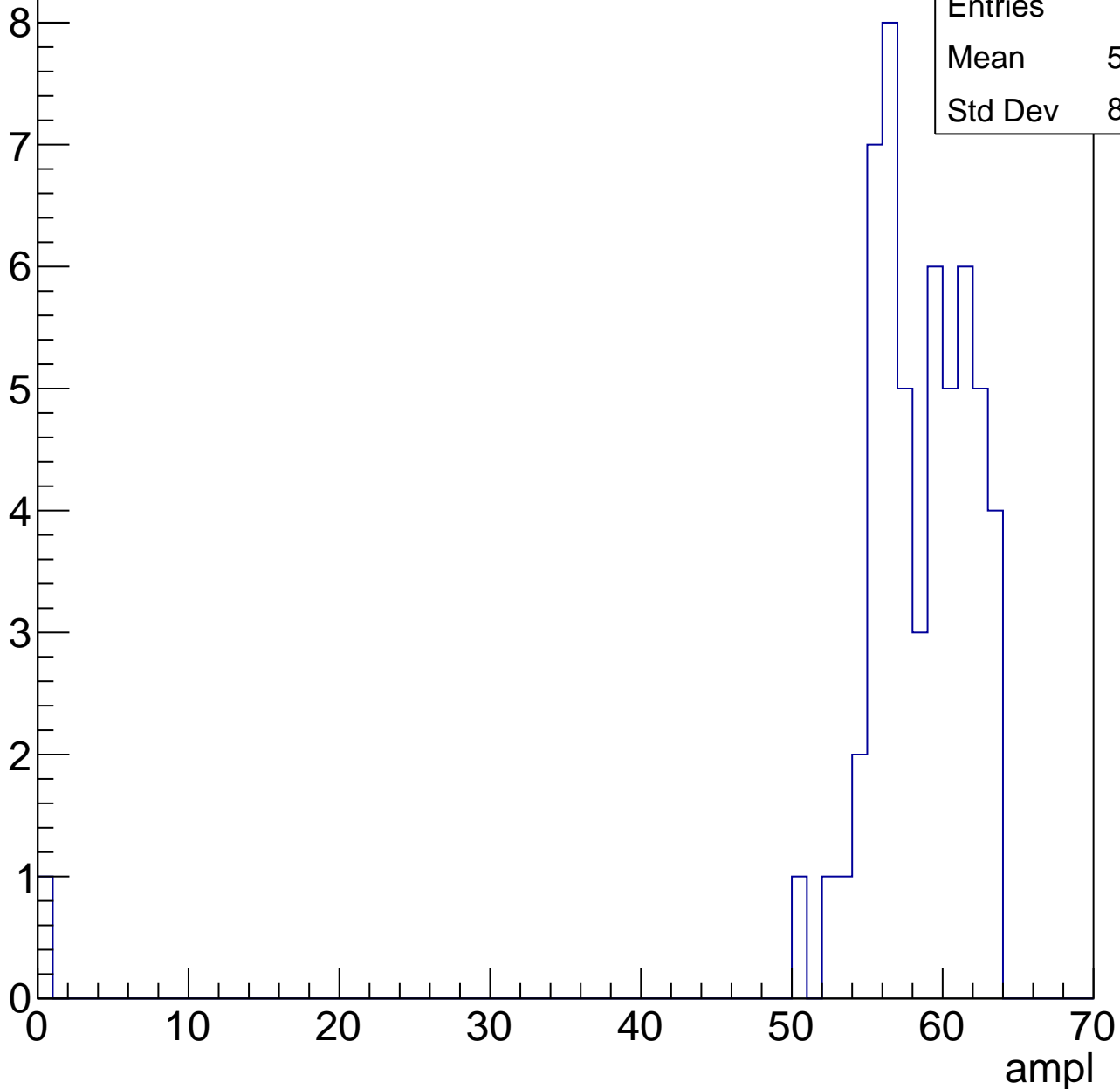
Entries	68
Mean	53.15
Std Dev	3.758

B1L103S, U10-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.04
Std Dev	8.345

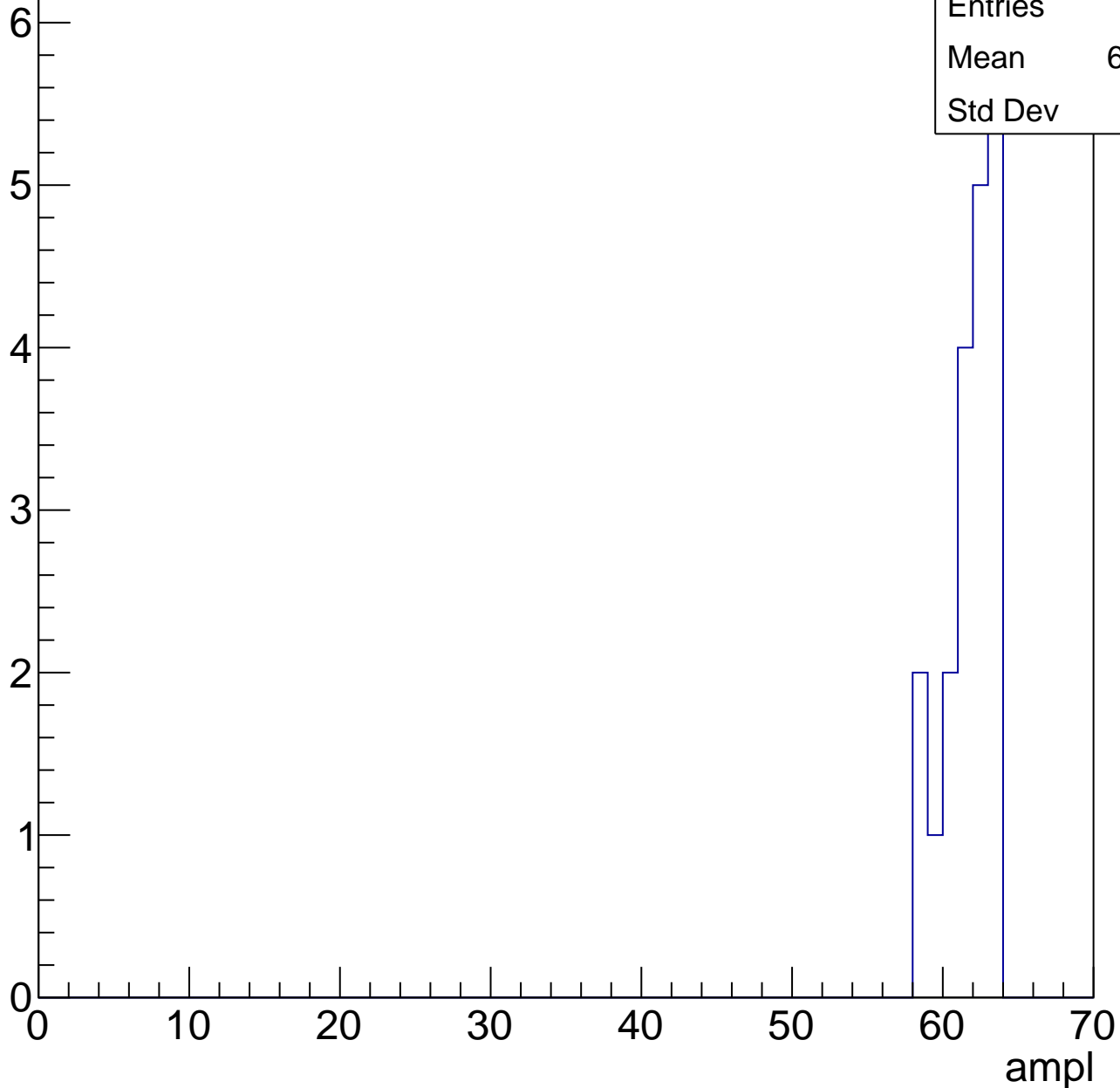


B1L103S, U10-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.35
Std Dev	1.59



B1L103S, U10-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch11, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	28.07
Std Dev	8.111

Gaus mean : 30.6714

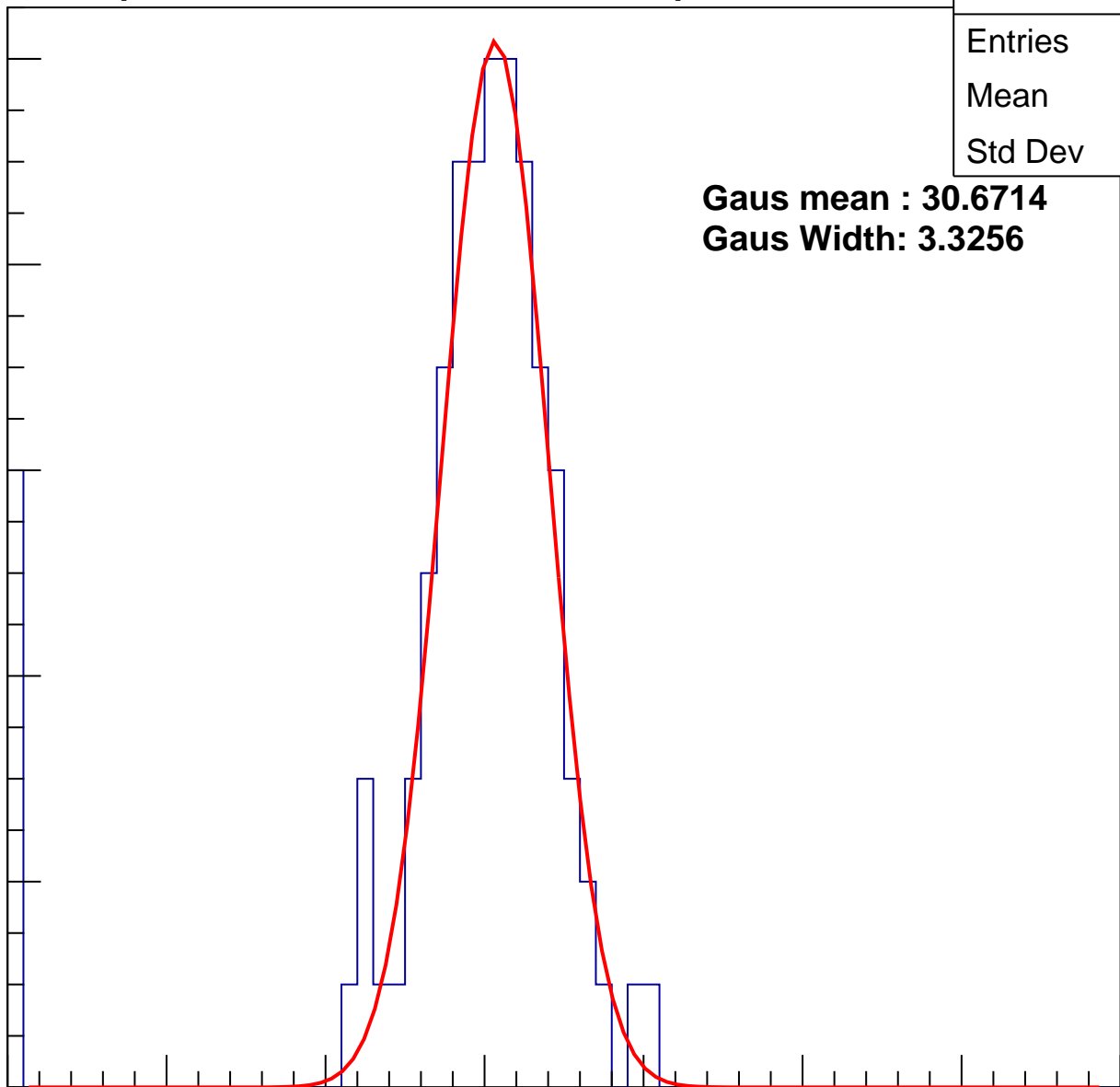
Gaus Width: 3.3256

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch11, adc1

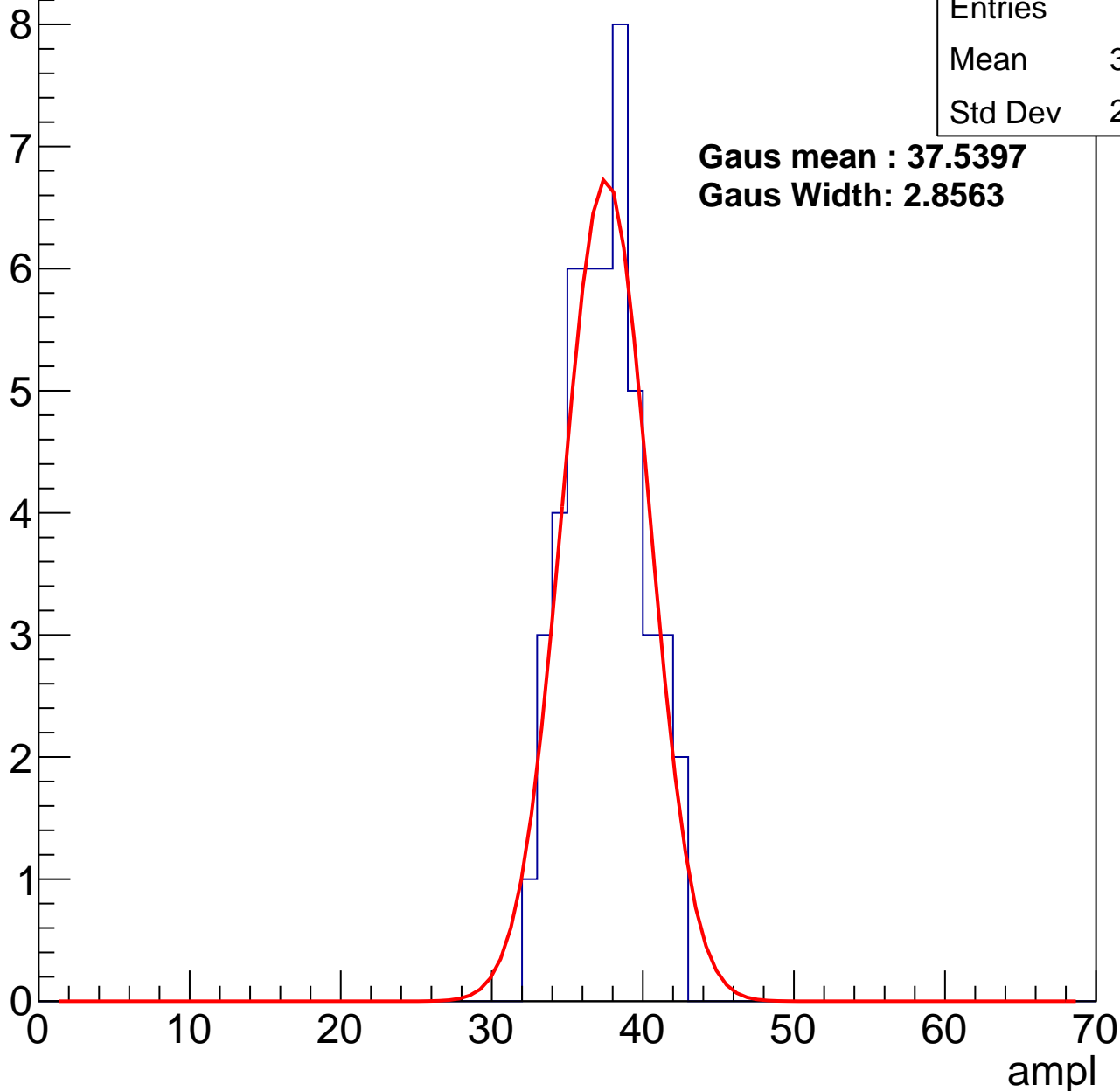
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	37.04
Std Dev	2.492

Gaus mean : 37.5397

Gaus Width: 2.8563



B1L103S, U10-ch11, adc2

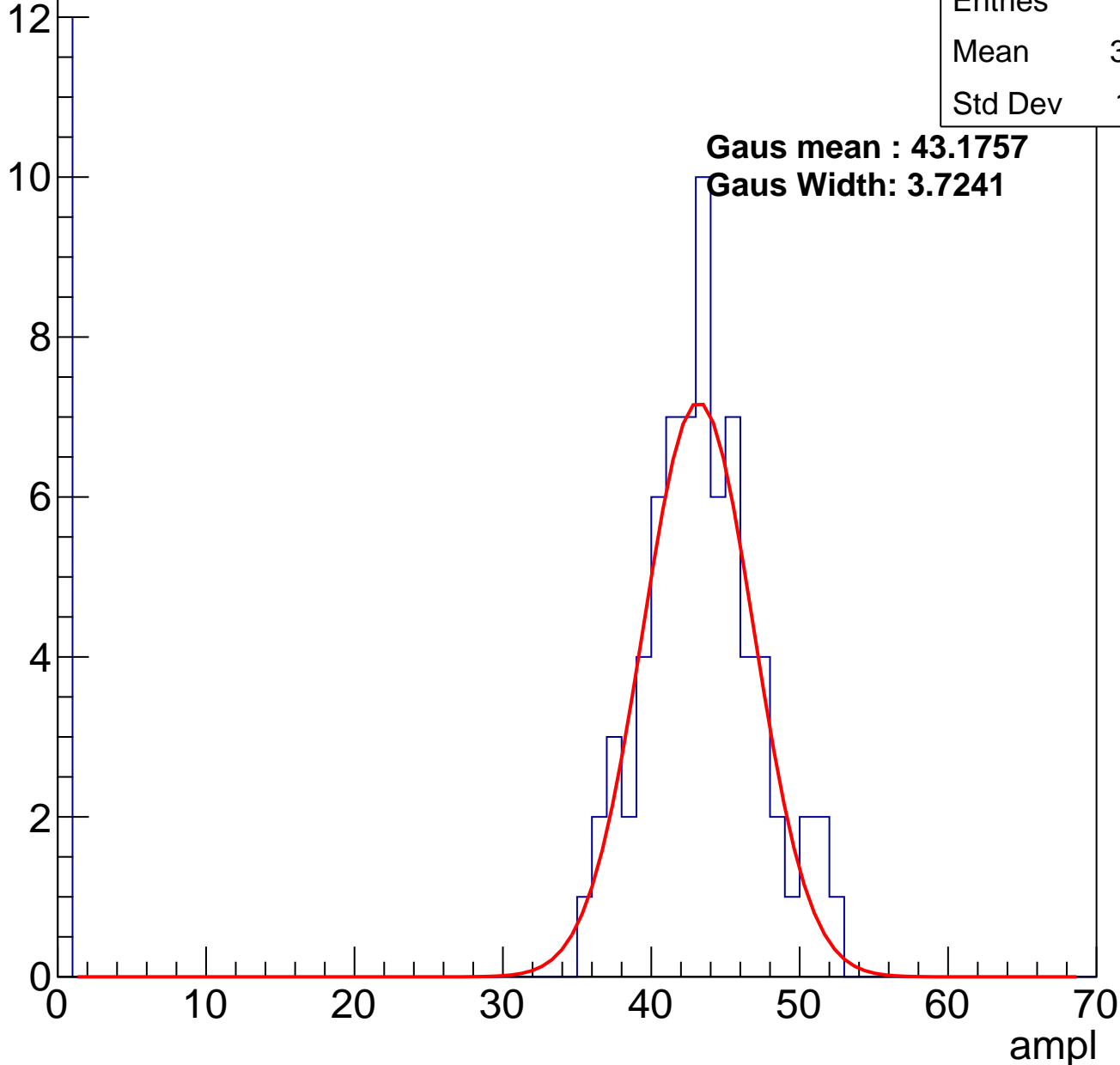
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	36.76
Std Dev	15.51

Gaus mean : 43.1757

Gaus Width: 3.7241

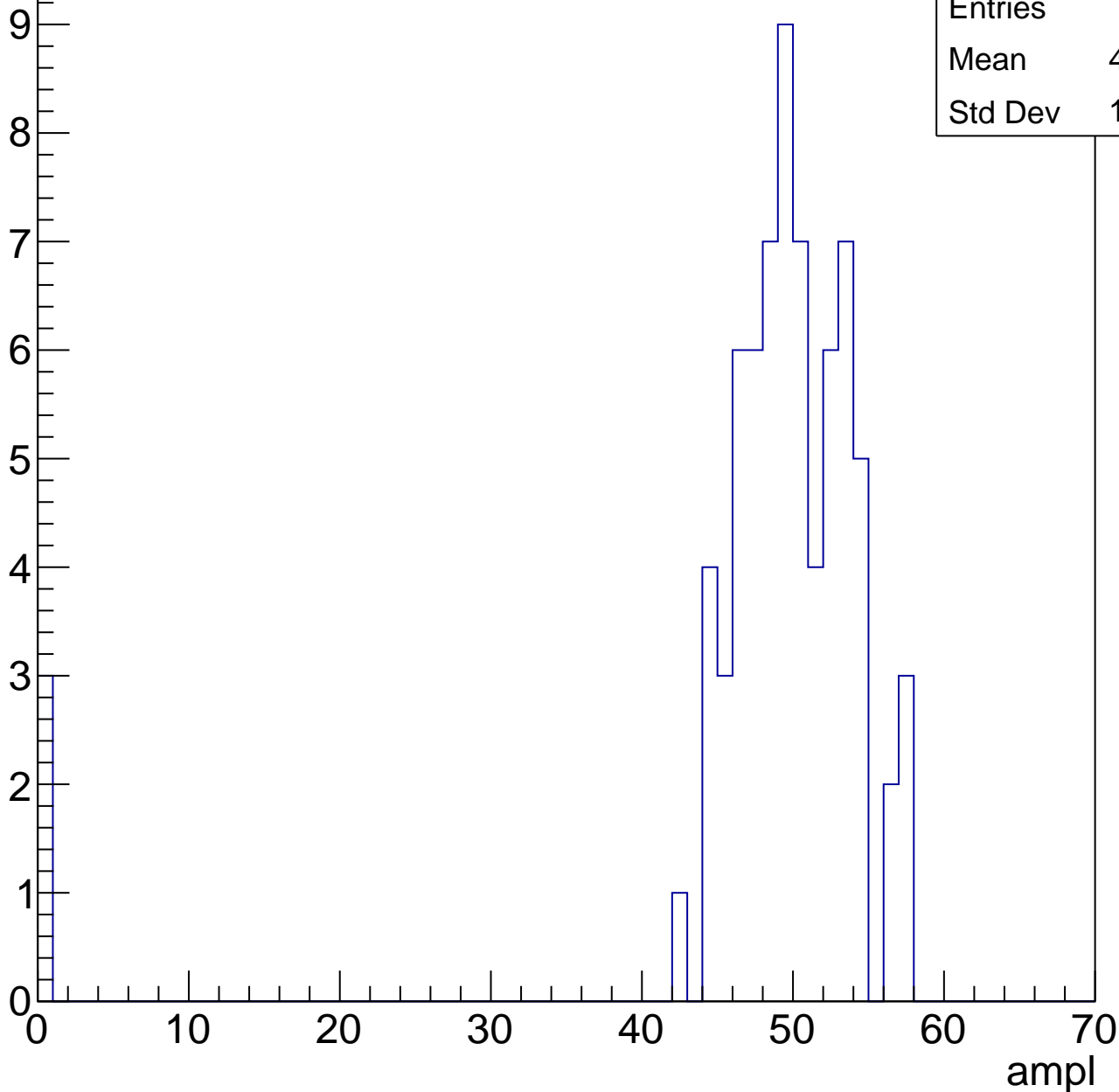


B1L103S, U10-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	47.64
Std Dev	10.44



B1L103S, U10-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	55
Mean	55.73
Std Dev	2.951

Entry

10

8

6

4

2

0

0

10

20

30

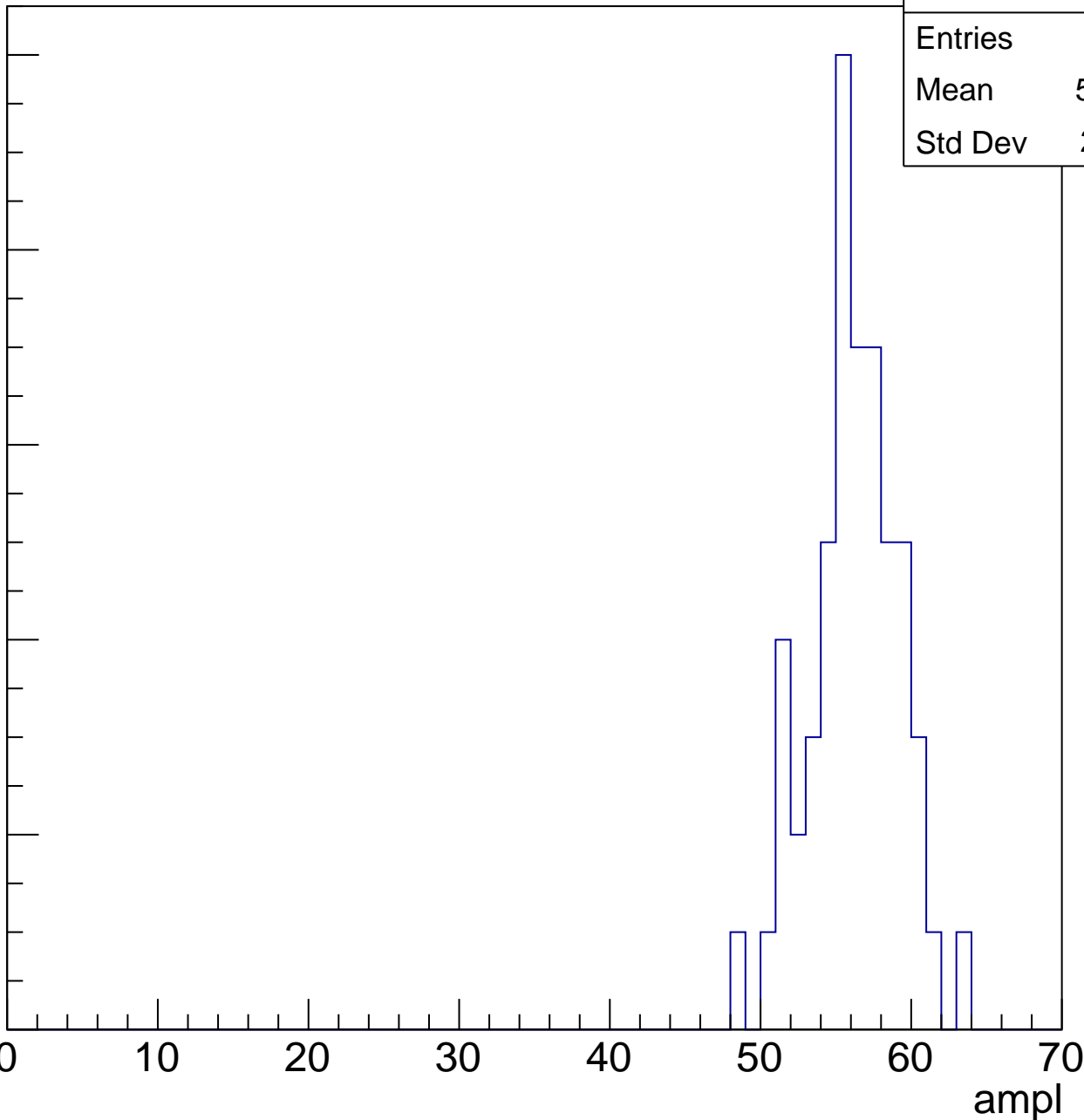
40

50

60

70

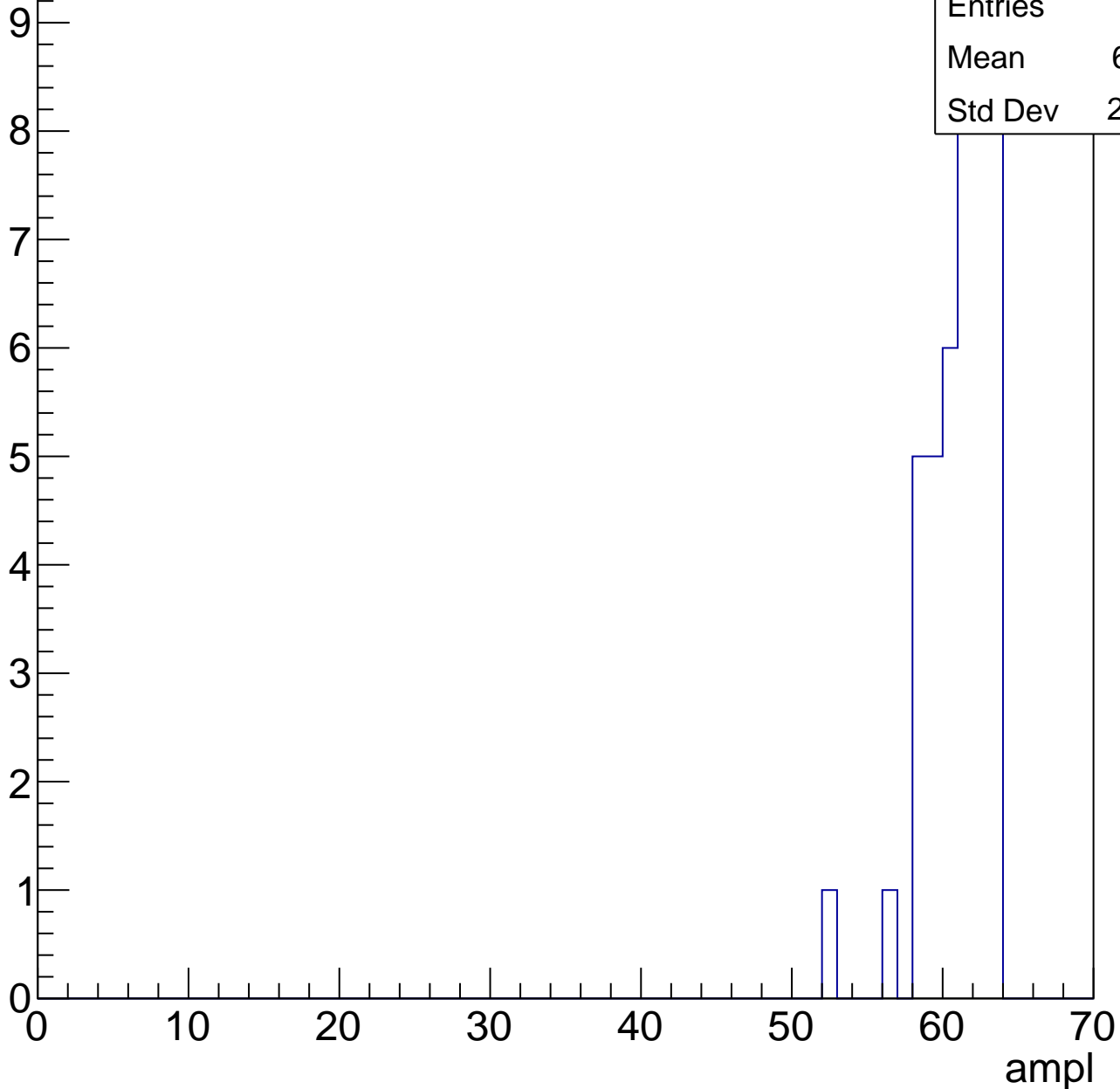
ampl



B1L103S, U10-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

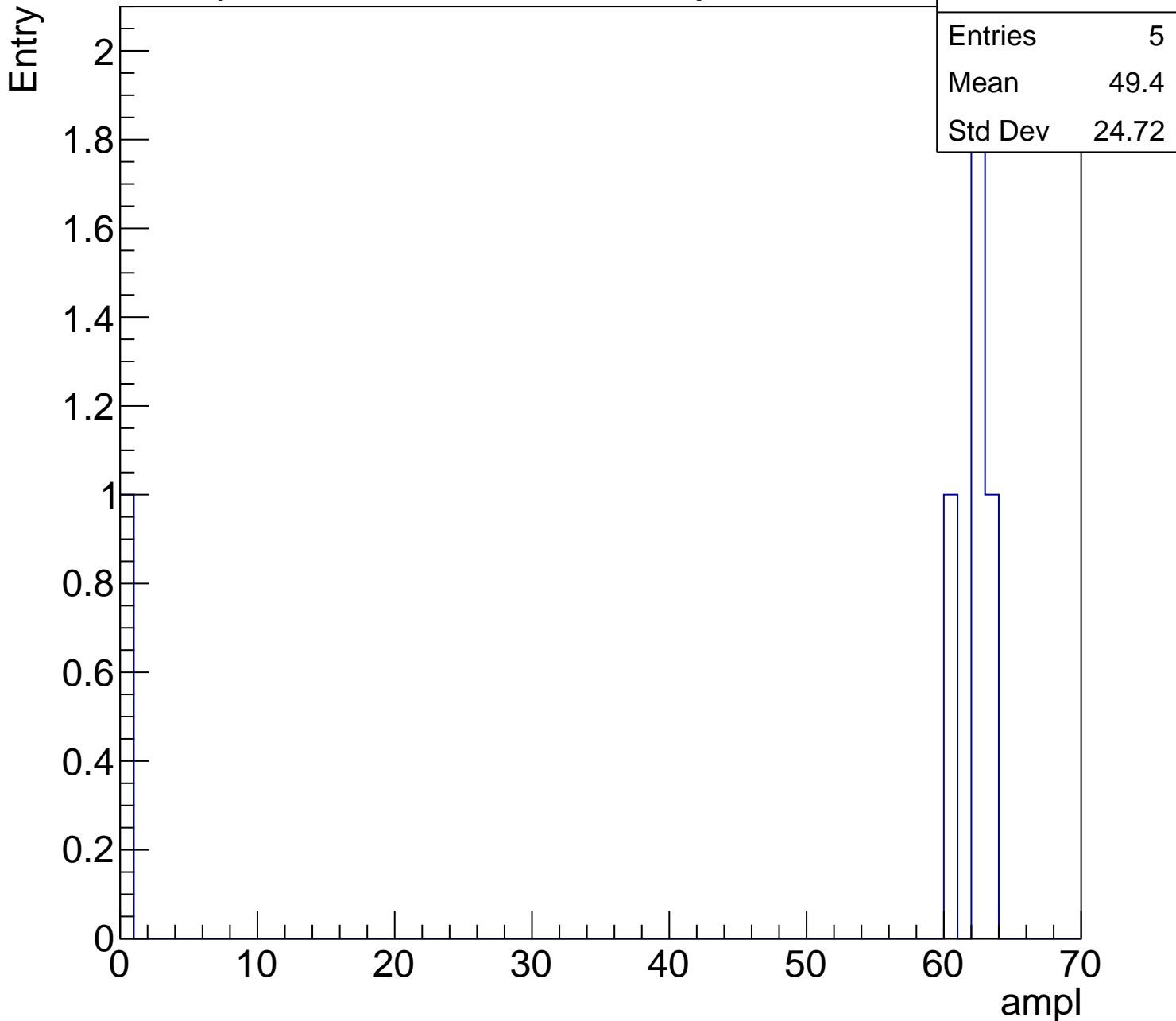
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.72

ampl

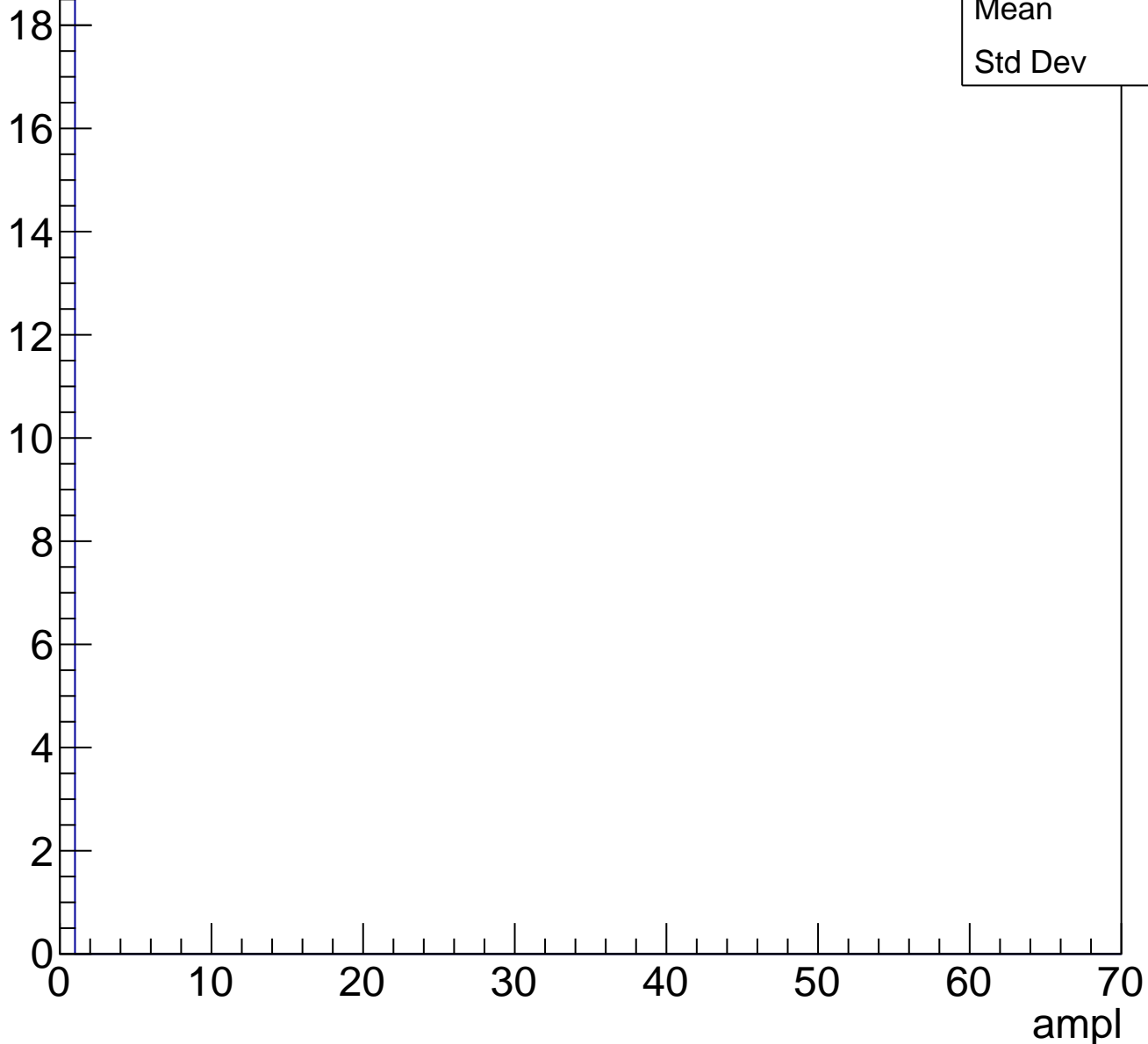
0 10 20 30 40 50 60 70



B1L103S, U10-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch12, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	25.35
Std Dev	10.67

Gaus mean : 29.3425

Gaus Width: 3.0941

Entry

10

8

6

4

2

0

0

10

20

30

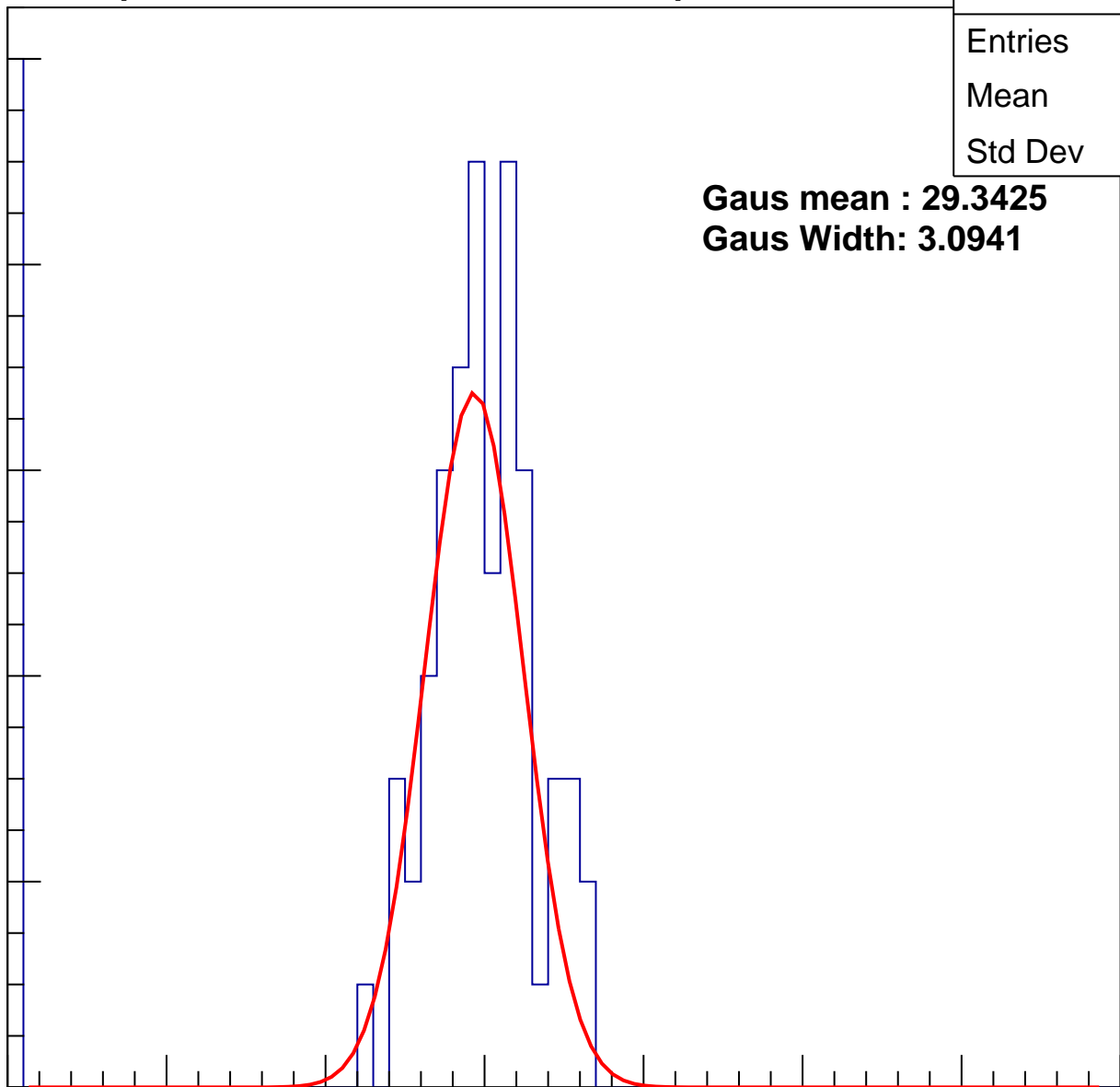
40

50

60

70

ampl



B1L103S, U10-ch12, adc1

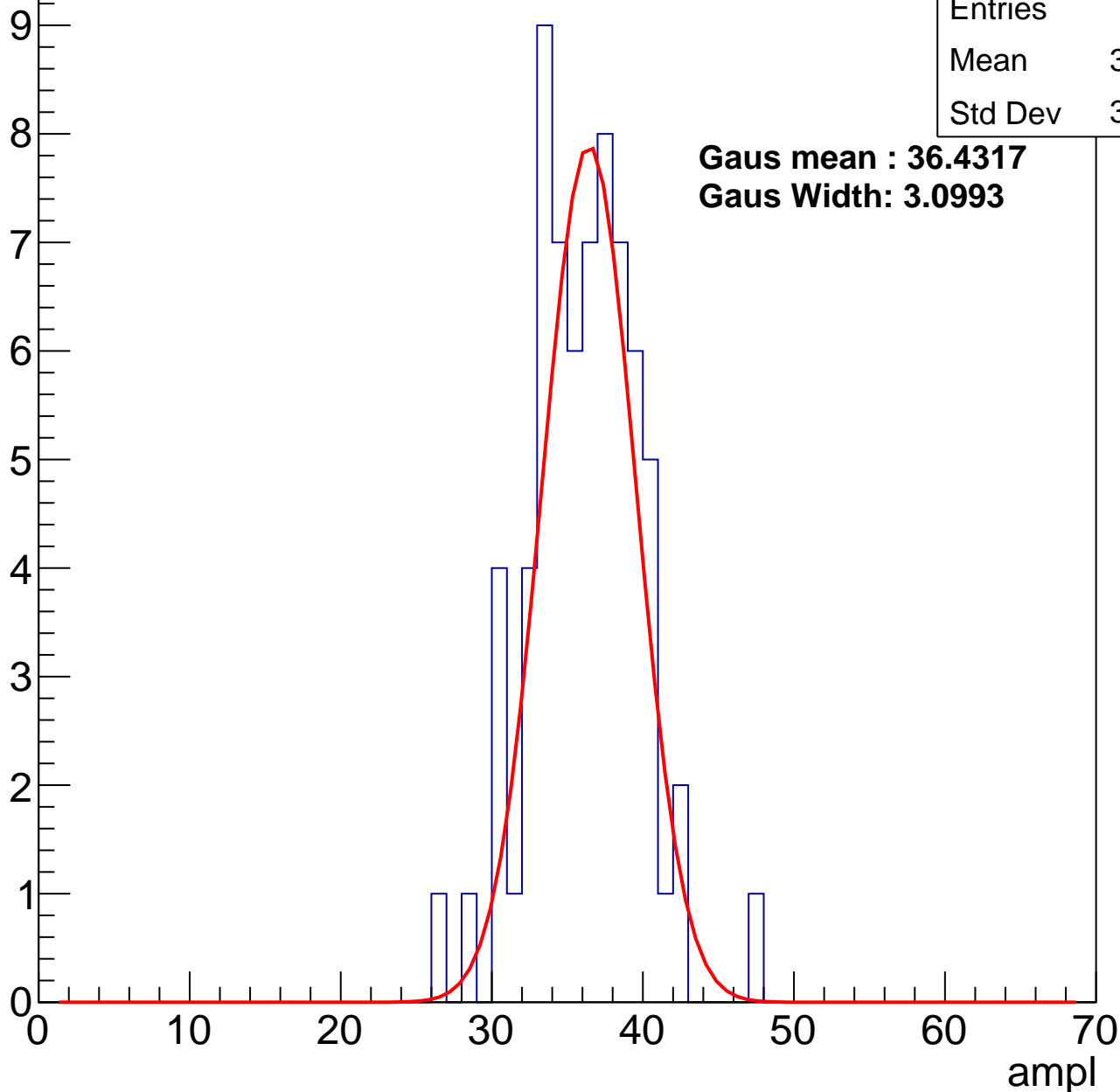
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	35.69
Std Dev	3.572

Gaus mean : 36.4317

Gaus Width: 3.0993



B1L103S, U10-ch12, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.79
Std Dev	10.01

Gaus mean : 43.5103

Gaus Width: 3.5302

10

8

6

4

2

0

0

10

20

30

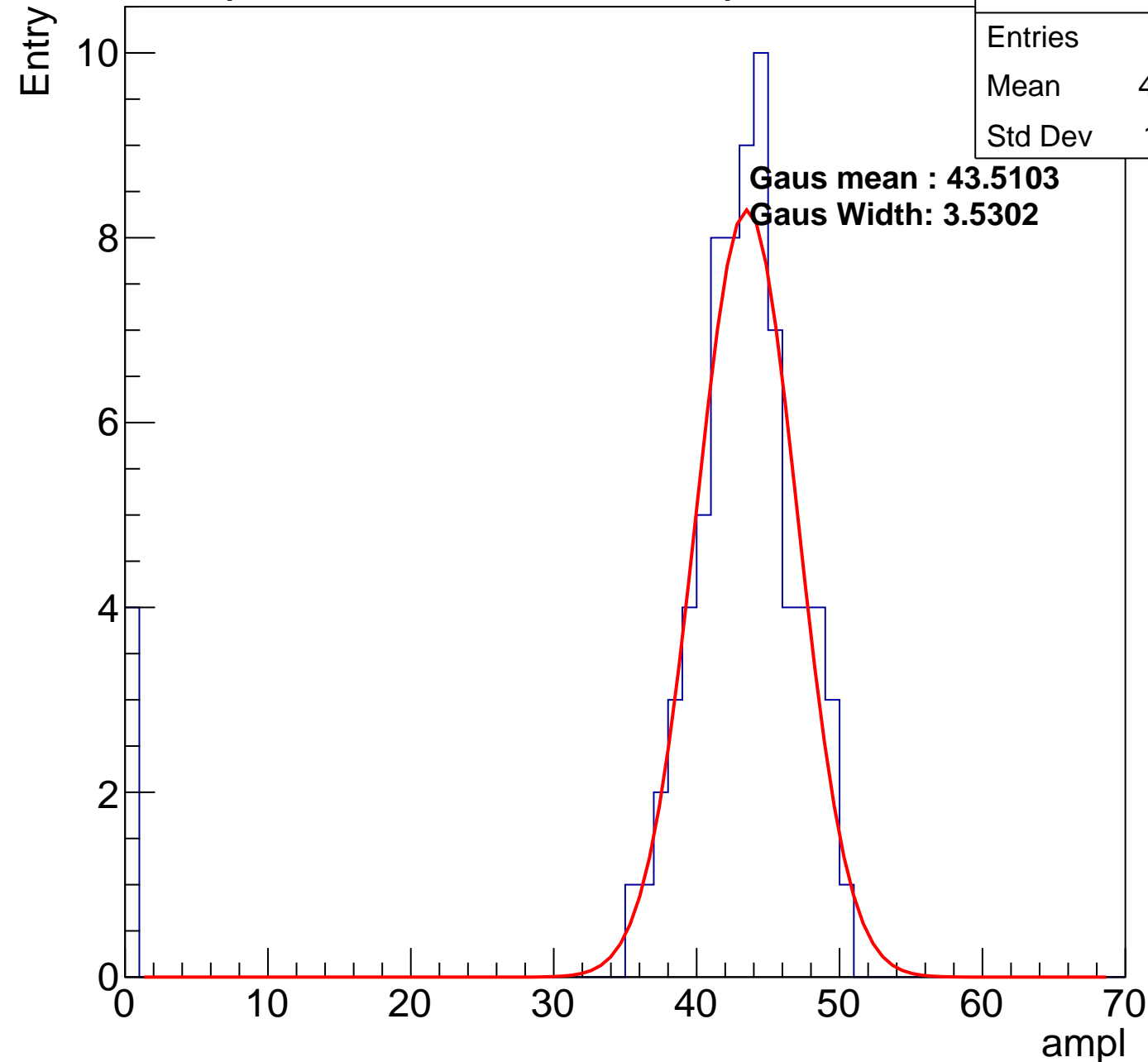
40

50

60

70

ampl

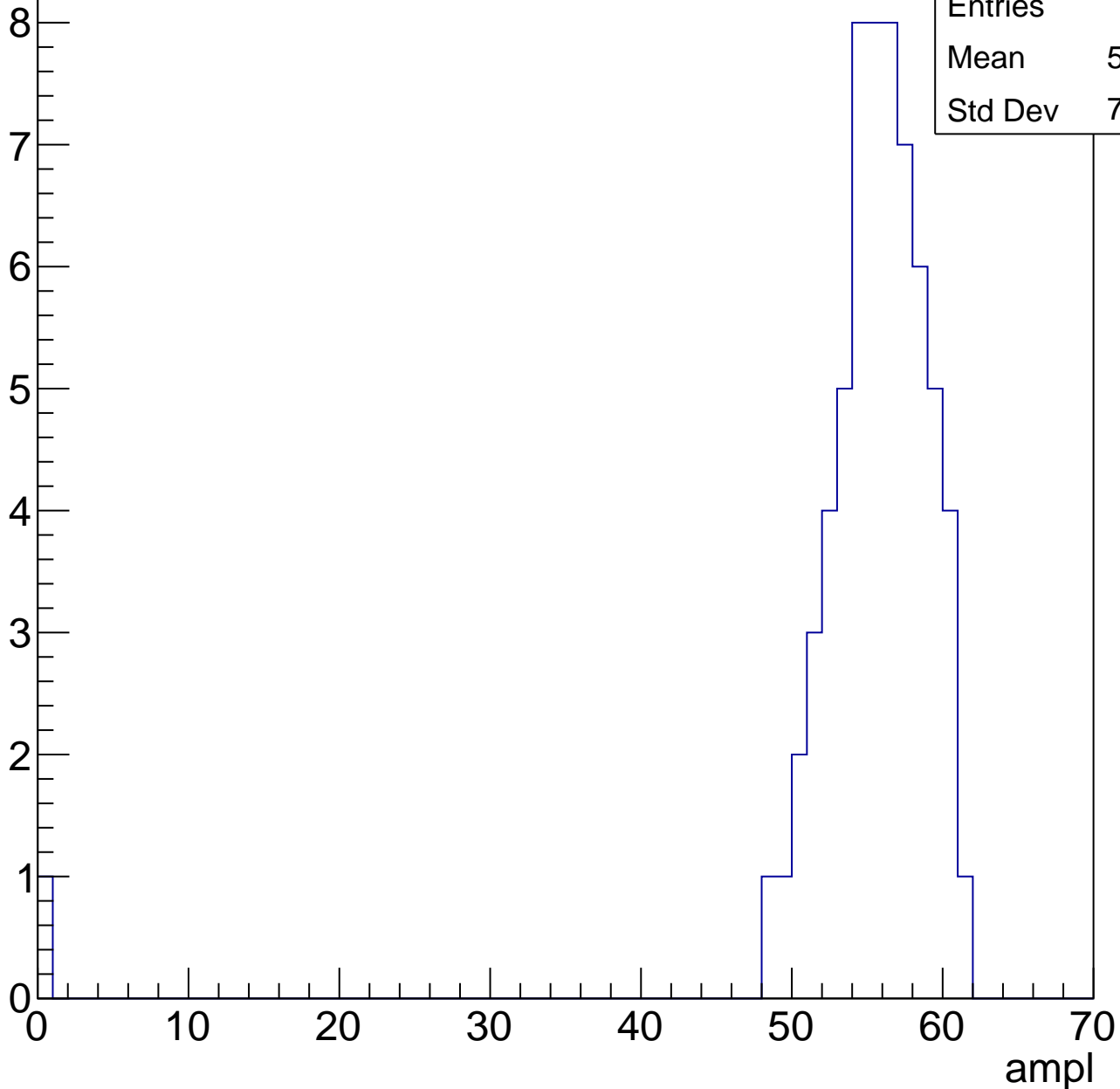


B1L103S, U10-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.47
Std Dev	7.458

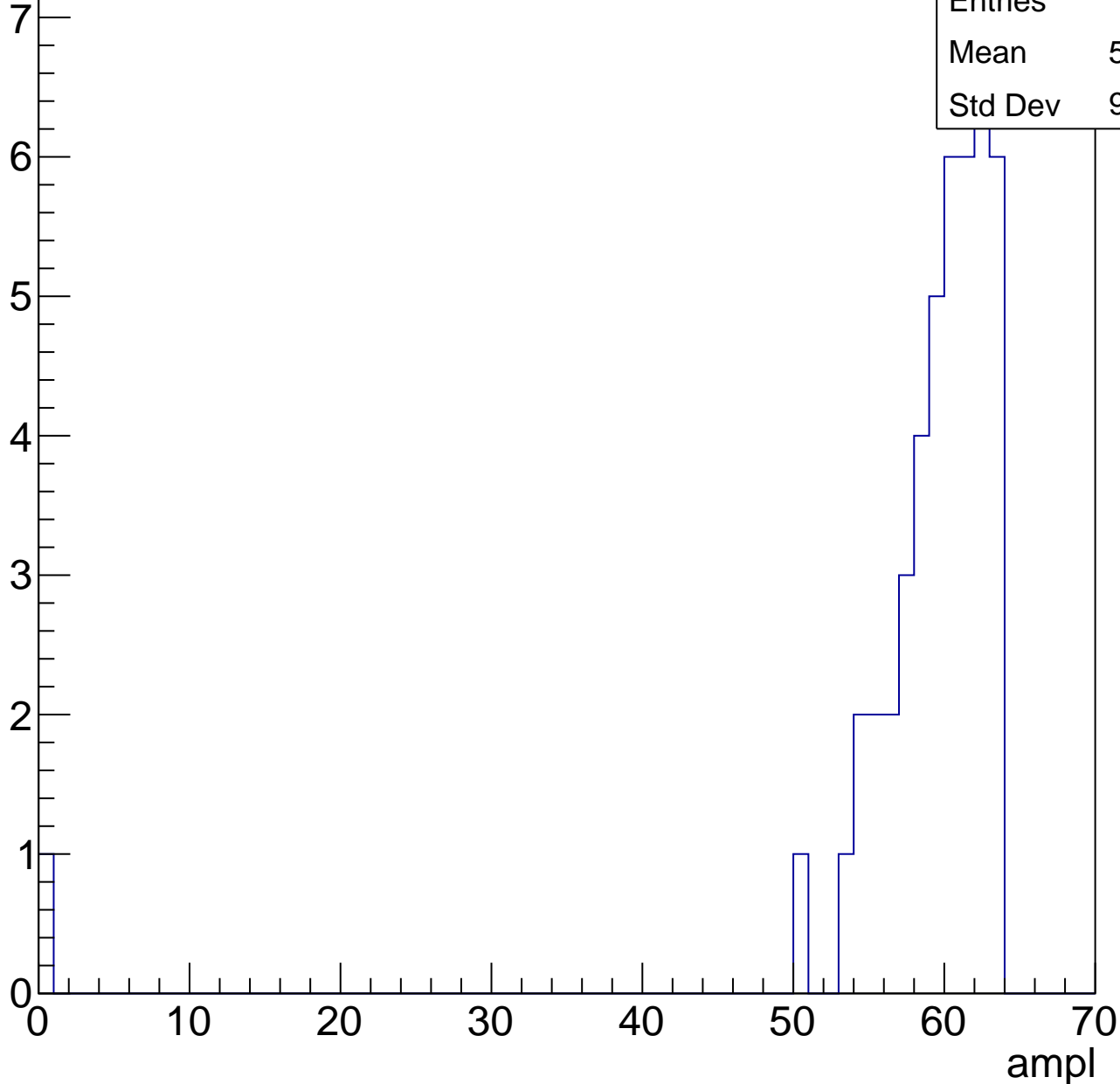


B1L103S, U10-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

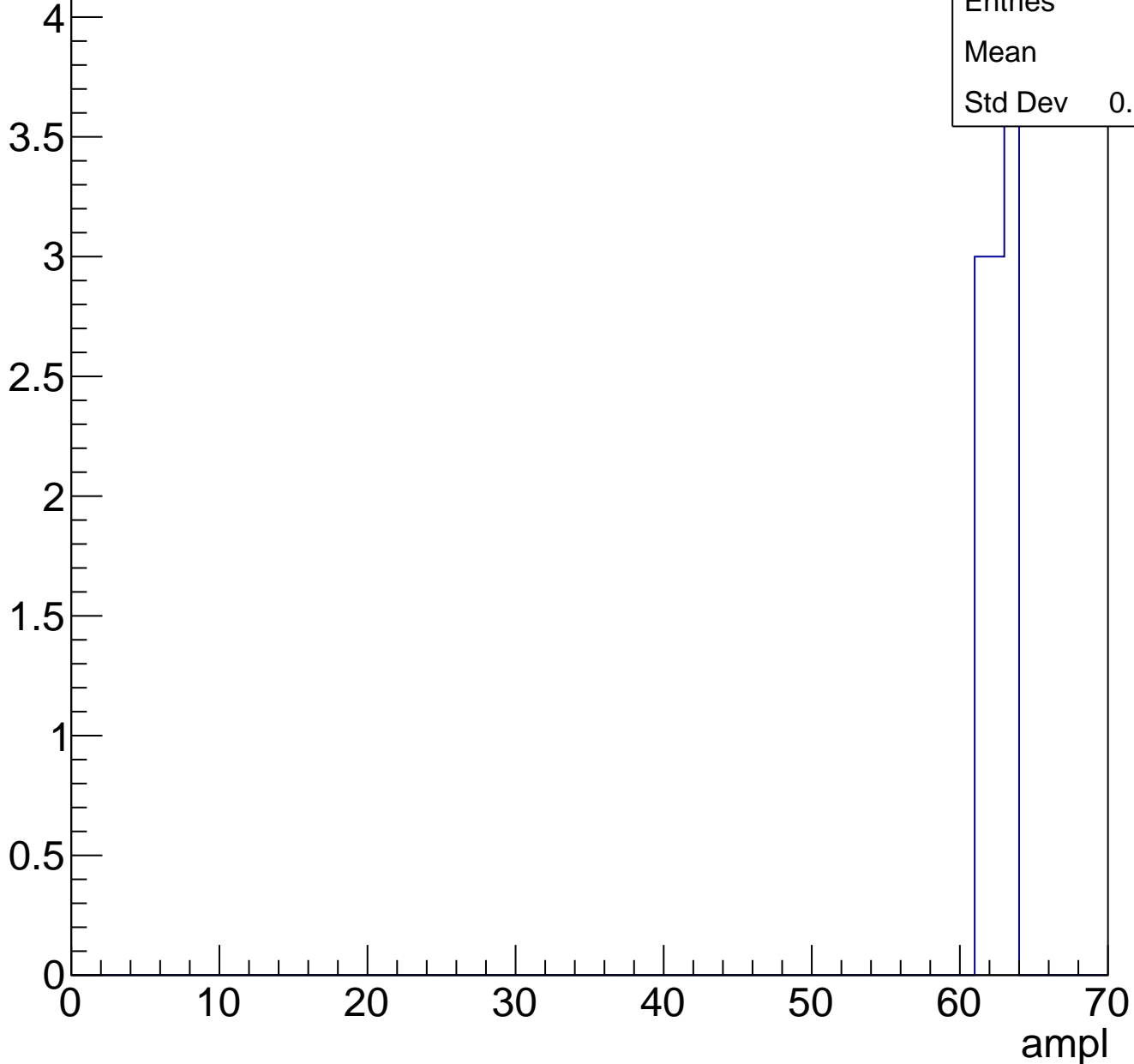
Entries	46
Mean	58.02
Std Dev	9.159



B1L103S, U10-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch13, adc0

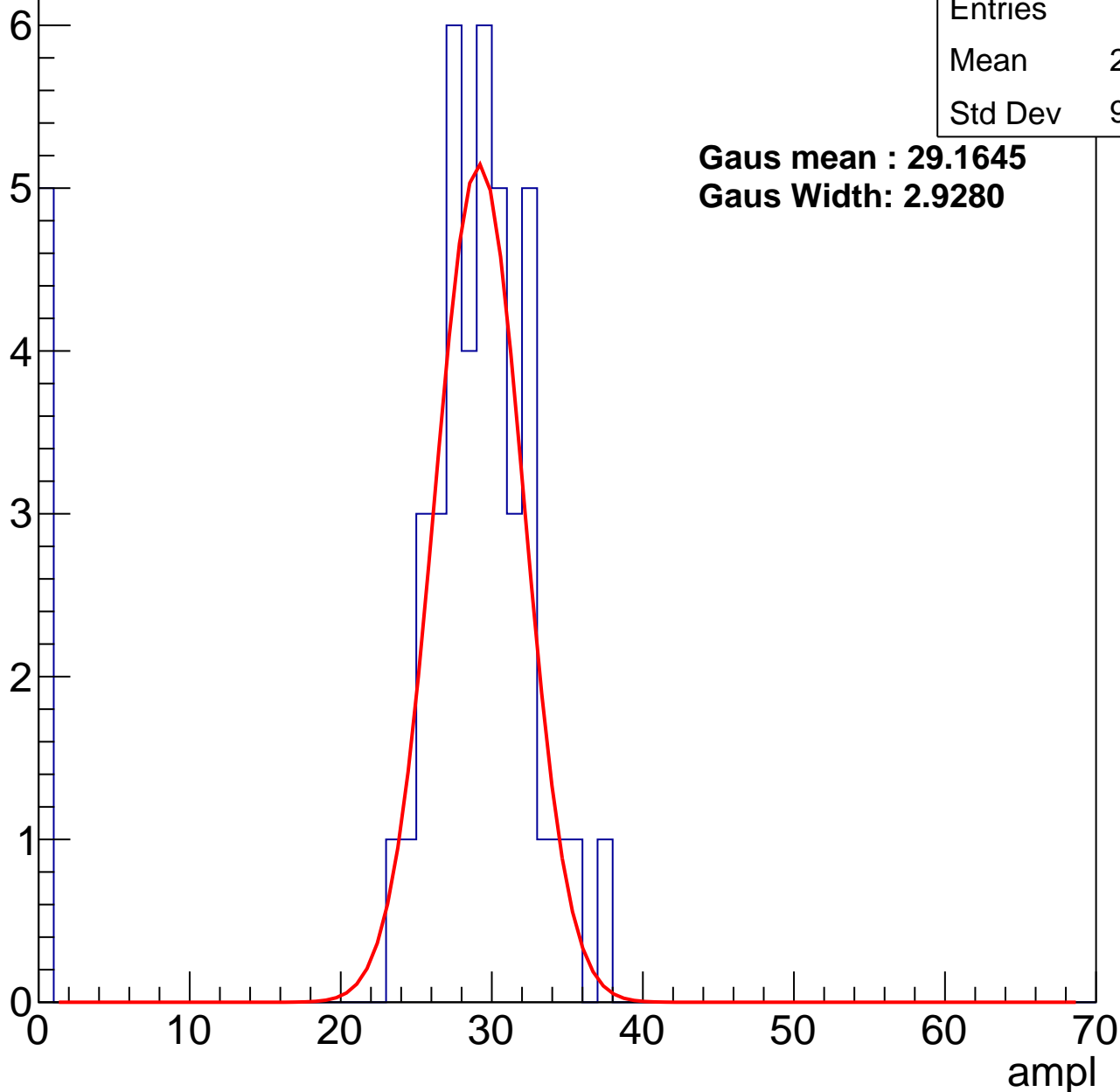
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	25.87
Std Dev	9.465

Gaus mean : 29.1645

Gaus Width: 2.9280



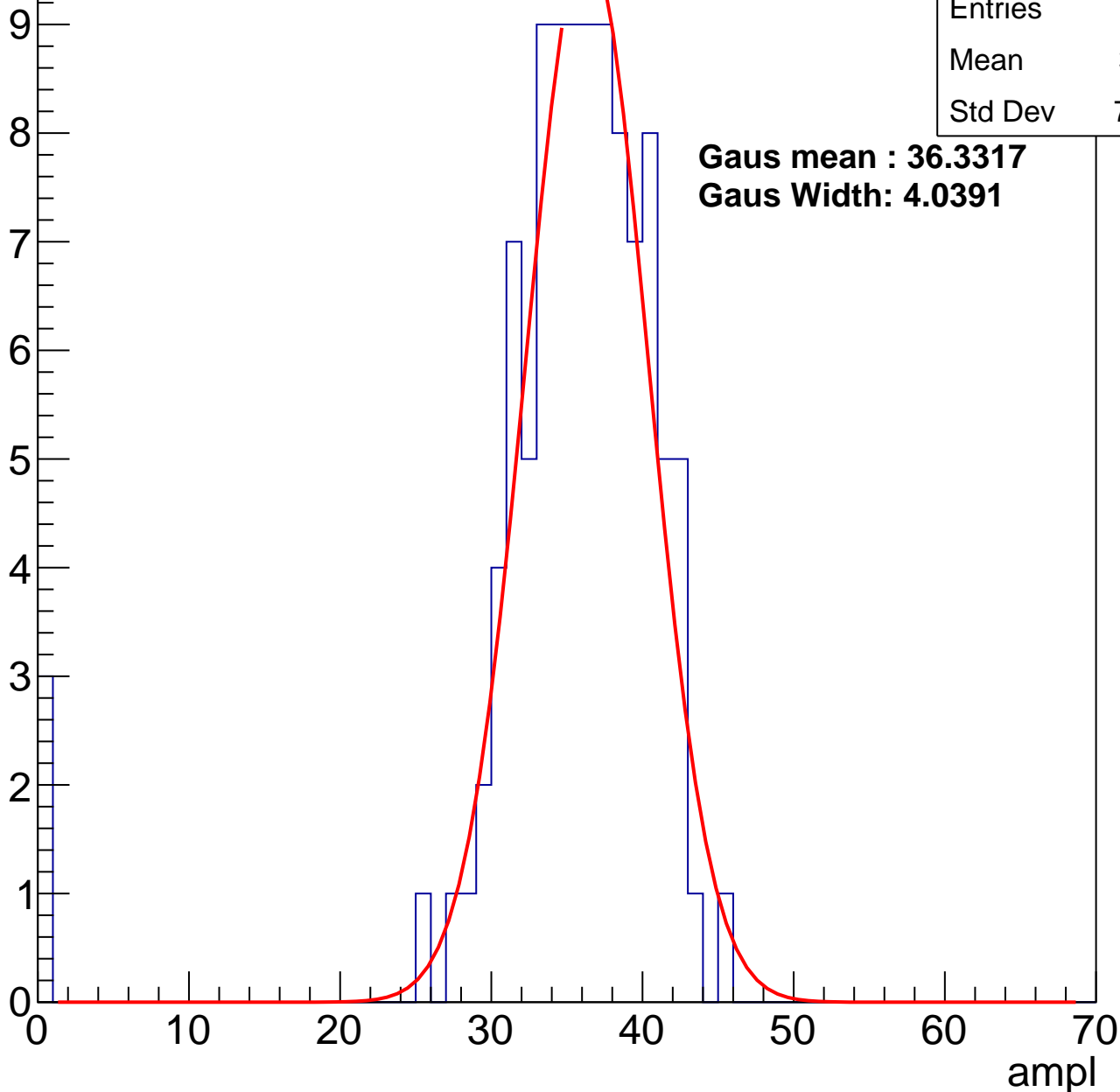
B1L103S, U10-ch13, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	104
Mean	34.71
Std Dev	7.119

Gaus mean : 36.3317
Gaus Width: 4.0391



B1L103S, U10-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	36.28
Std Dev	16.62

Gaus mean : 43.4506

Gaus Width: 4.0467

Entry

10

8

6

4

2

0

0

10

20

30

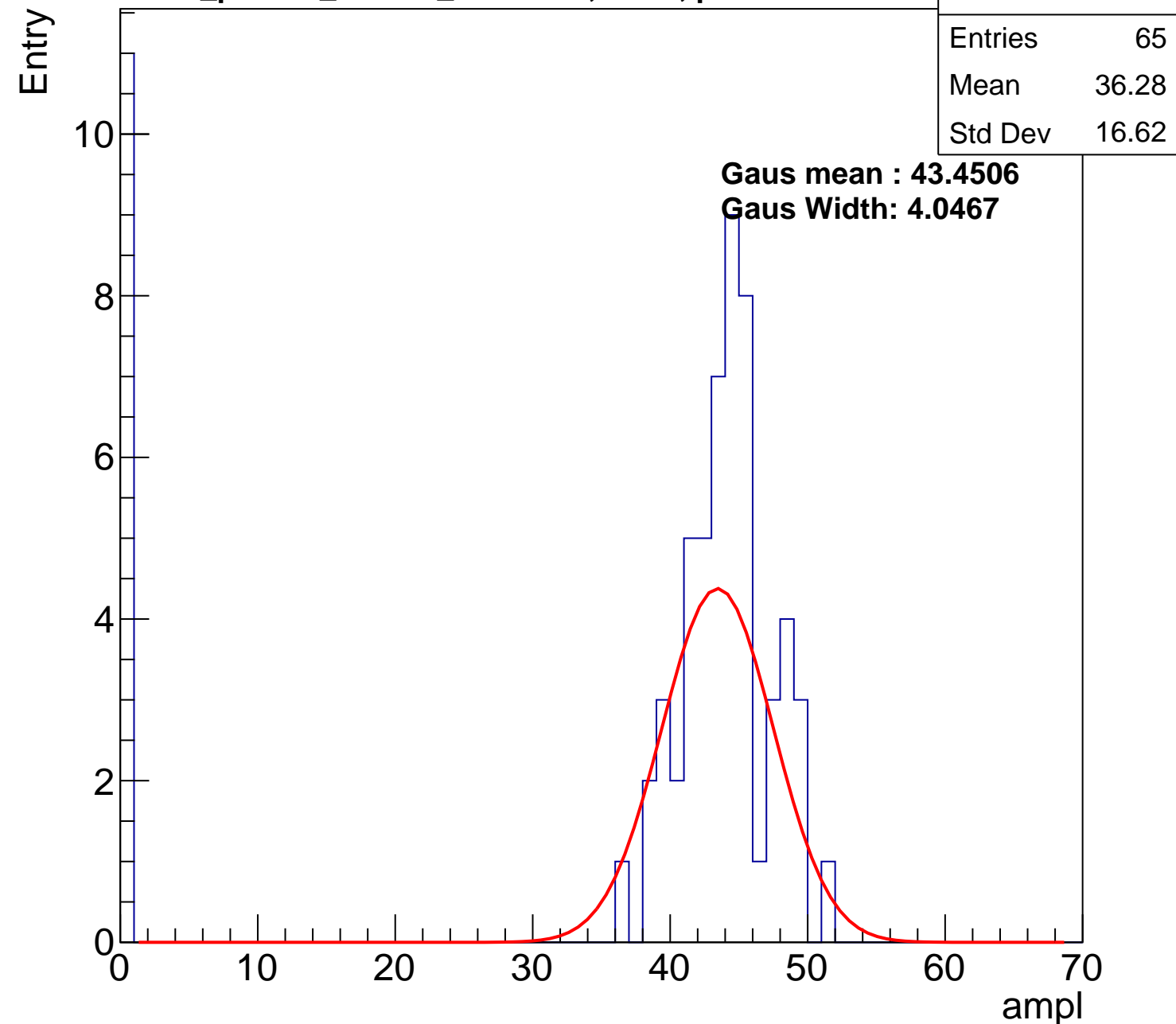
40

50

60

70

ampl

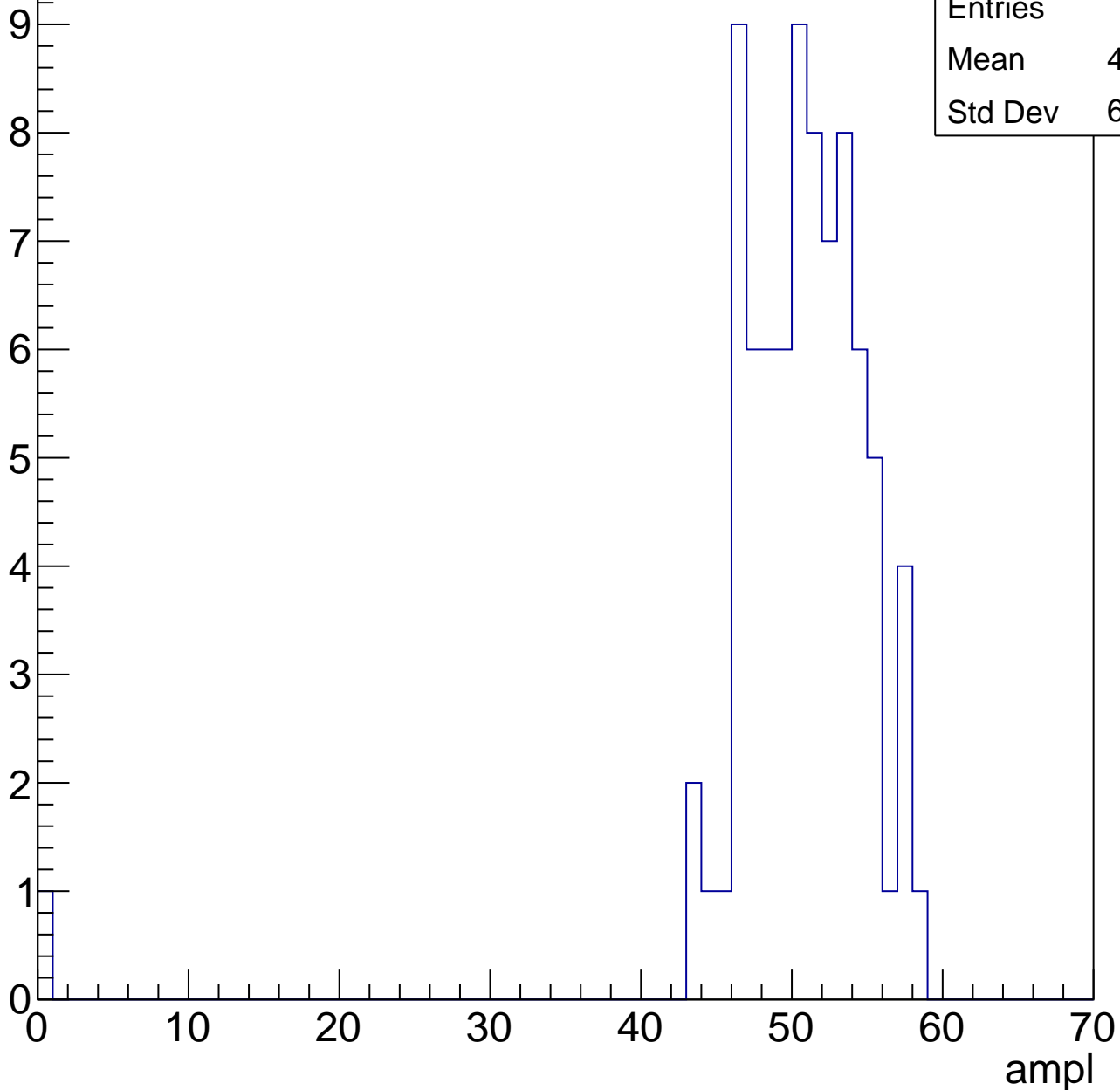


B1L103S, U10-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	49.88
Std Dev	6.584

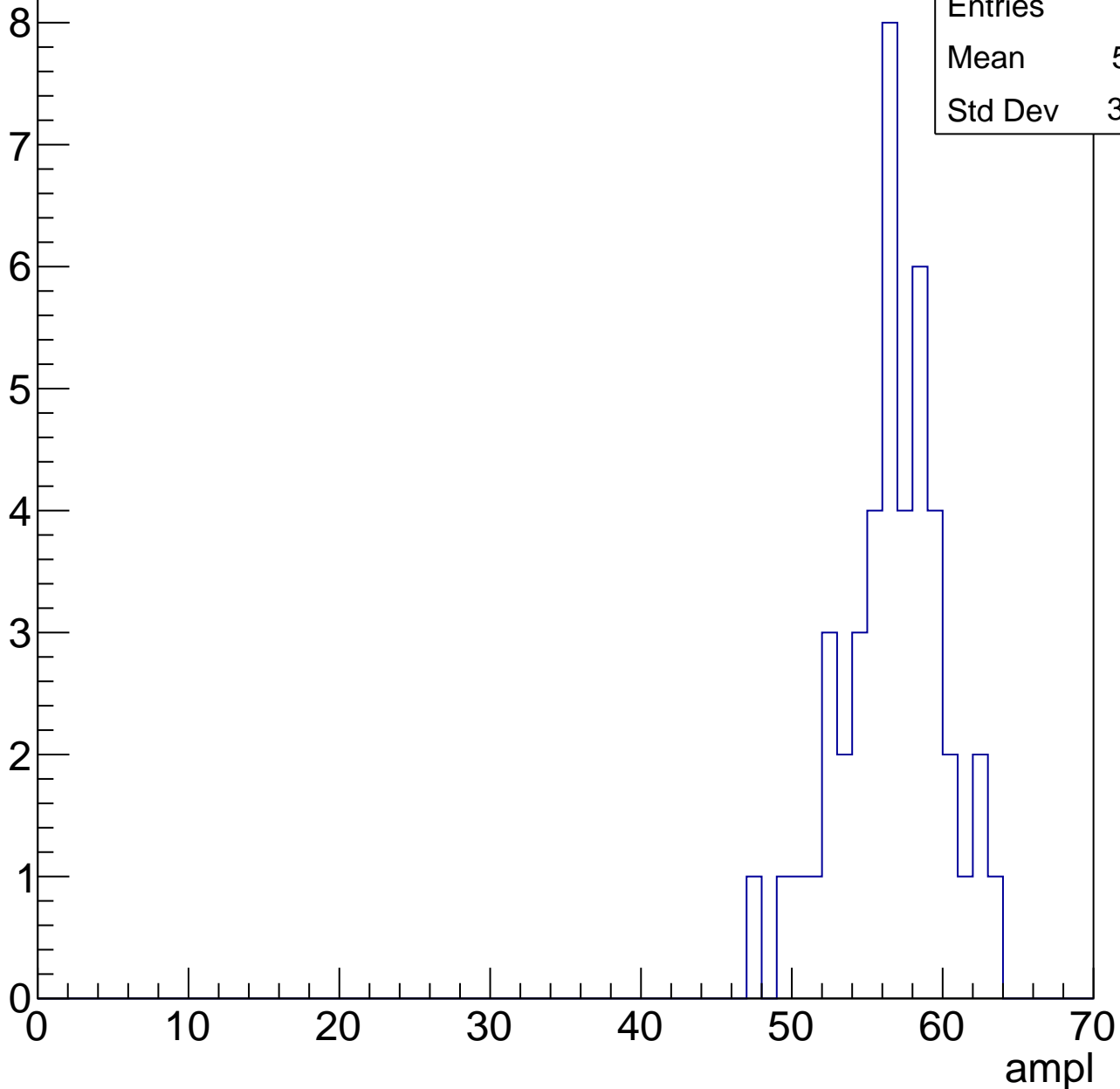


B1L103S, U10-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

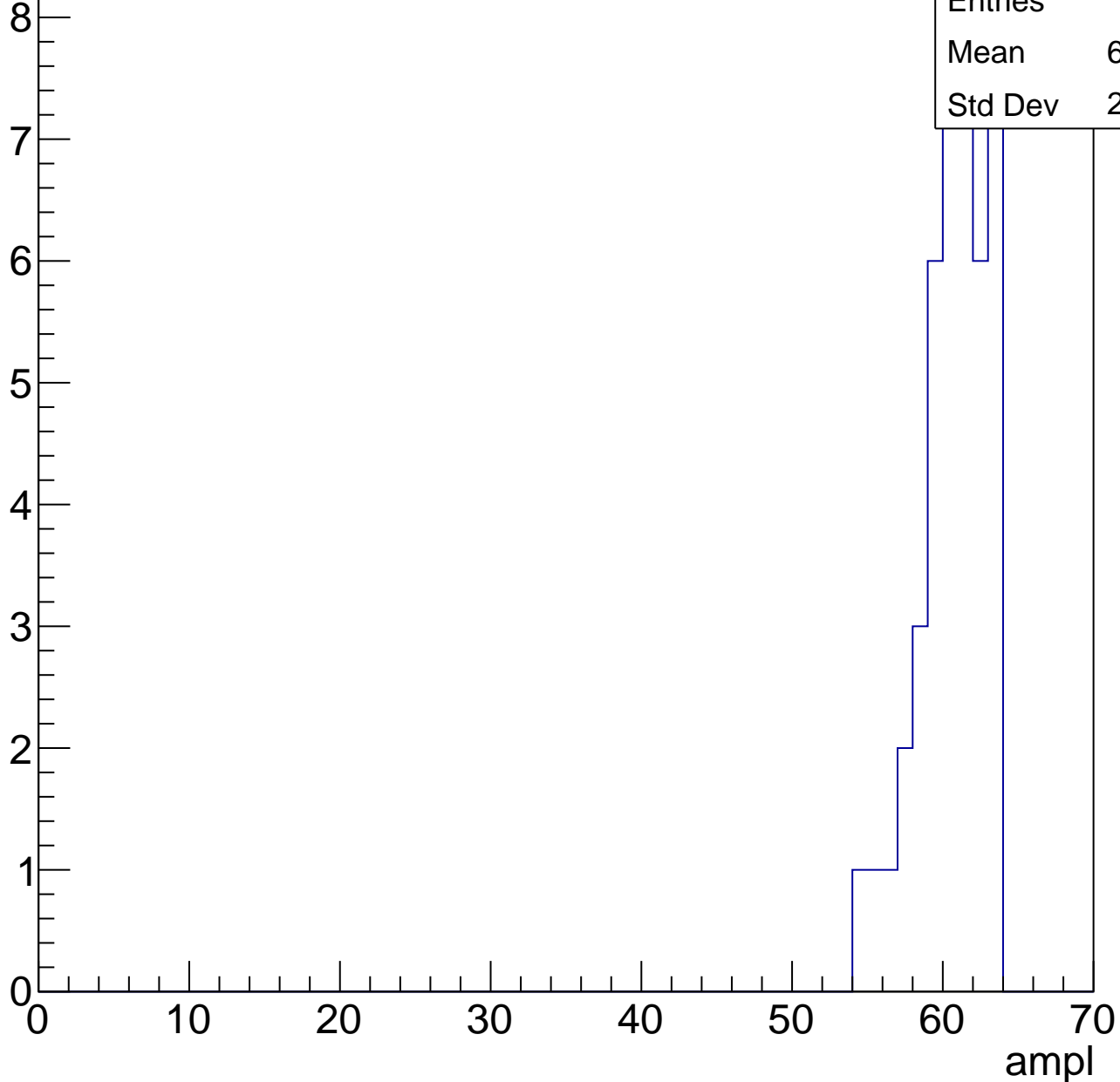
Entries	44
Mean	56.11
Std Dev	3.419



B1L103S, U10-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B1L103S, U10-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch14, adc0

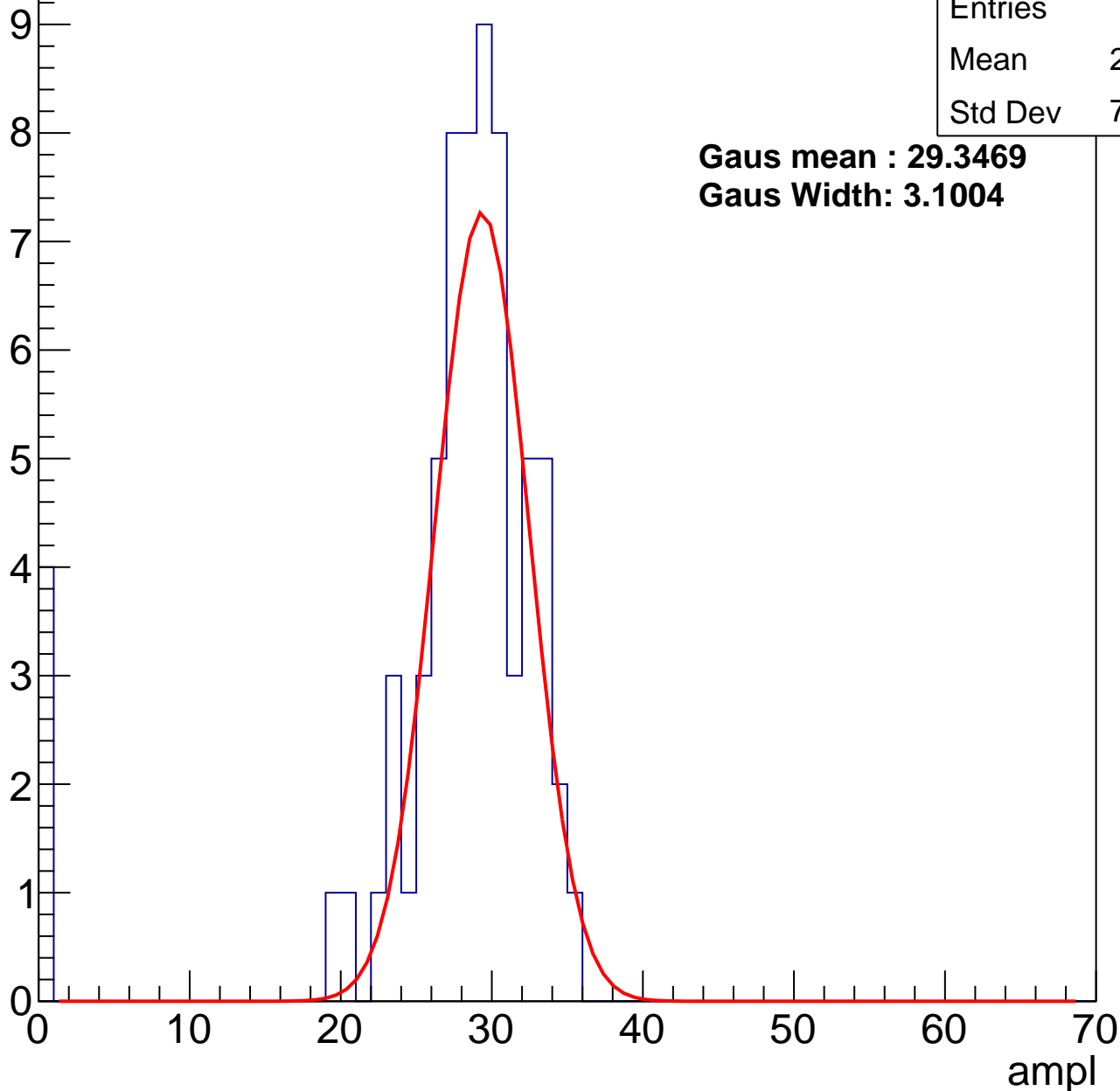
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	26.78
Std Dev	7.432

Gaus mean : 29.3469

Gaus Width: 3.1004



B1L103S, U10-ch14, adc1

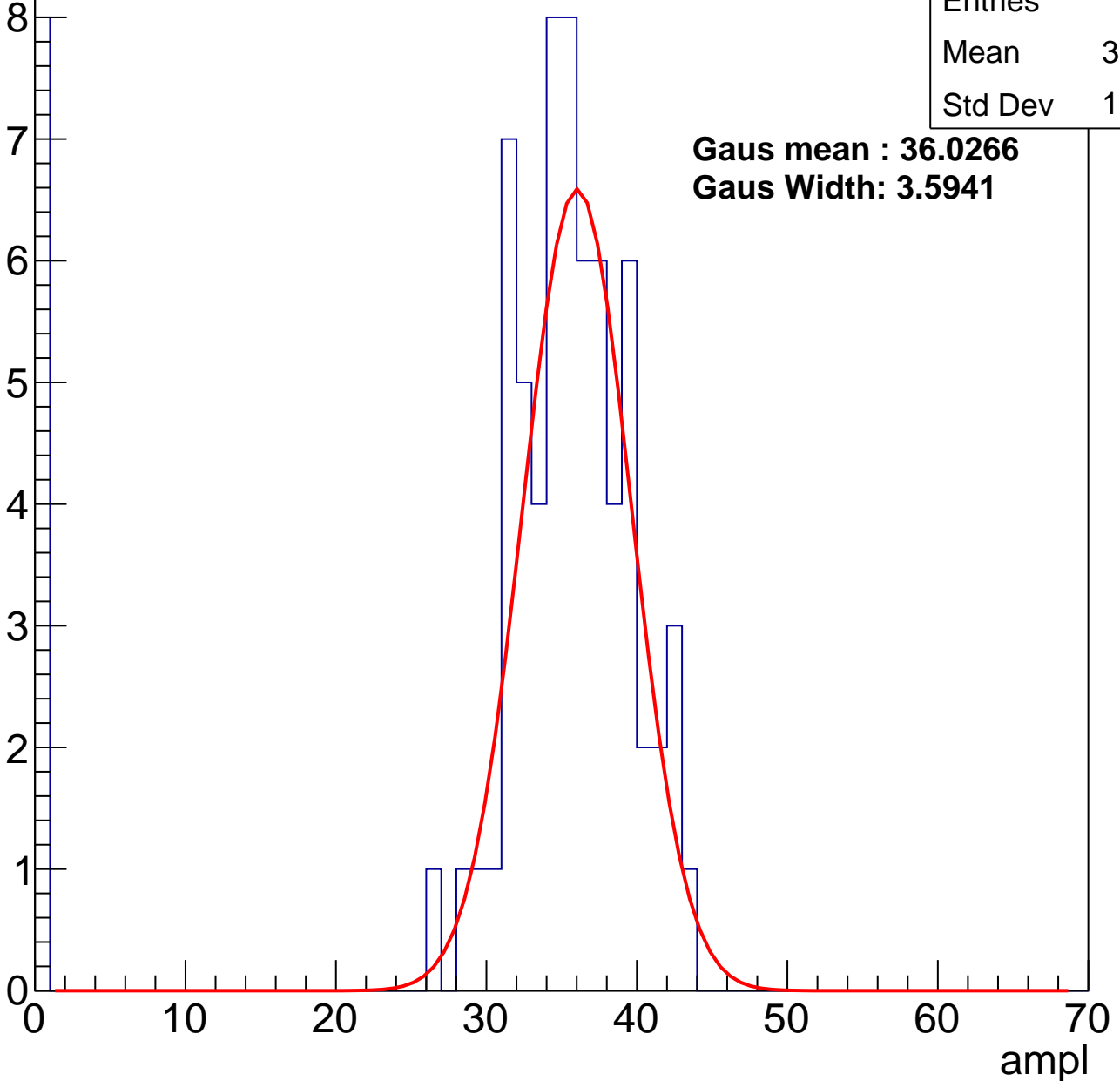
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	31.47
Std Dev	11.48

Gaus mean : 36.0266

Gaus Width: 3.5941



B1L103S, U10-ch14, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	40.18
Std Dev	8.972

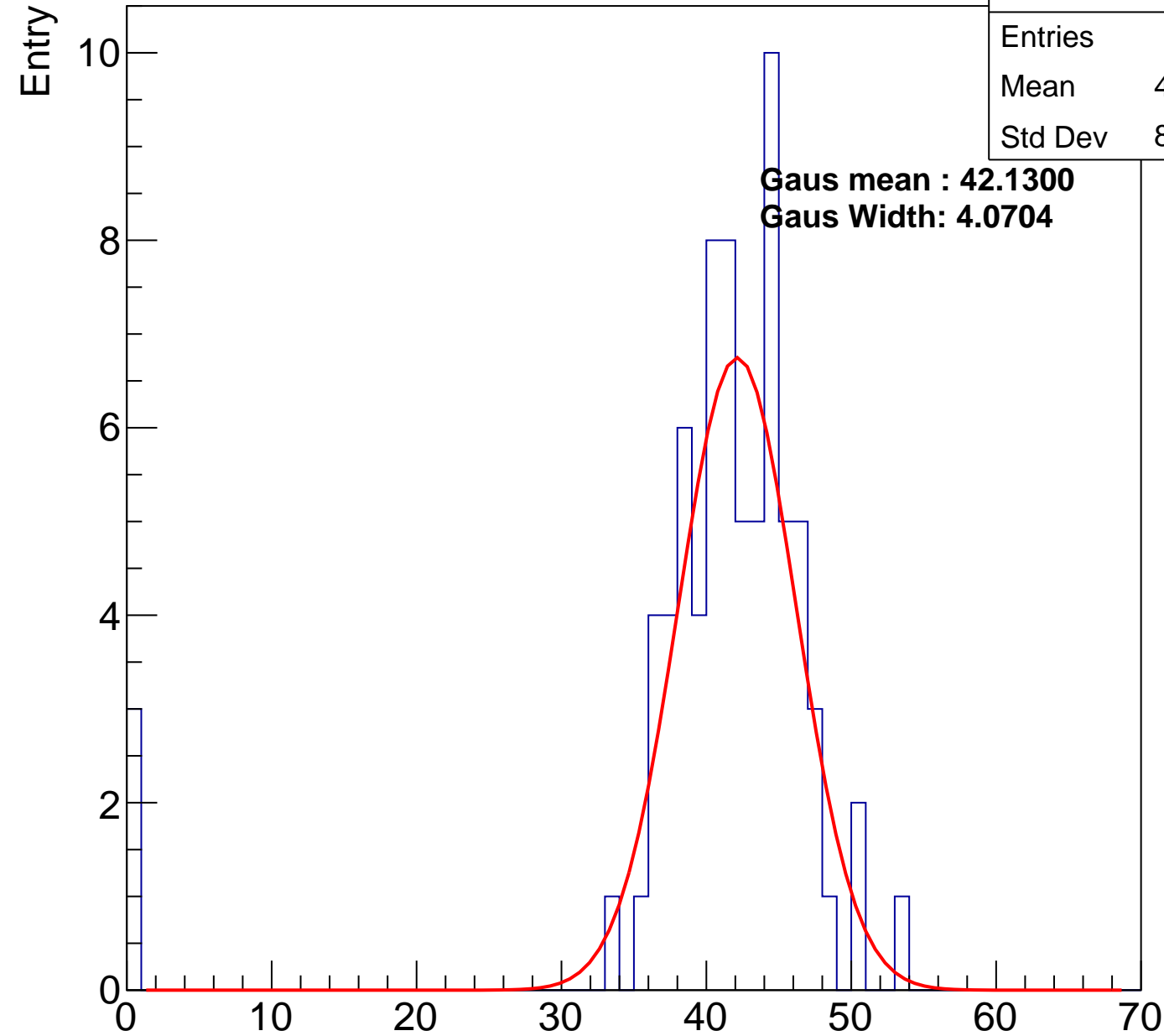
Gaus mean : 42.1300

Gaus Width: 4.0704

Entry

10
8
6
4
2
0

ampl

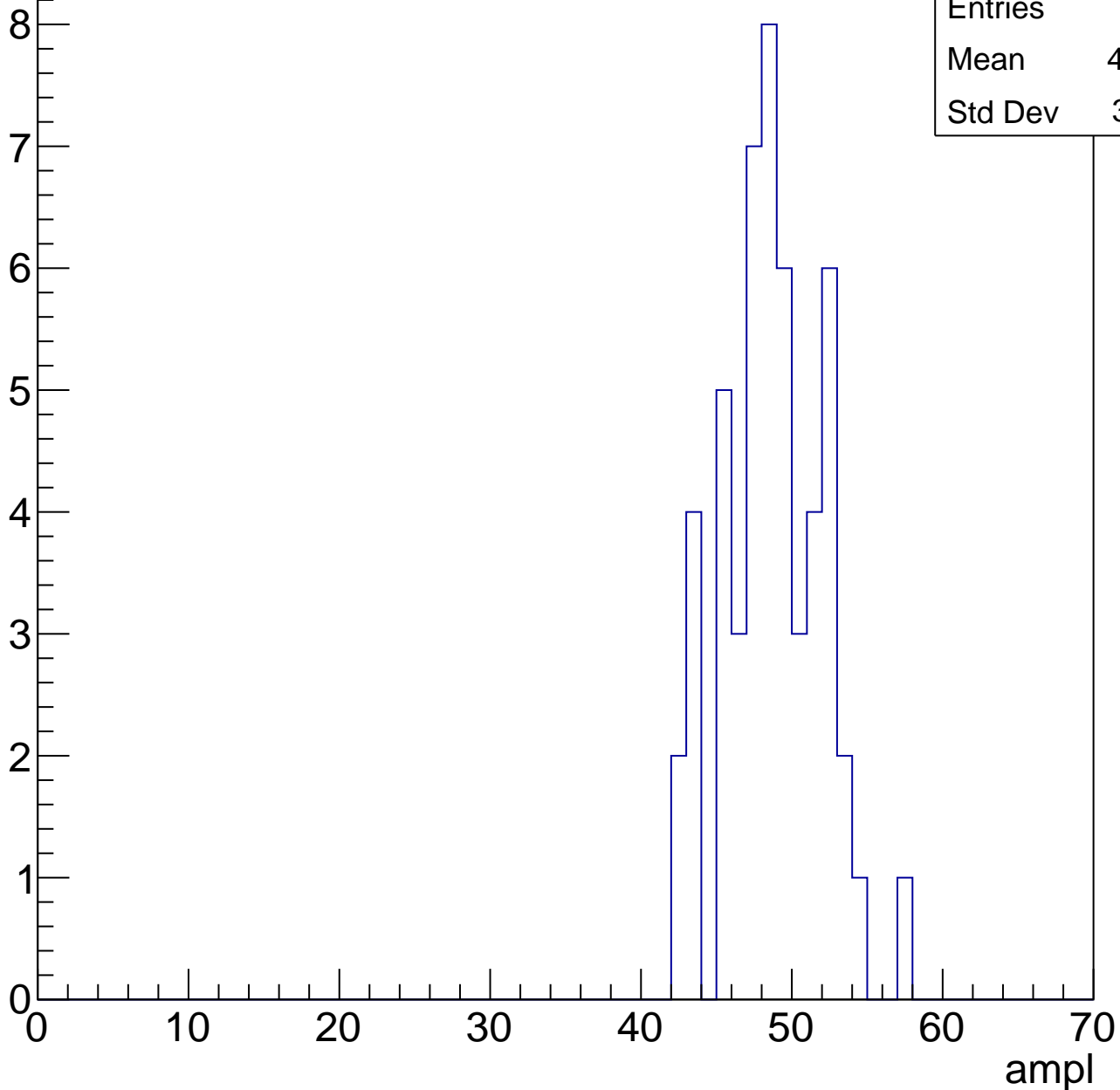


B1L103S, U10-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	48.25
Std Dev	3.251

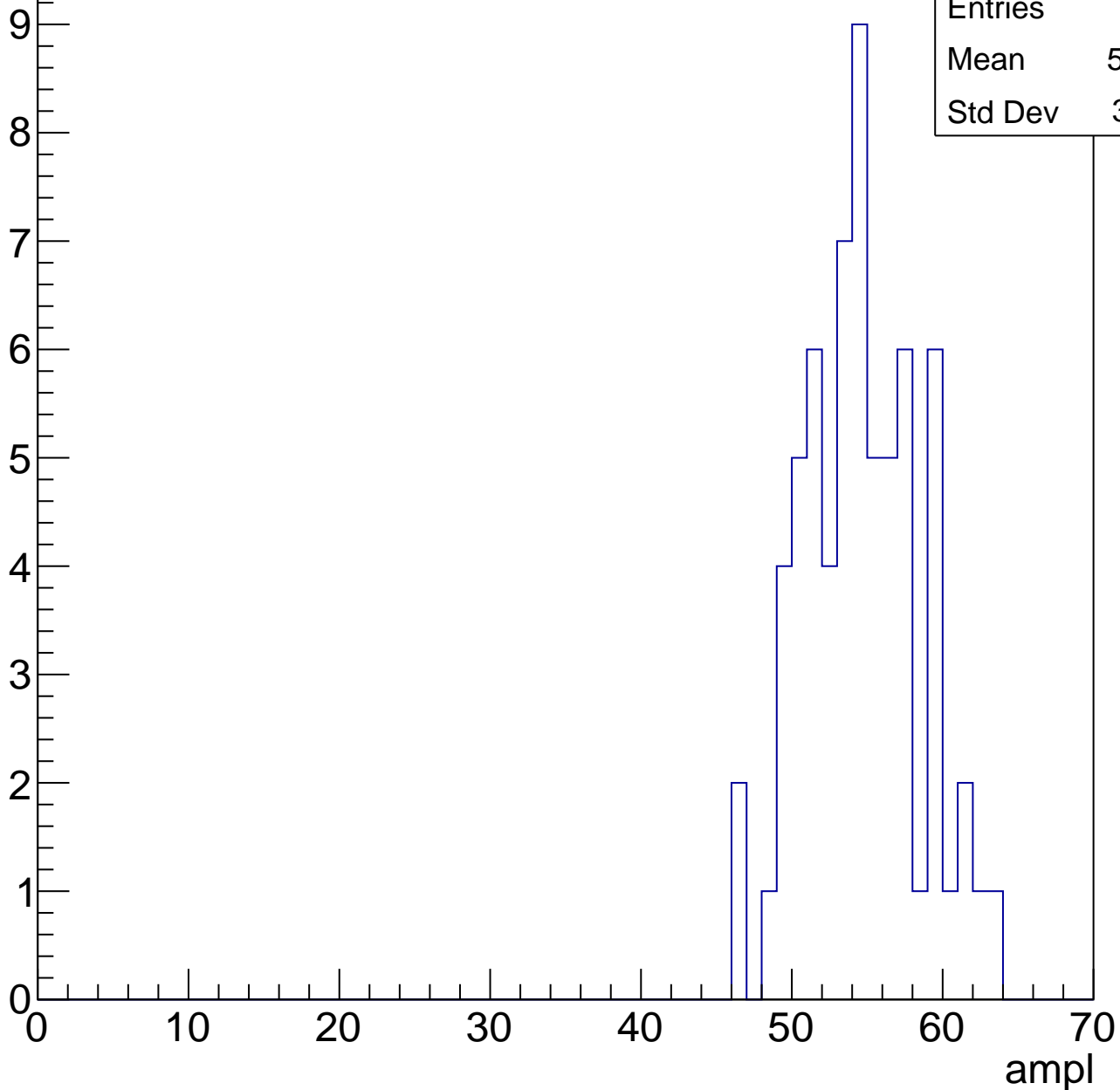


B1L103S, U10-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	54.14
Std Dev	3.781

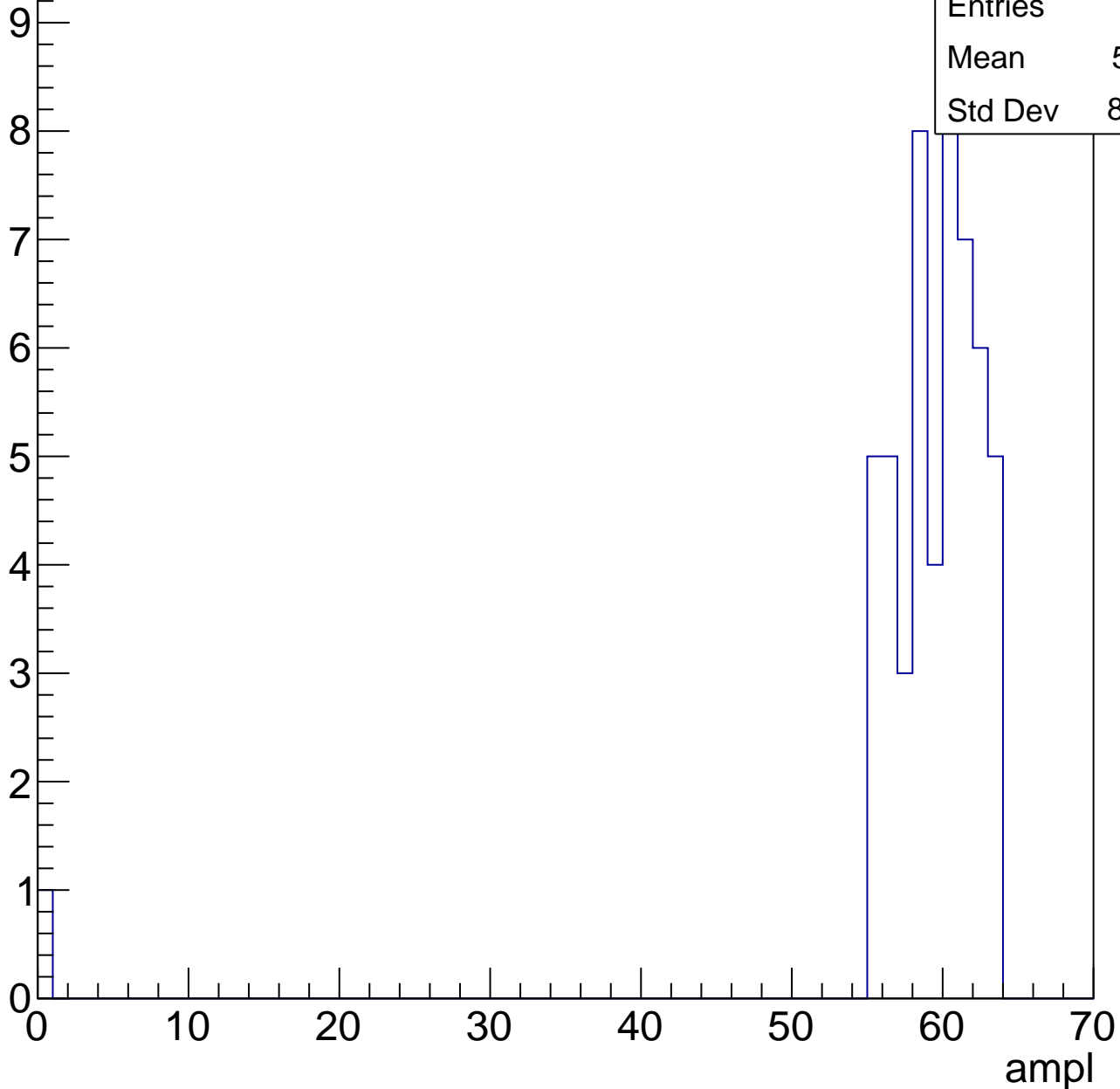


B1L103S, U10-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

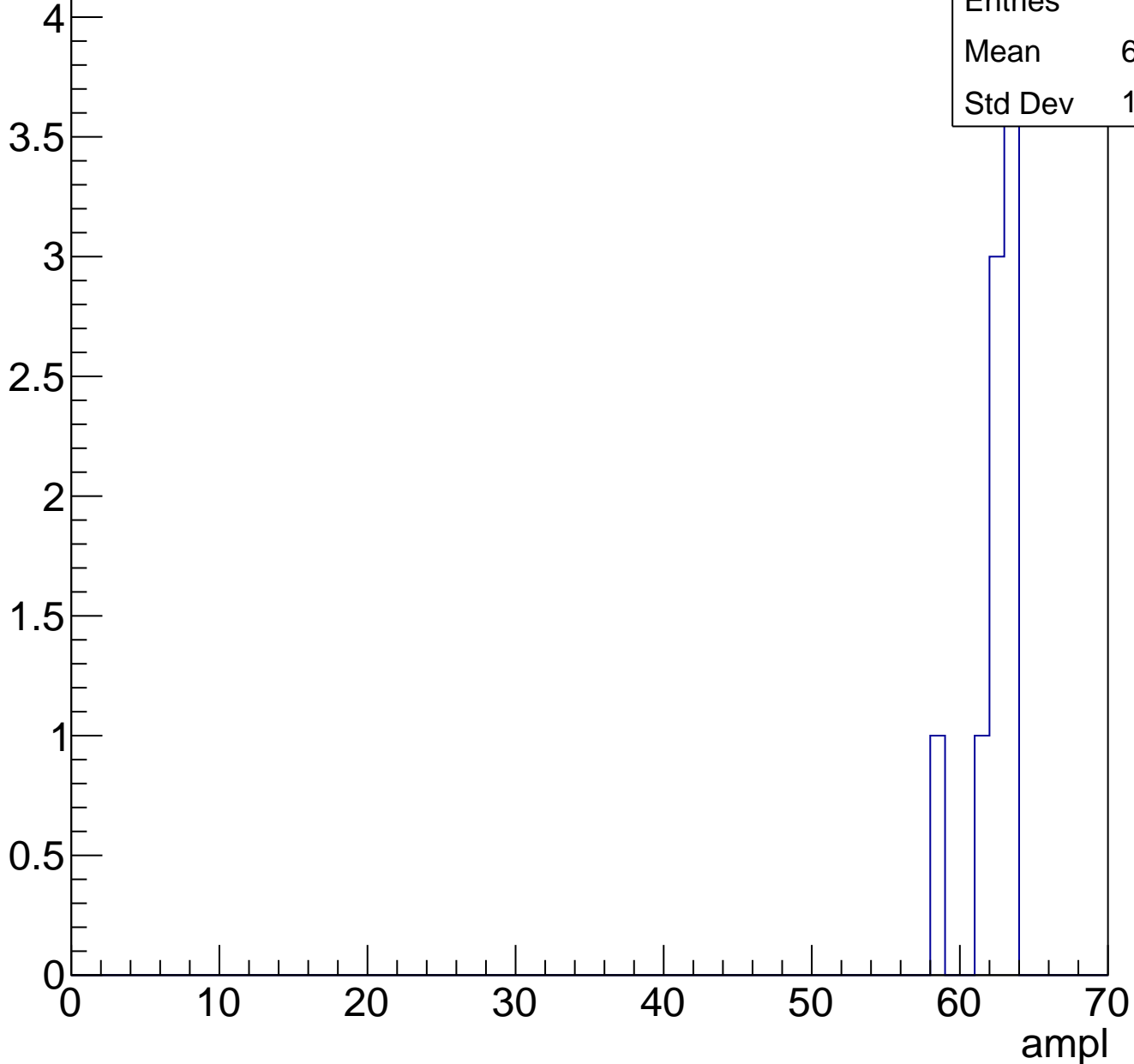
Entries	53
Mean	58.11
Std Dev	8.418



B1L103S, U10-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch15, adc0

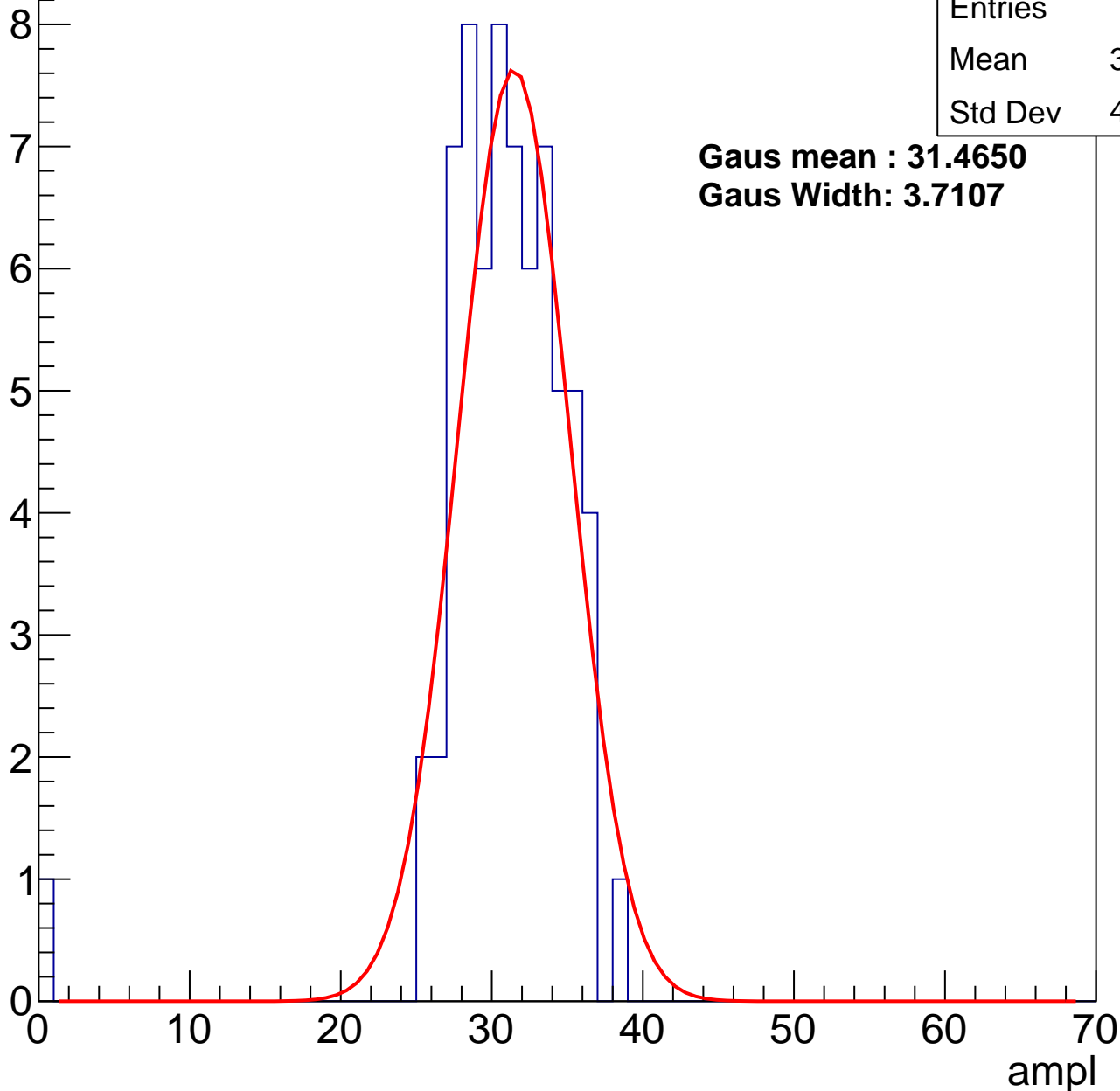
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	30.38
Std Dev	4.786

Gaus mean : 31.4650

Gaus Width: 3.7107



B1L103S, U10-ch15, adc1

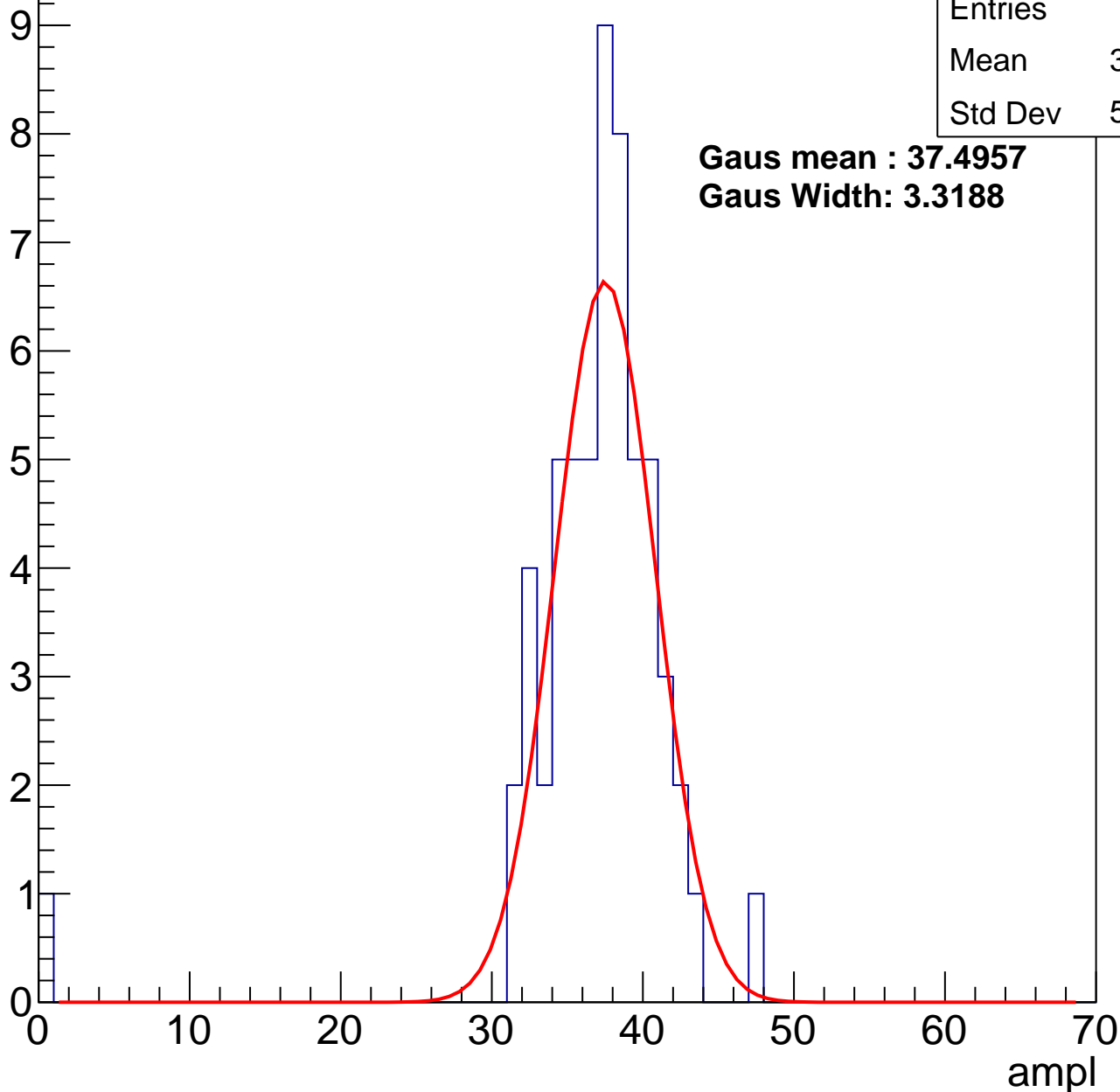
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	36.38
Std Dev	5.765

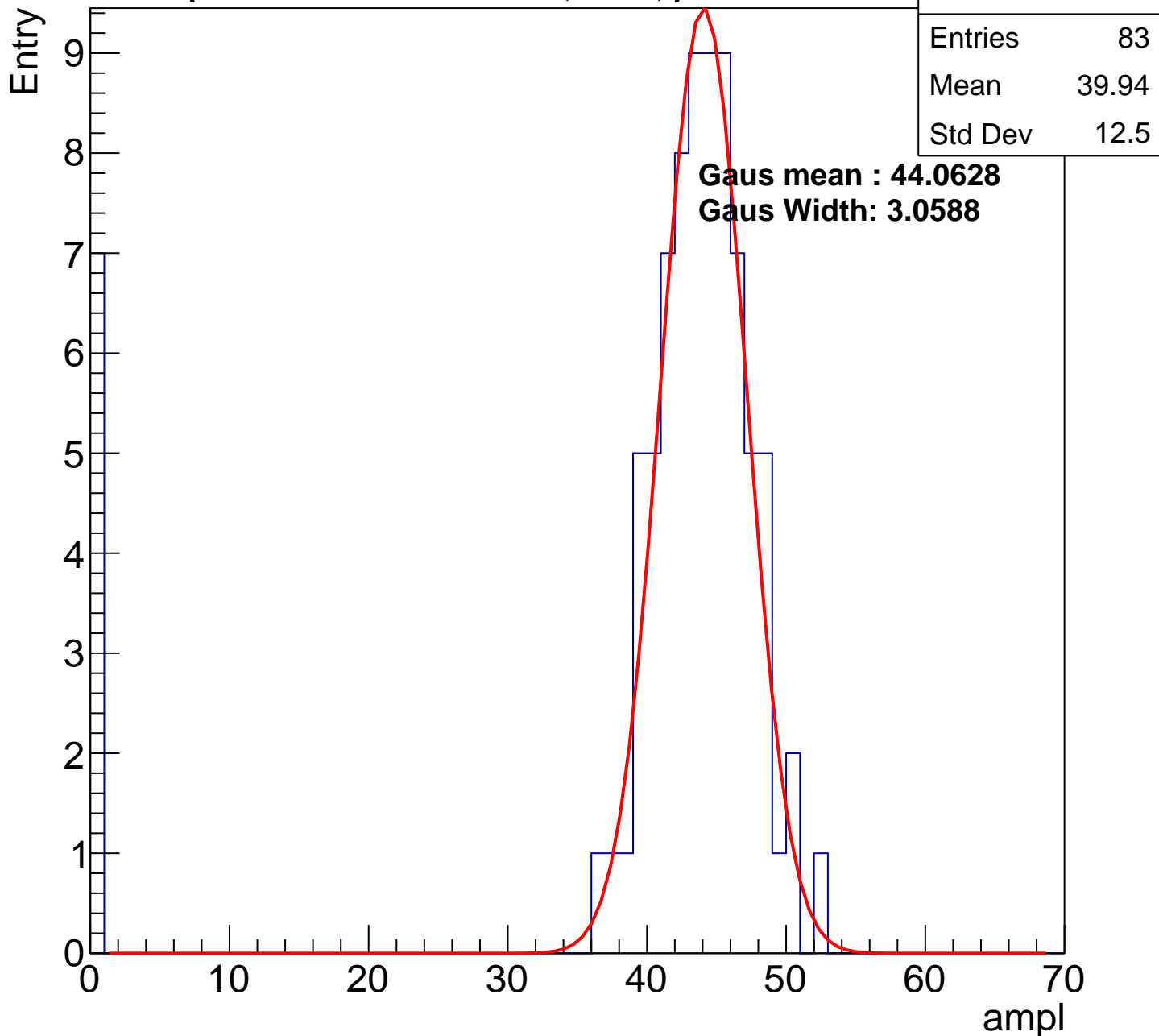
Gaus mean : 37.4957

Gaus Width: 3.3188



B1L103S, U10-ch15, adc2

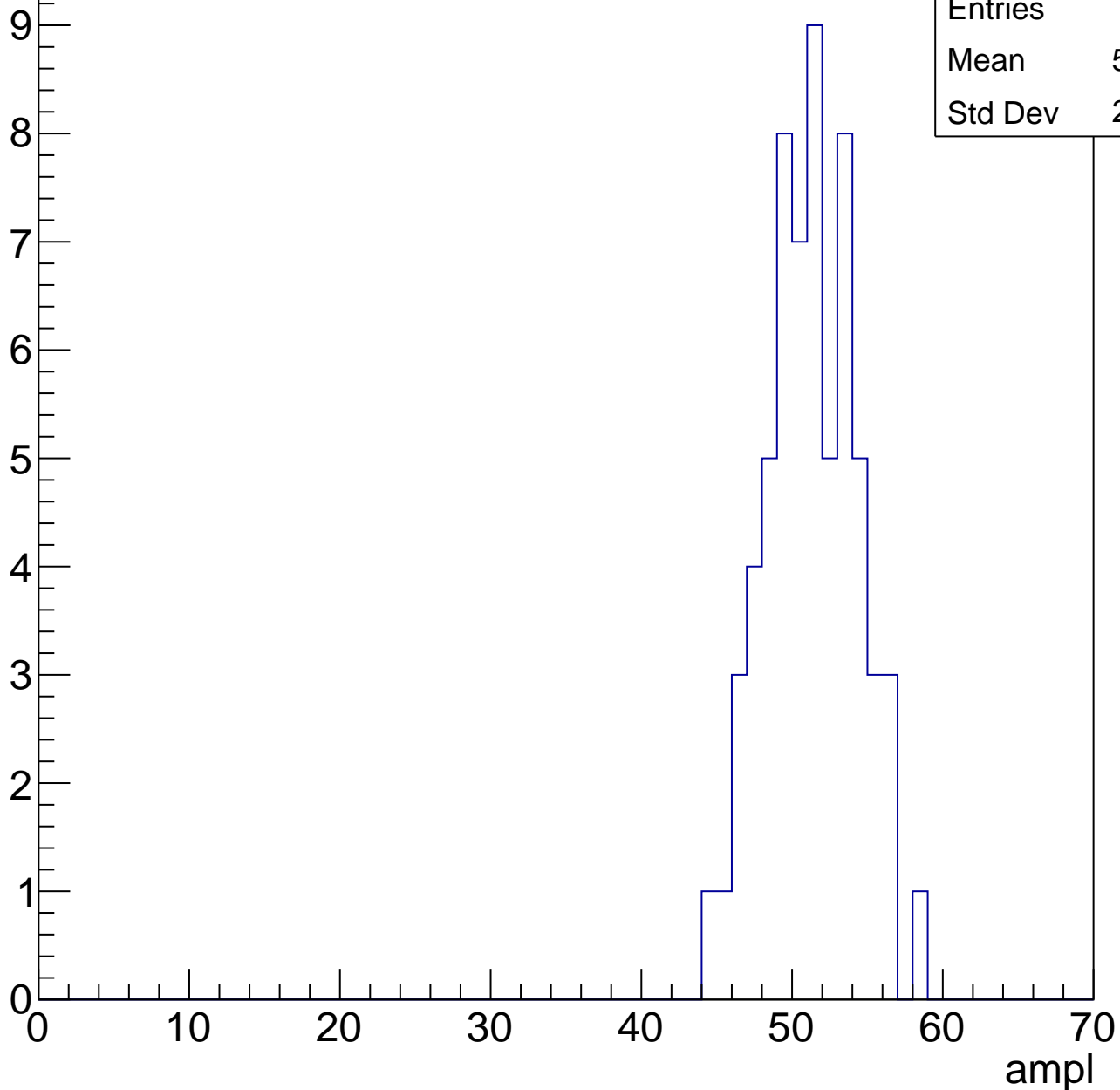
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



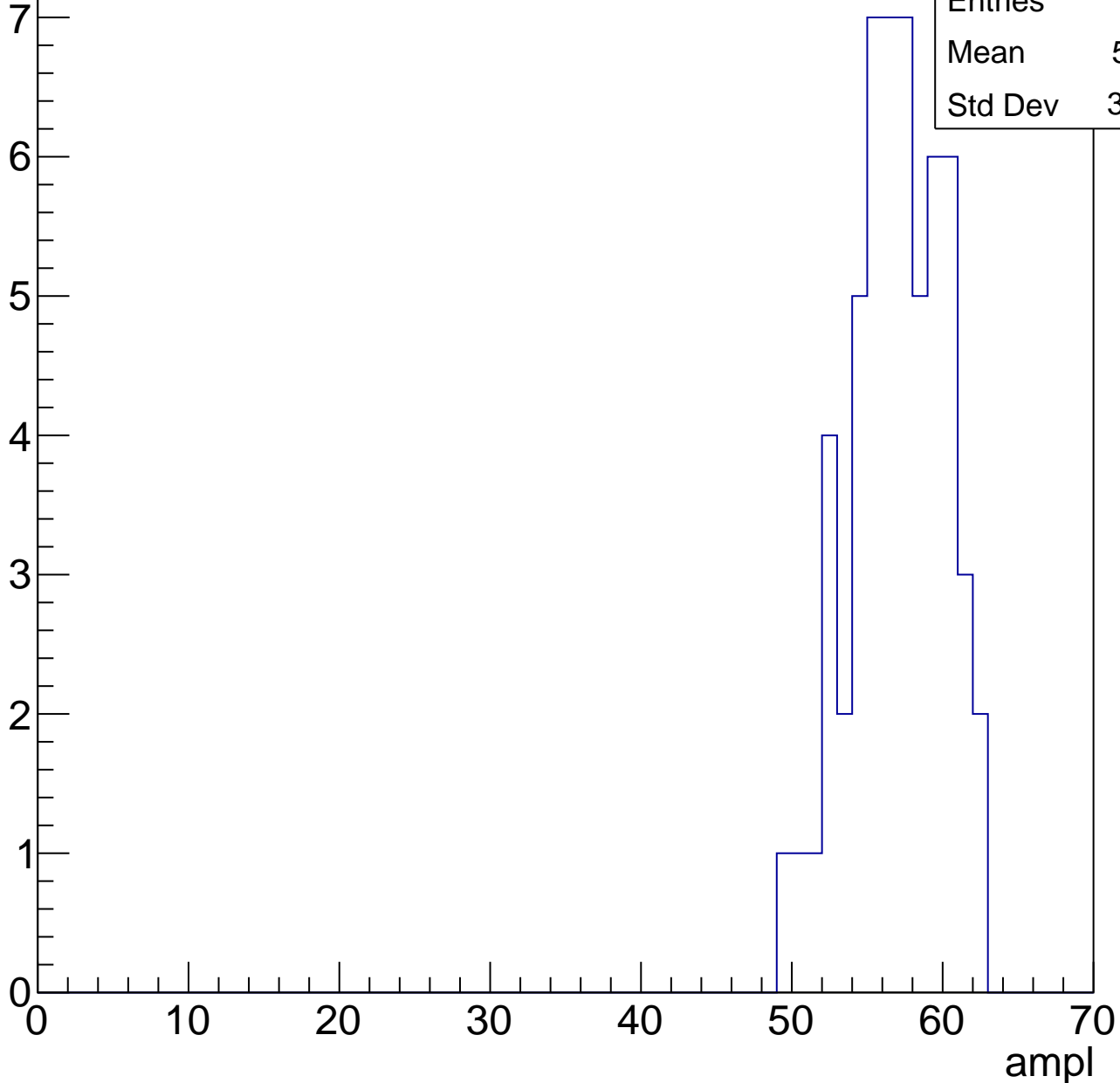
Entries	63
Mean	50.81
Std Dev	2.981

B1L103S, U10-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	56.51
Std Dev	3.044

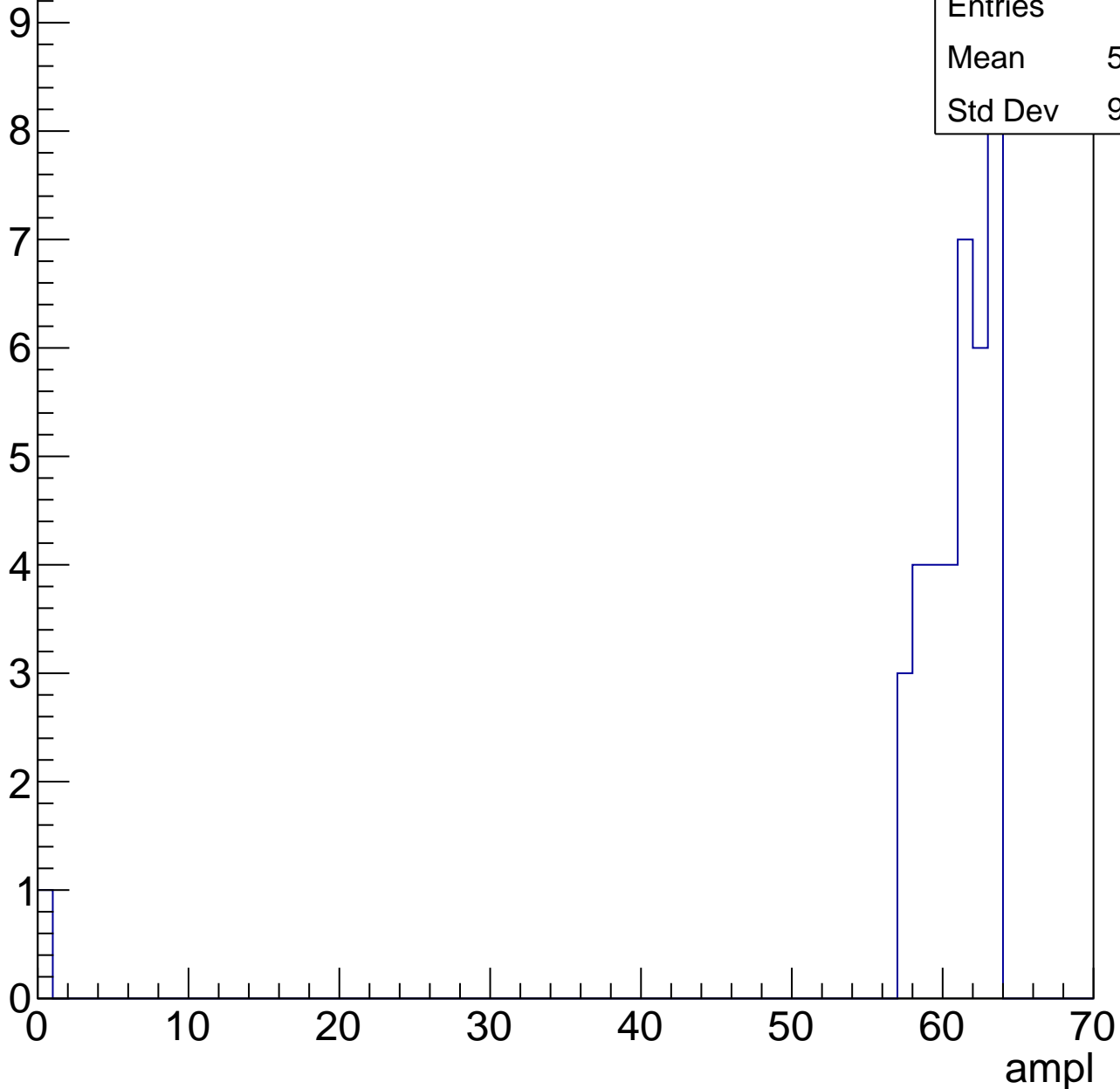


B1L103S, U10-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	59.08
Std Dev	9.903



B1L103S, U10-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

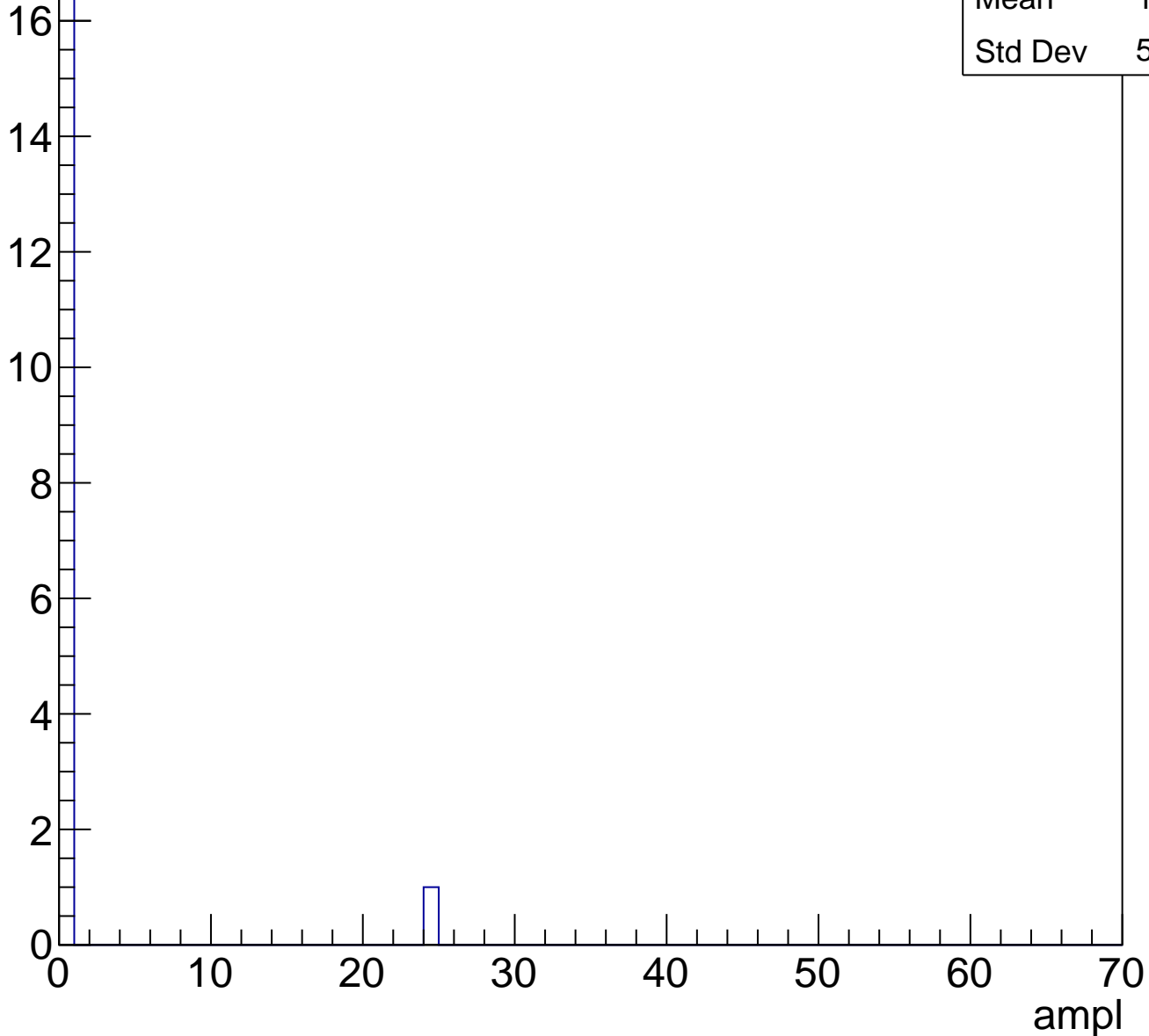
0 10 20 30 40 50 60 70

B1L103S, U10-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.333
Std Dev	5.497

Entry



B1L103S, U10-ch16, adc0

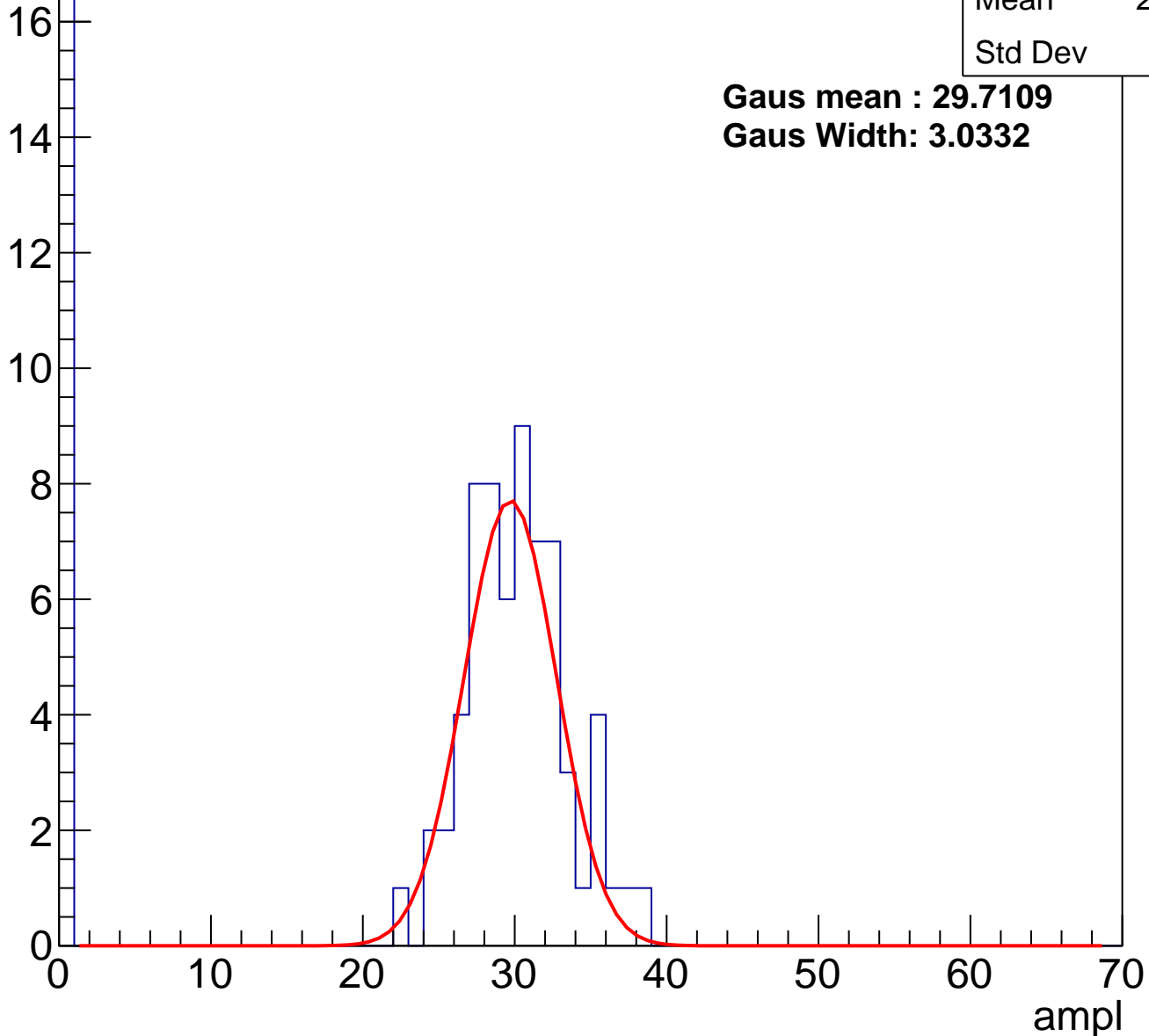
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.57
Std Dev	12.4

Gaus mean : 29.7109

Gaus Width: 3.0332

Entry



B1L103S, U10-ch16, adc1

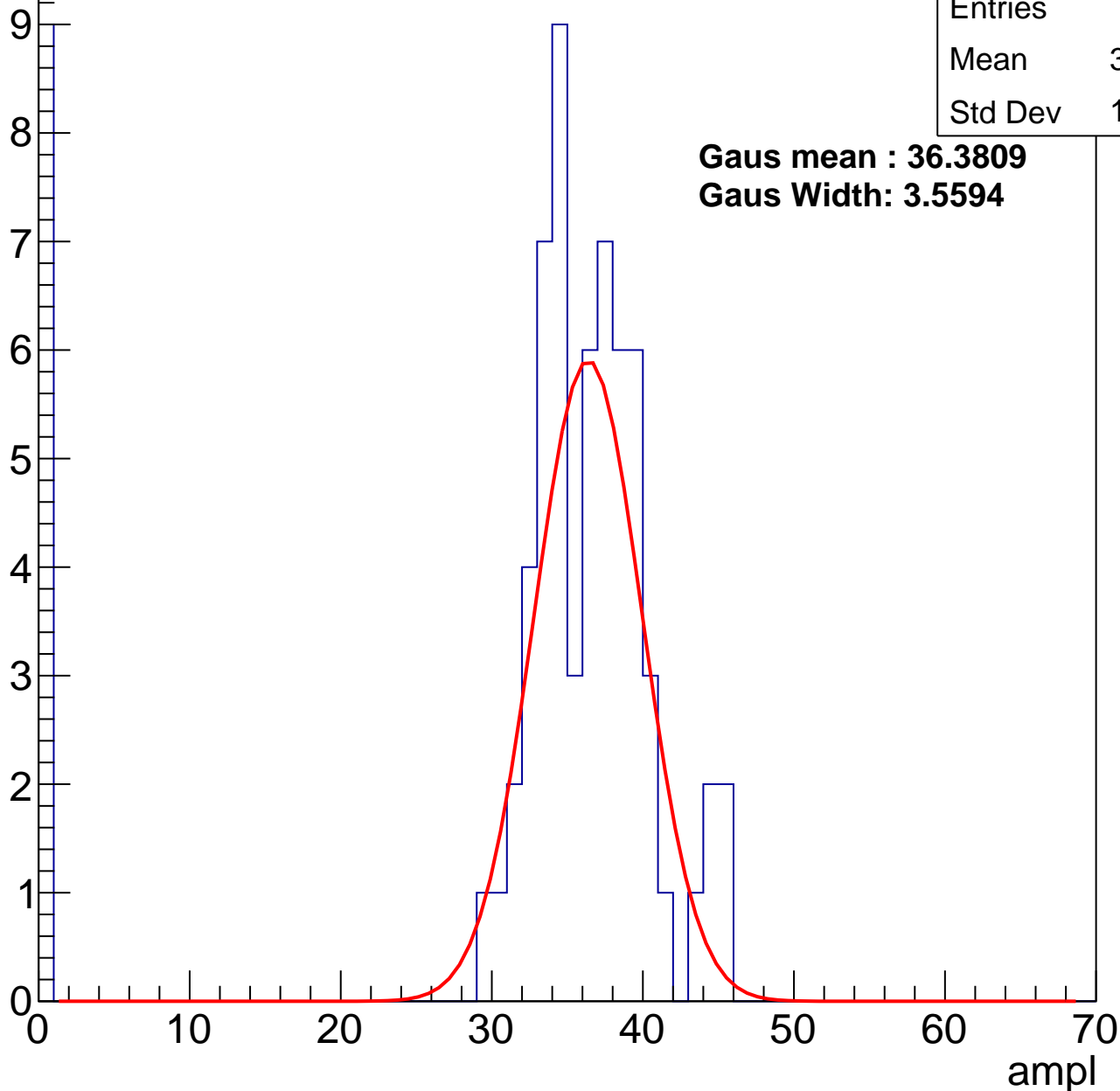
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	31.57
Std Dev	12.59

Gaus mean : 36.3809

Gaus Width: 3.5594



B1L103S, U10-ch16, adc2

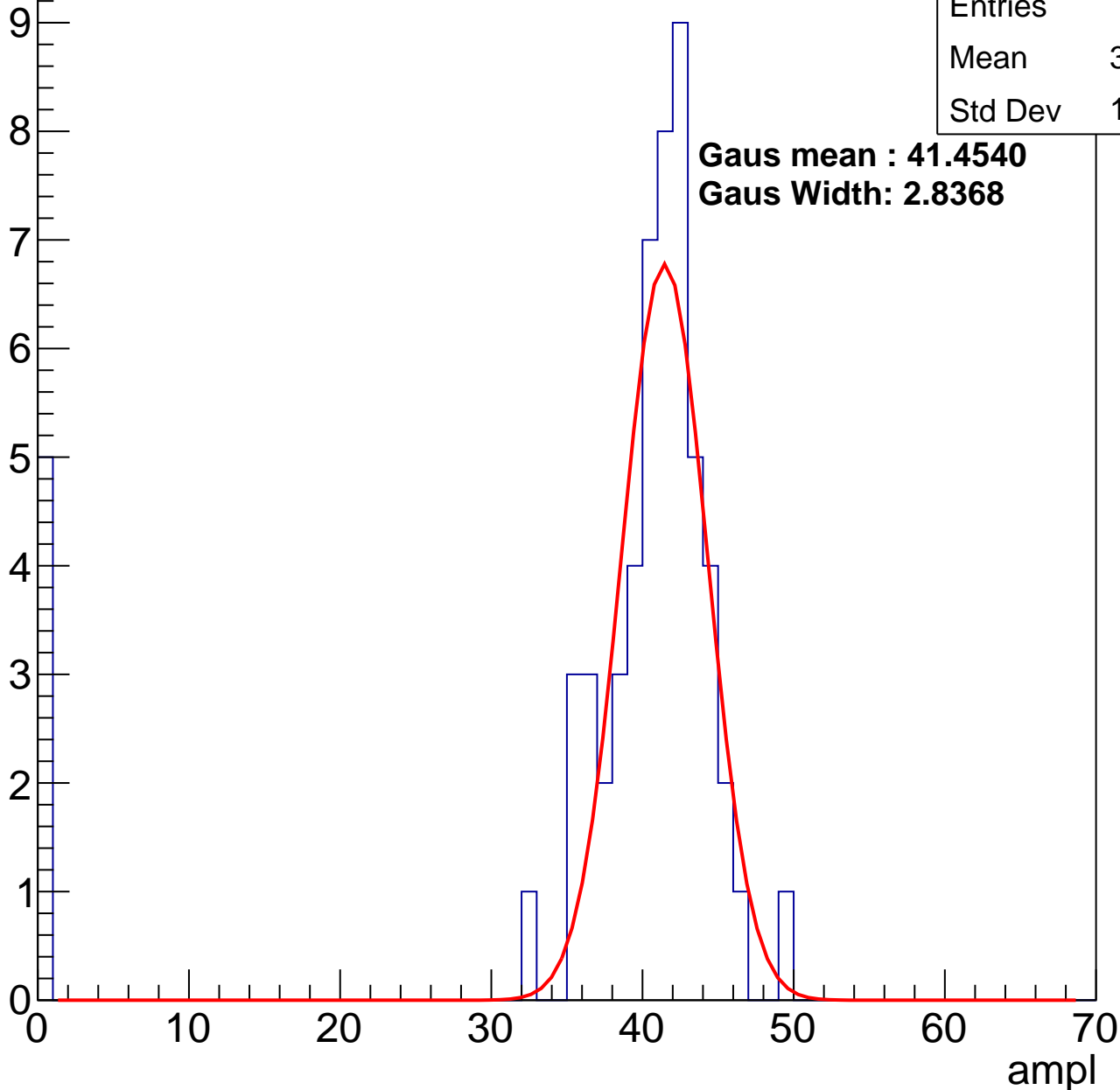
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	37.09
Std Dev	11.78

Gaus mean : 41.4540

Gaus Width: 2.8368

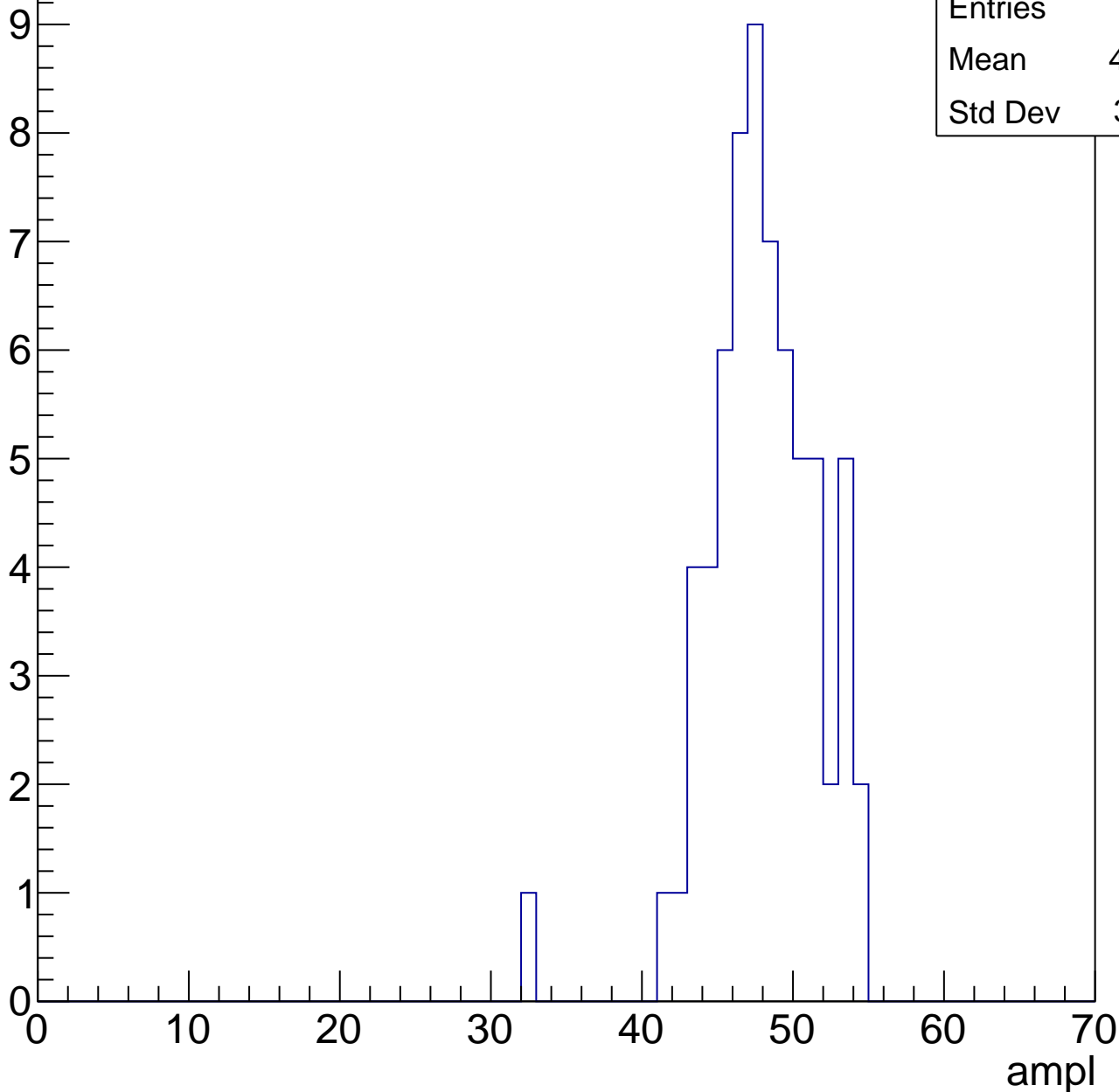


B1L103S, U10-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

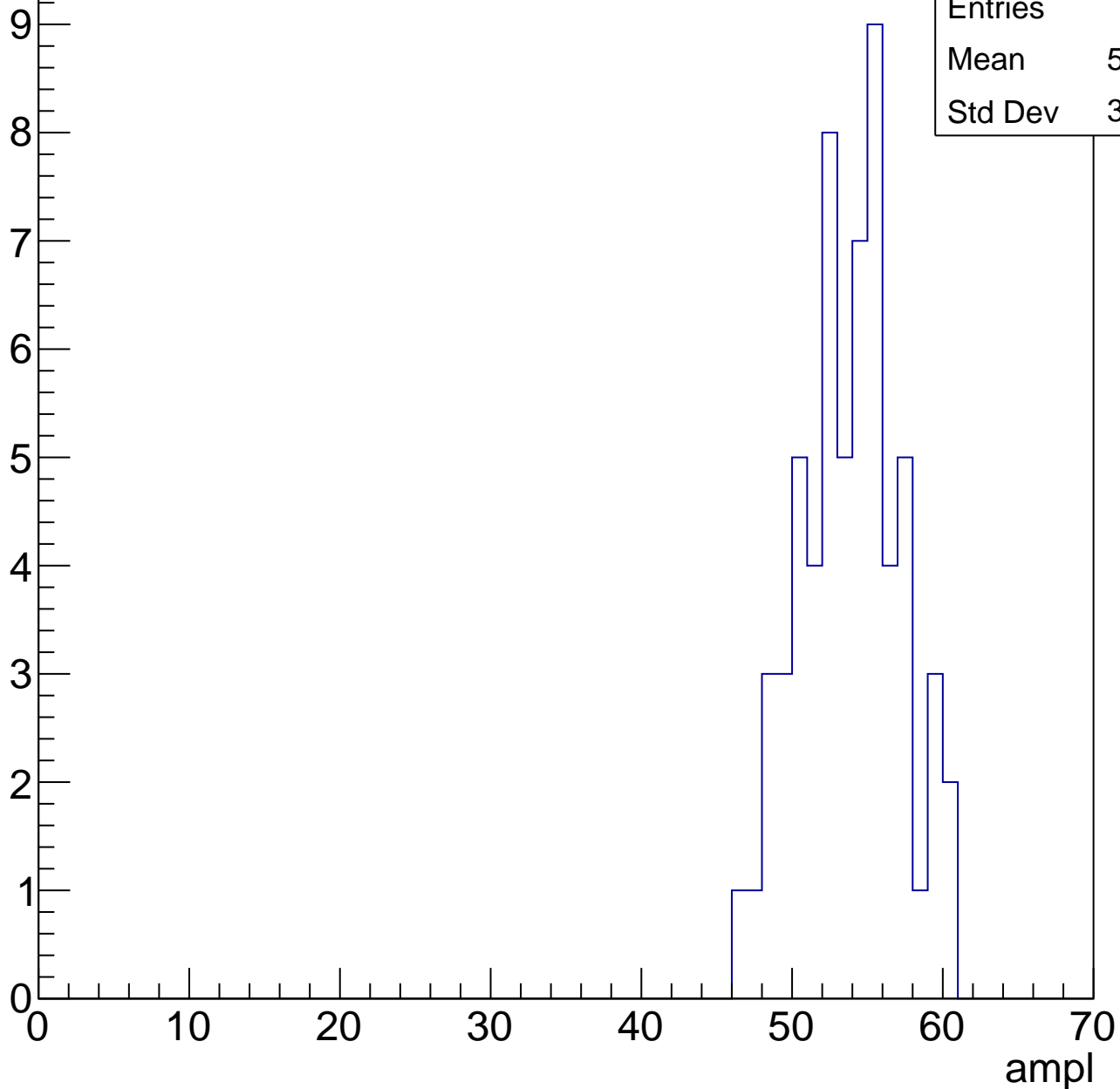
Entries	66
Mean	47.52
Std Dev	3.661



B1L103S, U10-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

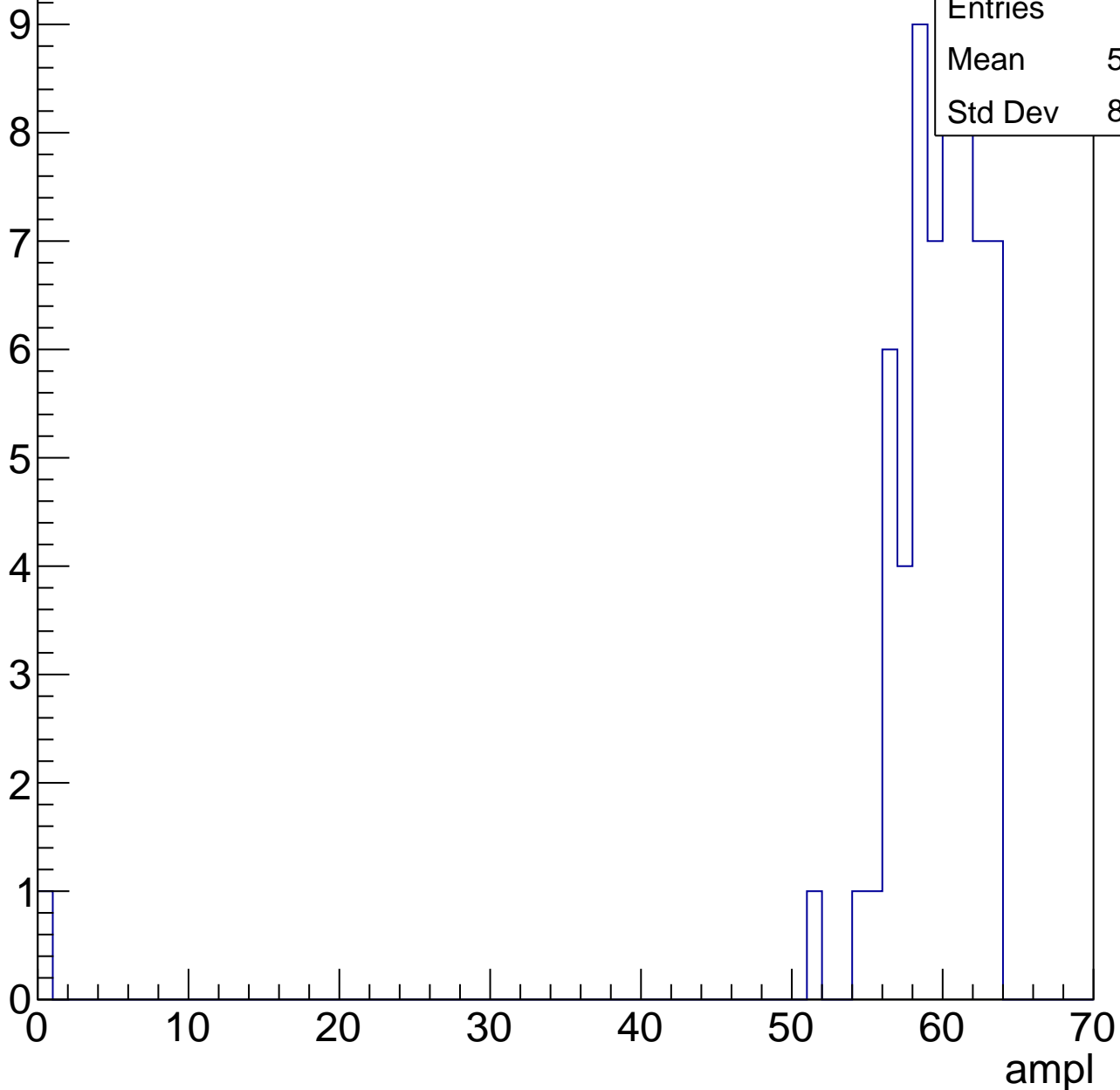


B1L103S, U10-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

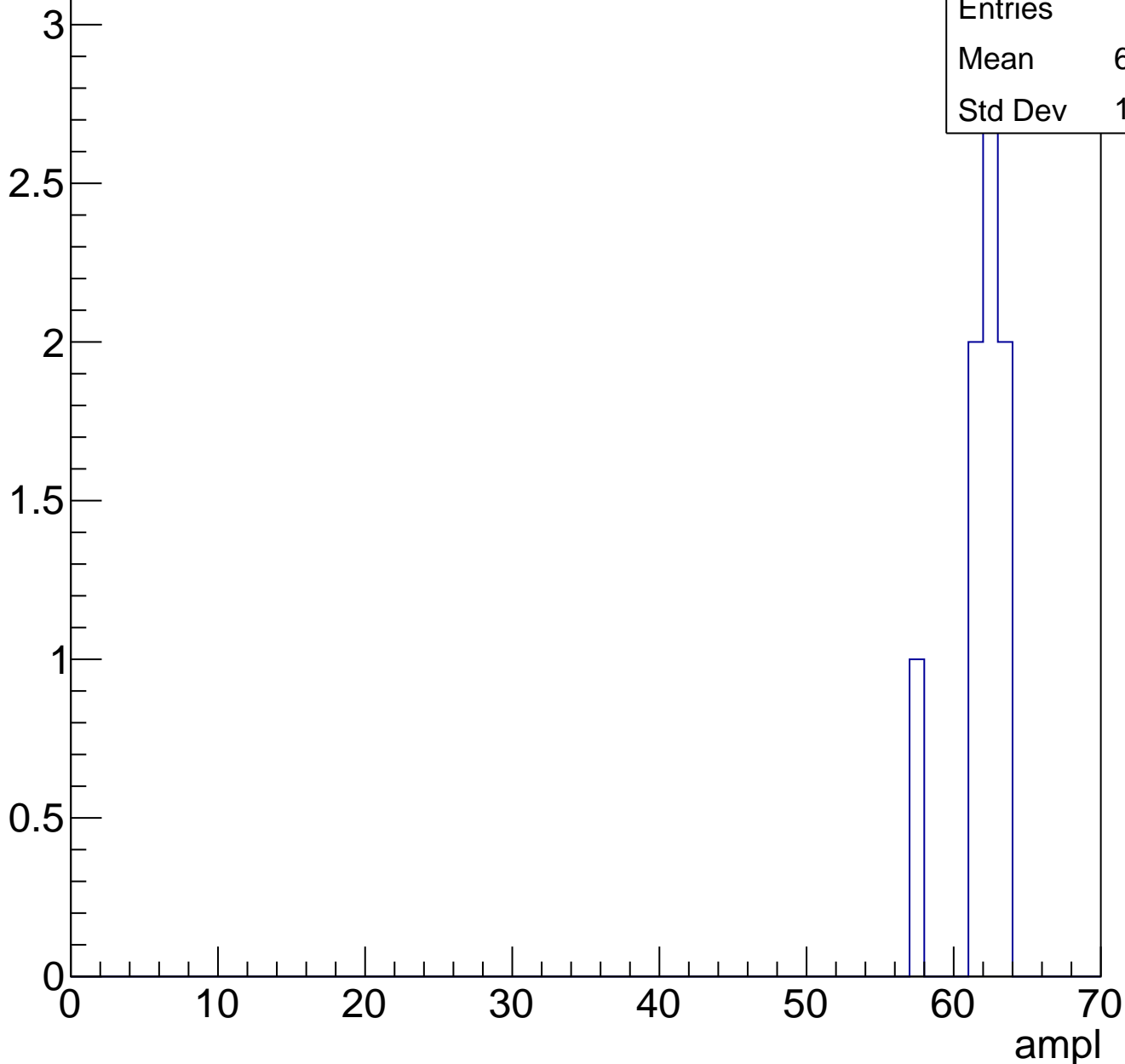
Entries	60
Mean	58.37
Std Dev	8.017



B1L103S, U10-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U10-ch17, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	25.12
Std Dev	11.76

Gaus mean : 30.7830

Gaus Width: 3.6833

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

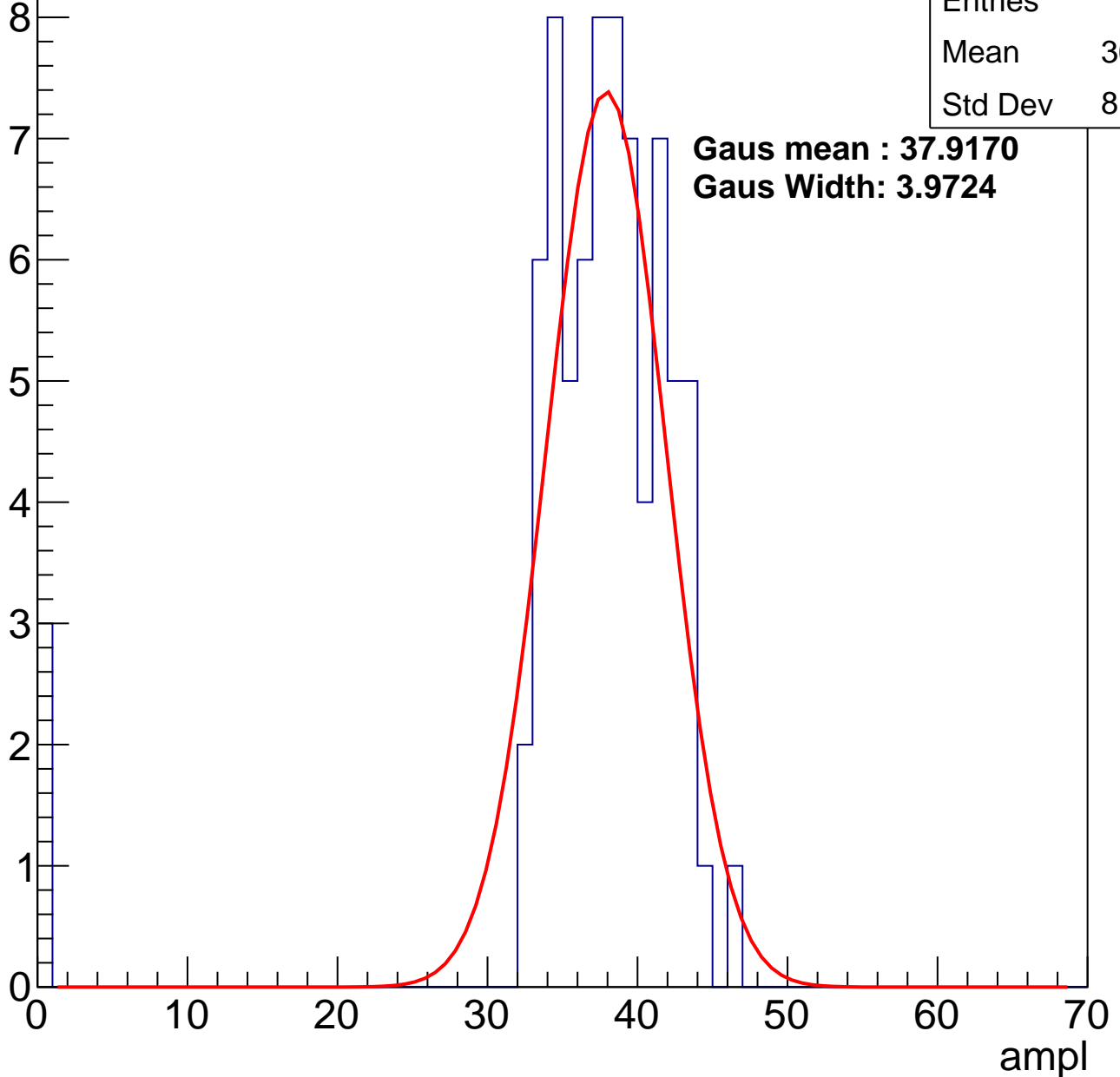
B1L103S, U10-ch17, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.32
Std Dev	8.058

Gaus mean : 37.9170
Gaus Width: 3.9724



B1L103S, U10-ch17, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	36.91
Std Dev	16.53

Gaus mean : 44.0977

Gaus Width: 3.9967

Entry

10

8

6

4

2

0

0

10

20

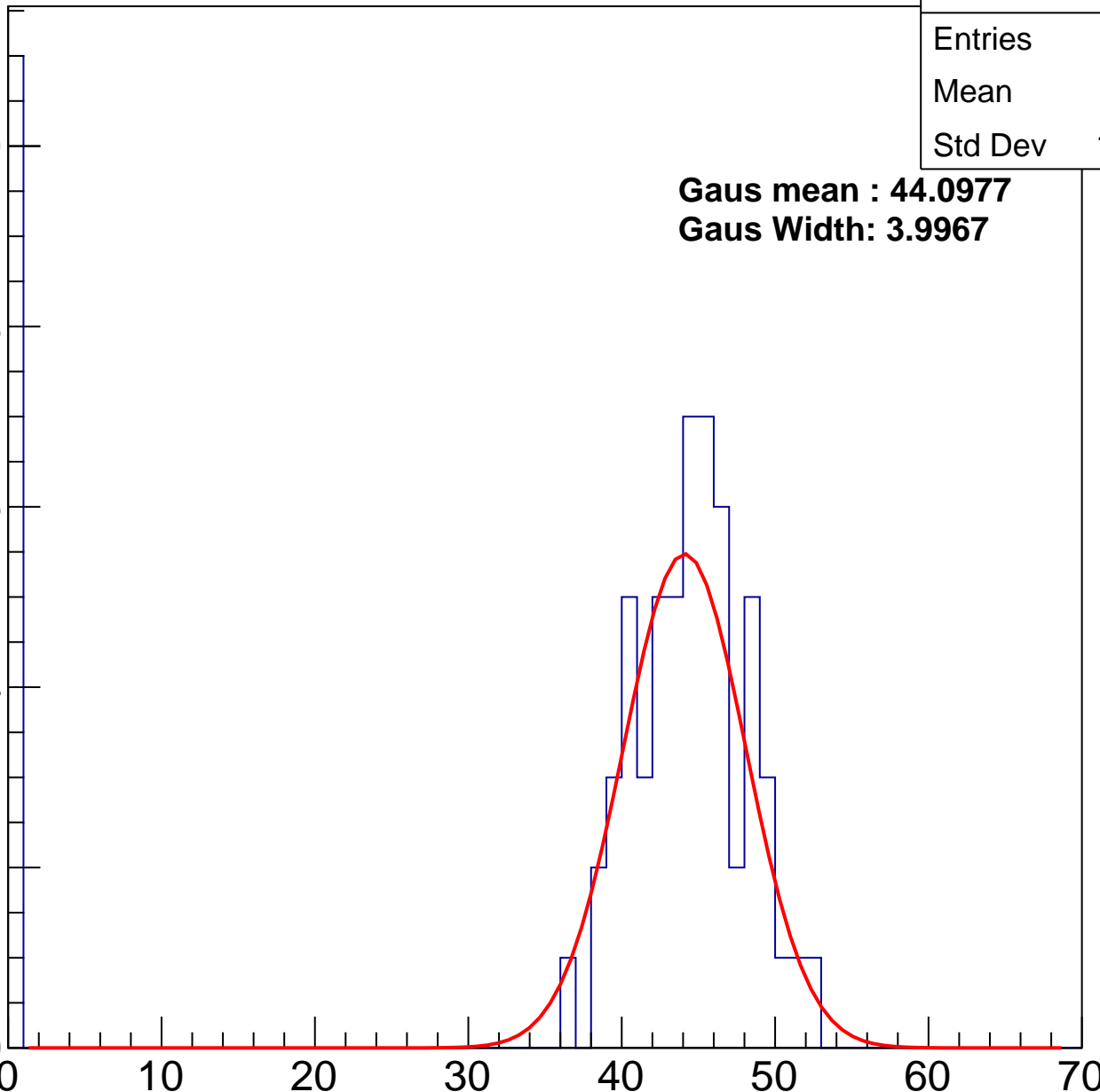
30

40

50

60

ampl

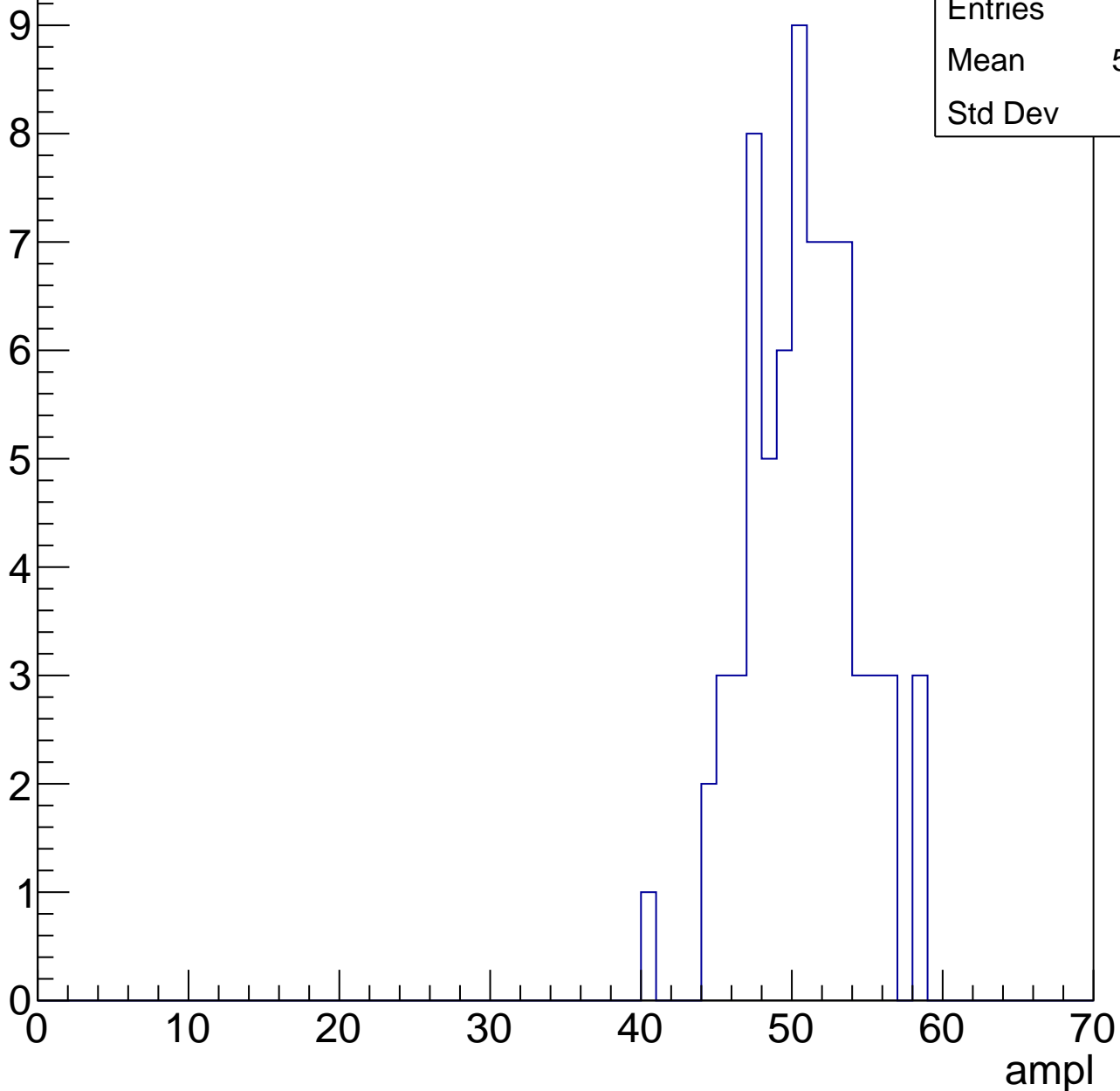


B1L103S, U10-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	50.31
Std Dev	3.6

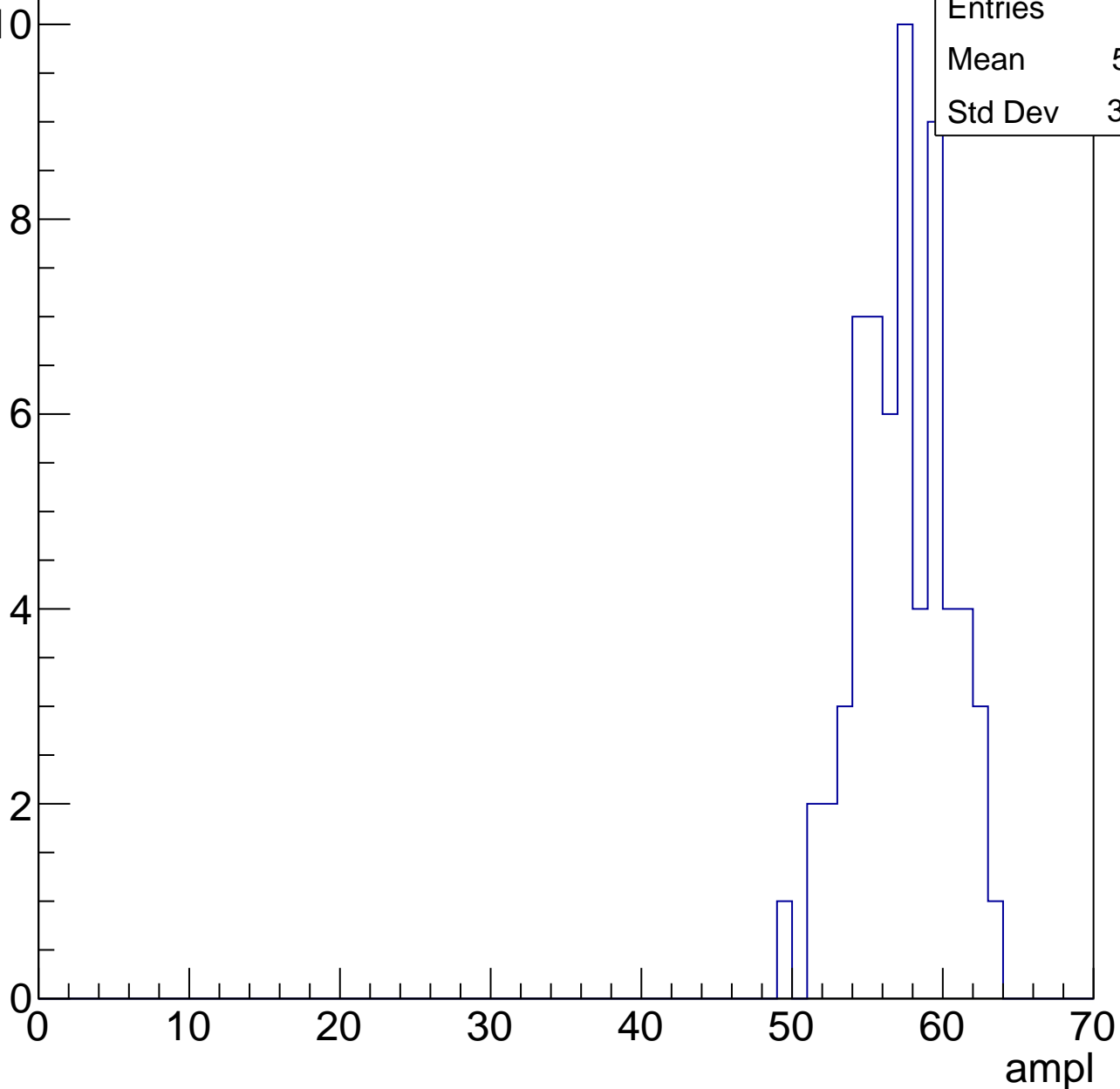


B1L103S, U10-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.81
Std Dev	3.033

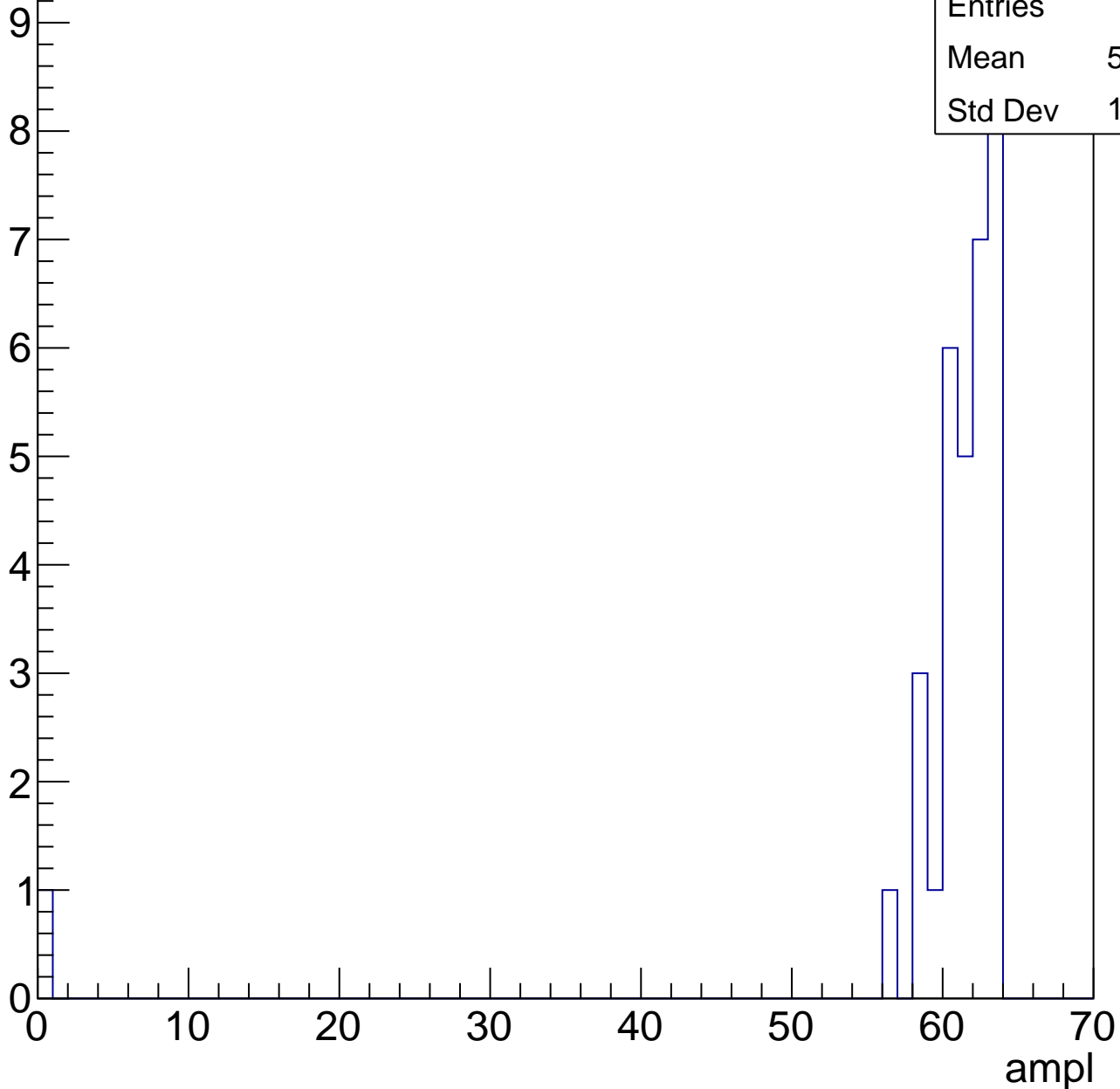


B1L103S, U10-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	59.24
Std Dev	10.62



B1L103S, U10-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch18, adc0

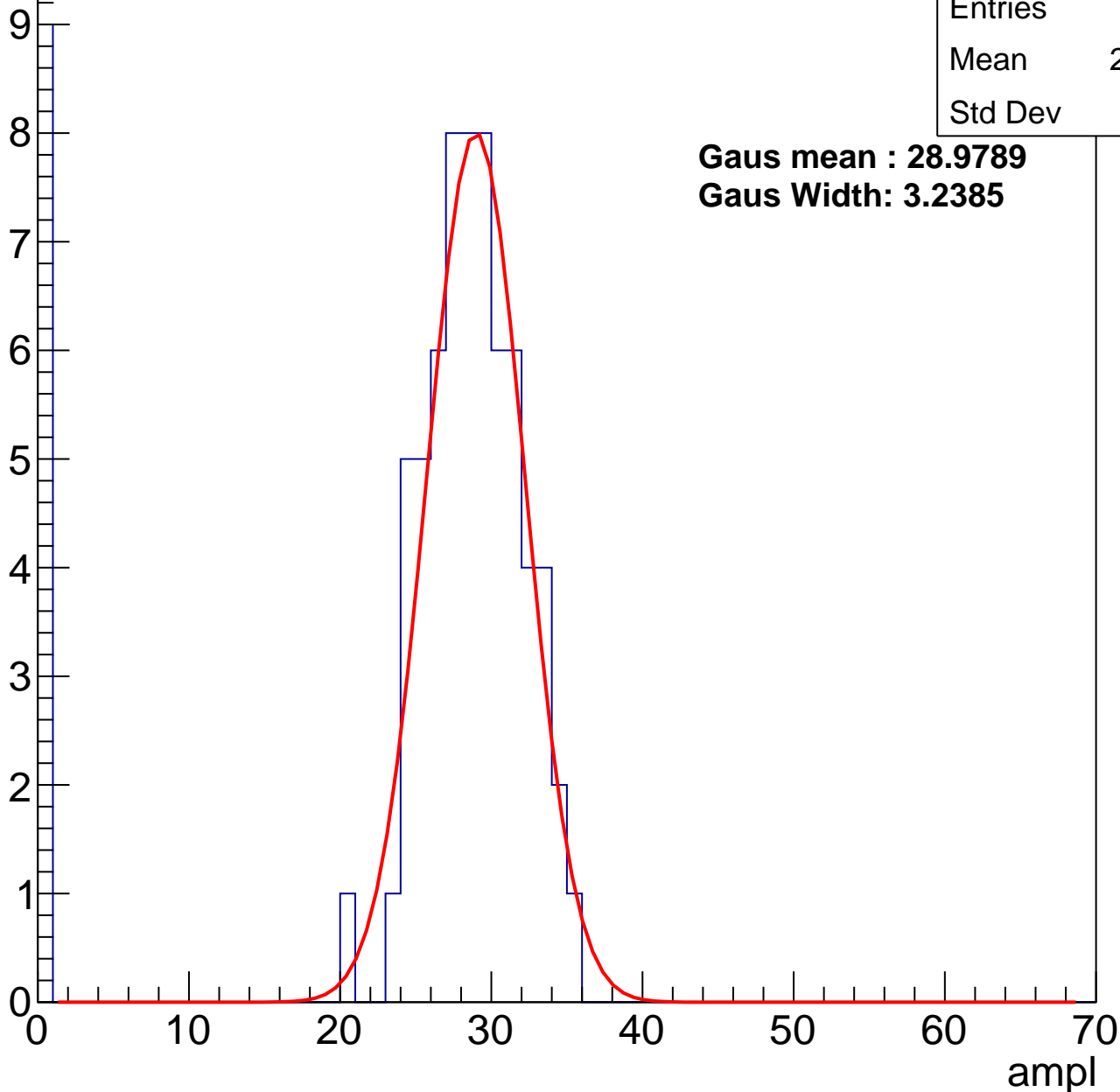
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	24.93
Std Dev	9.71

Gaus mean : 28.9789

Gaus Width: 3.2385



B1L103S, U10-ch18, adc1

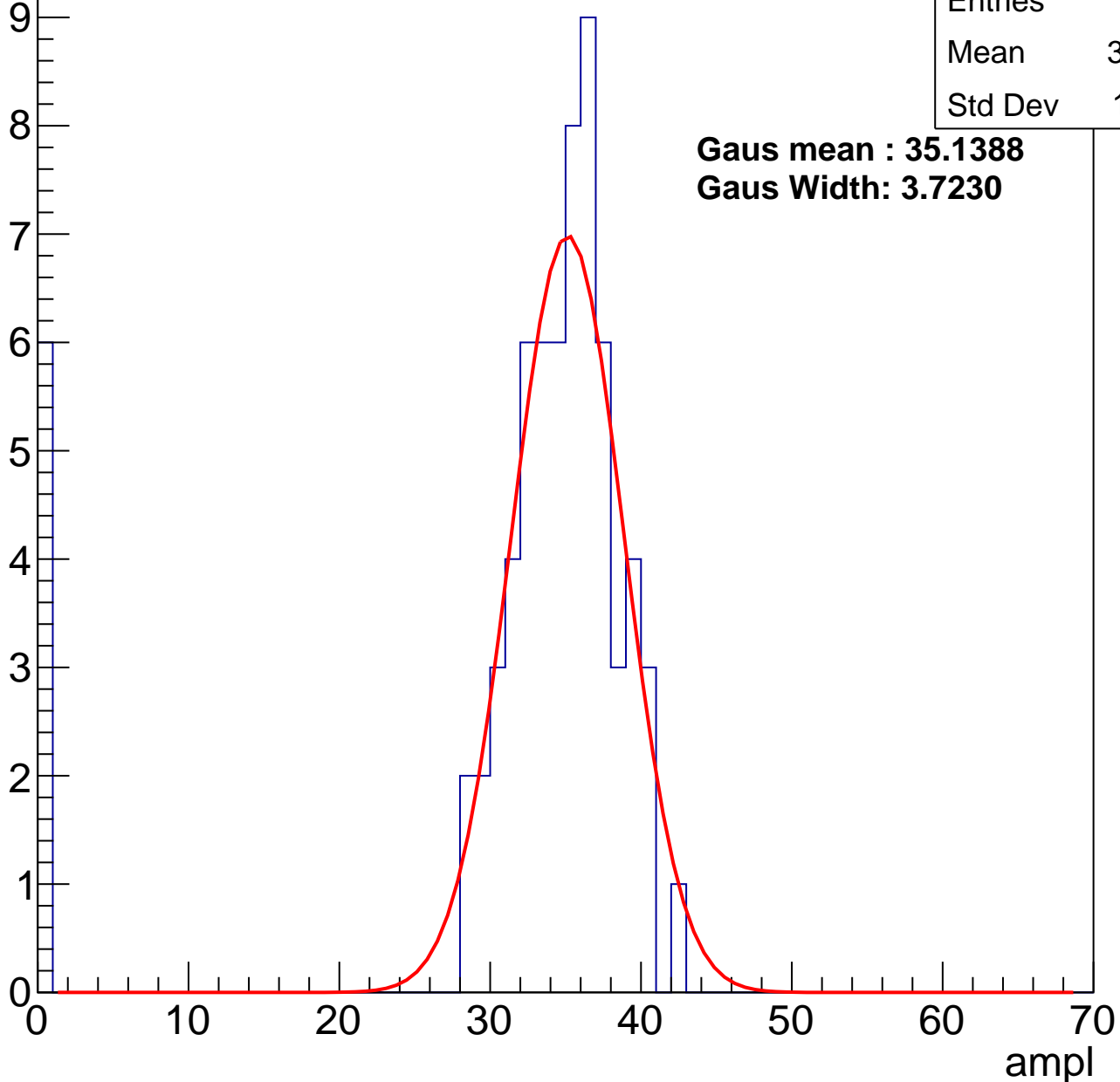
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	31.59
Std Dev	10.21

Gaus mean : 35.1388

Gaus Width: 3.7230



B1L103S, U10-ch18, adc2

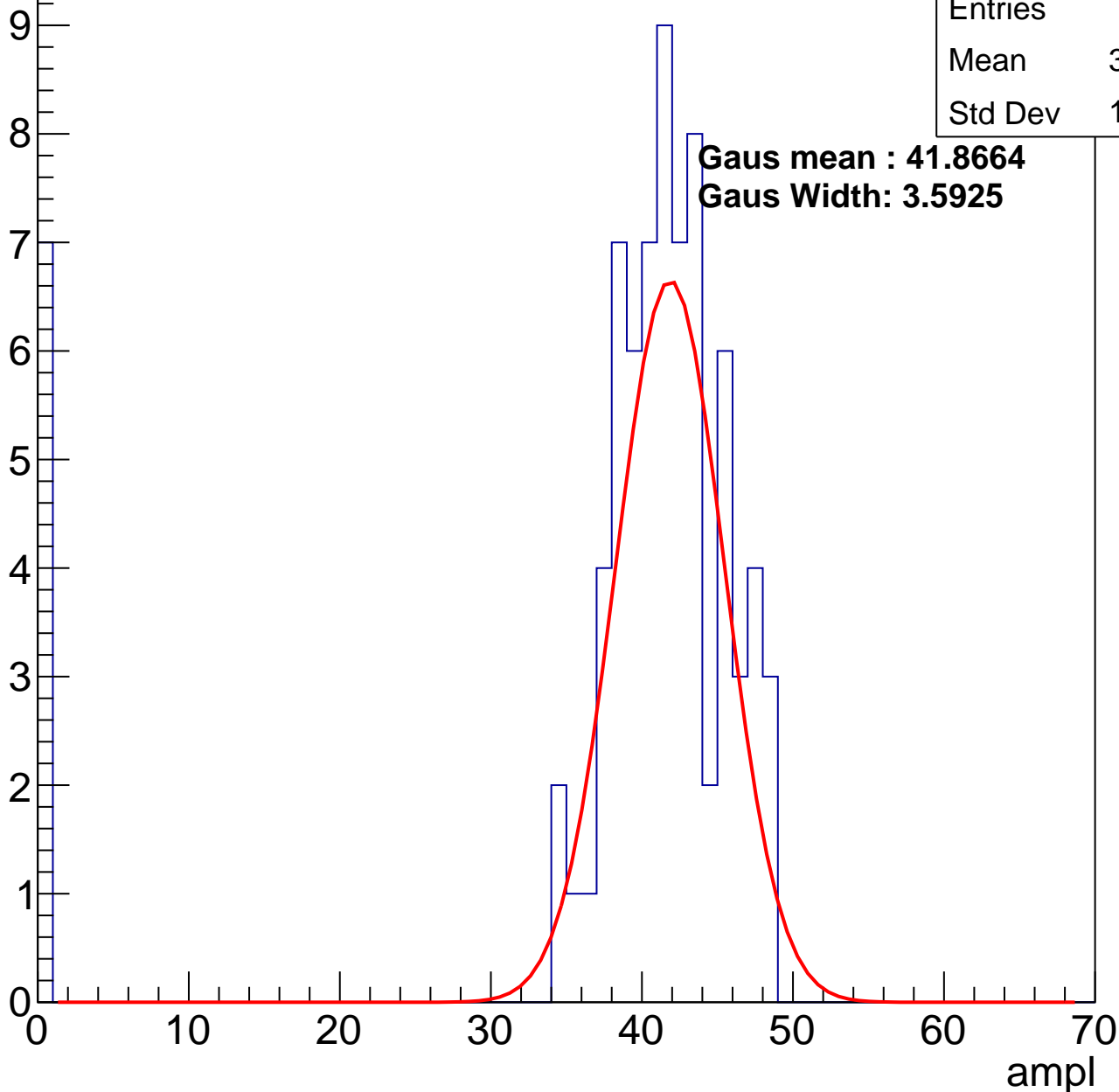
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	37.69
Std Dev	12.36

Gaus mean : 41.8664

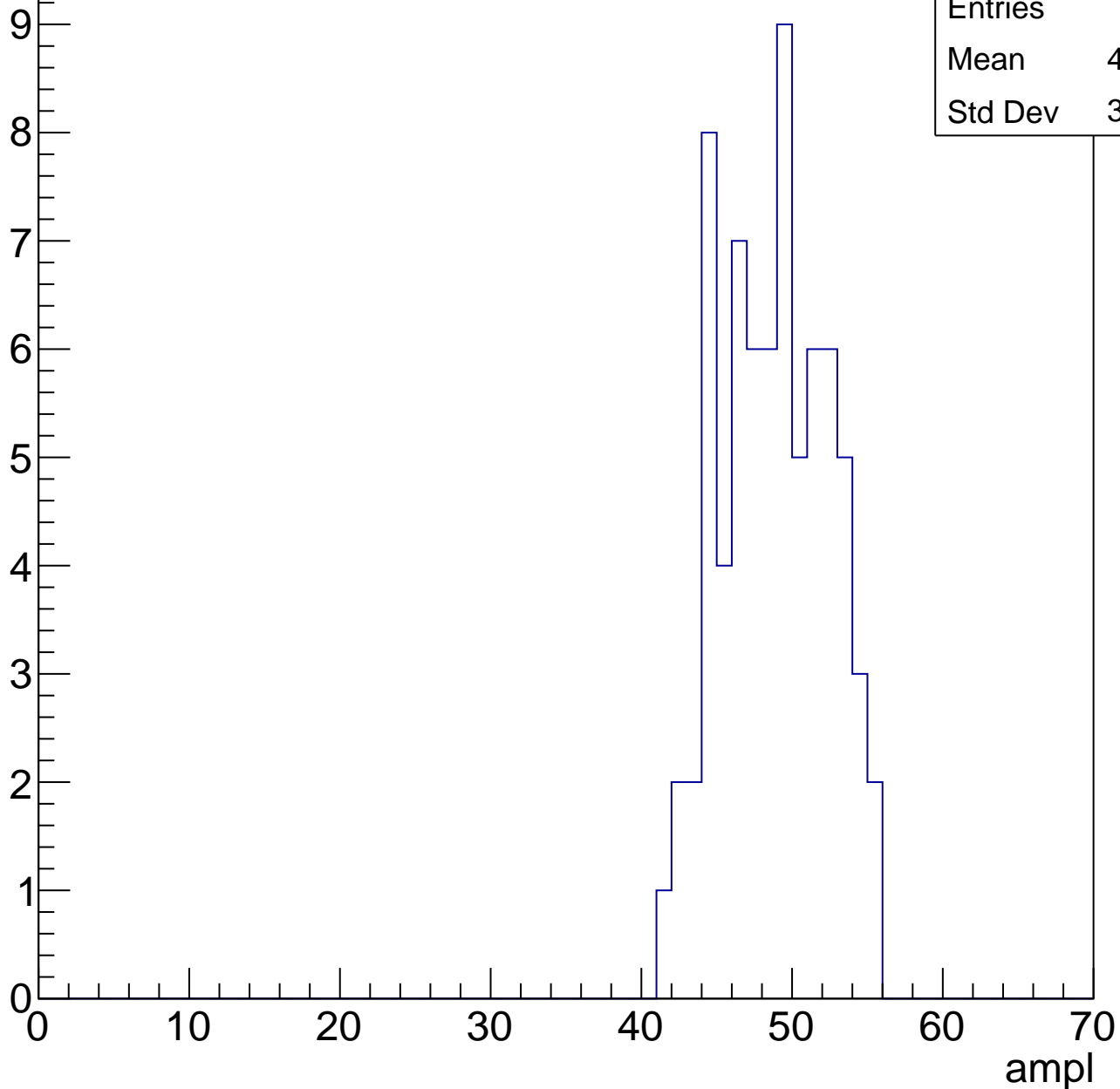
Gaus Width: 3.5925



B1L103S, U10-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

52

Mean

54.75

Std Dev

3.119

0

0

10

20

30

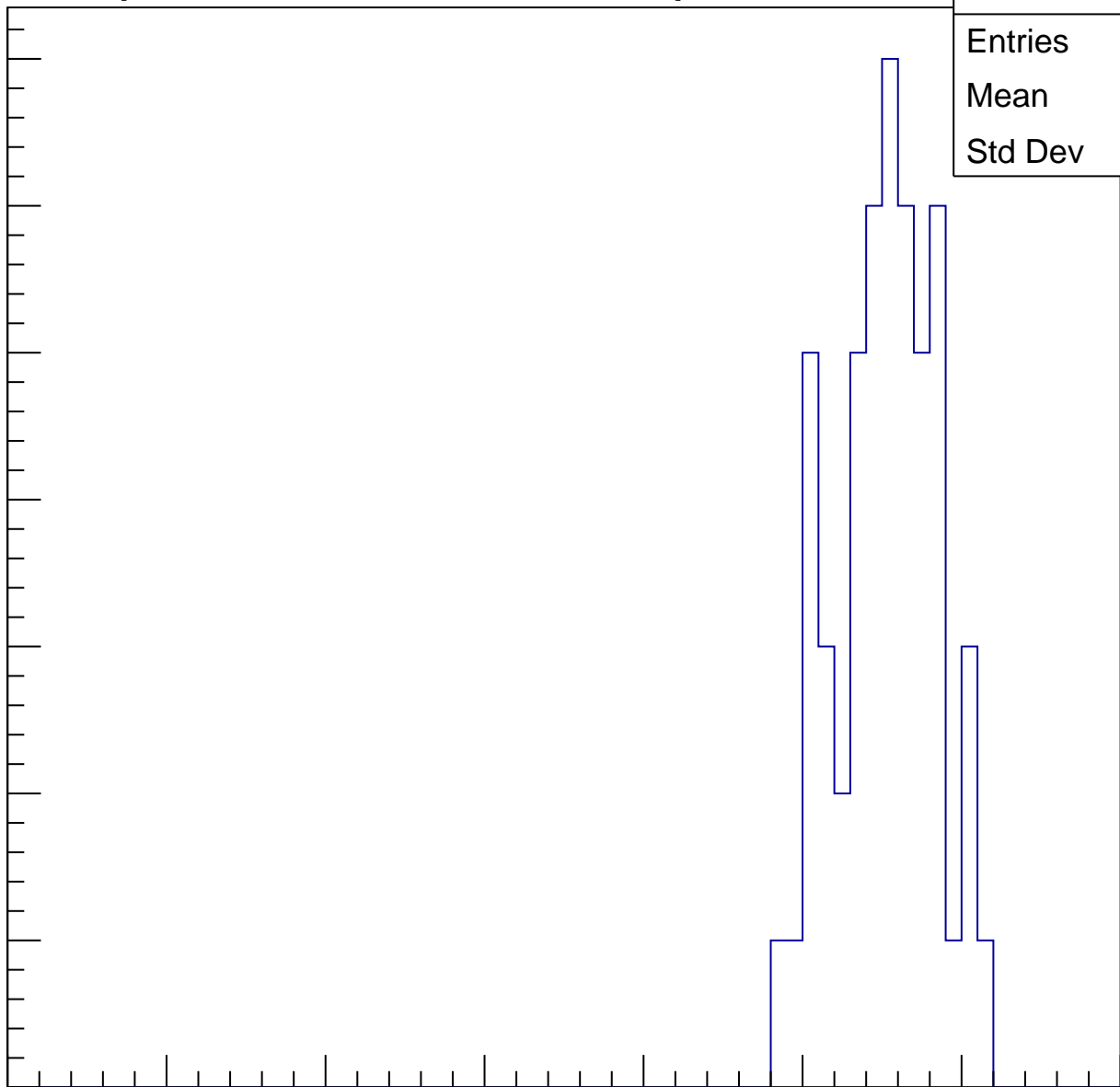
40

50

60

70

ampl

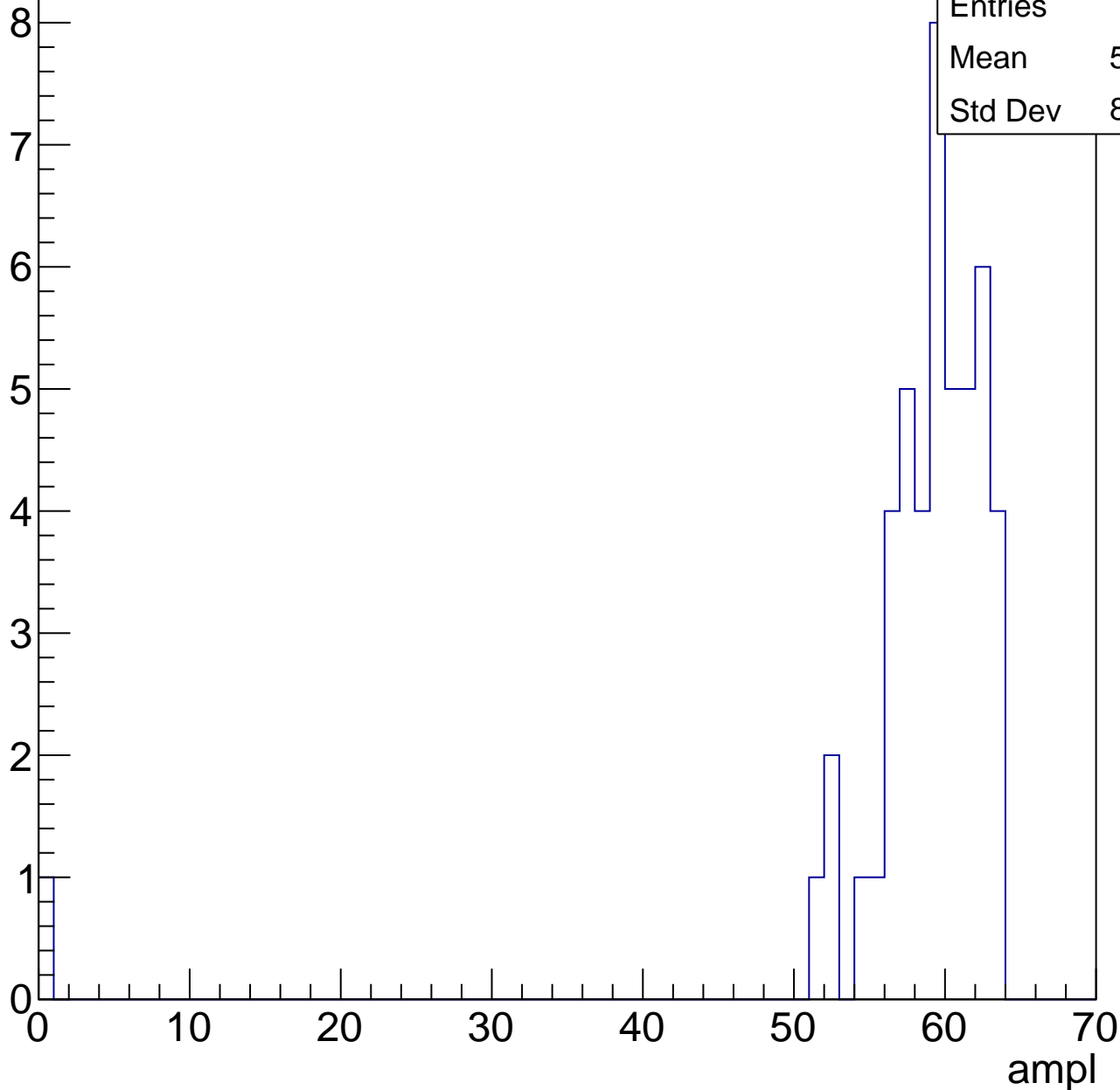


B1L103S, U10-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	57.57
Std Dev	8.982

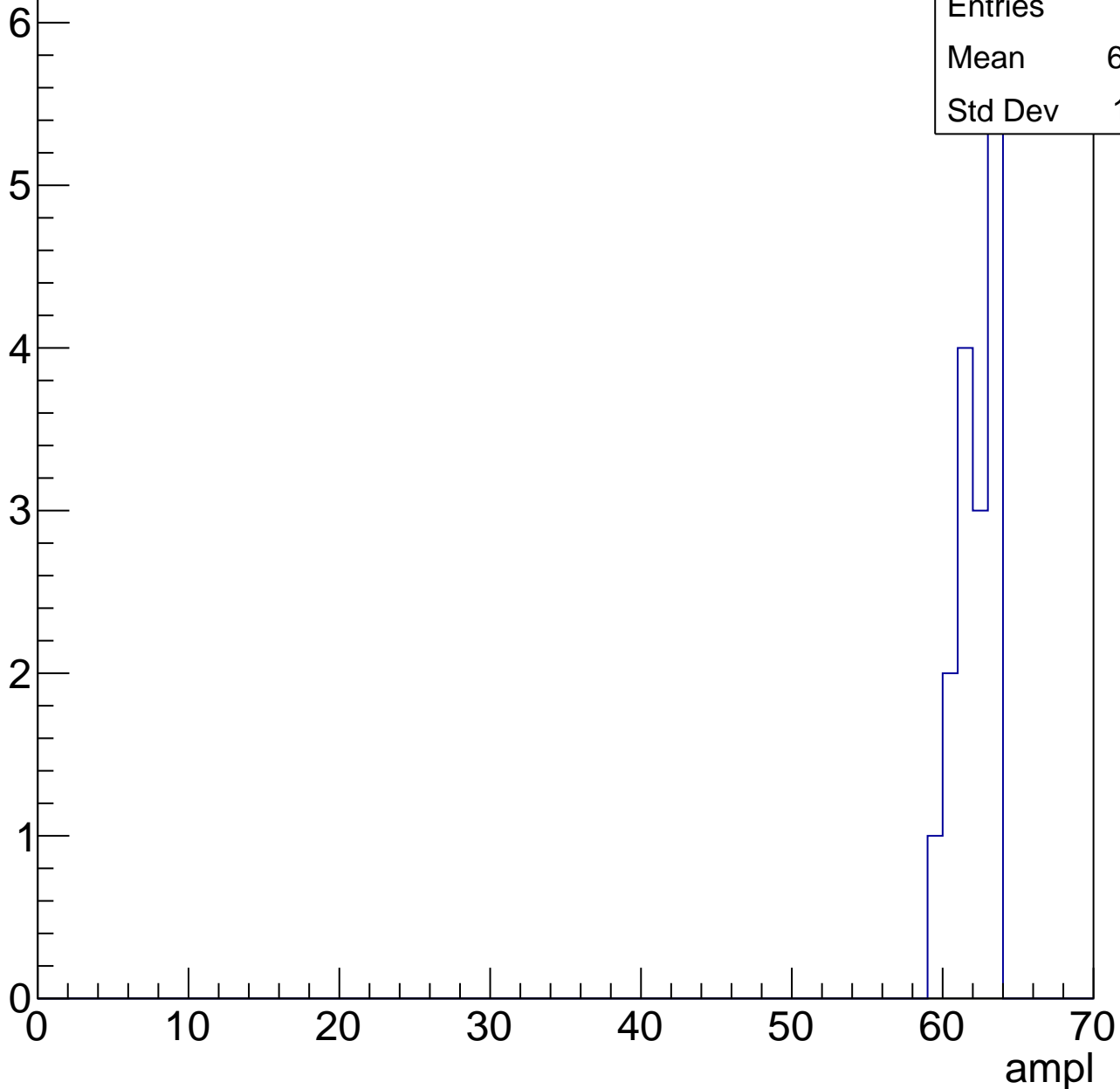


B1L103S, U10-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.69
Std Dev	1.261

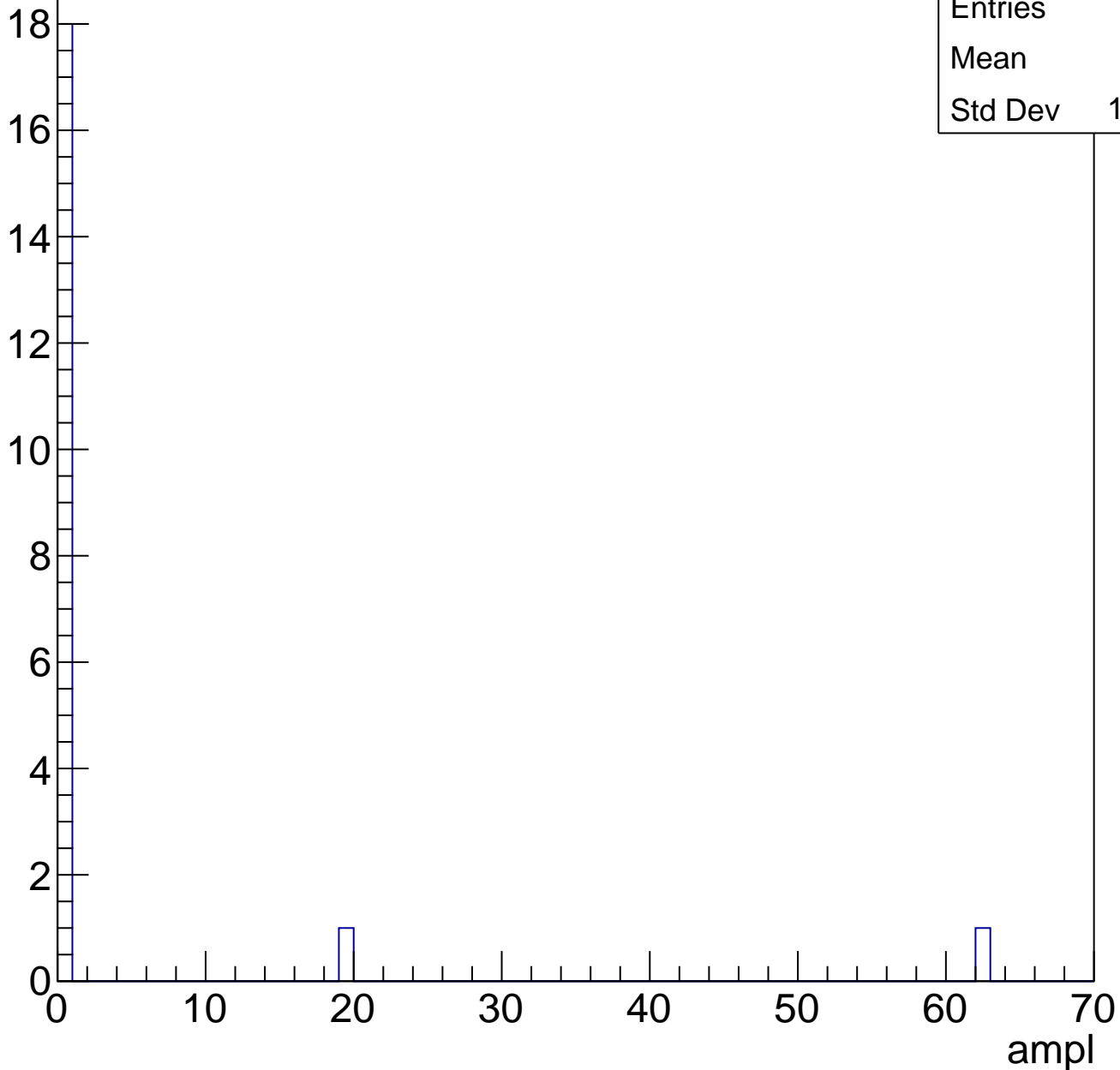


B1L103S, U10-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.05
Std Dev	13.92

Entry



B1L103S, U10-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	27.63
Std Dev	9.811

Gaus mean : 31.3306

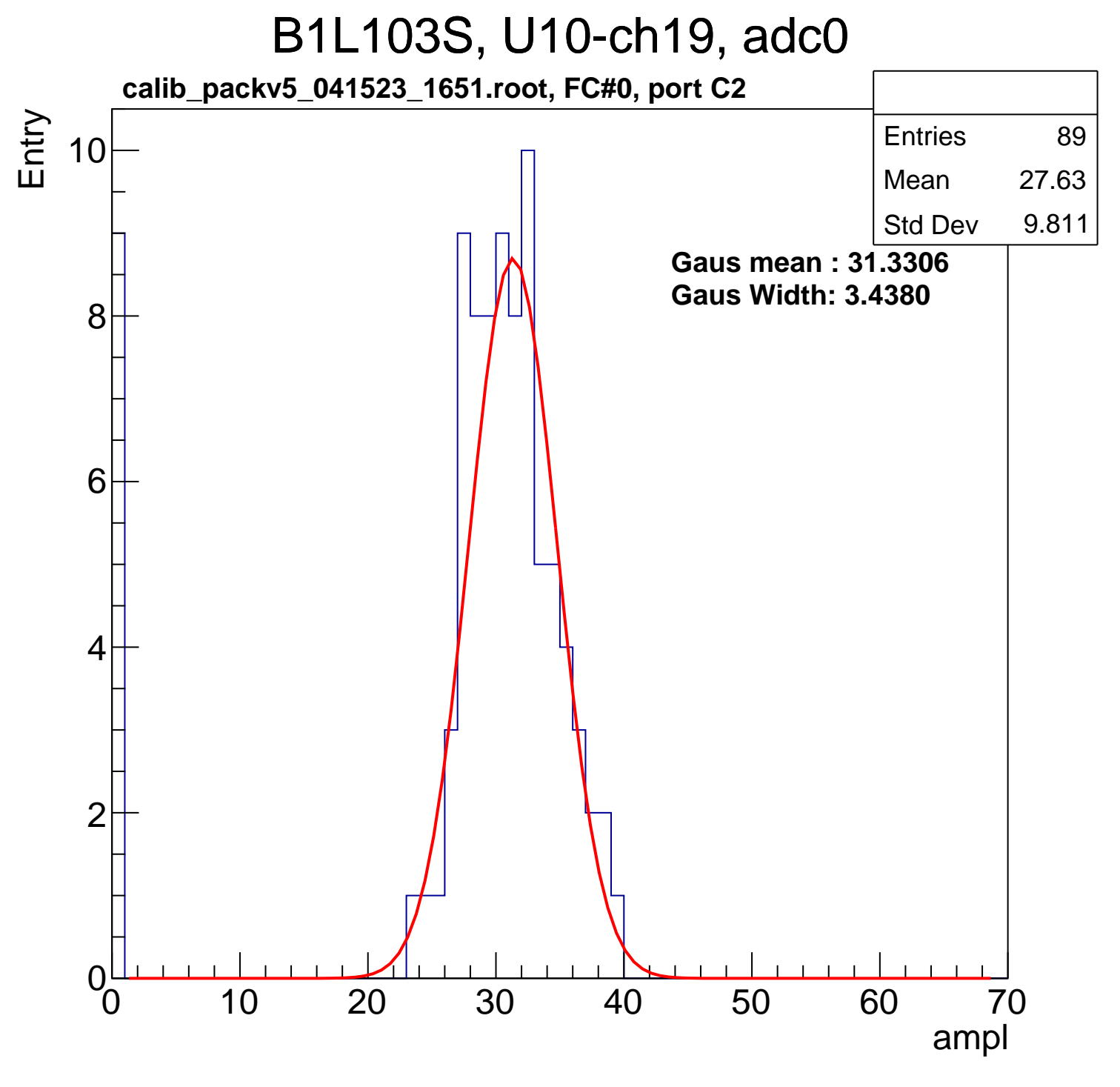
Gaus Width: 3.4380

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch19, adc1

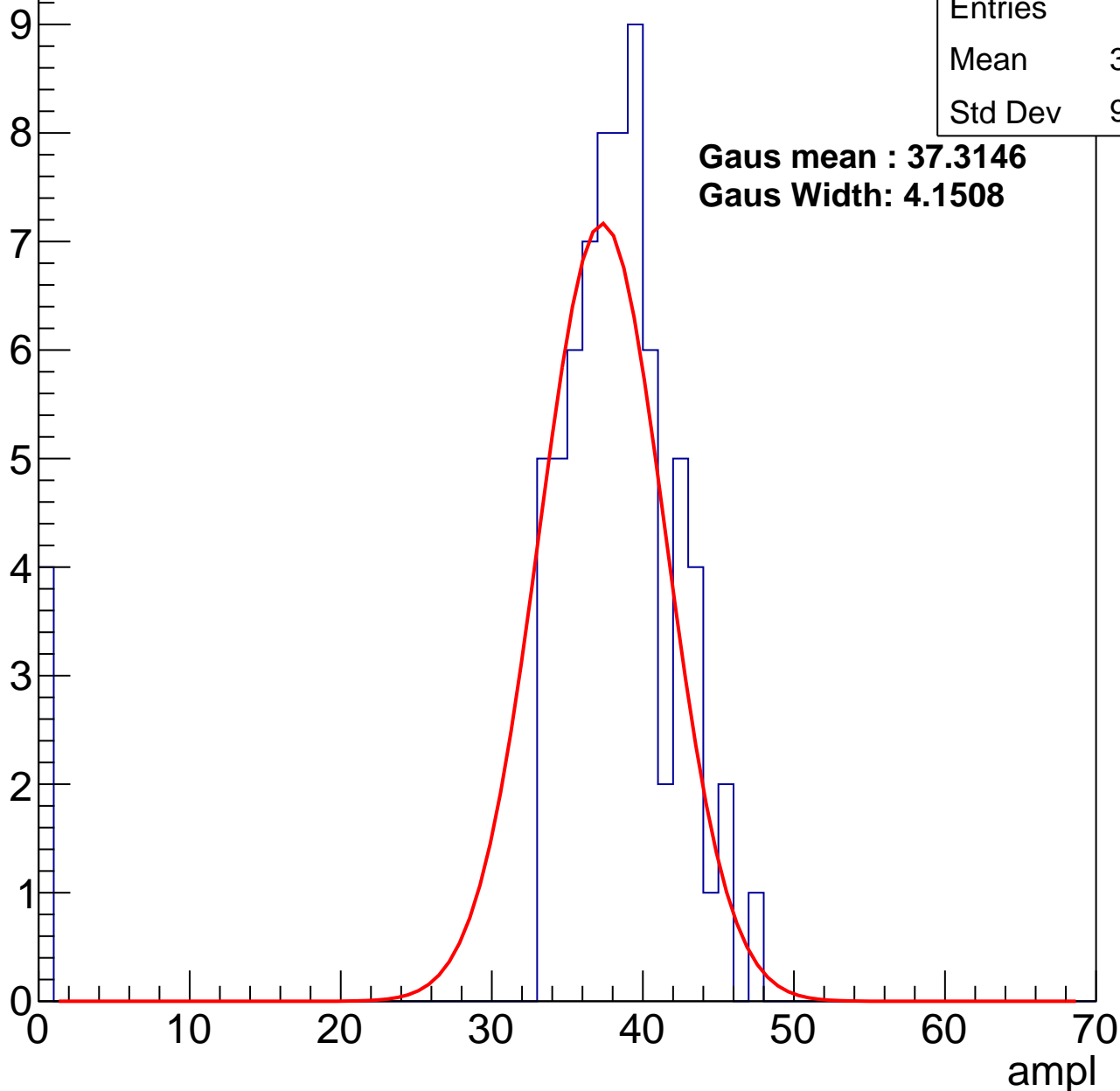
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.07
Std Dev	9.249

Gaus mean : 37.3146

Gaus Width: 4.1508



B1L103S, U10-ch19, adc2

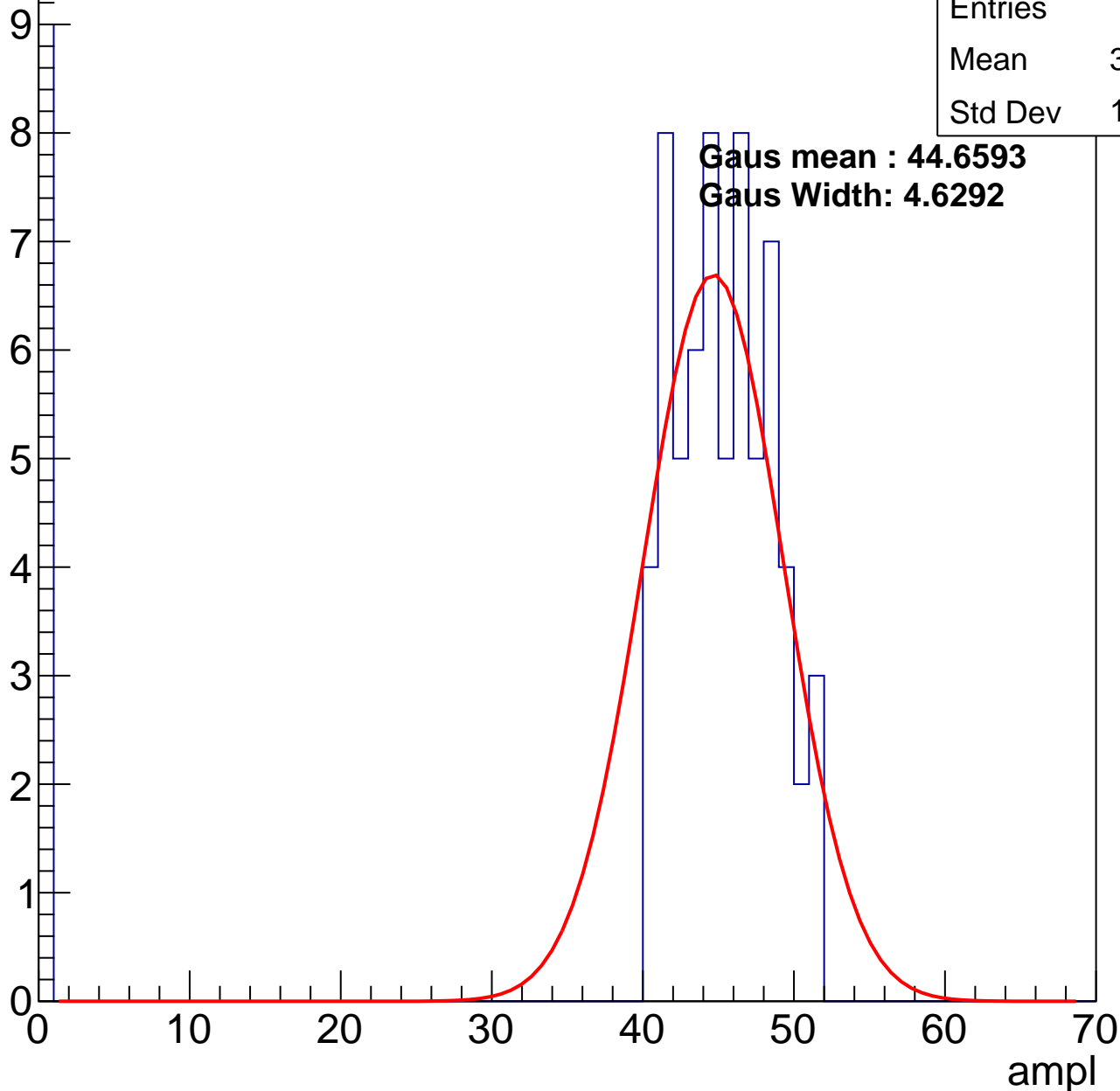
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	39.47
Std Dev	14.97

Gaus mean : 44.6593

Gaus Width: 4.6292

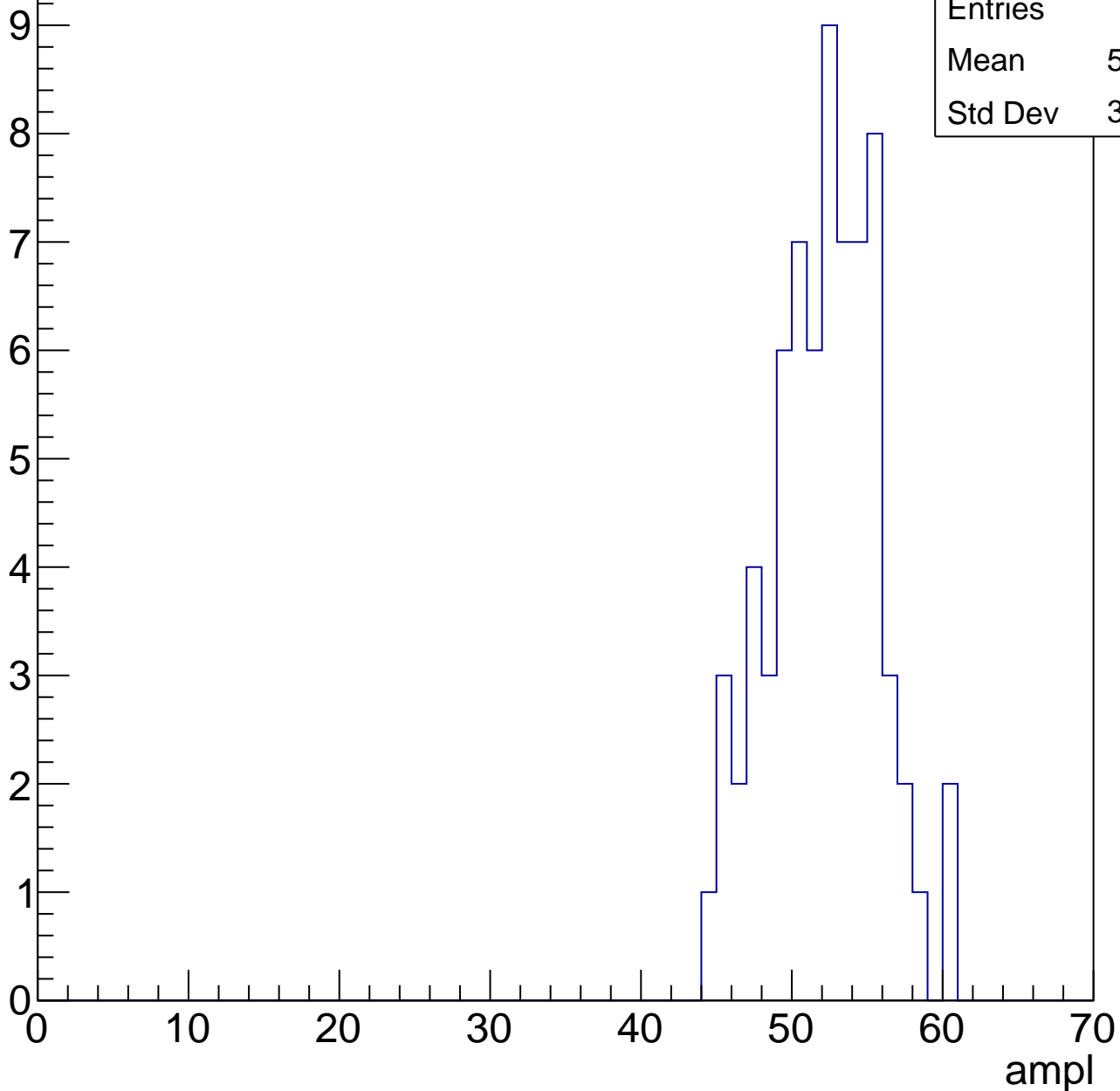


B1L103S, U10-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

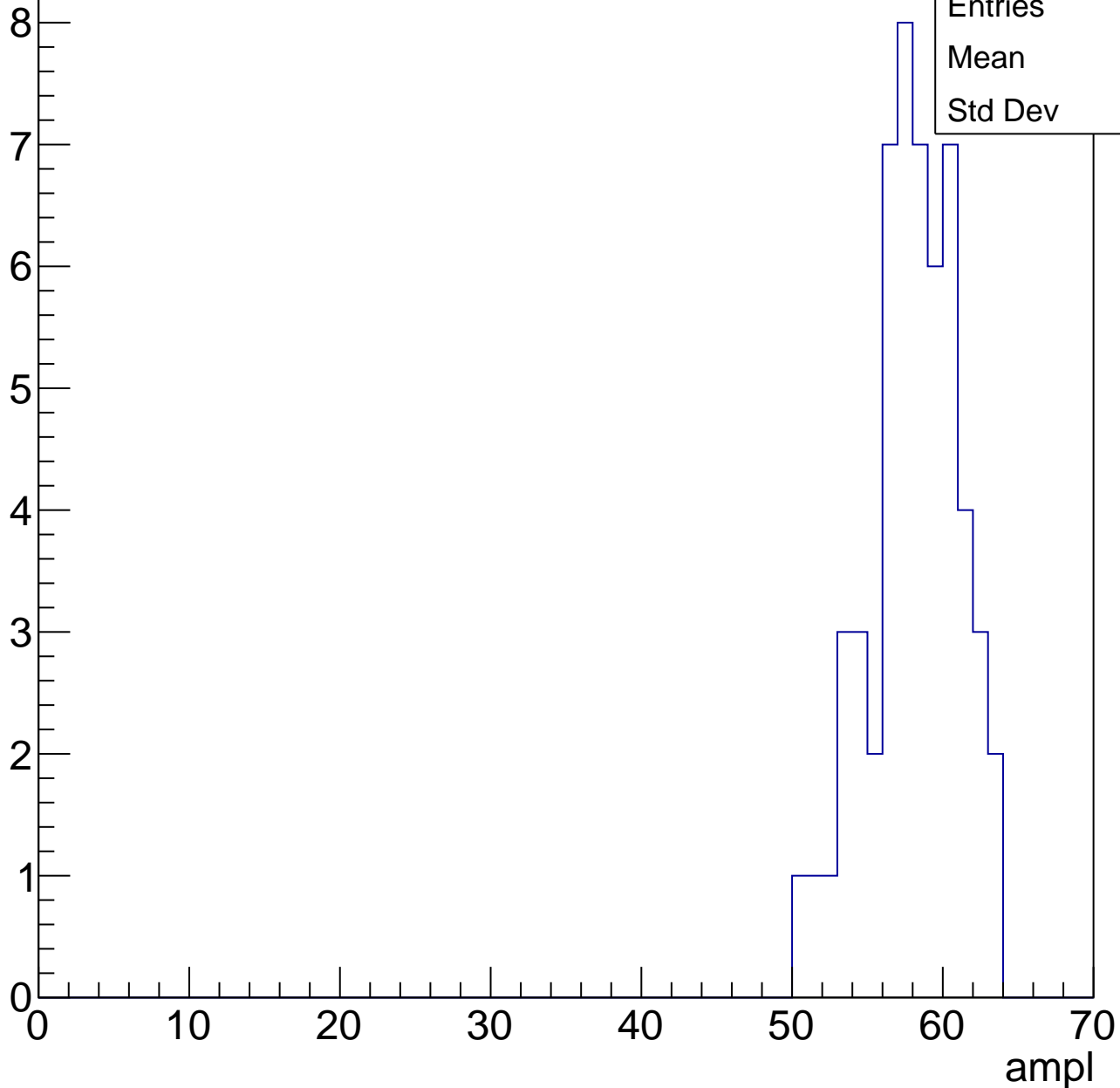
Entries	71
Mean	51.69
Std Dev	3.523



B1L103S, U10-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



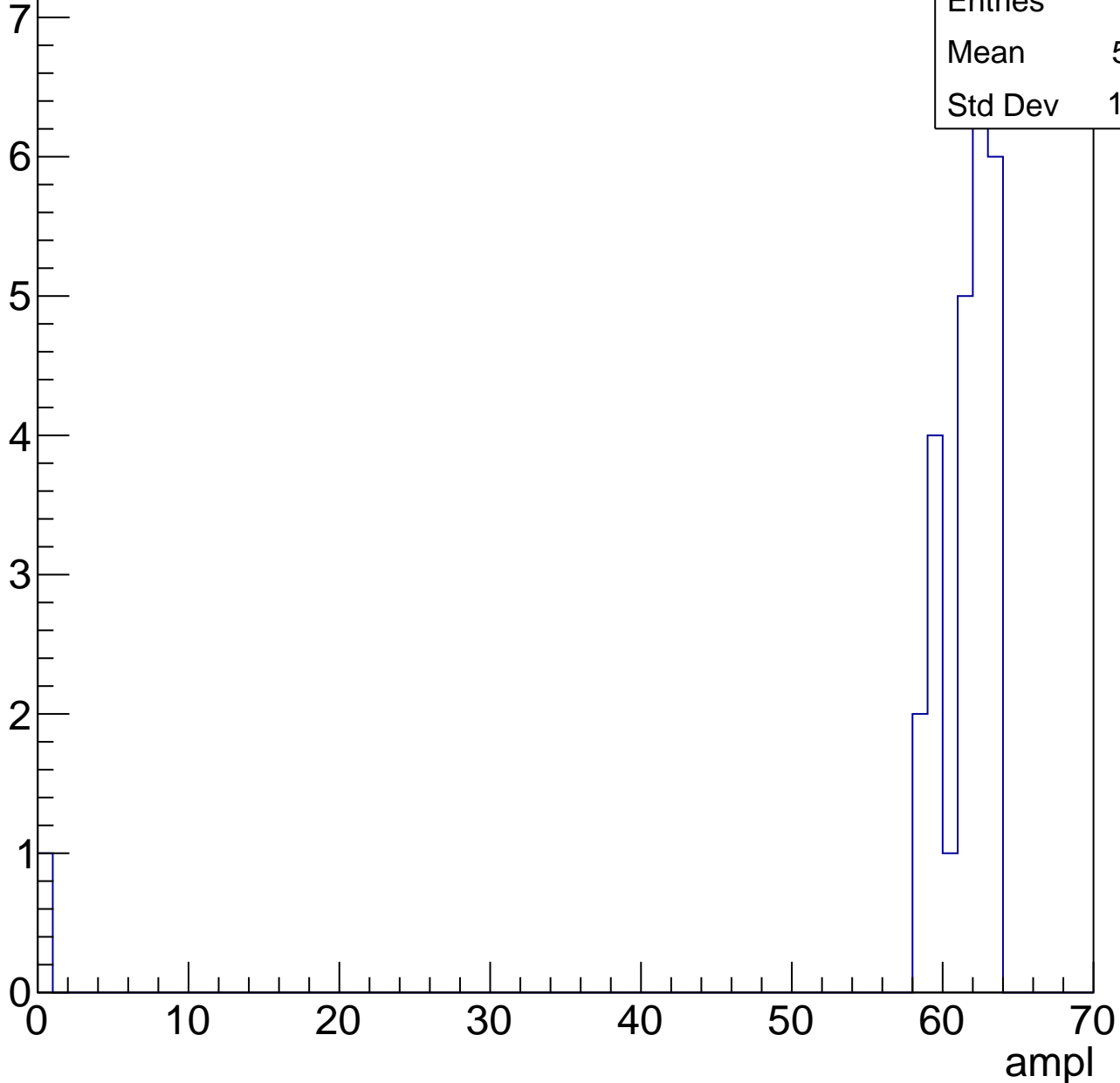
Entries	55
Mean	57.6
Std Dev	2.97

B1L103S, U10-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.81
Std Dev	11.87



B1L103S, U10-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch20, adc0

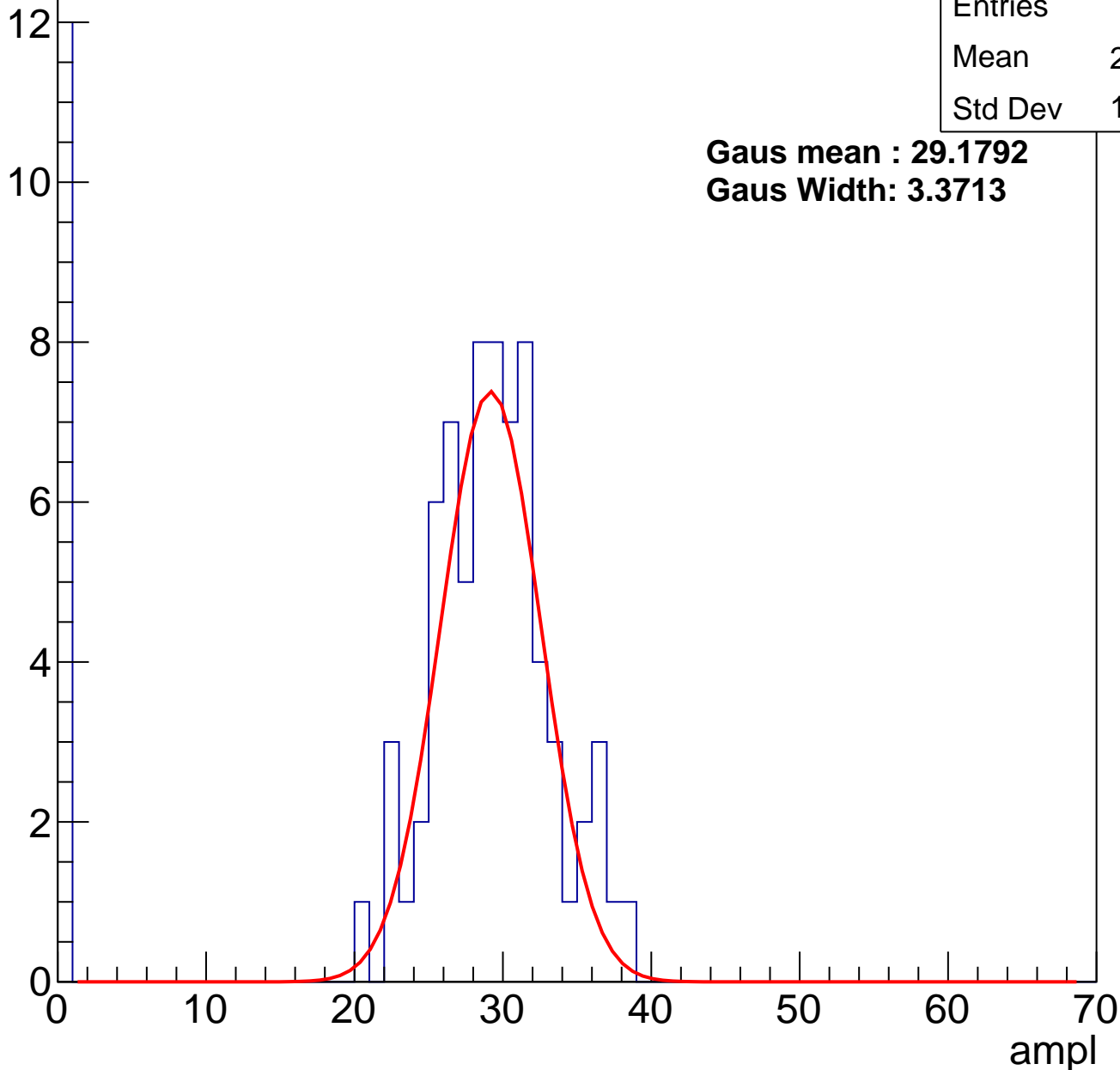
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.72
Std Dev	10.75

Gaus mean : 29.1792

Gaus Width: 3.3713

Entry



B1L103S, U10-ch20, adc1

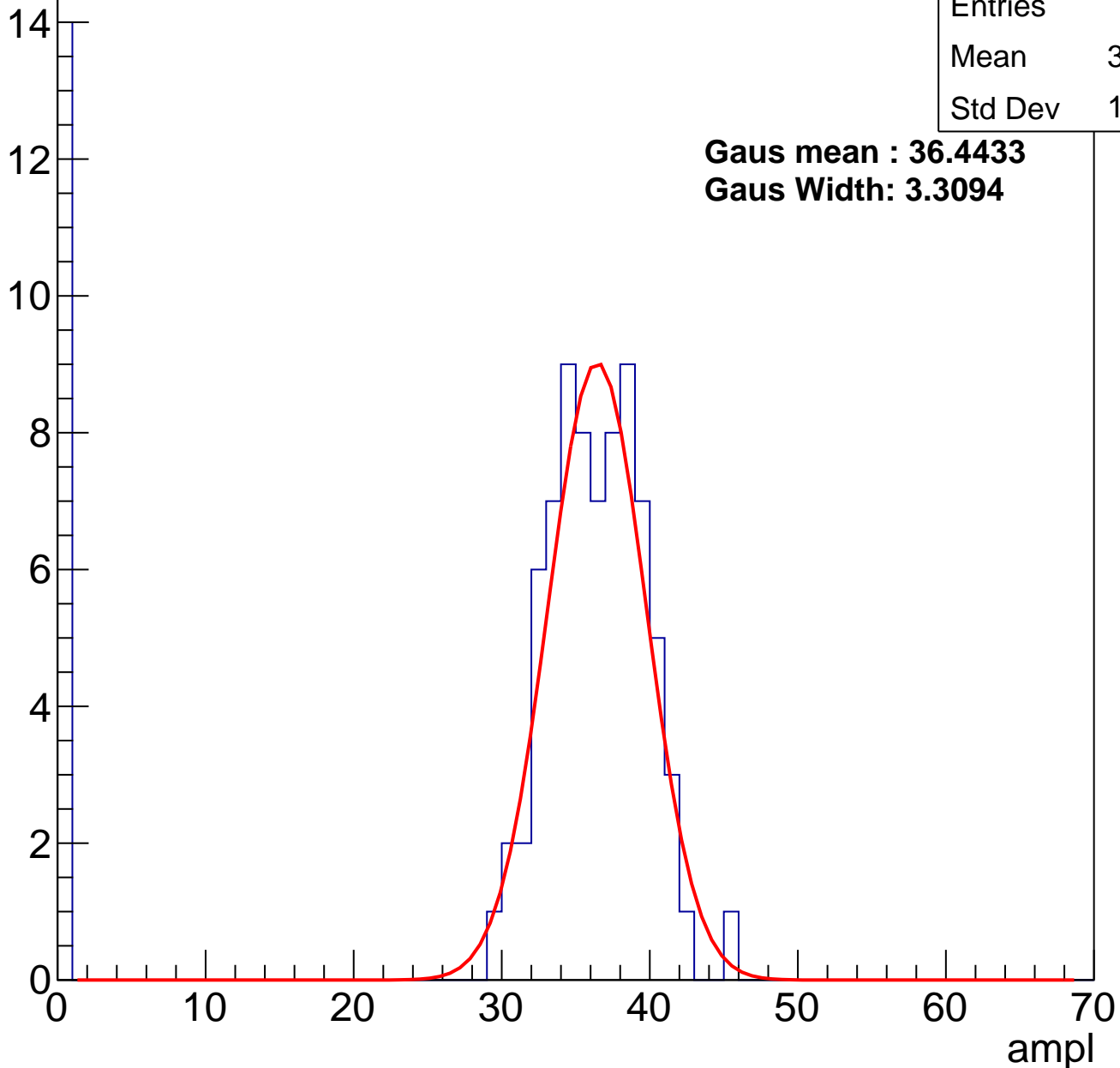
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	30.37
Std Dev	13.35

Gaus mean : 36.4433

Gaus Width: 3.3094

Entry



B1L103S, U10-ch20, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	43.62
Std Dev	6.017

Gaus mean : 44.2952

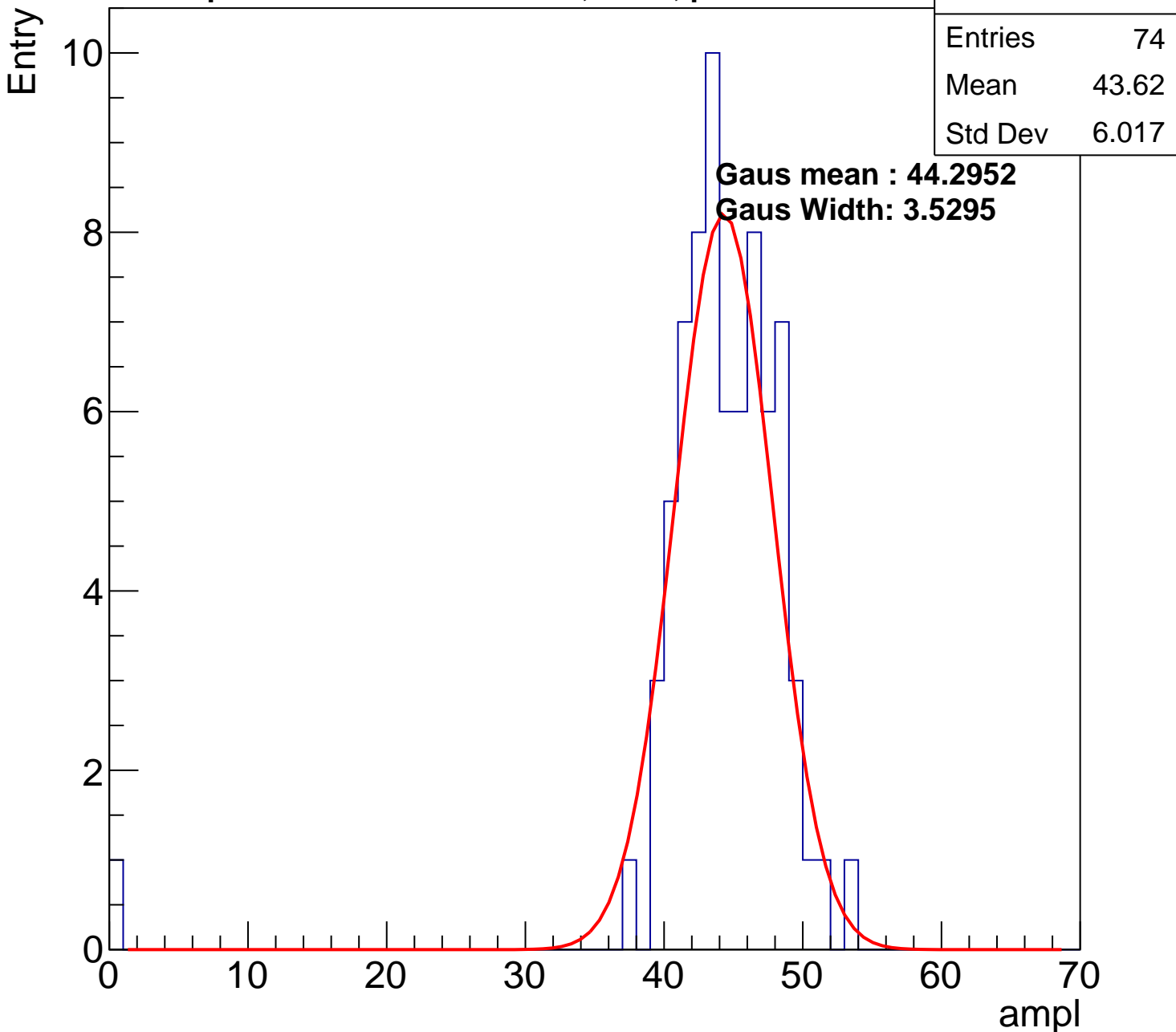
Gaus Width: 3.5295

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

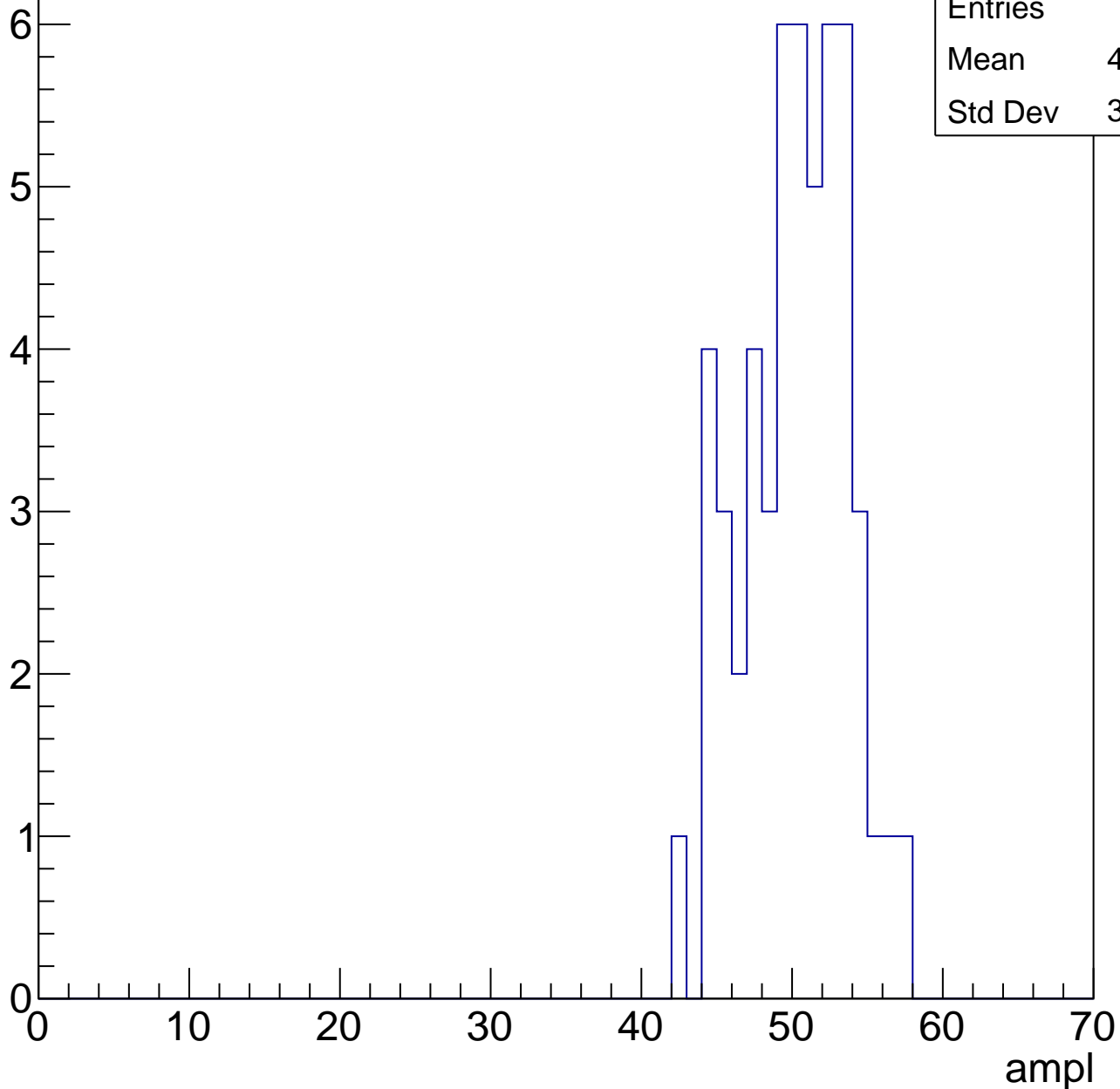


B1L103S, U10-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	49.73
Std Dev	3.414

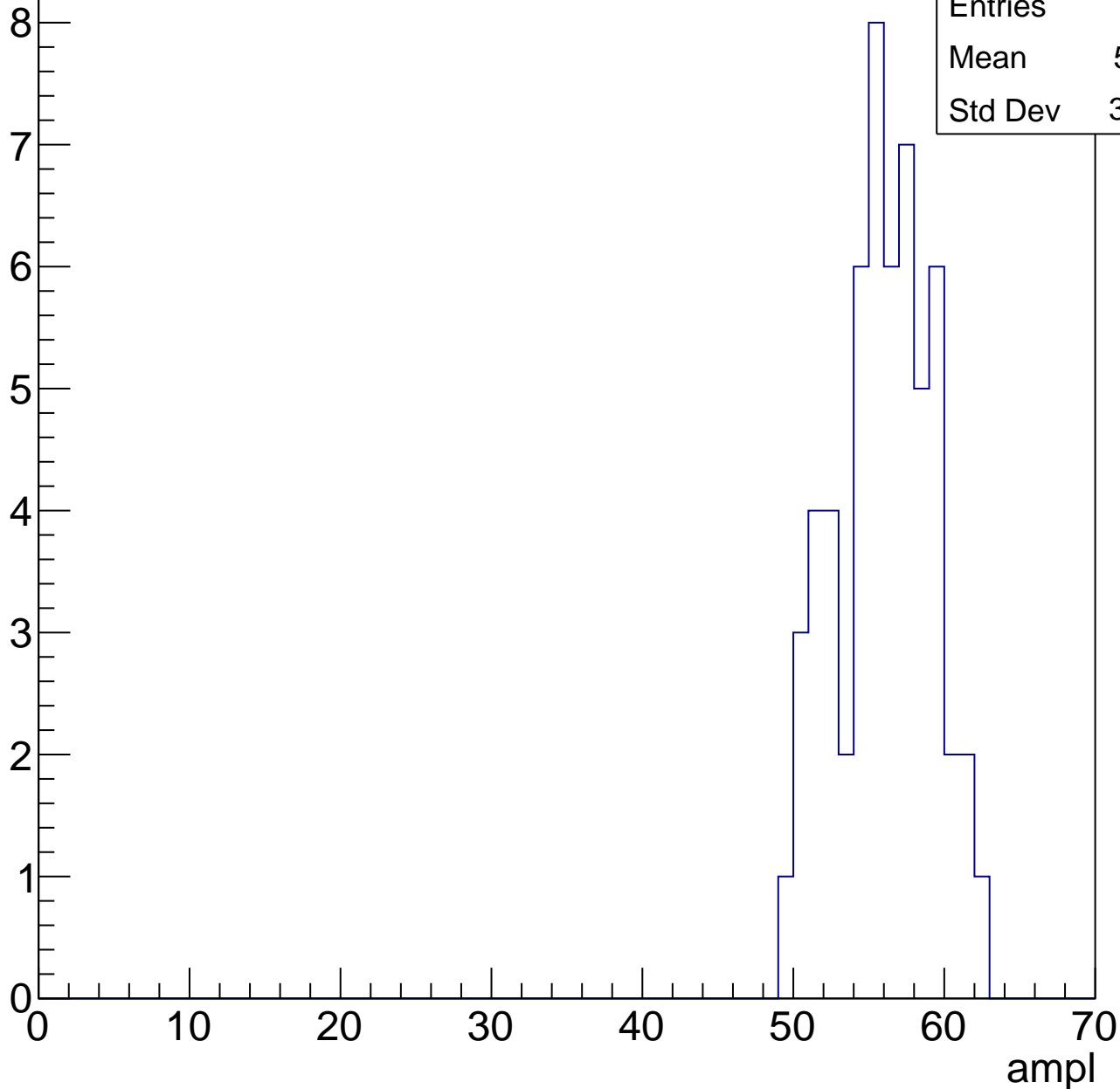


B1L103S, U10-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

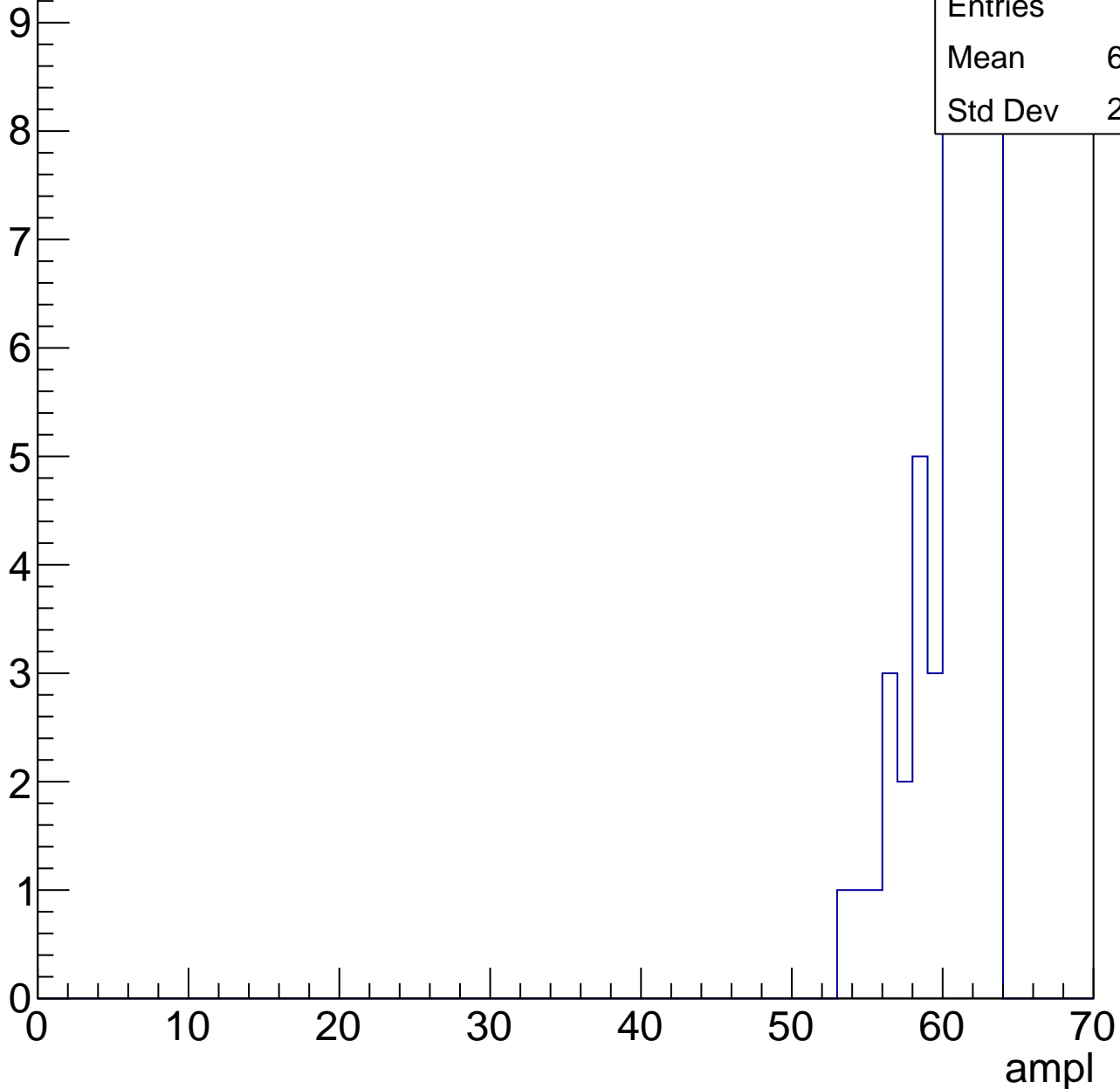
Entries	57
Mean	55.51
Std Dev	3.124



B1L103S, U10-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

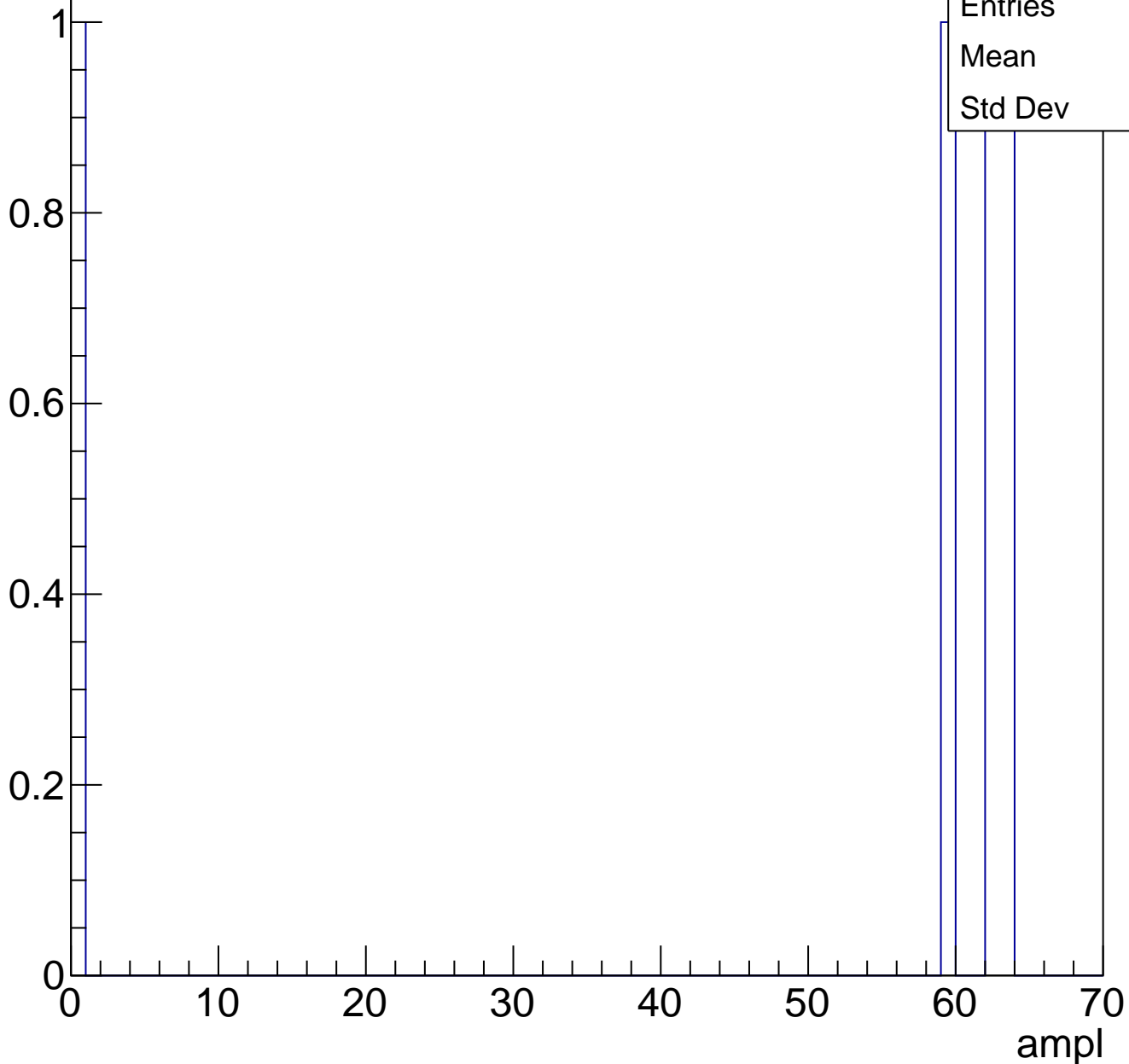
Entry



B1L103S, U10-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

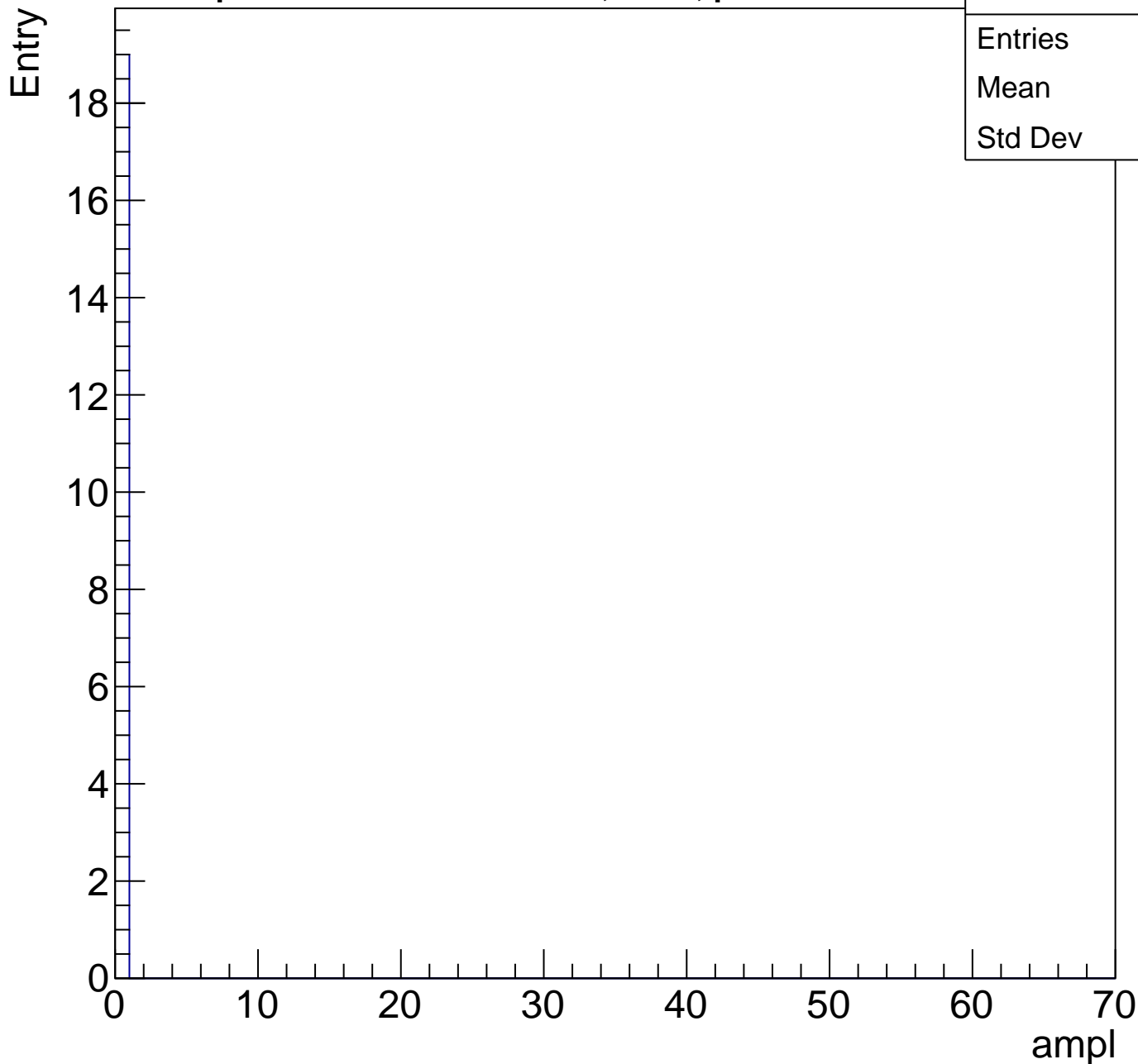
Entry



B1L103S, U10-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U10-ch21, adc0

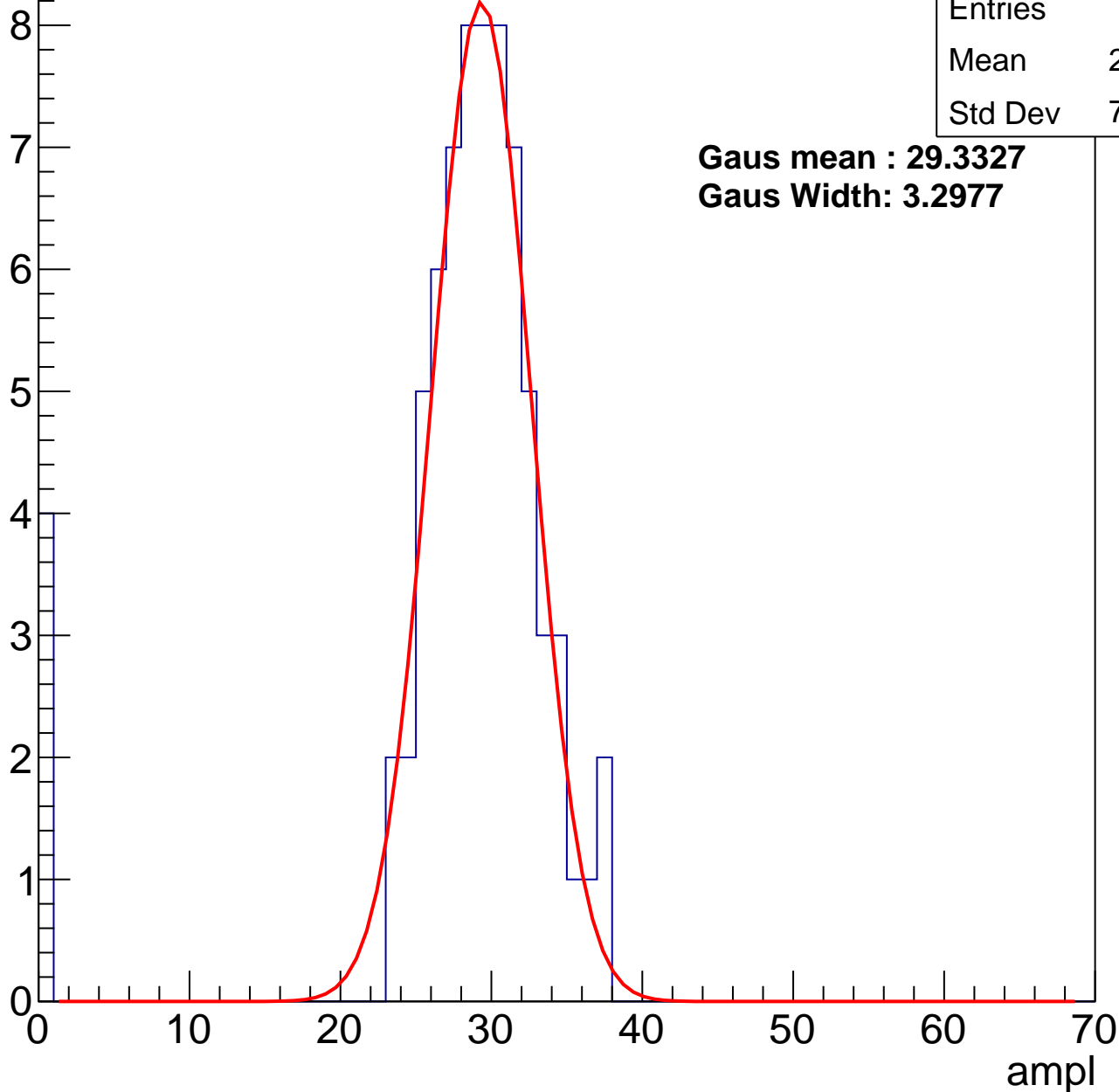
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	27.54
Std Dev	7.379

Gaus mean : 29.3327

Gaus Width: 3.2977



B1L103S, U10-ch21, adc1

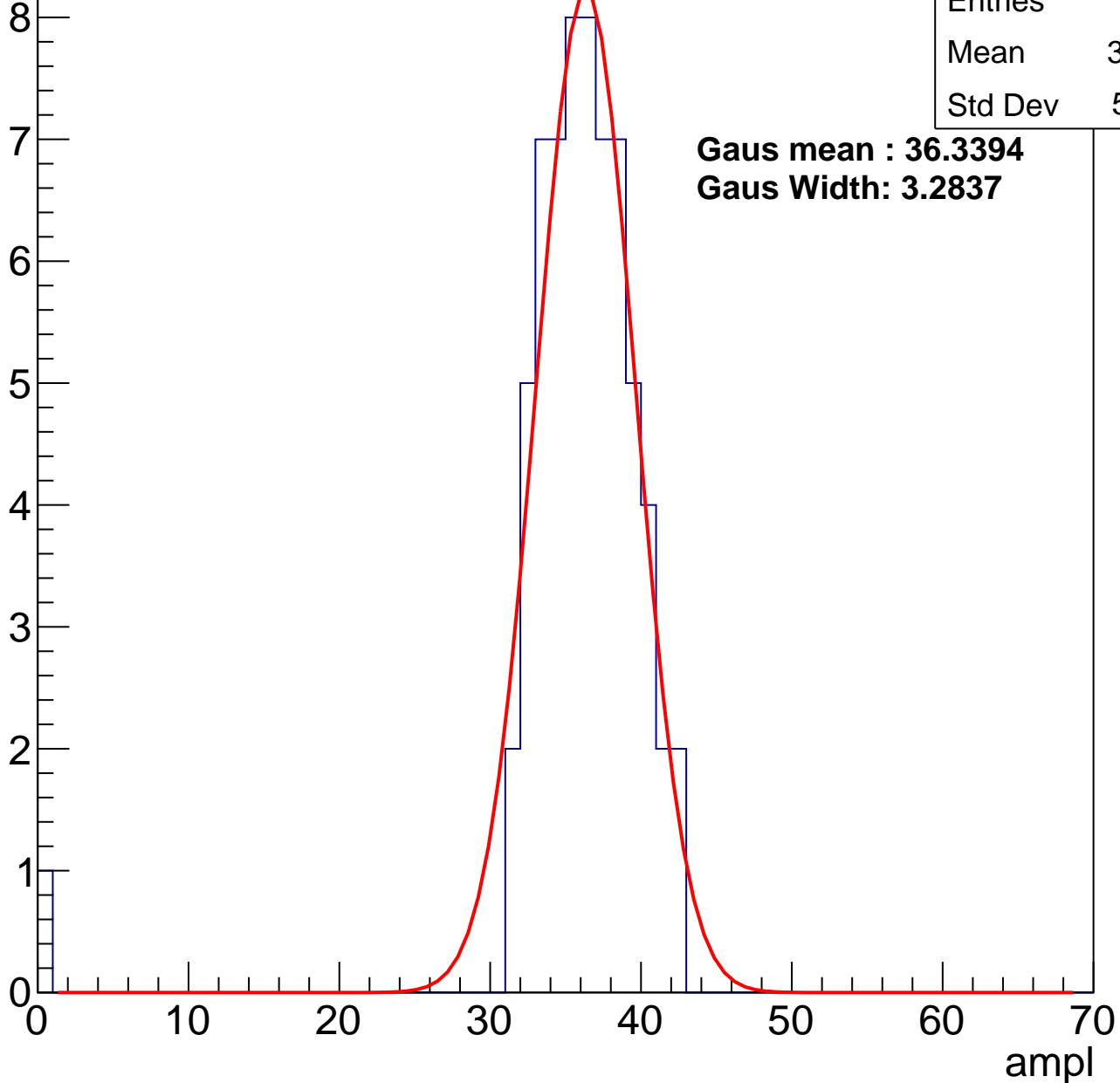
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.46
Std Dev	5.221

Gaus mean : 36.3394

Gaus Width: 3.2837



B1L103S, U10-ch21, adc2

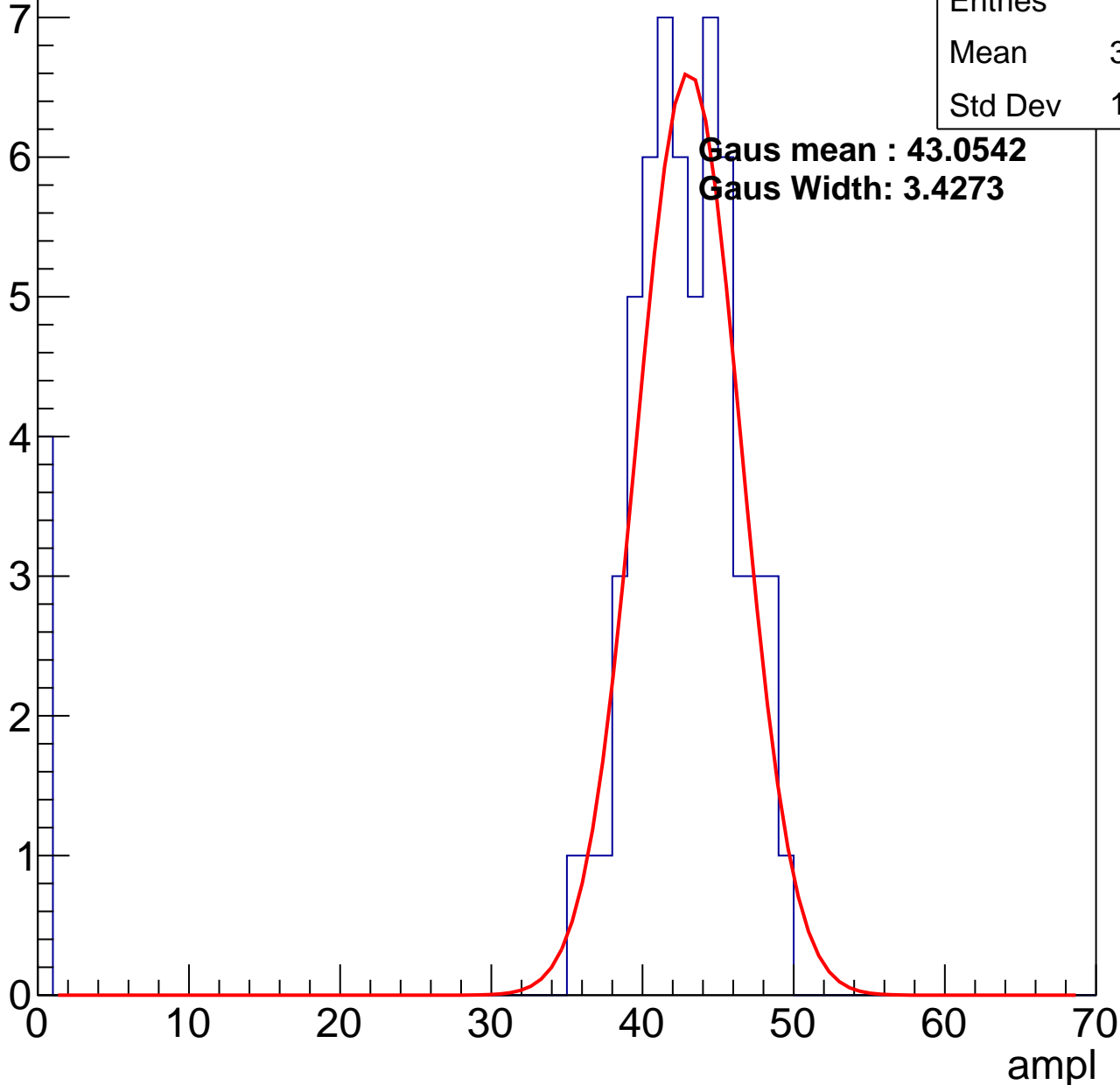
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.69
Std Dev	10.87

Gaus mean : 43.0542

Gaus Width: 3.4273



B1L103S, U10-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

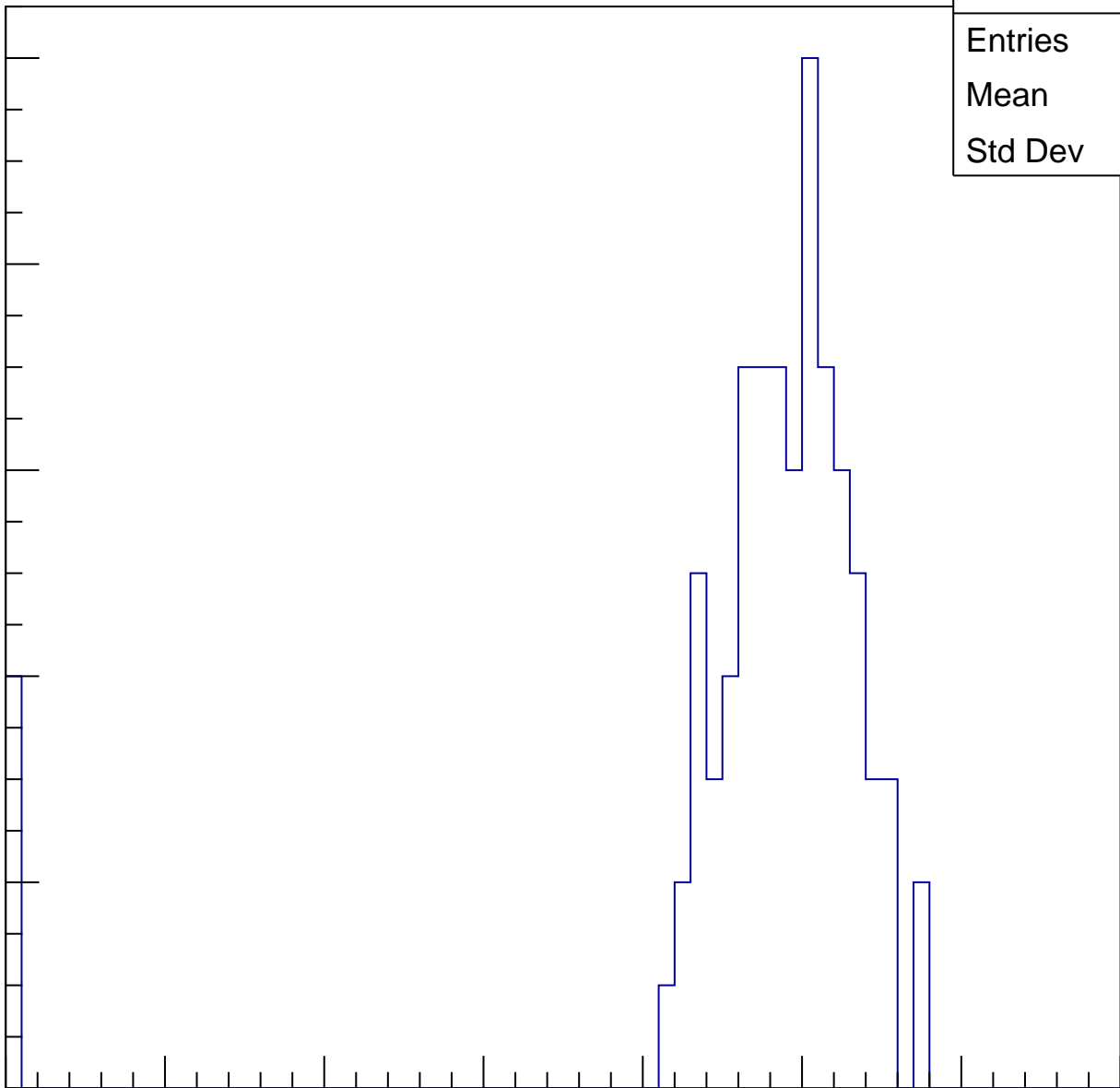
Entries	82
Mean	46.44
Std Dev	11.11

Entry

10
8
6
4
2
0

ampl

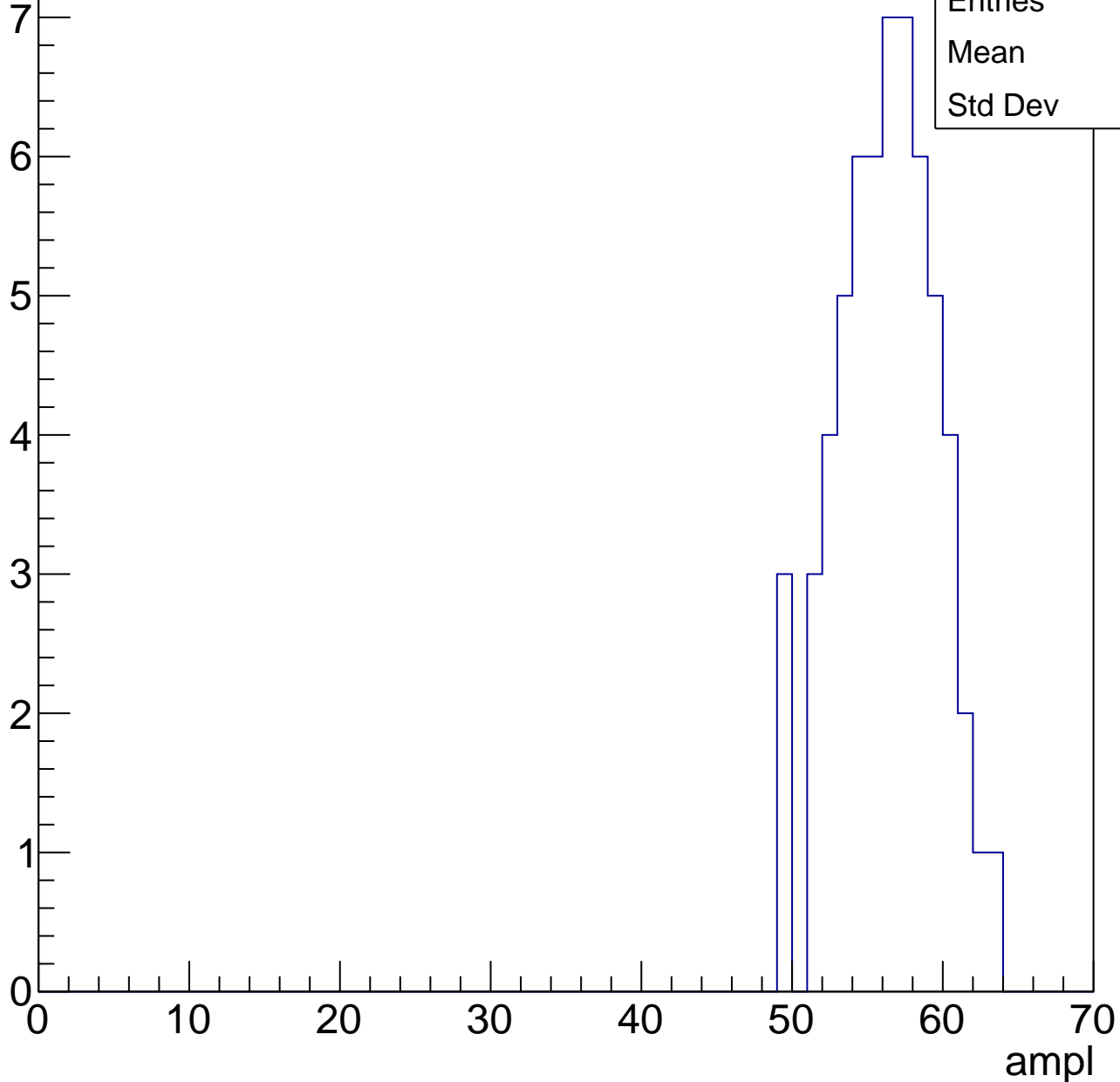
0 10 20 30 40 50 60 70



B1L103S, U10-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

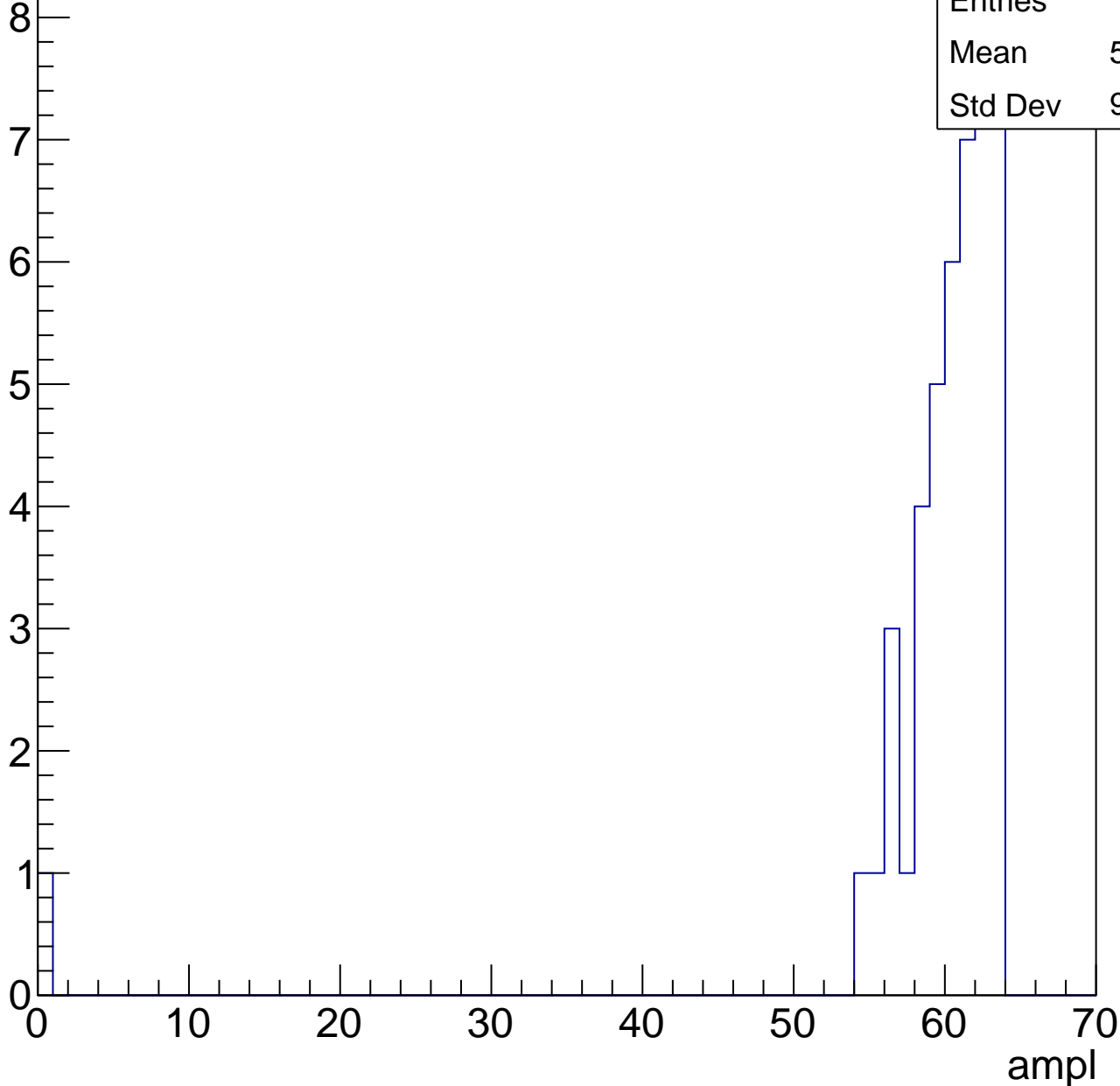


B1L103S, U10-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.84
Std Dev	9.177



B1L103S, U10-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch22, adc0

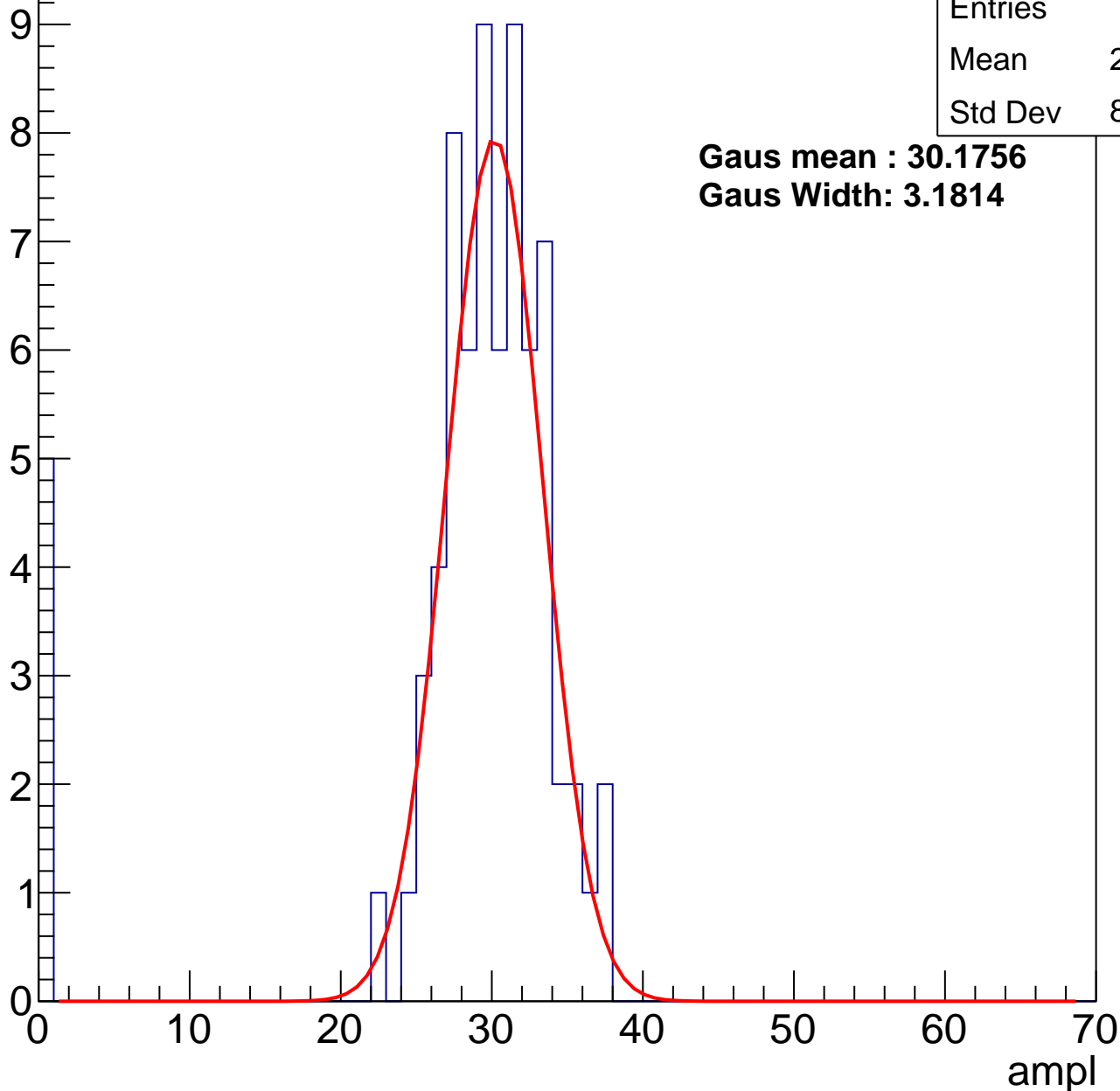
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	27.78
Std Dev	8.165

Gaus mean : 30.1756

Gaus Width: 3.1814



B1L103S, U10-ch22, adc1

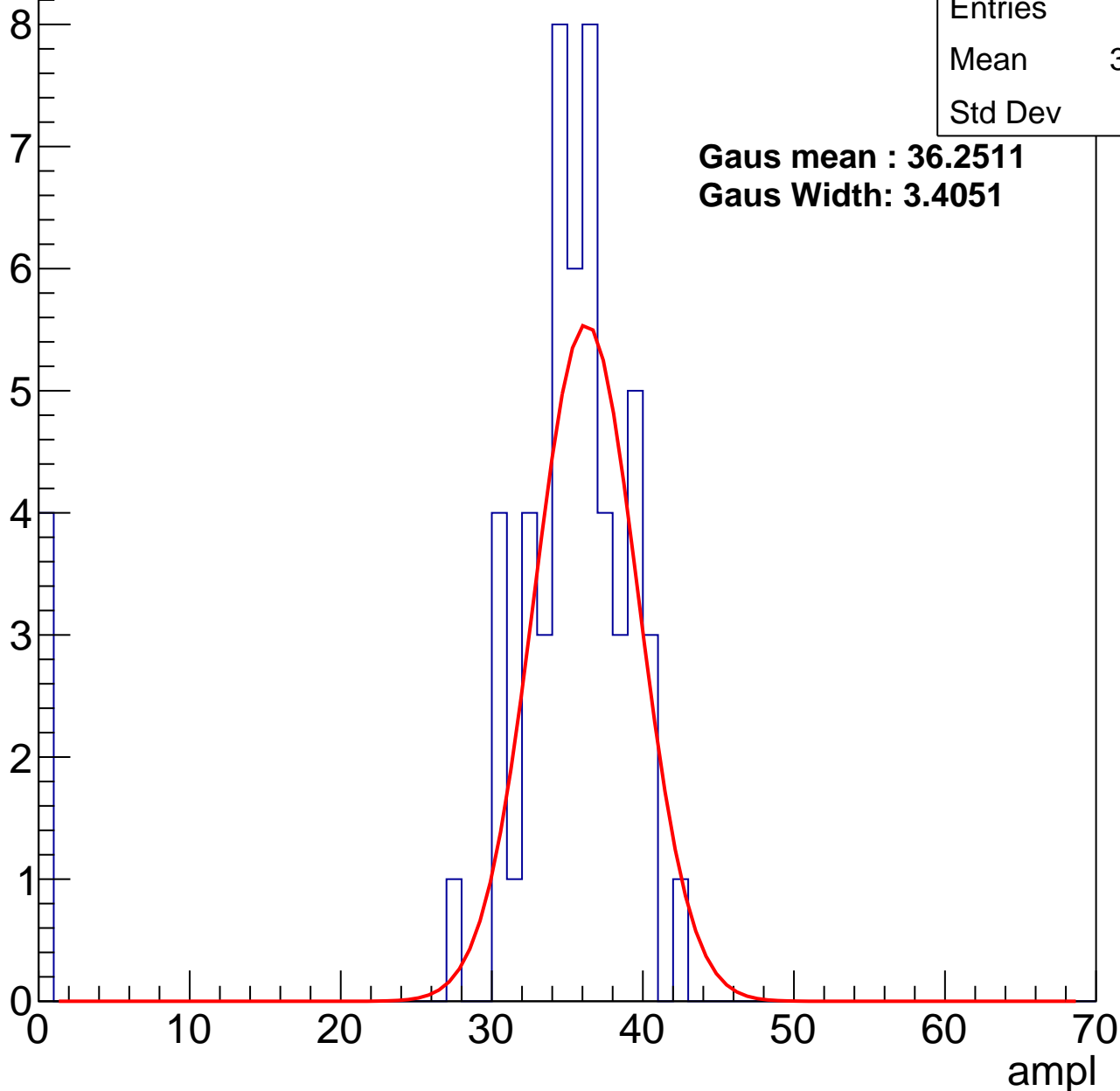
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	32.62
Std Dev	9.61

Gaus mean : 36.2511

Gaus Width: 3.4051



B1L103S, U10-ch22, adc2

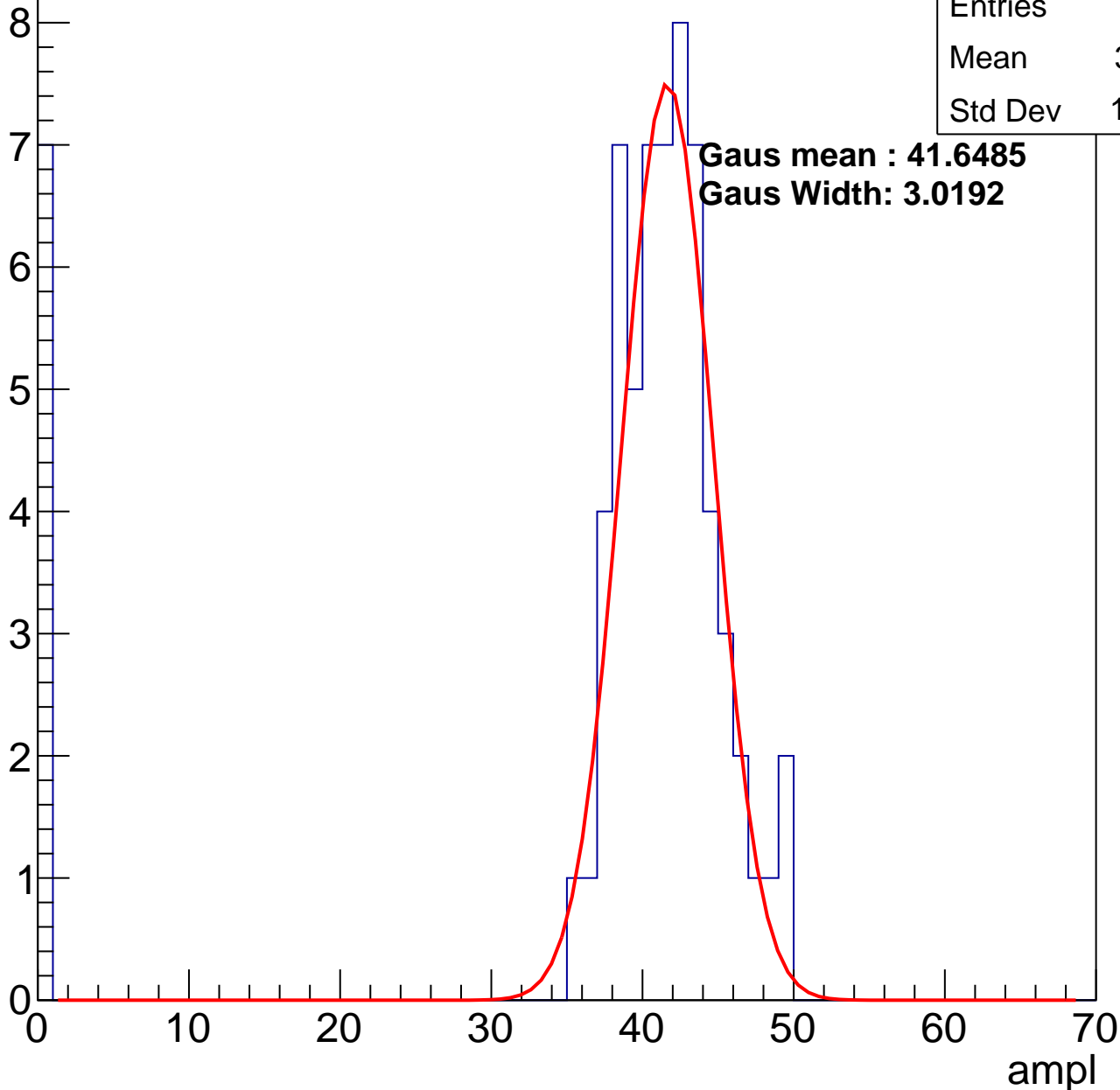
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.01
Std Dev	12.99

Gaus mean : 41.6485

Gaus Width: 3.0192

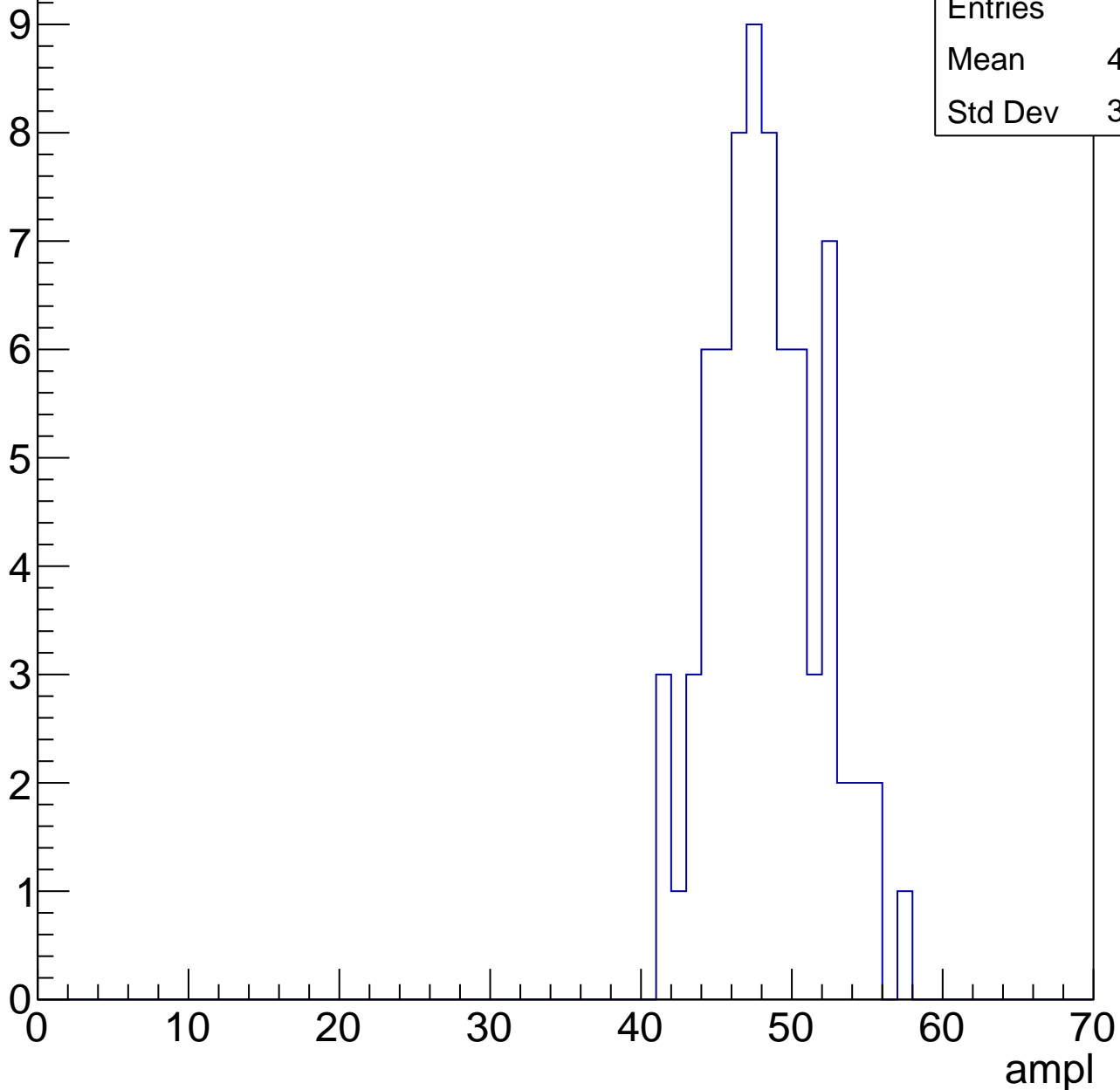


B1L103S, U10-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	47.88
Std Dev	3.546

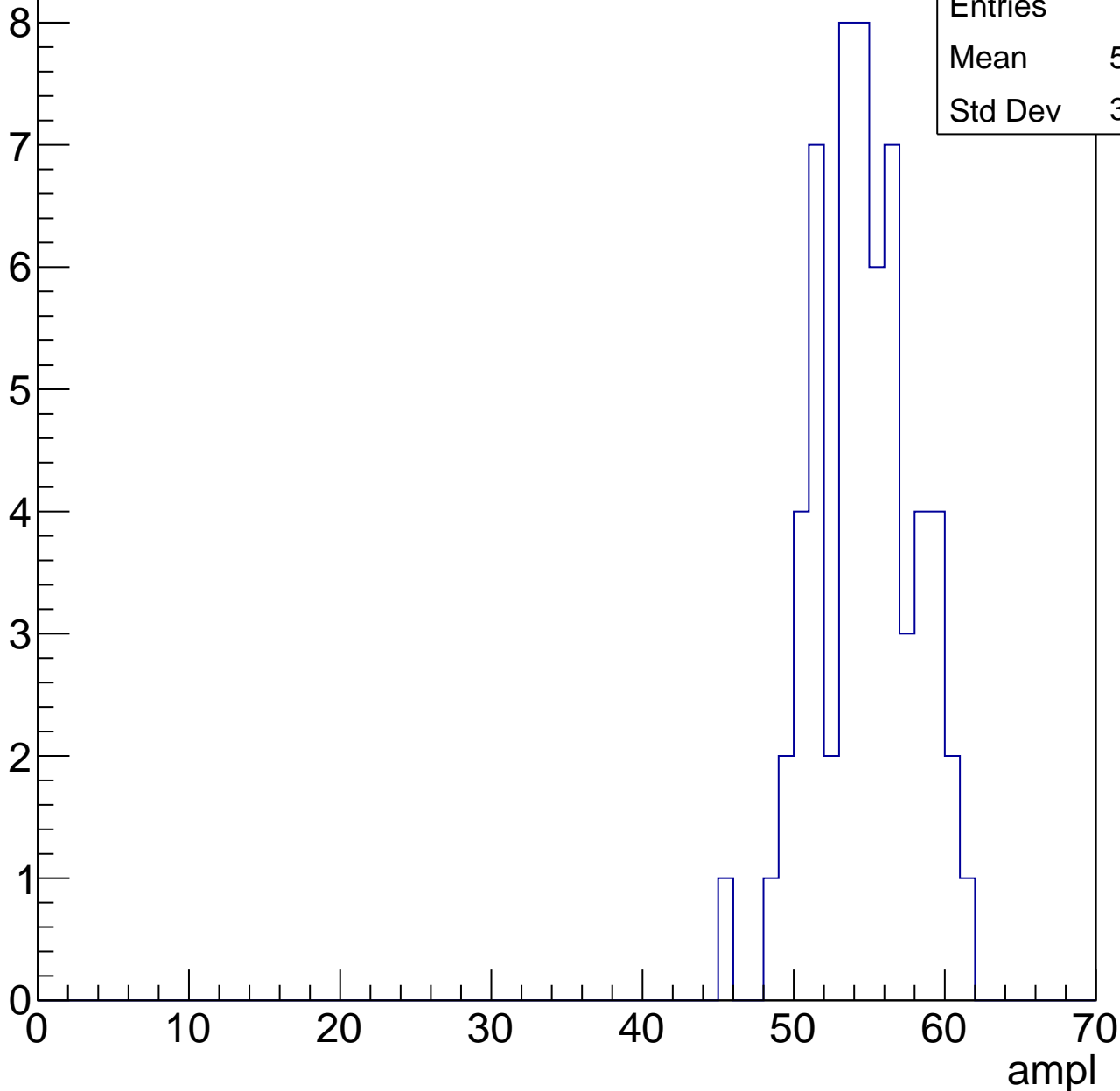


B1L103S, U10-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.17
Std Dev	3.302

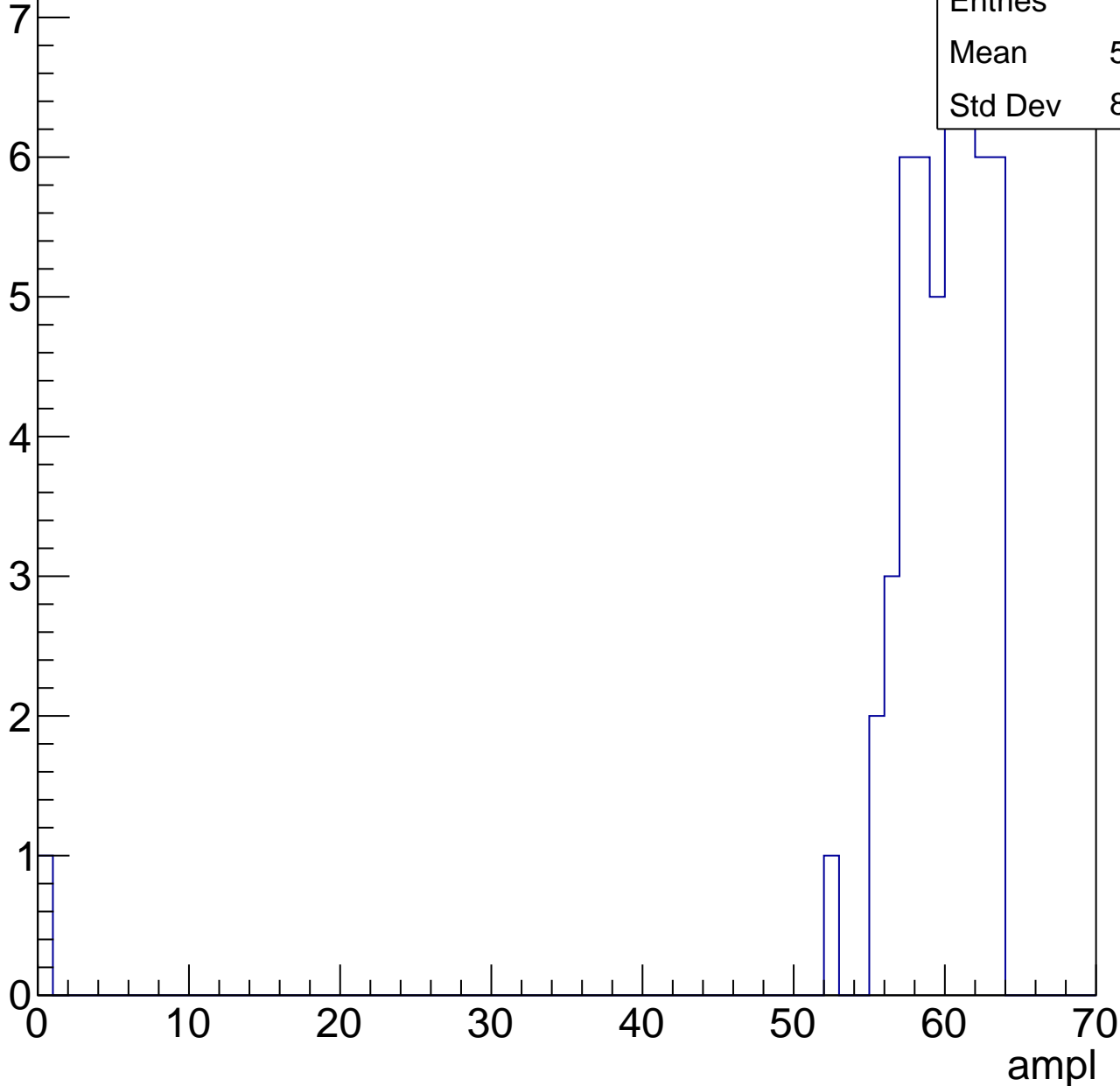


B1L103S, U10-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

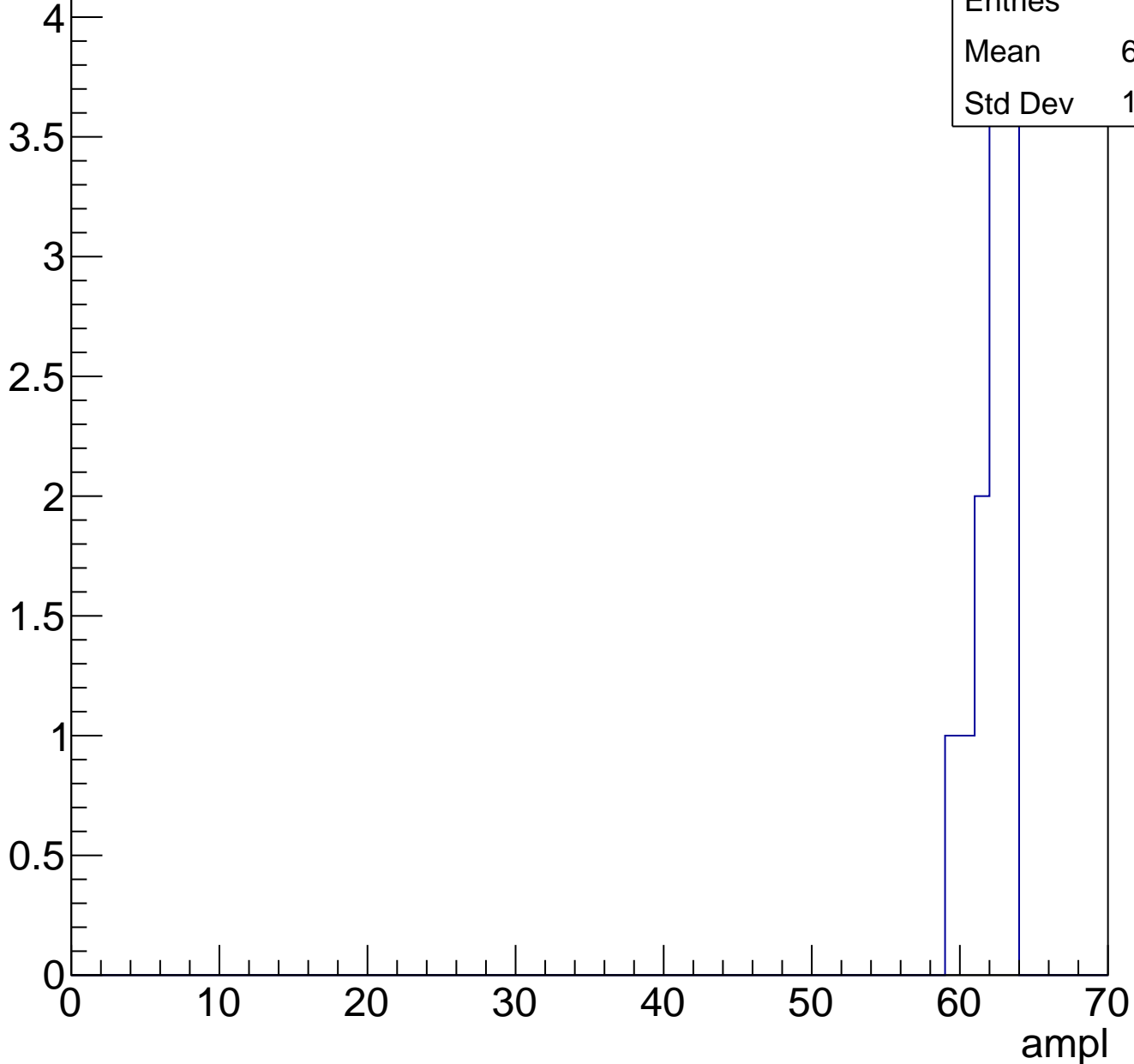
Entries	50
Mean	58.24
Std Dev	8.689



B1L103S, U10-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

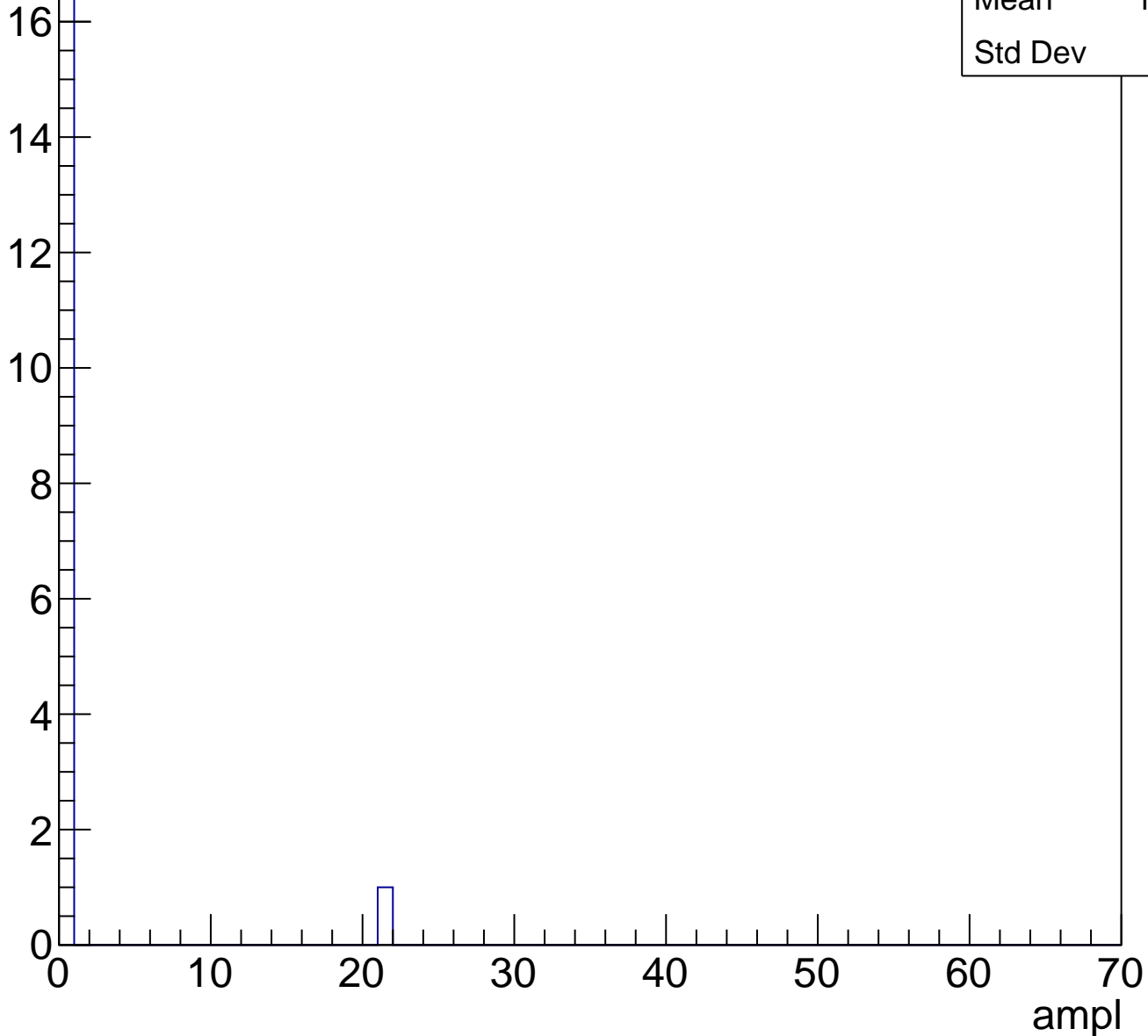


B1L103S, U10-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.167
Std Dev	4.81

Entry



B1L103S, U10-ch23, adc0

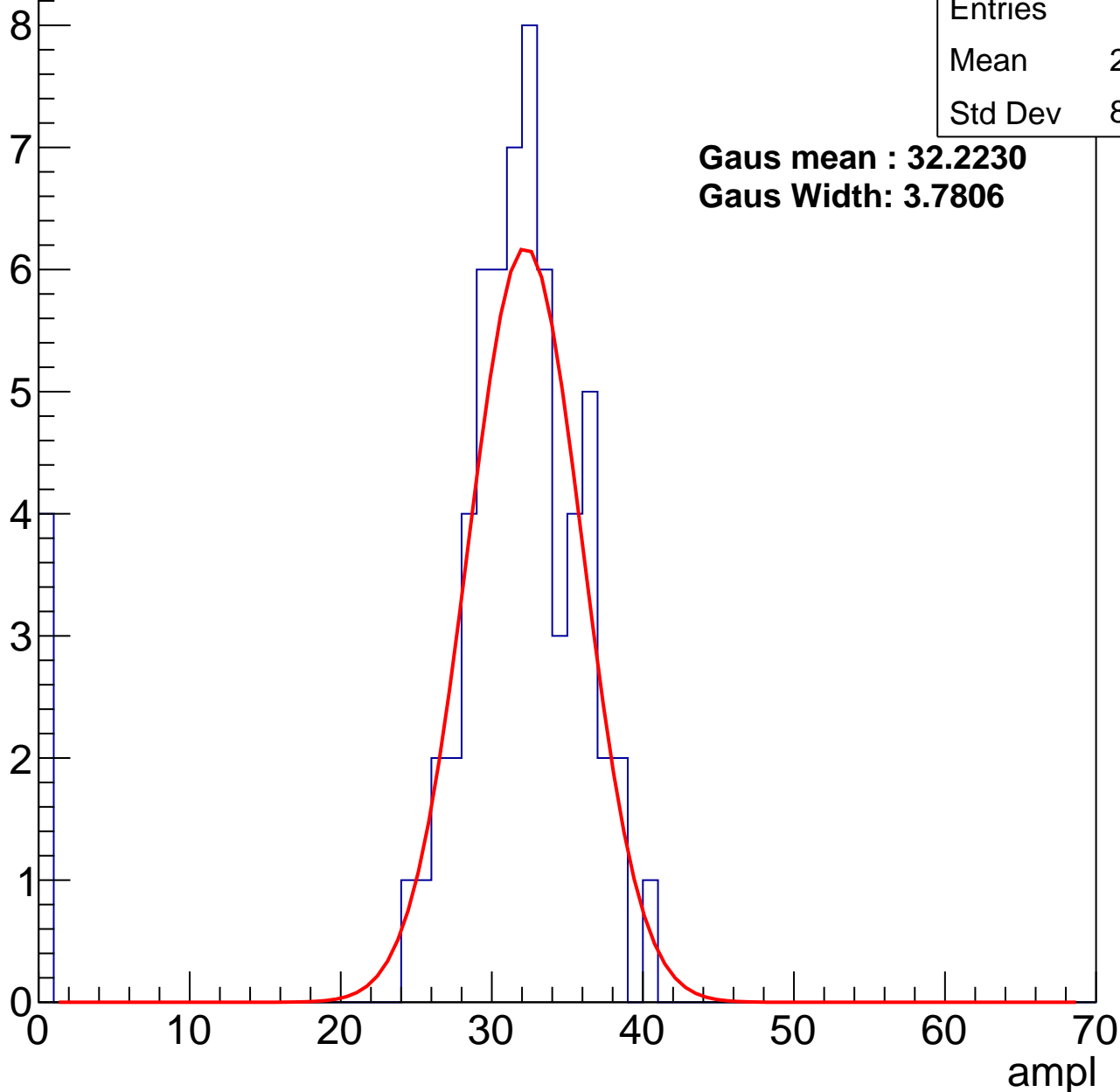
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	29.75
Std Dev	8.363

Gaus mean : 32.2230

Gaus Width: 3.7806



B1L103S, U10-ch23, adc1

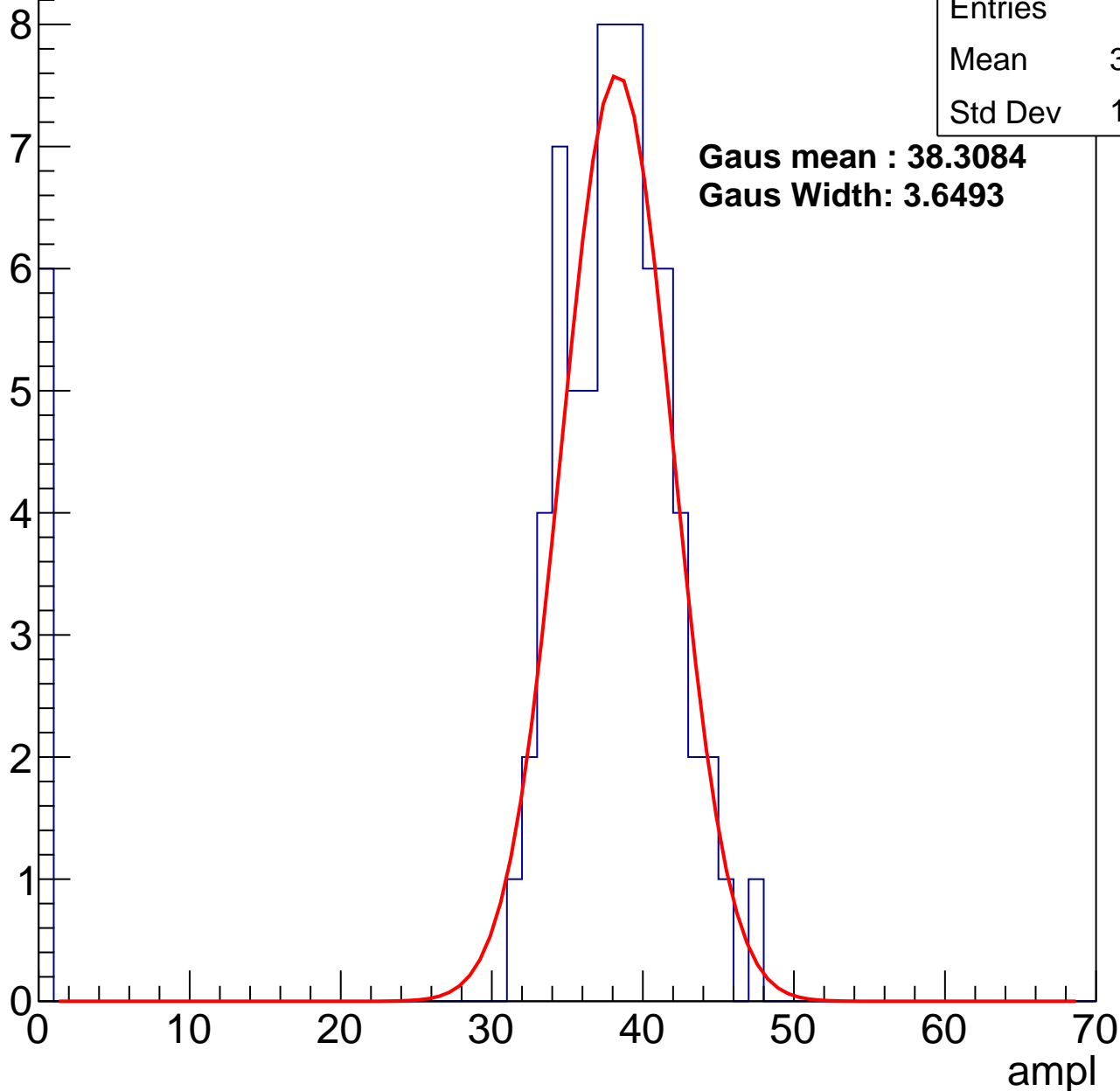
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.89
Std Dev	10.72

Gaus mean : 38.3084

Gaus Width: 3.6493



B1L103S, U10-ch23, adc2

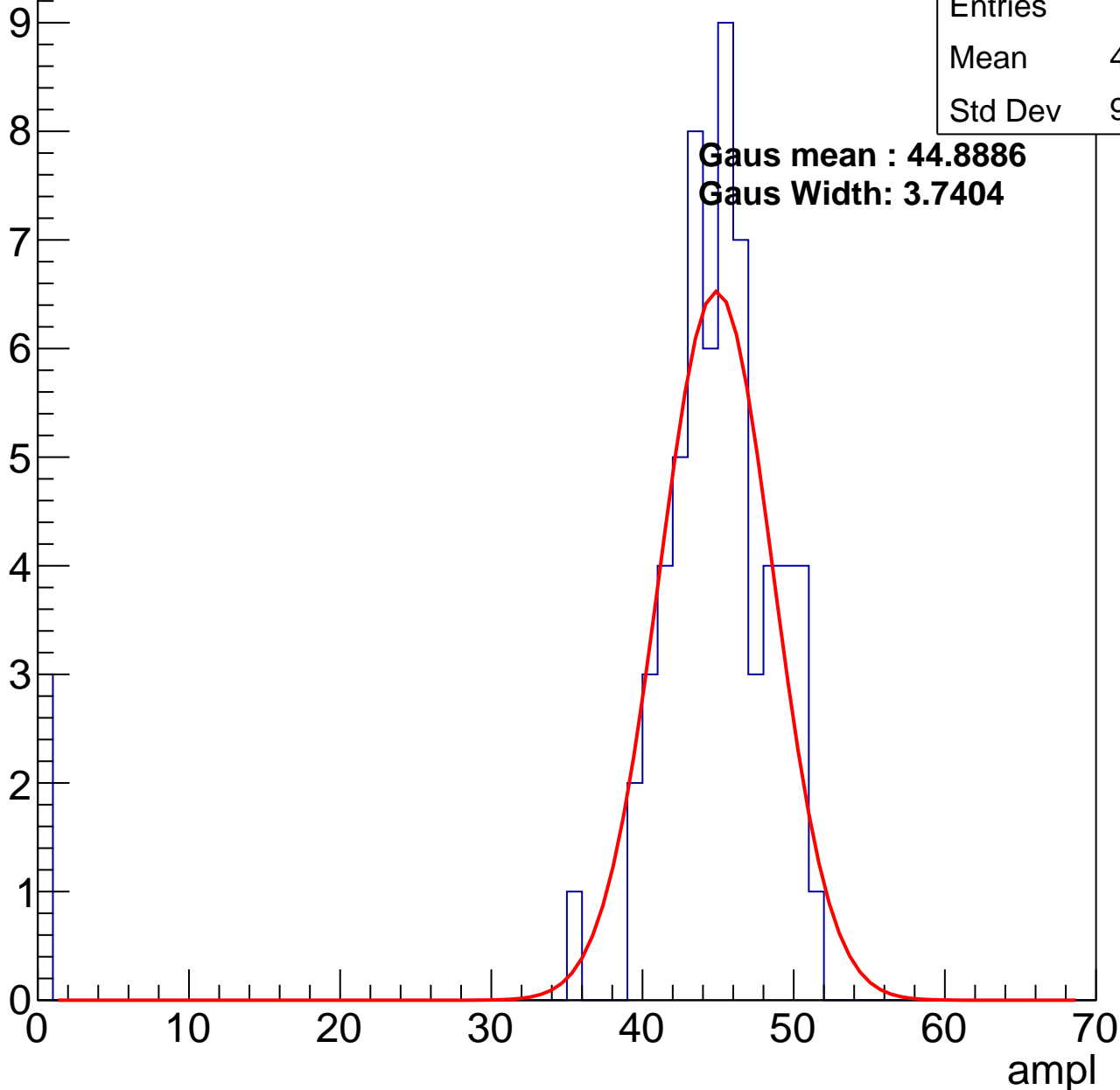
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.53
Std Dev	9.948

Gaus mean : 44.8886

Gaus Width: 3.7404

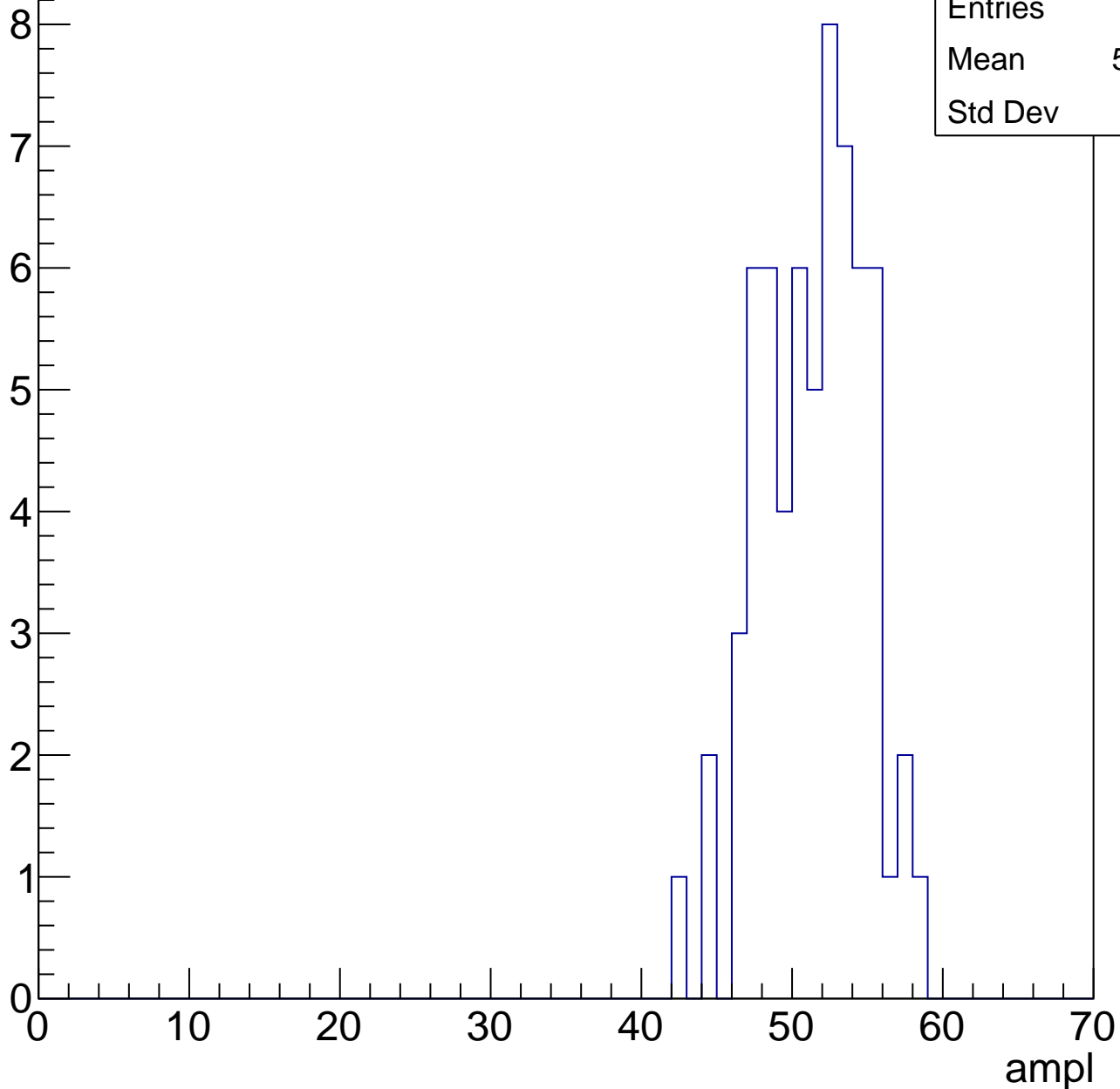


B1L103S, U10-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

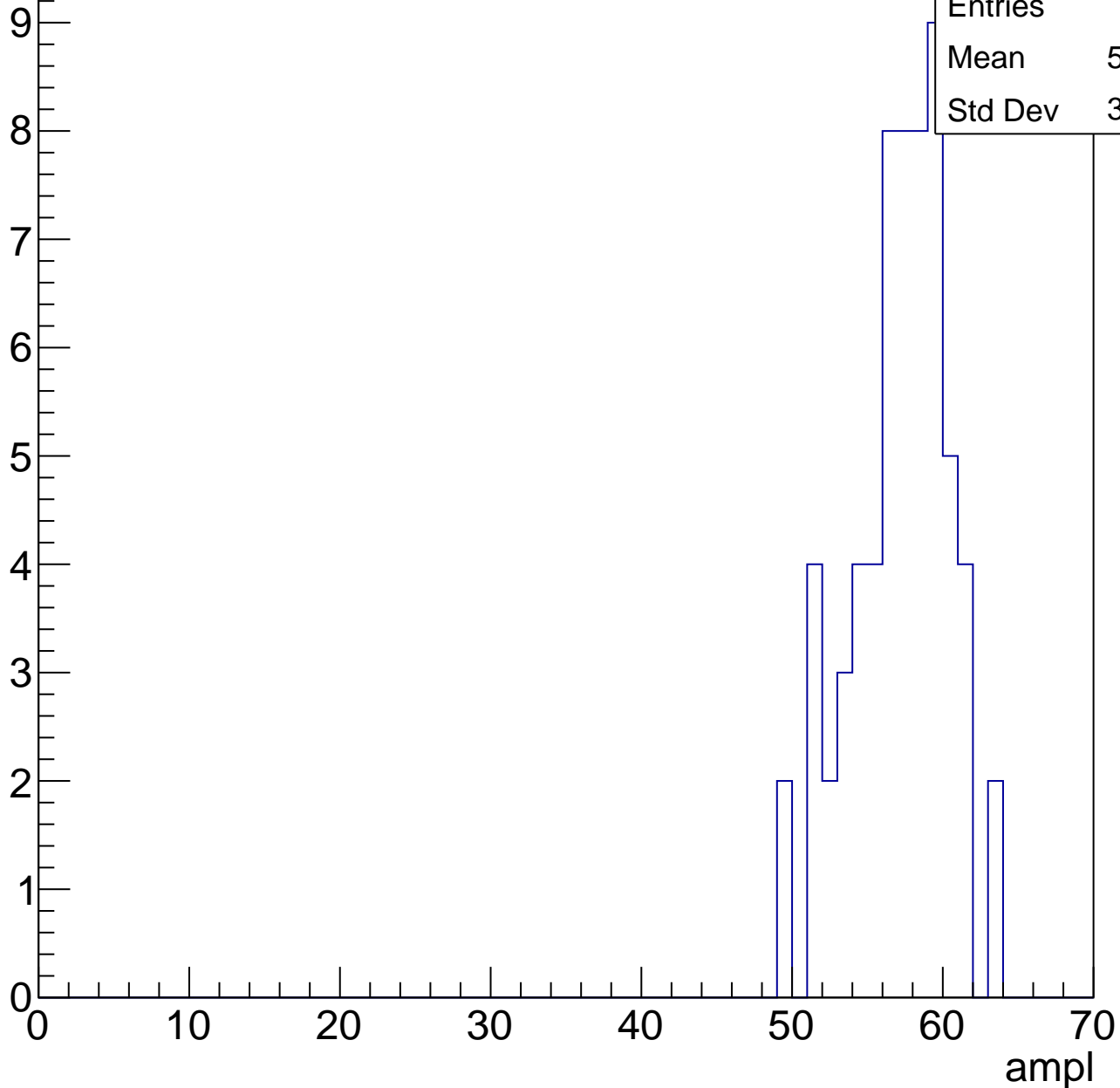
Entries	64
Mean	50.91
Std Dev	3.44



B1L103S, U10-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	31
Mean	59.32
Std Dev	10.95

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

B1L103S, U10-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U10-ch24, adc0

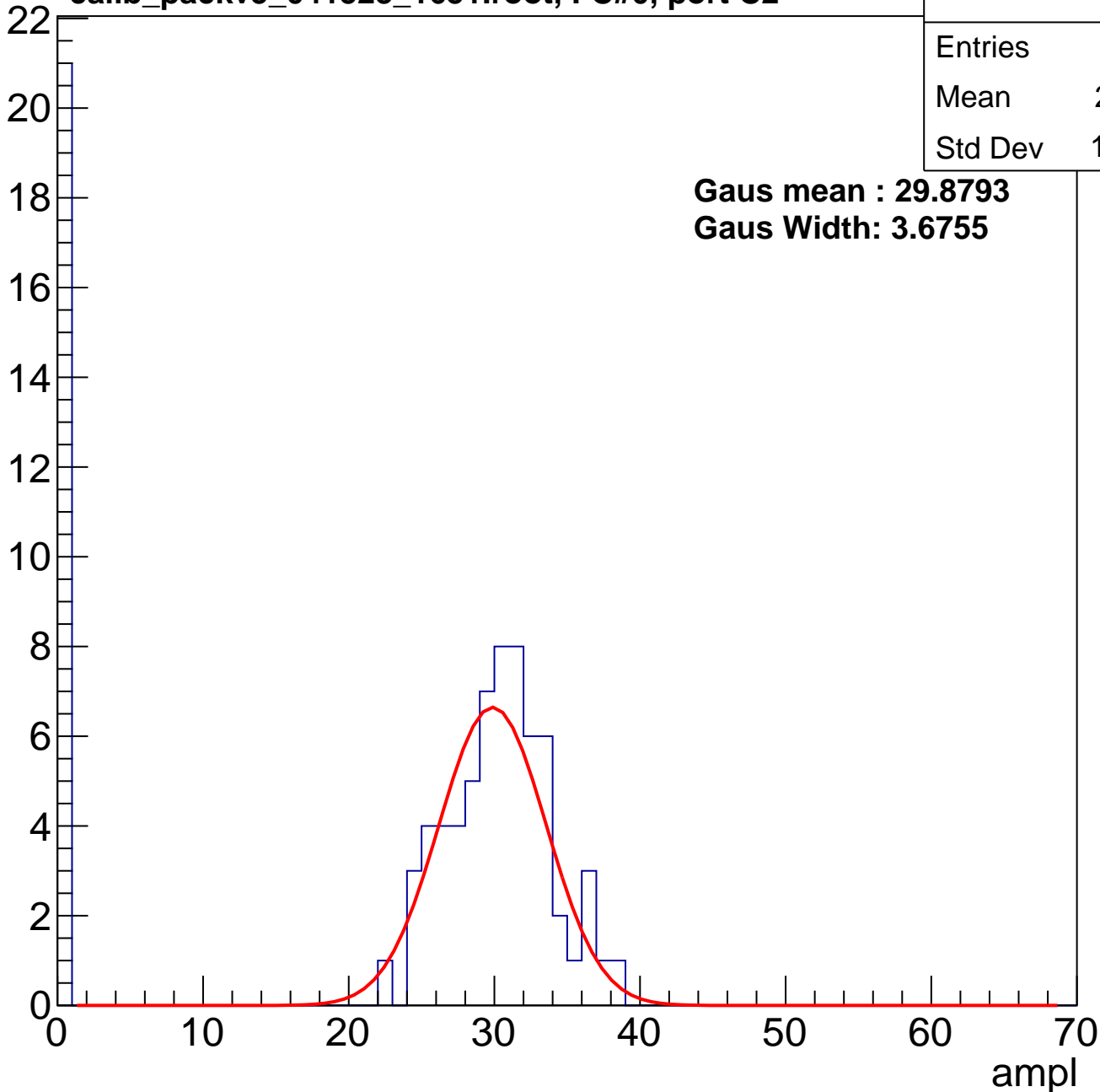
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.51
Std Dev	13.24

Gaus mean : 29.8793

Gaus Width: 3.6755

Entry



B1L103S, U10-ch24, adc1

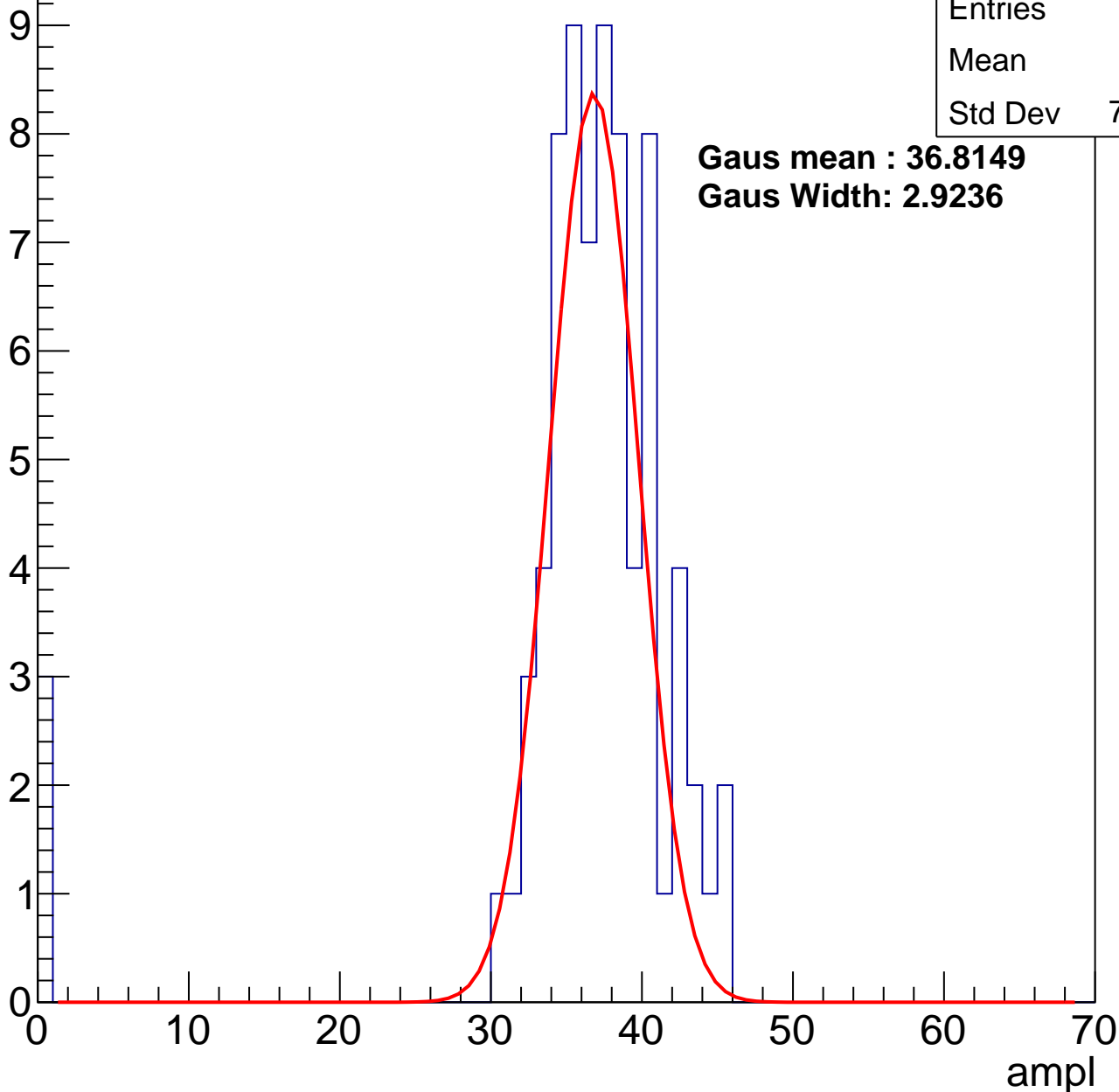
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.6
Std Dev	7.967

Gaus mean : 36.8149

Gaus Width: 2.9236



B1L103S, U10-ch24, adc2

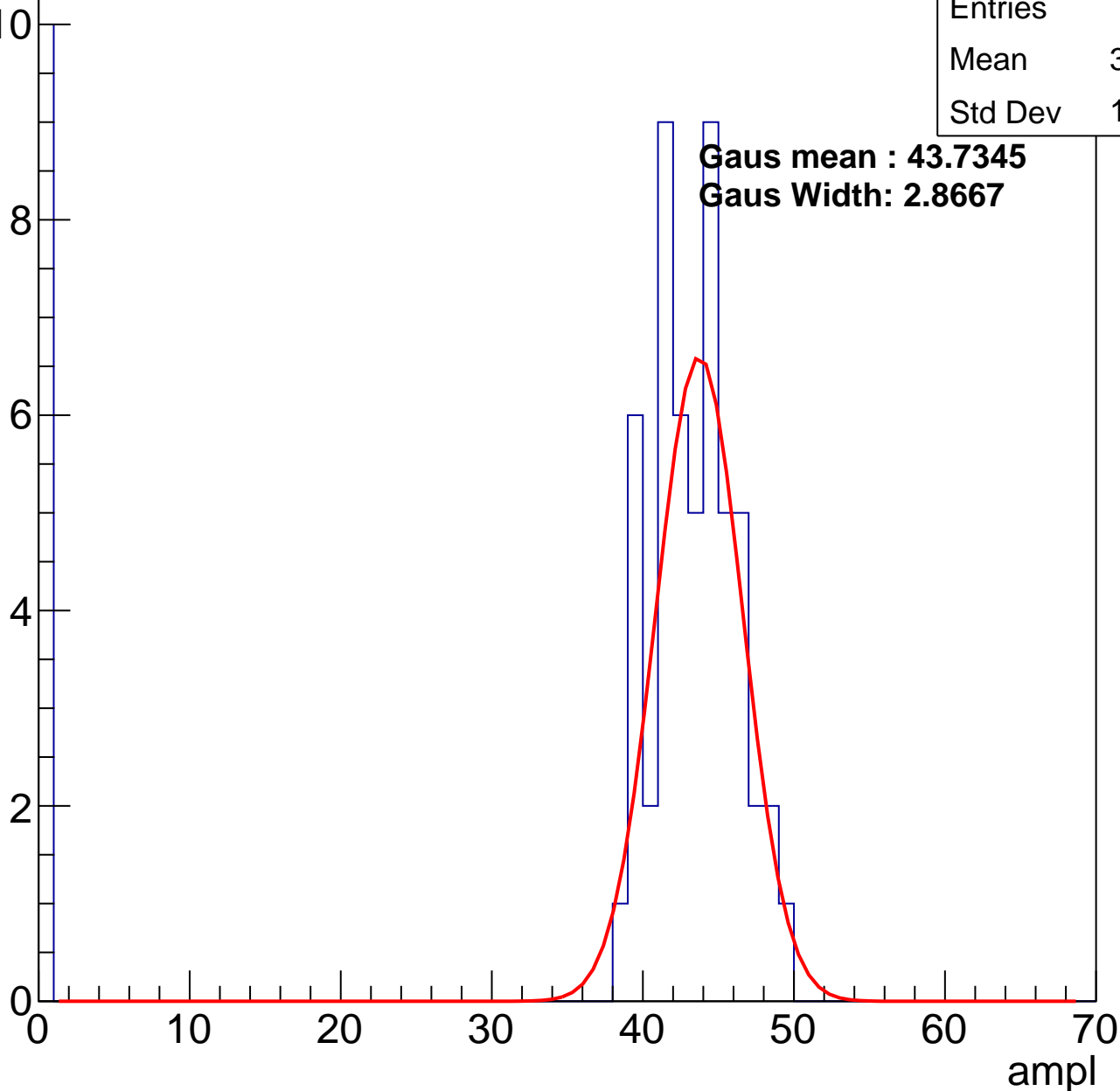
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.16
Std Dev	15.89

Gaus mean : 43.7345

Gaus Width: 2.8667

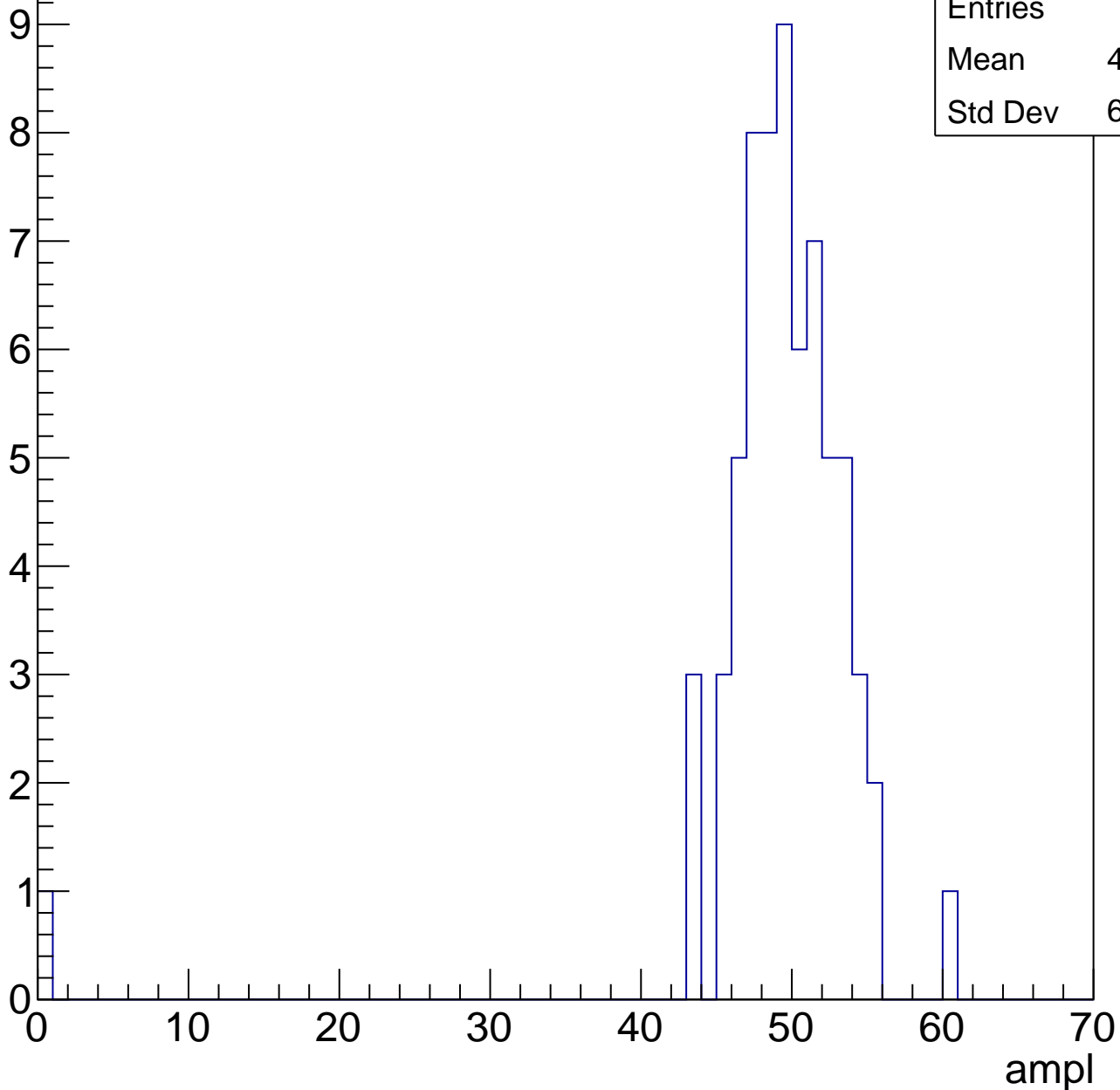


B1L103S, U10-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

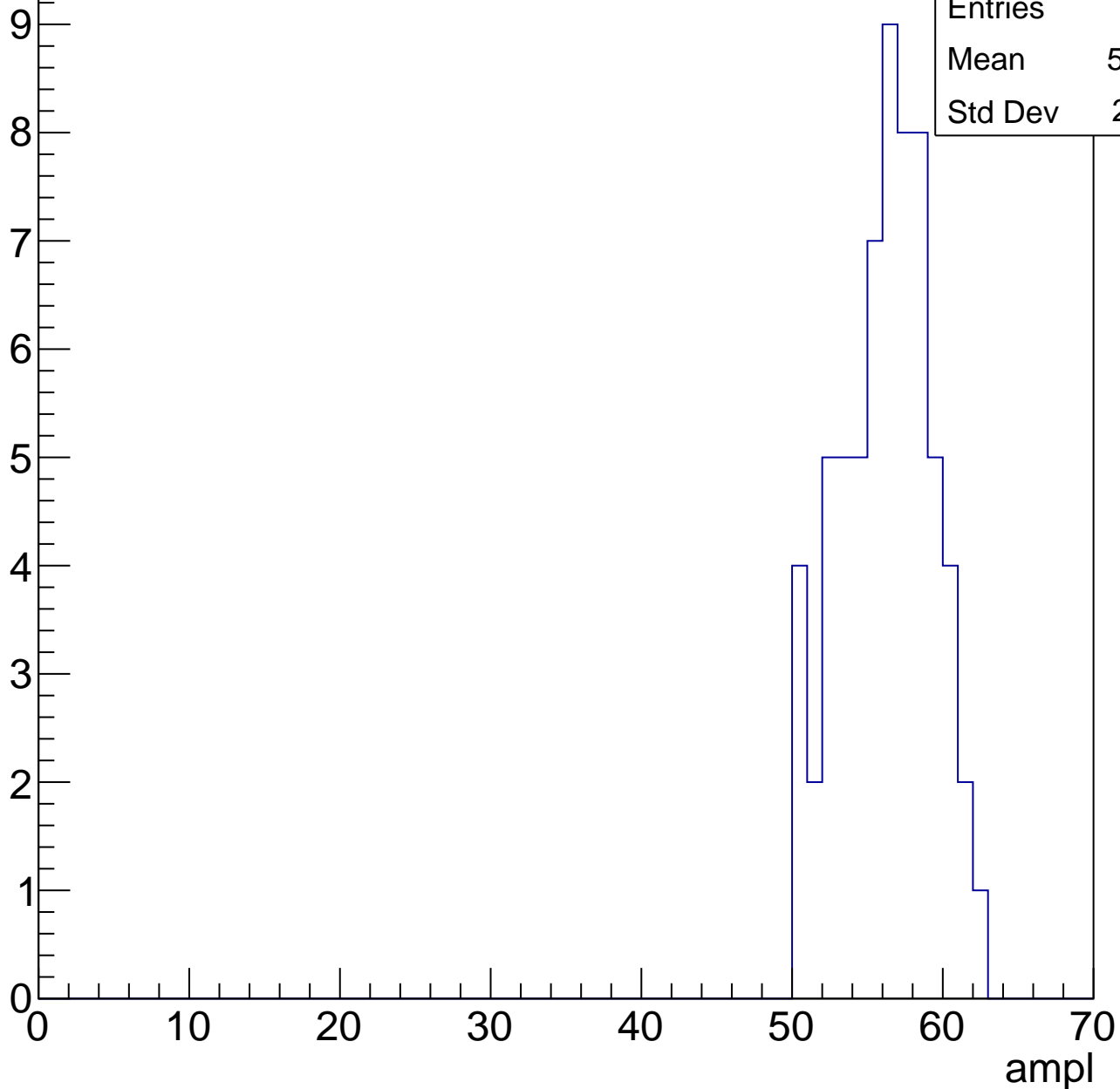
Entries	66
Mean	48.62
Std Dev	6.809



B1L103S, U10-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

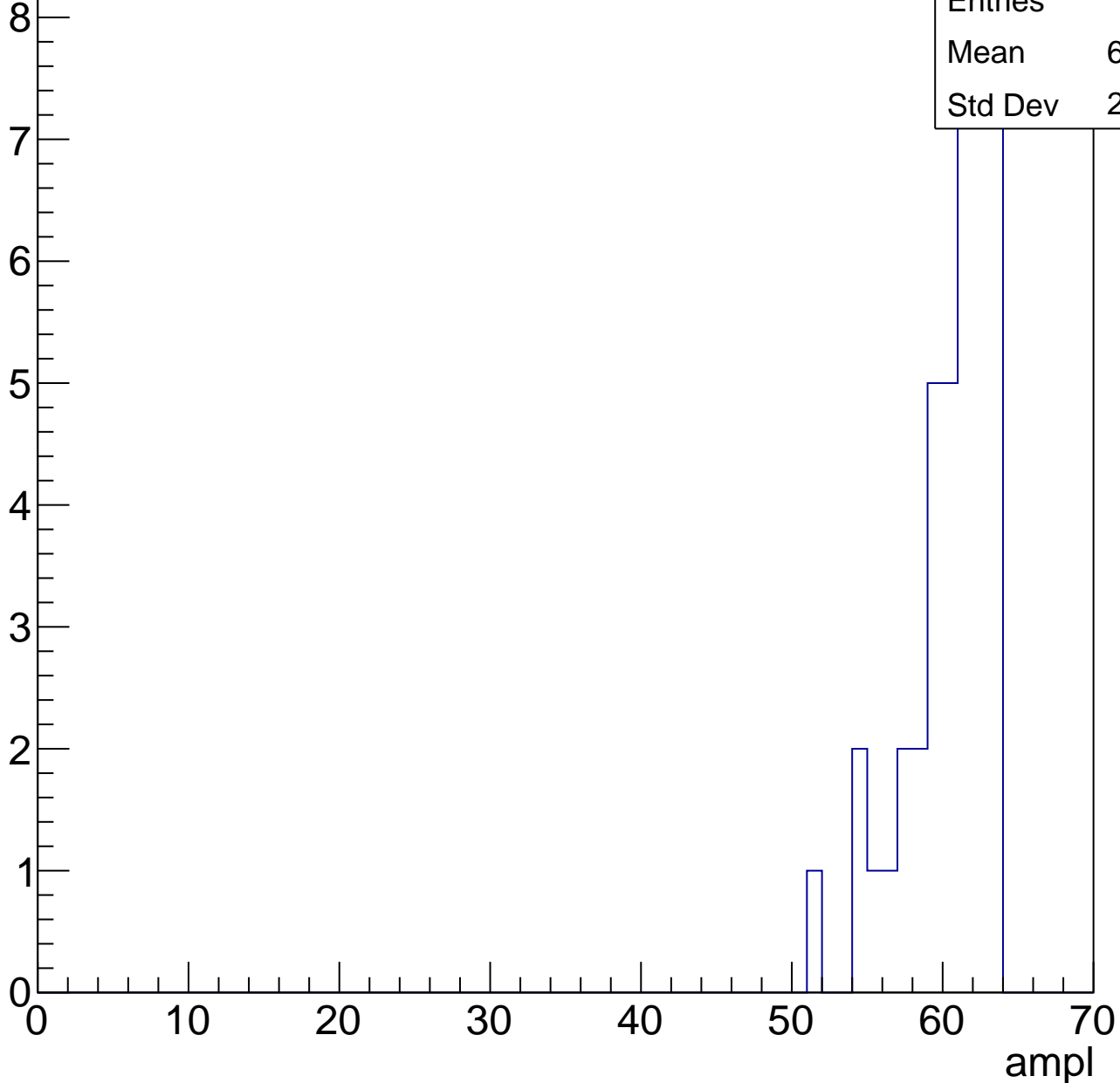


B1L103S, U10-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	60.07
Std Dev	2.799



B1L103S, U10-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch25, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	27.8
Std Dev	8.92

Gaus mean : 30.5892

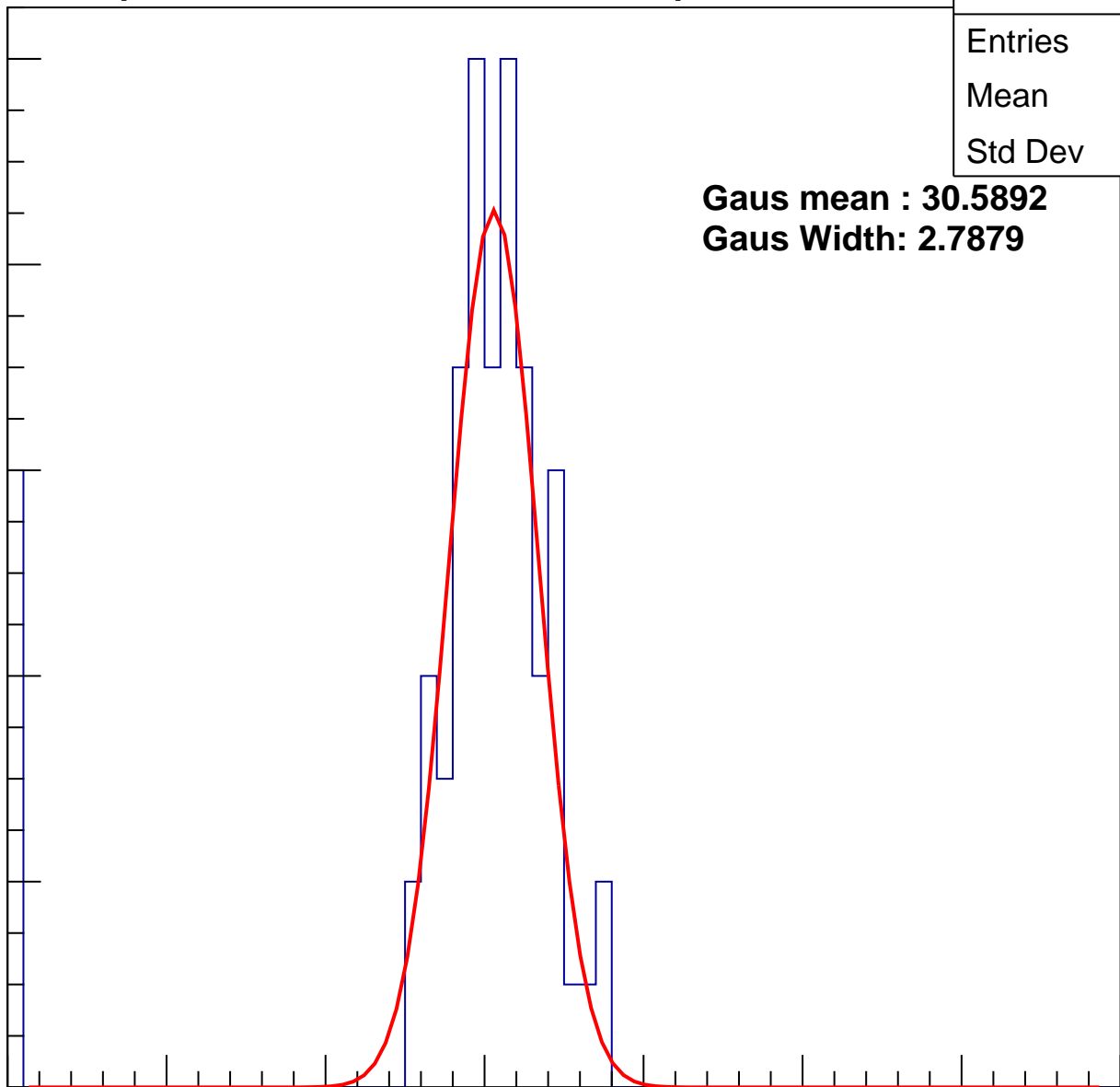
Gaus Width: 2.7879

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch25, adc1

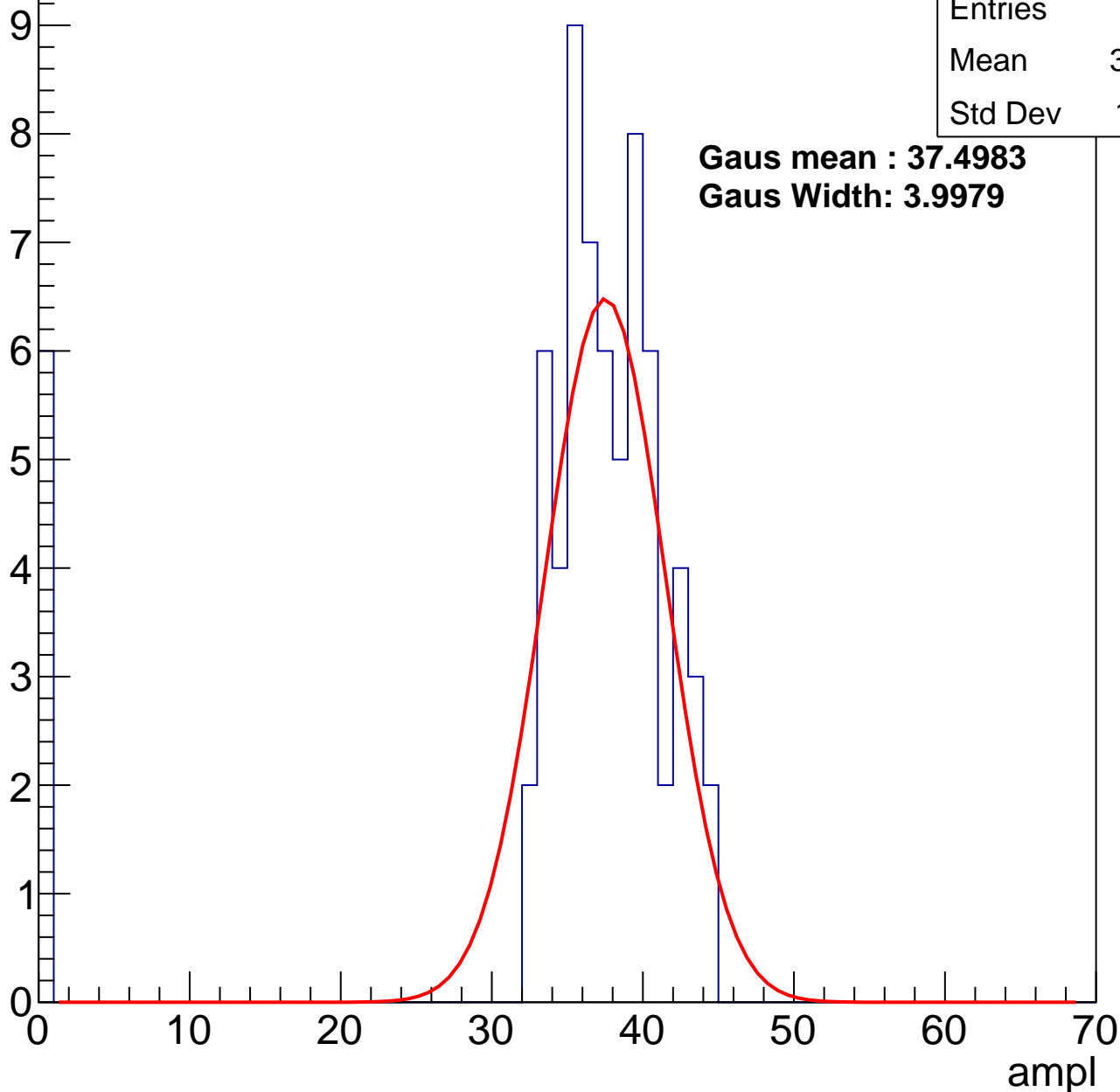
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.23
Std Dev	10.91

Gaus mean : 37.4983

Gaus Width: 3.9979



B1L103S, U10-ch25, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	39.54
Std Dev	12.8

Gaus mean : 44.5590

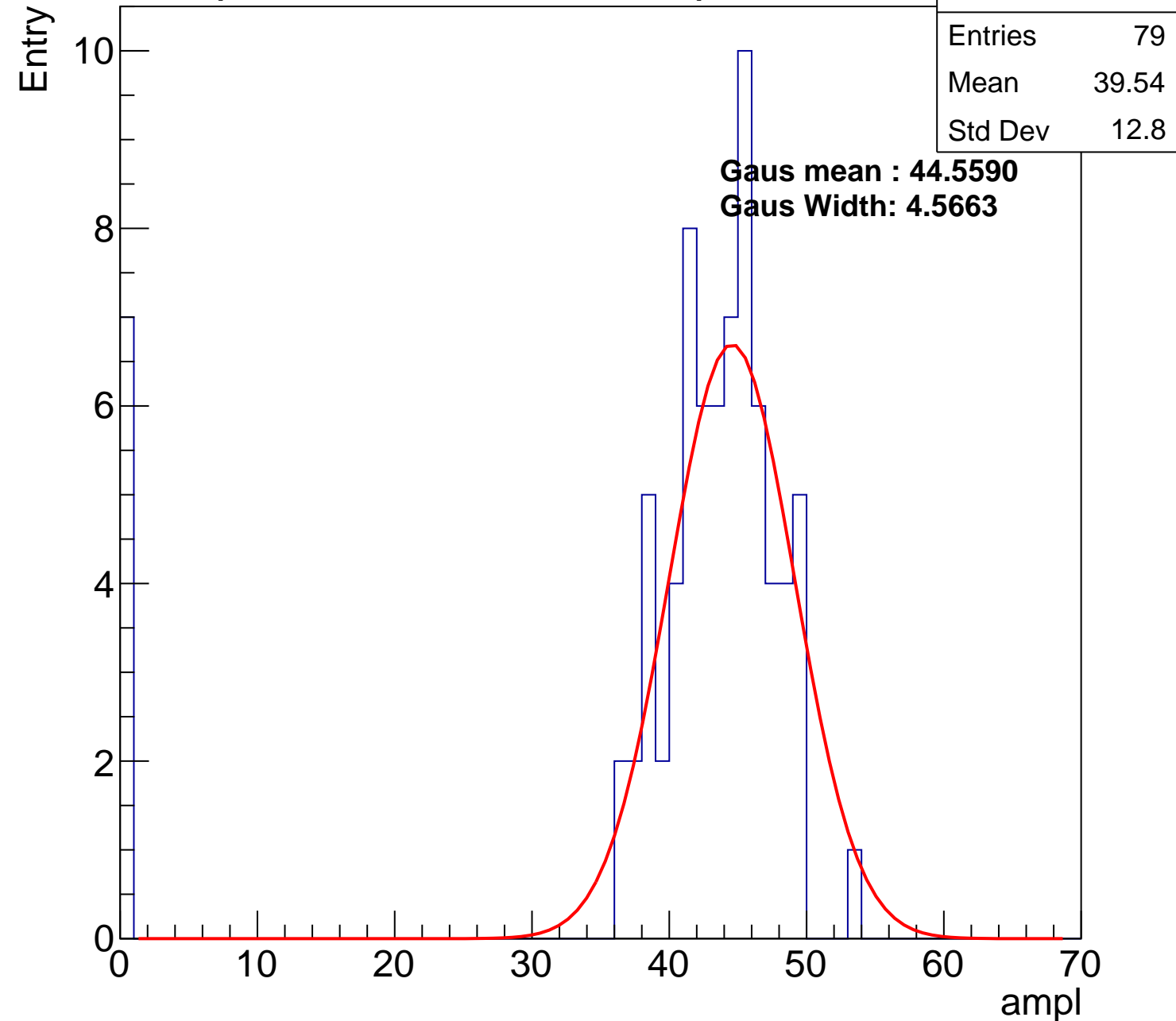
Gaus Width: 4.5663

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

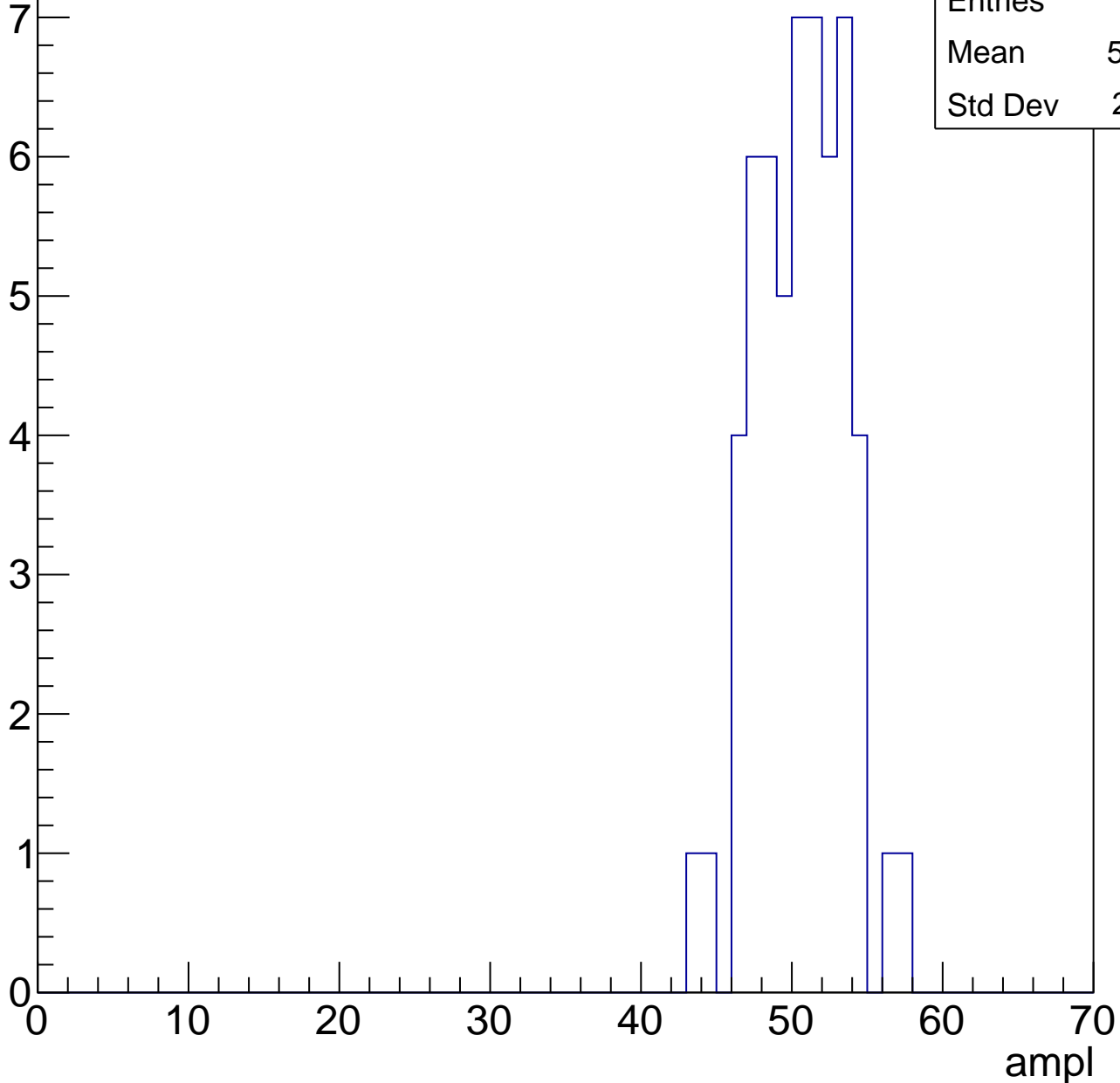


B1L103S, U10-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	50.09
Std Dev	2.911



B1L103S, U10-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

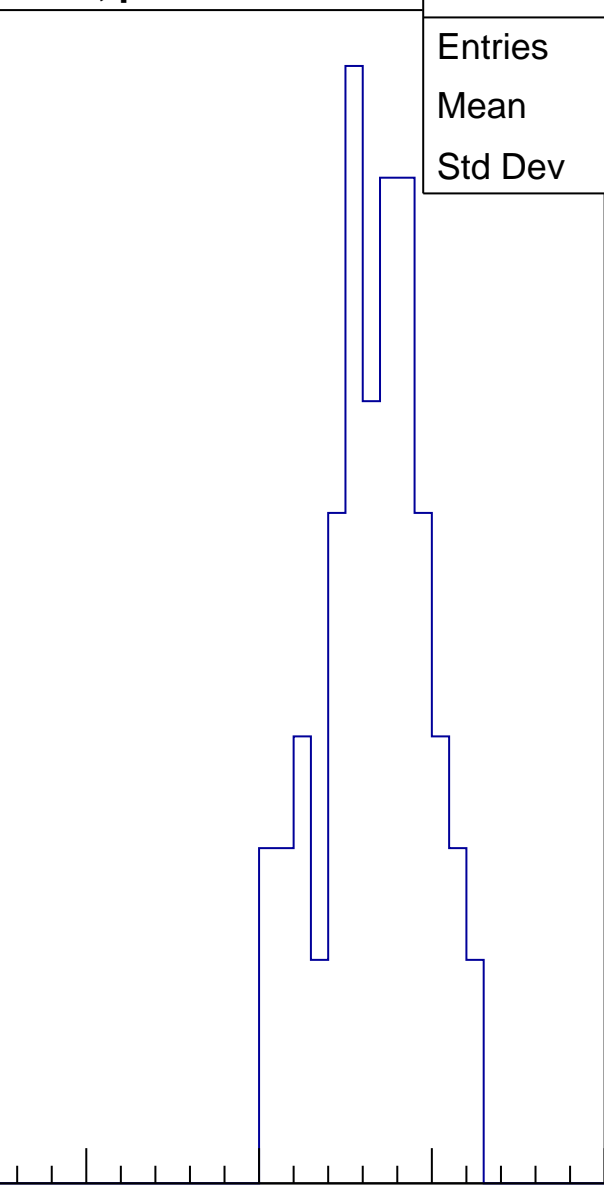
Entries	68
Mean	56.16
Std Dev	2.988

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

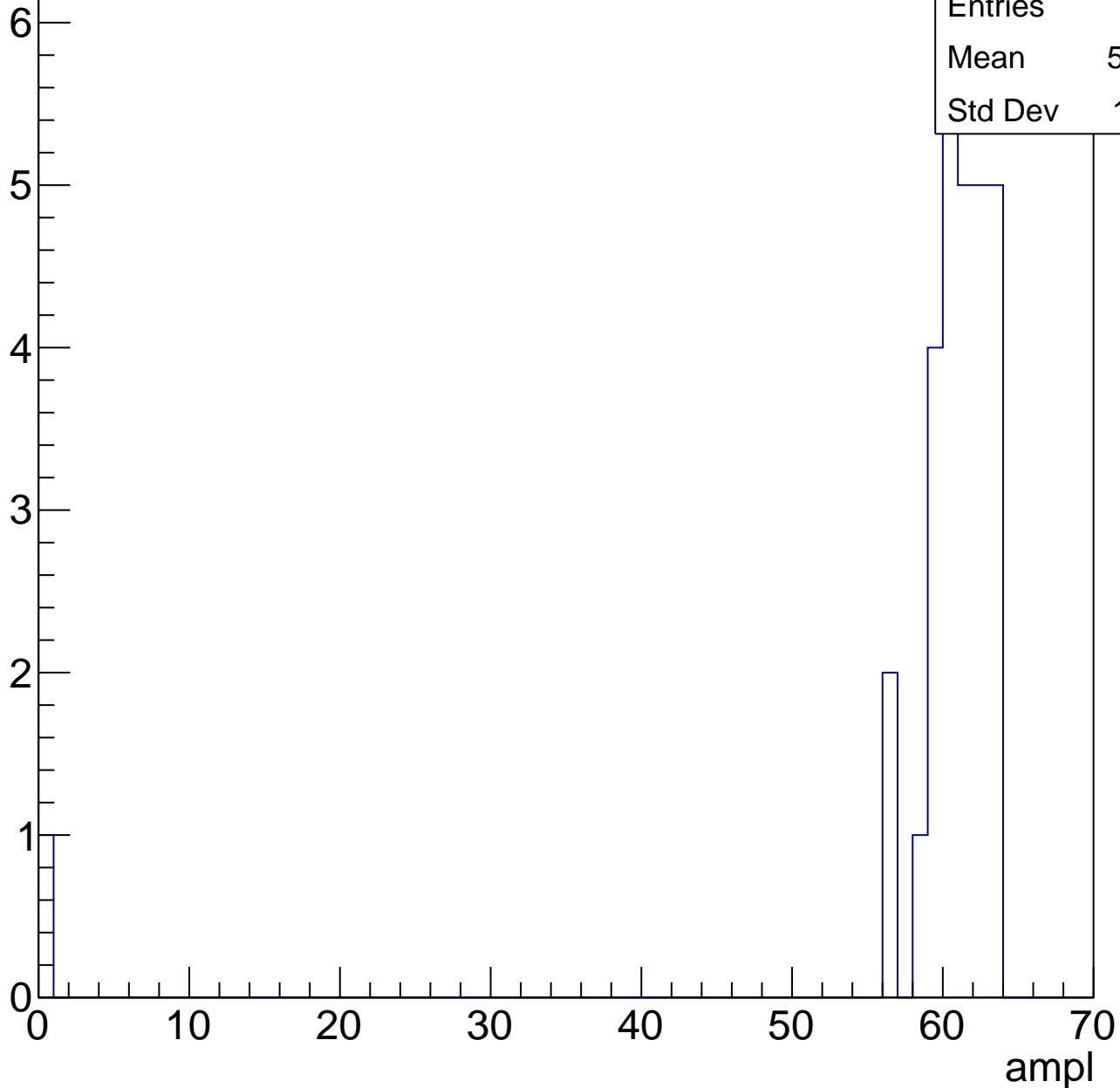
ampl



B1L103S, U10-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

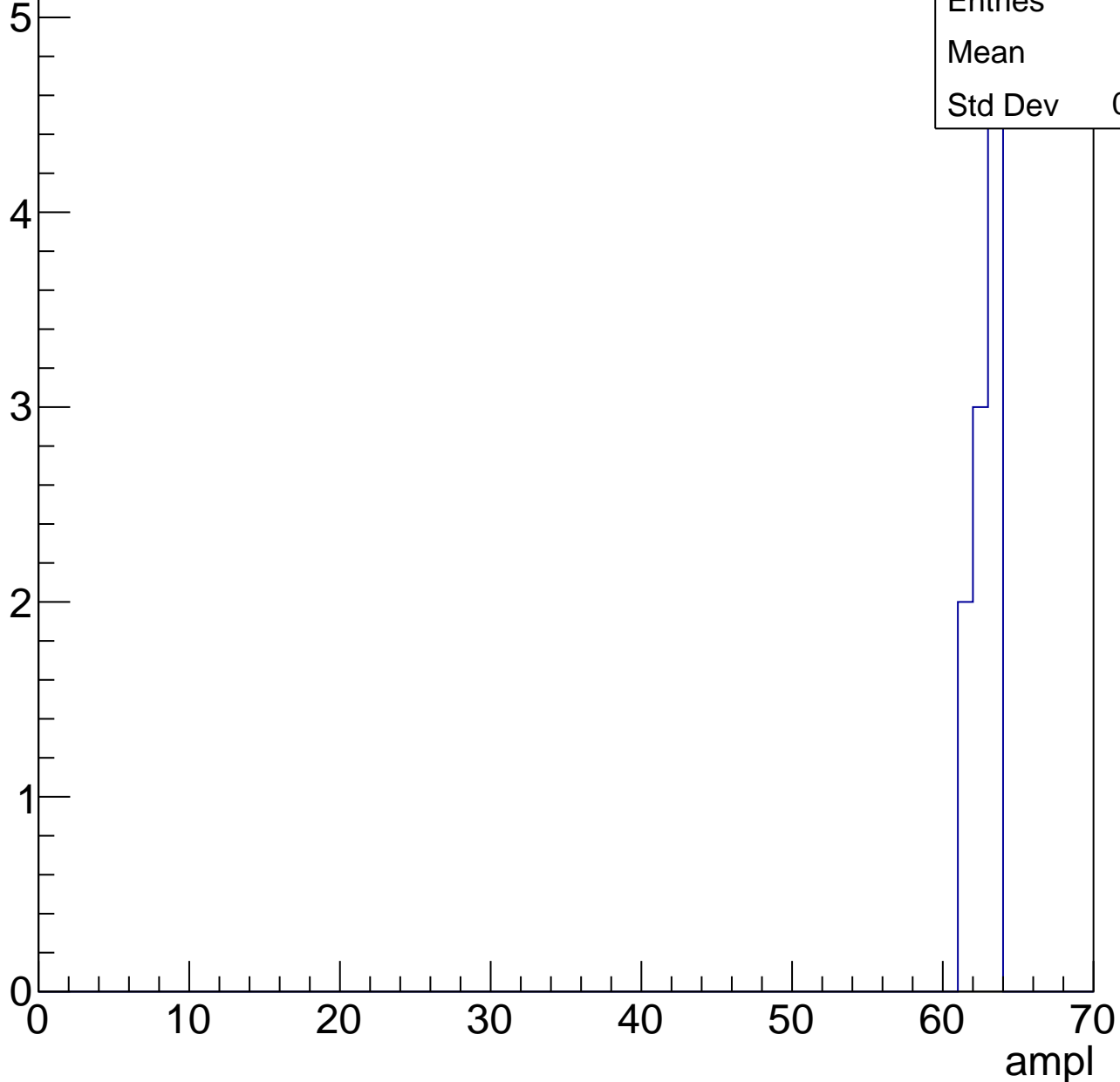


B1L103S, U10-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62.3
Std Dev	0.781



B1L103S, U10-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch26, adc0

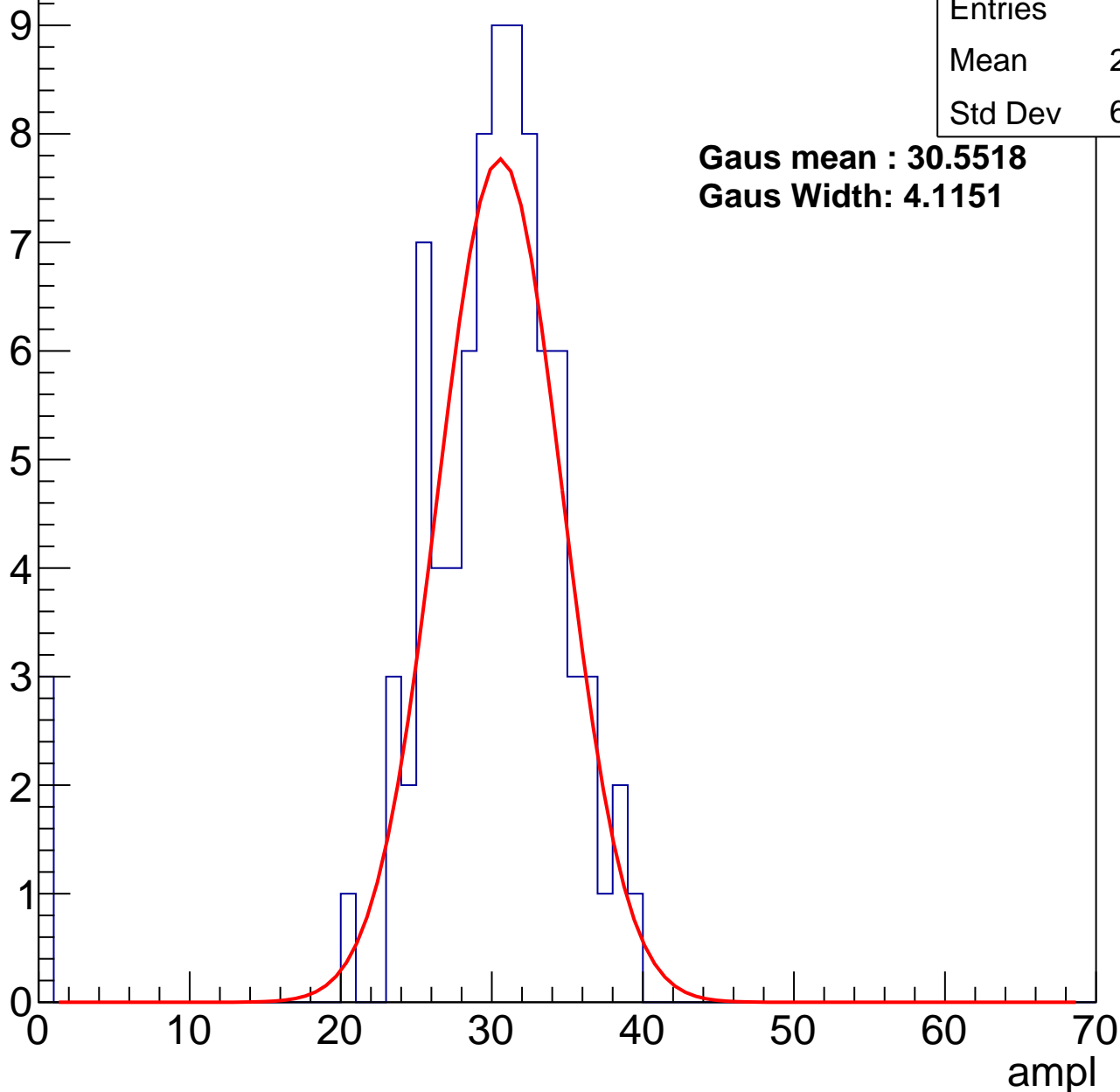
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	29.02
Std Dev	6.713

Gaus mean : 30.5518

Gaus Width: 4.1151



B1L103S, U10-ch26, adc1

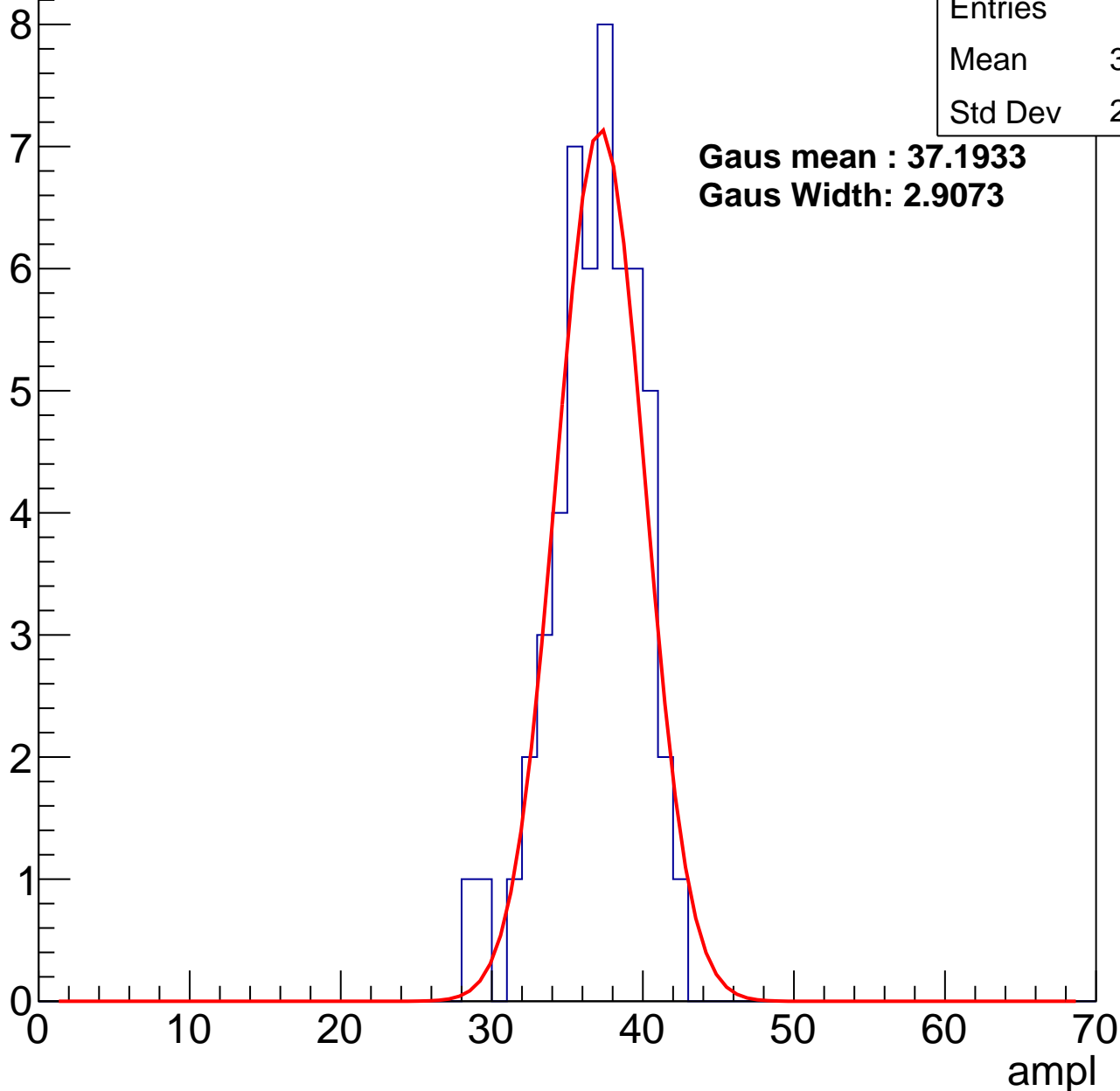
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	36.42
Std Dev	2.955

Gaus mean : 37.1933

Gaus Width: 2.9073



B1L103S, U10-ch26, adc2

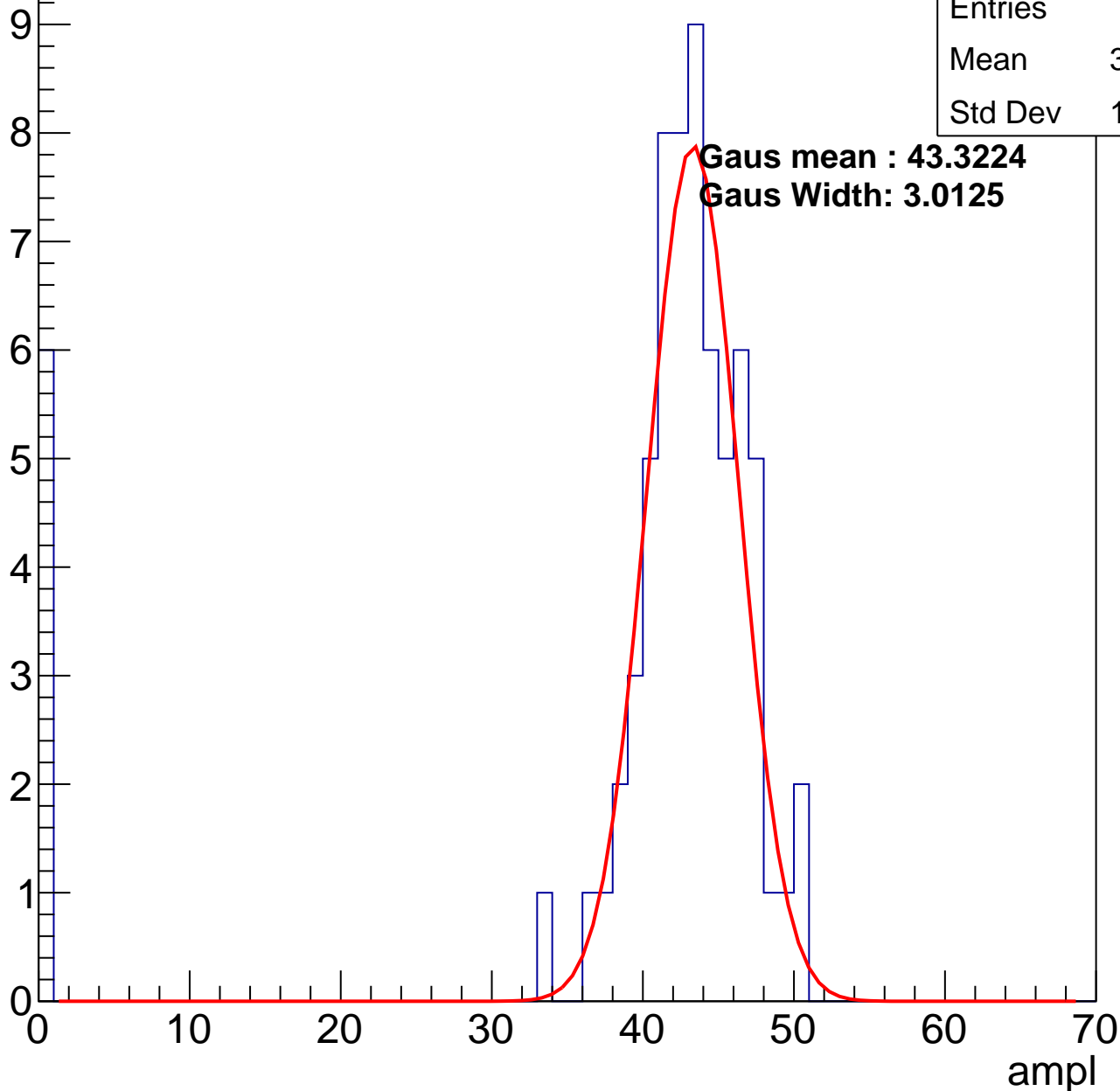
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	39.24
Std Dev	12.42

Gaus mean : 43.3224

Gaus Width: 3.0125

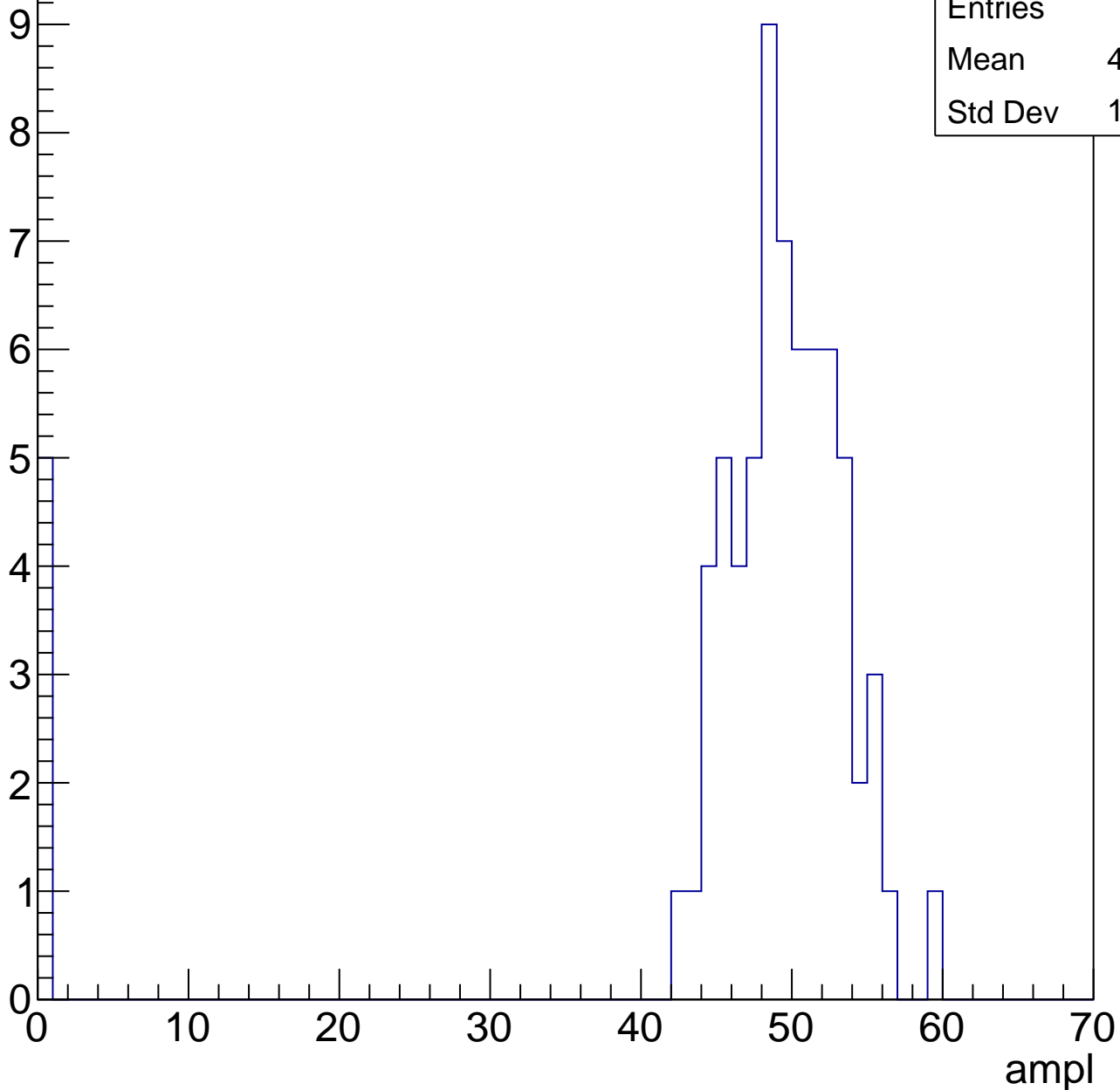


B1L103S, U10-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	45.79
Std Dev	13.04

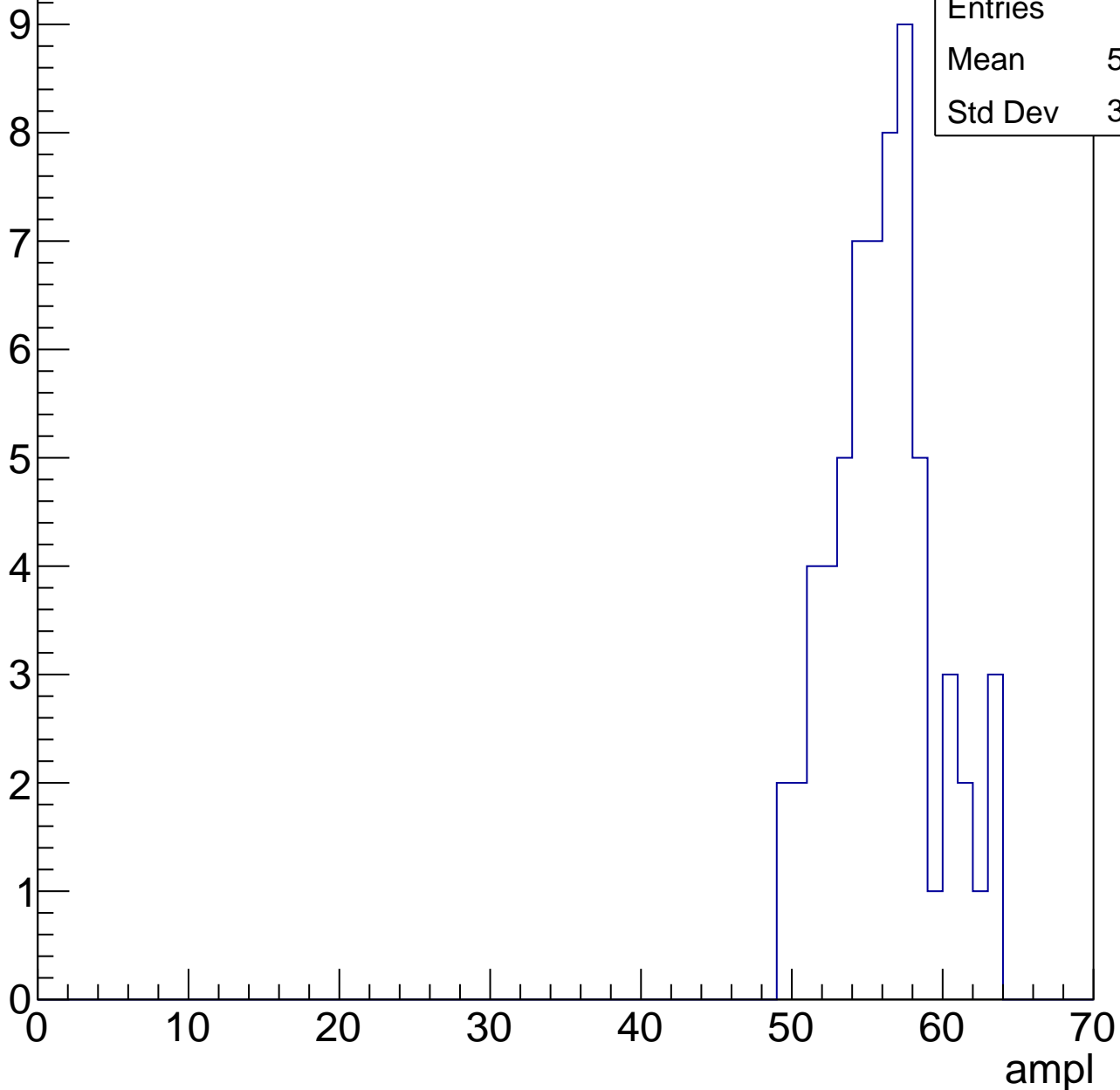


B1L103S, U10-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

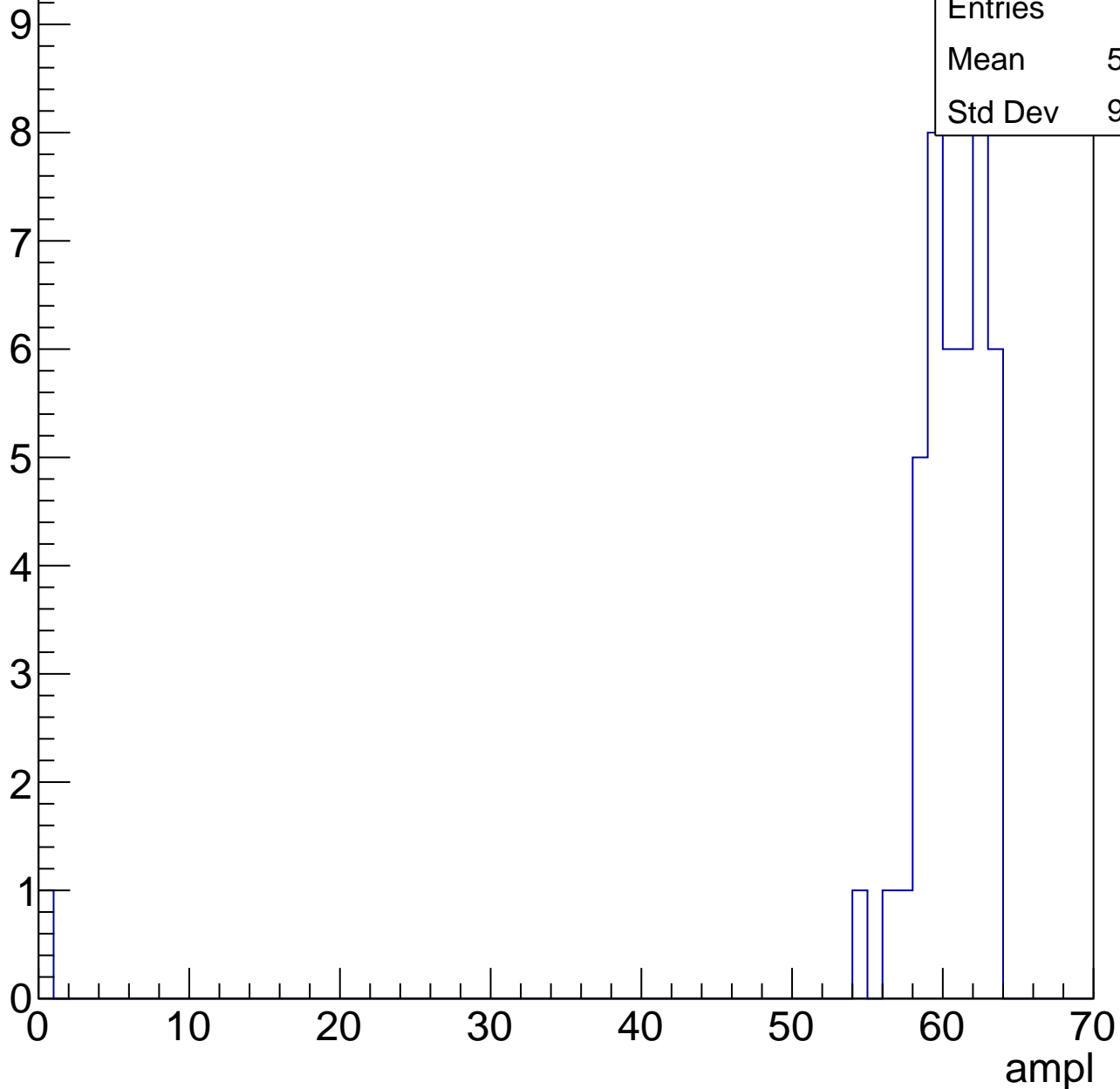
Entries	63
Mean	55.57
Std Dev	3.384



B1L103S, U10-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

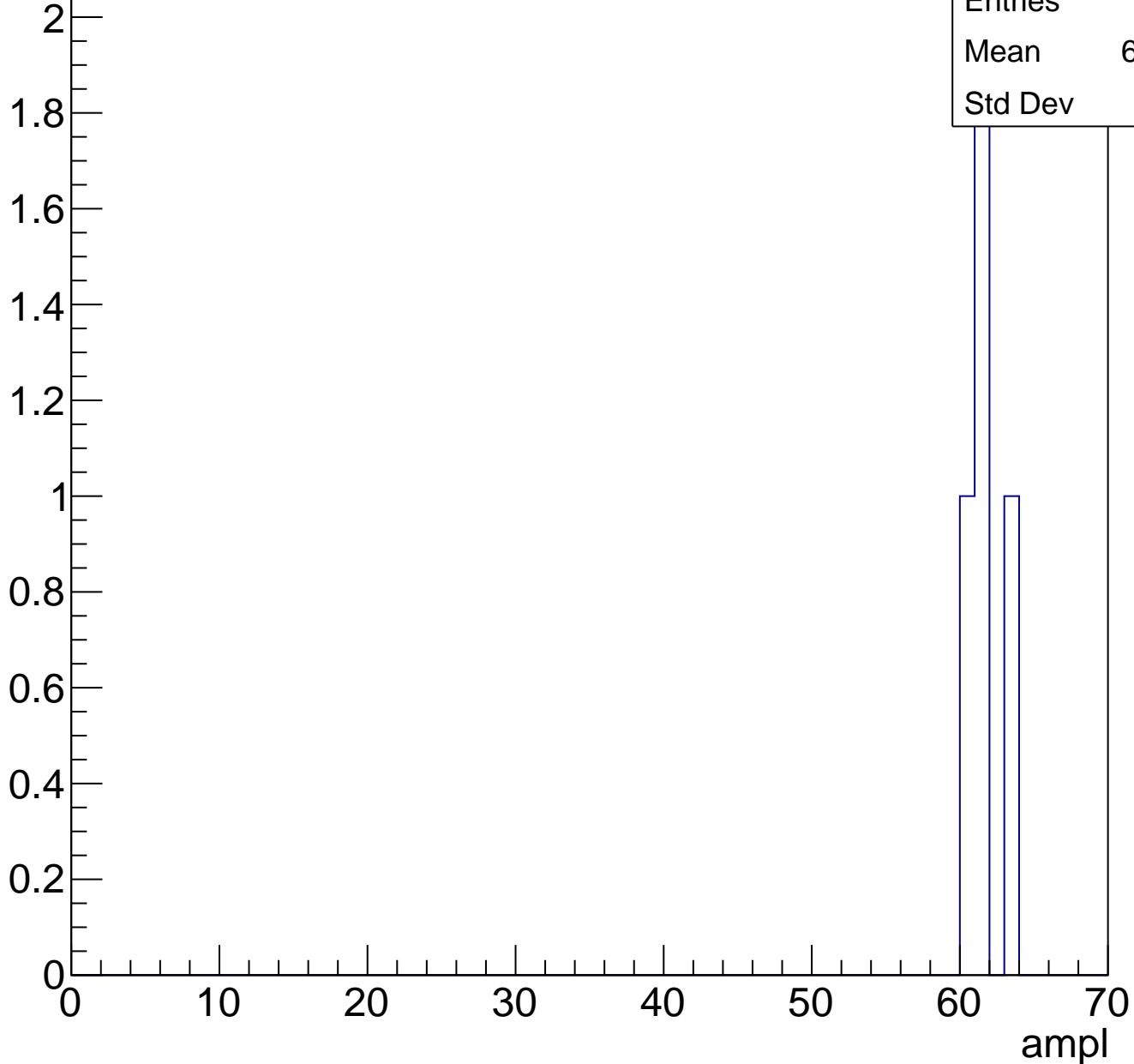
Entry



B1L103S, U10-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch27, adc0

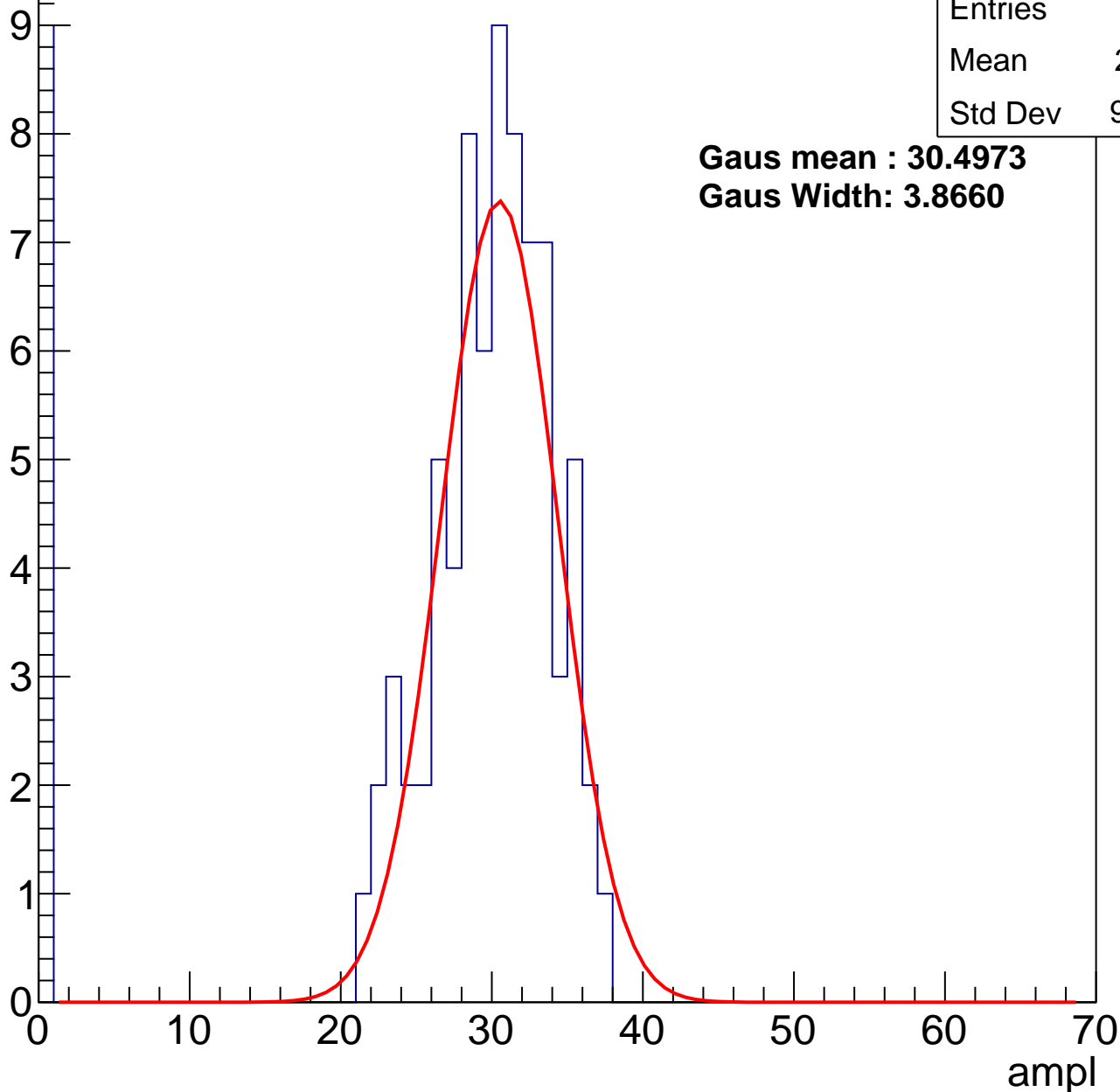
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	26.51
Std Dev	9.822

Gaus mean : 30.4973

Gaus Width: 3.8660



B1L103S, U10-ch27, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	33.23
Std Dev	11.74

Gaus mean : 37.4619

Gaus Width: 3.9112

Entry

10

8

6

4

2

0

0

10

20

30

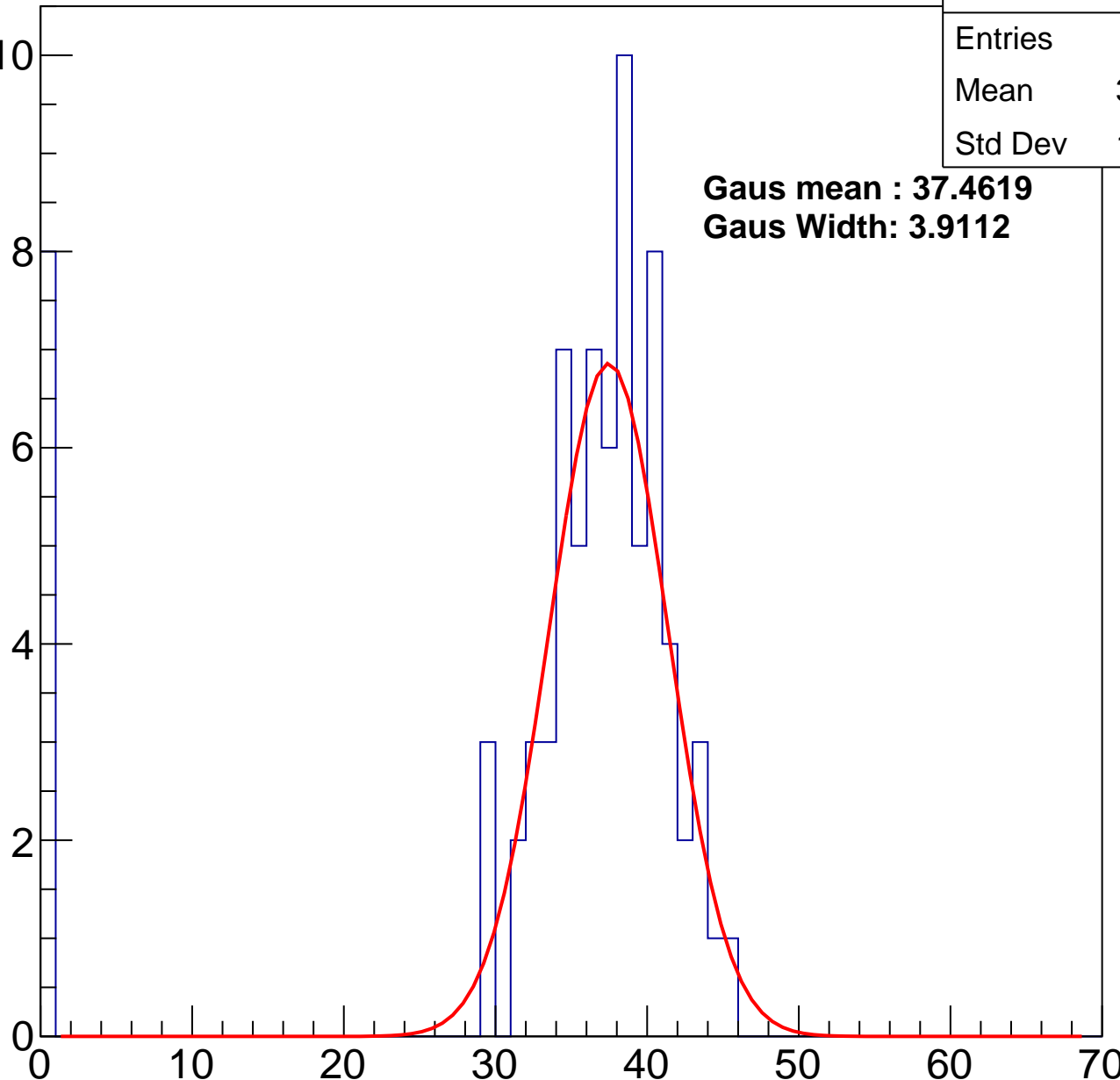
40

50

60

70

ampl



B1L103S, U10-ch27, adc2

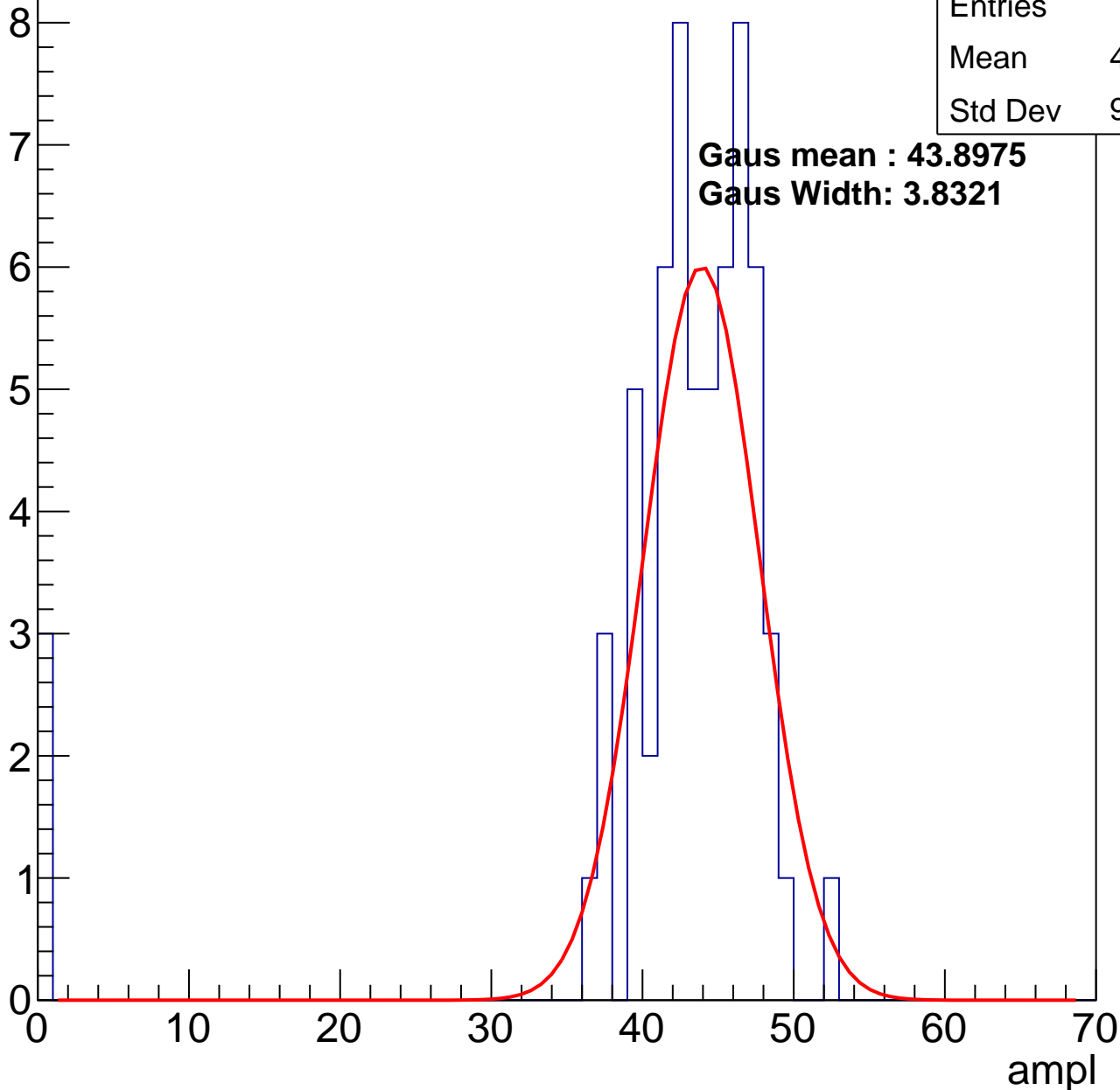
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.33
Std Dev	9.798

Gaus mean : 43.8975

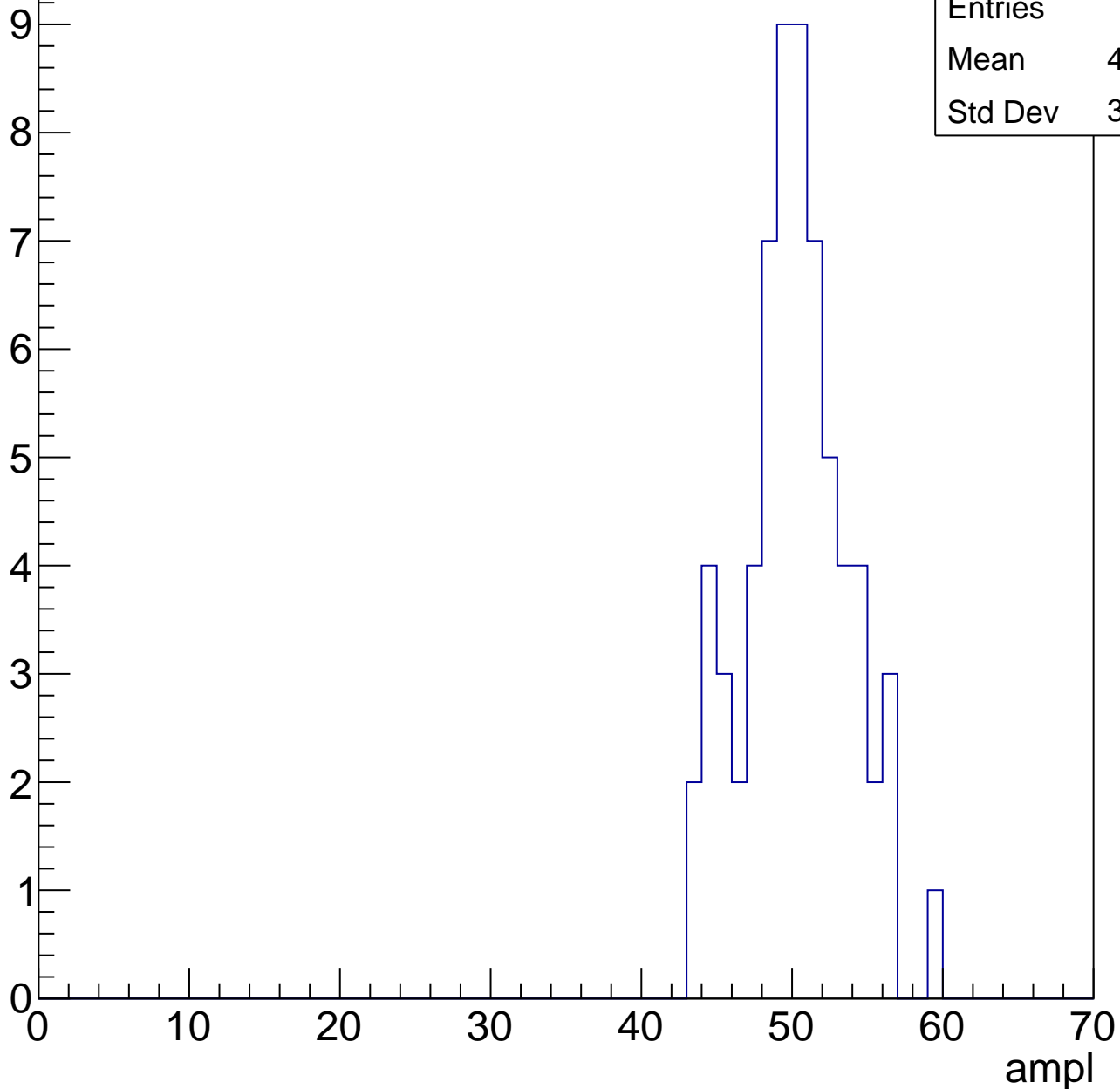
Gaus Width: 3.8321



B1L103S, U10-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



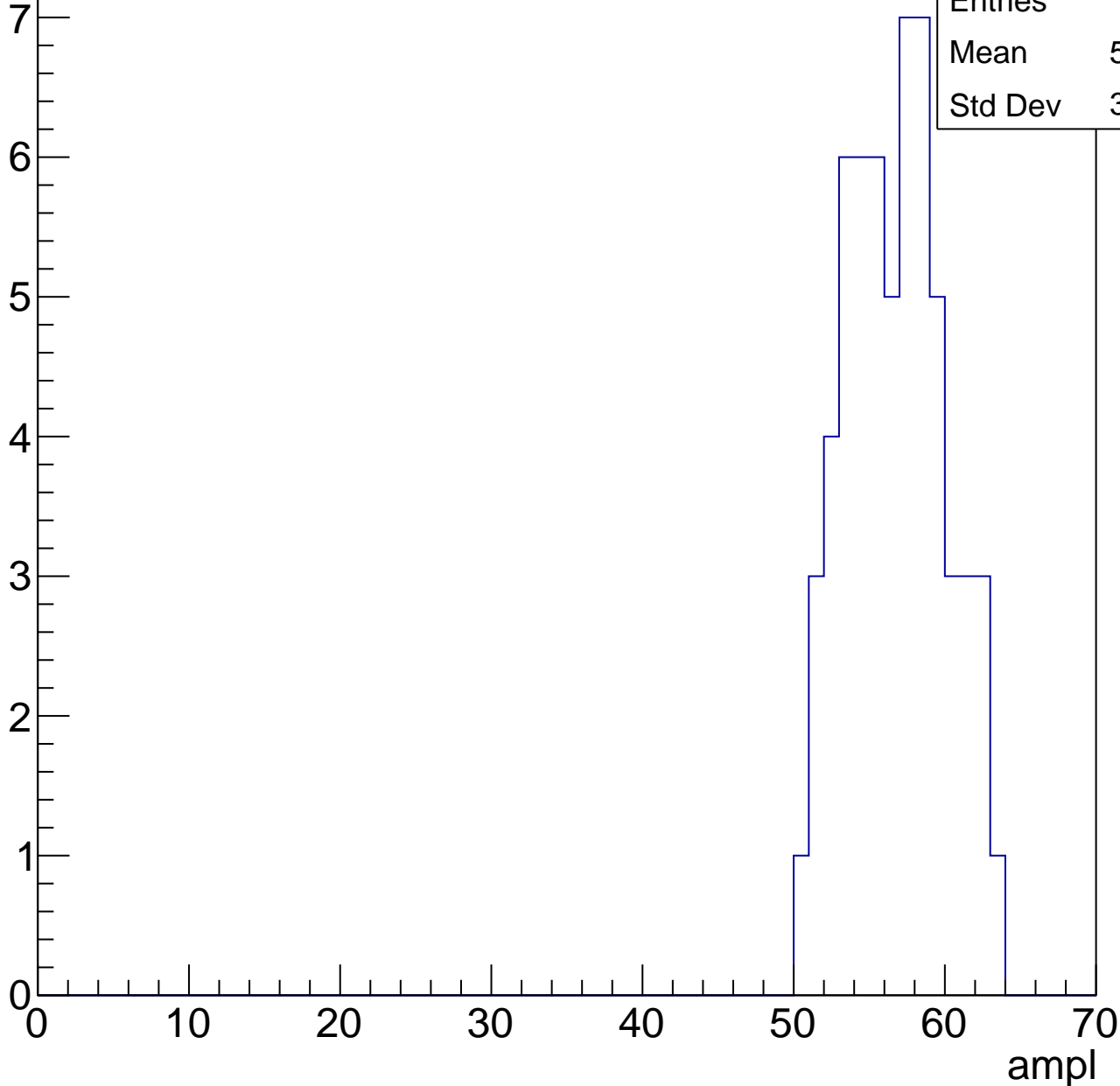
Entries	66
Mean	49.79
Std Dev	3.449

B1L103S, U10-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.25
Std Dev	3.202



B1L103S, U10-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries	42
Mean	58.67
Std Dev	9.446

ampl

B1L103S, U10-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch28, adc0

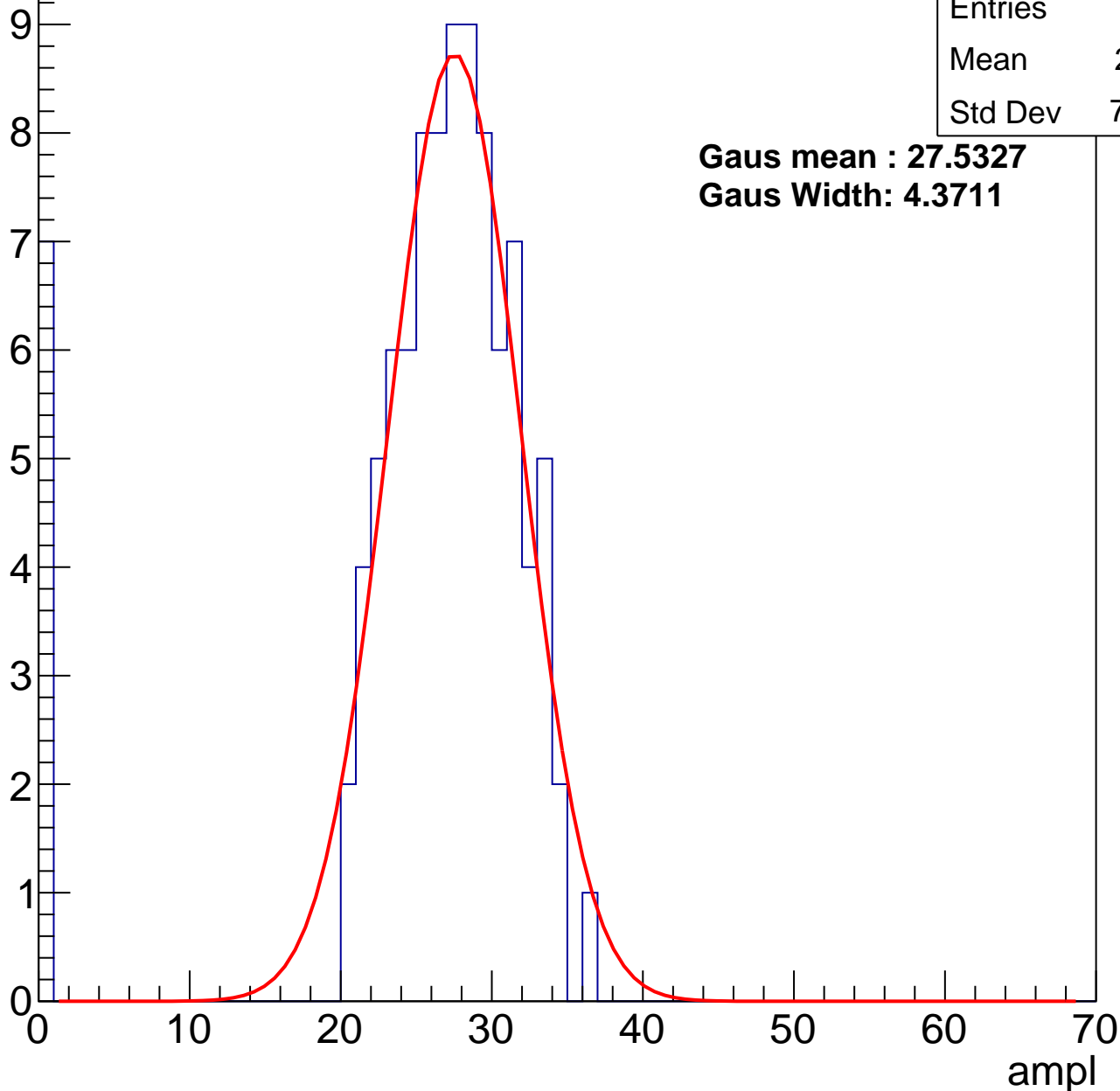
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	97
Mean	25.21
Std Dev	7.874

Gaus mean : 27.5327

Gaus Width: 4.3711



B1L103S, U10-ch28, adc1

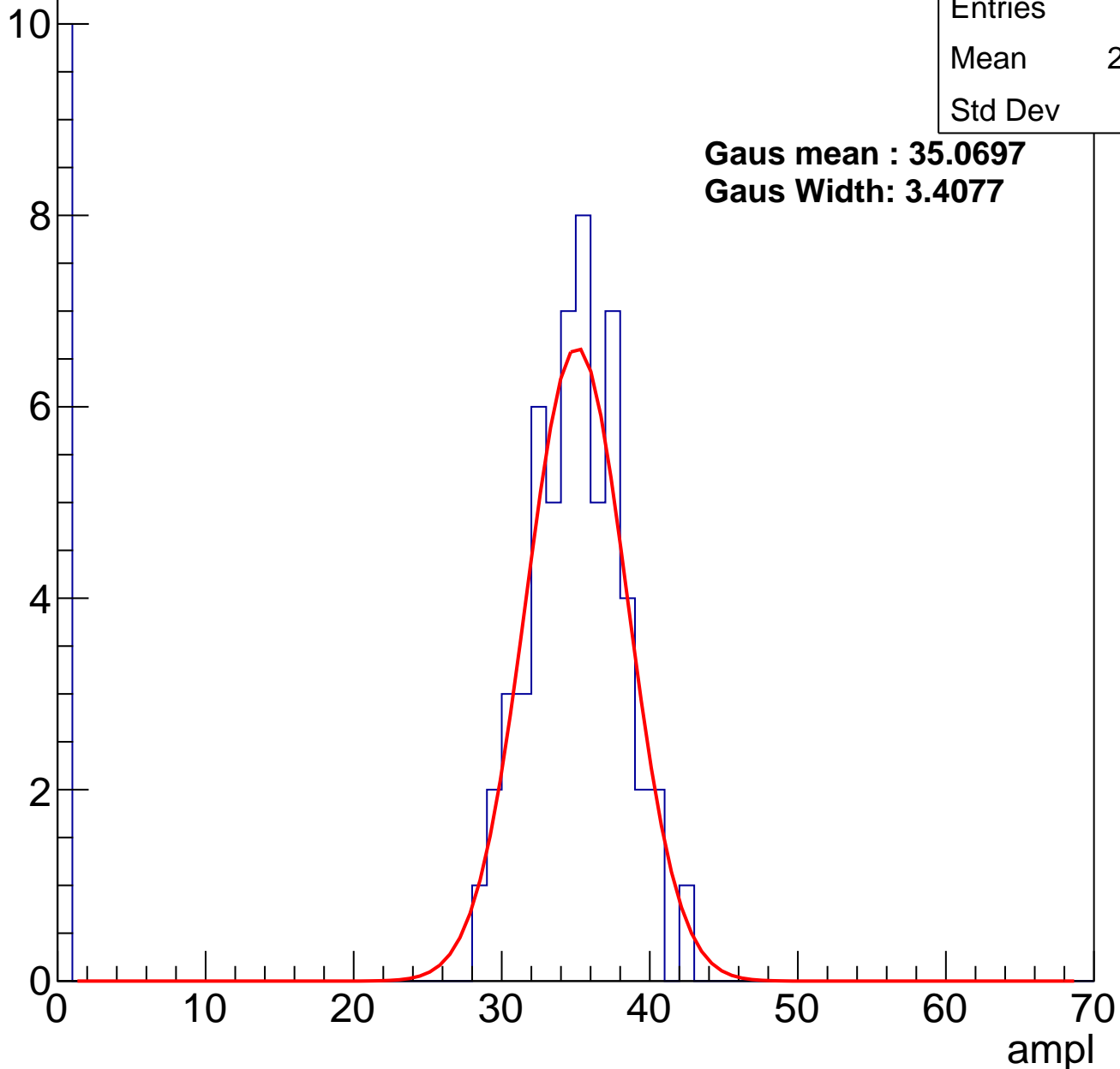
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	29.32
Std Dev	12.7

Gaus mean : 35.0697

Gaus Width: 3.4077

Entry



B1L103S, U10-ch28, adc2

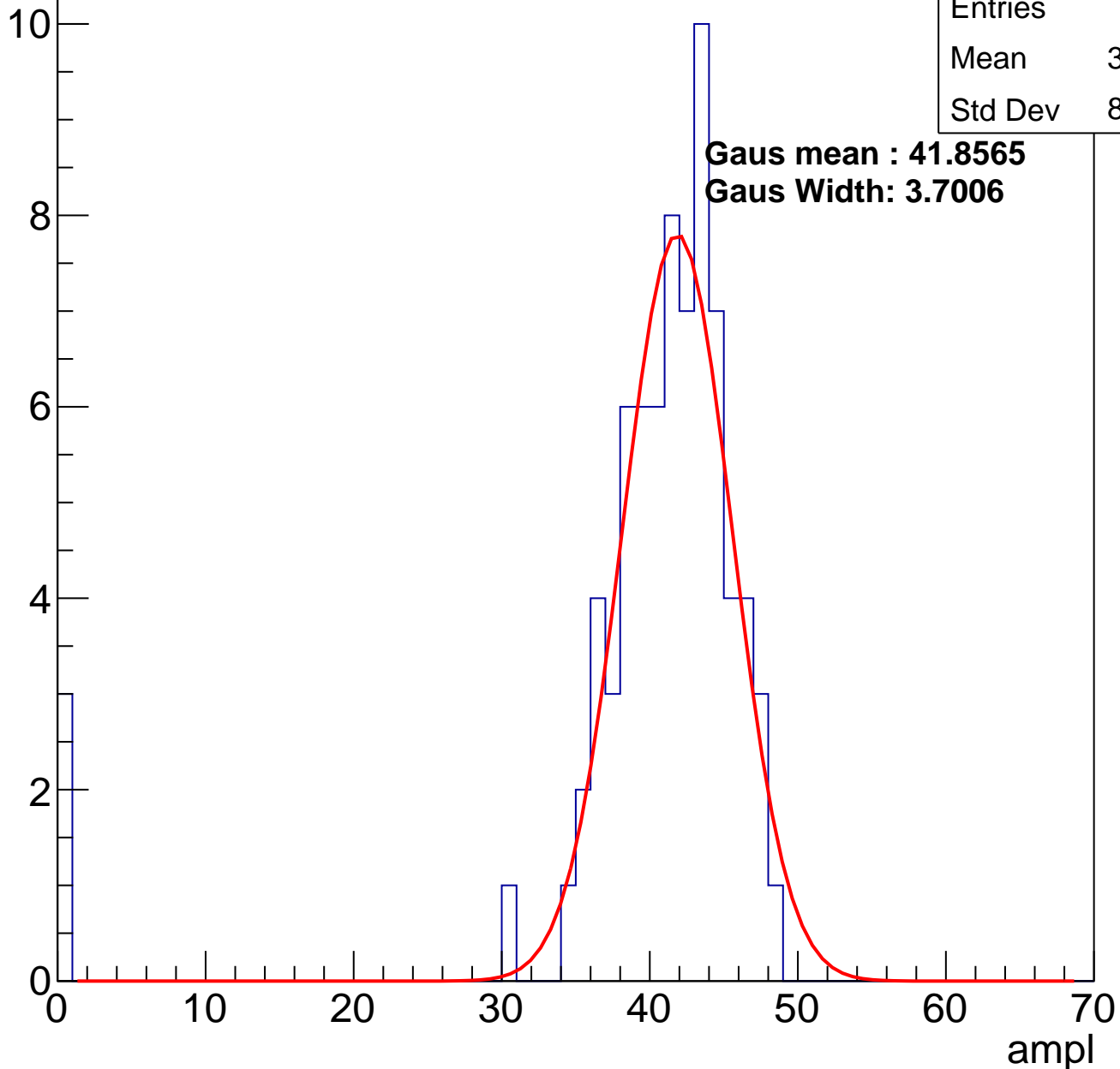
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	39.53
Std Dev	8.723

Gaus mean : 41.8565

Gaus Width: 3.7006

Entry

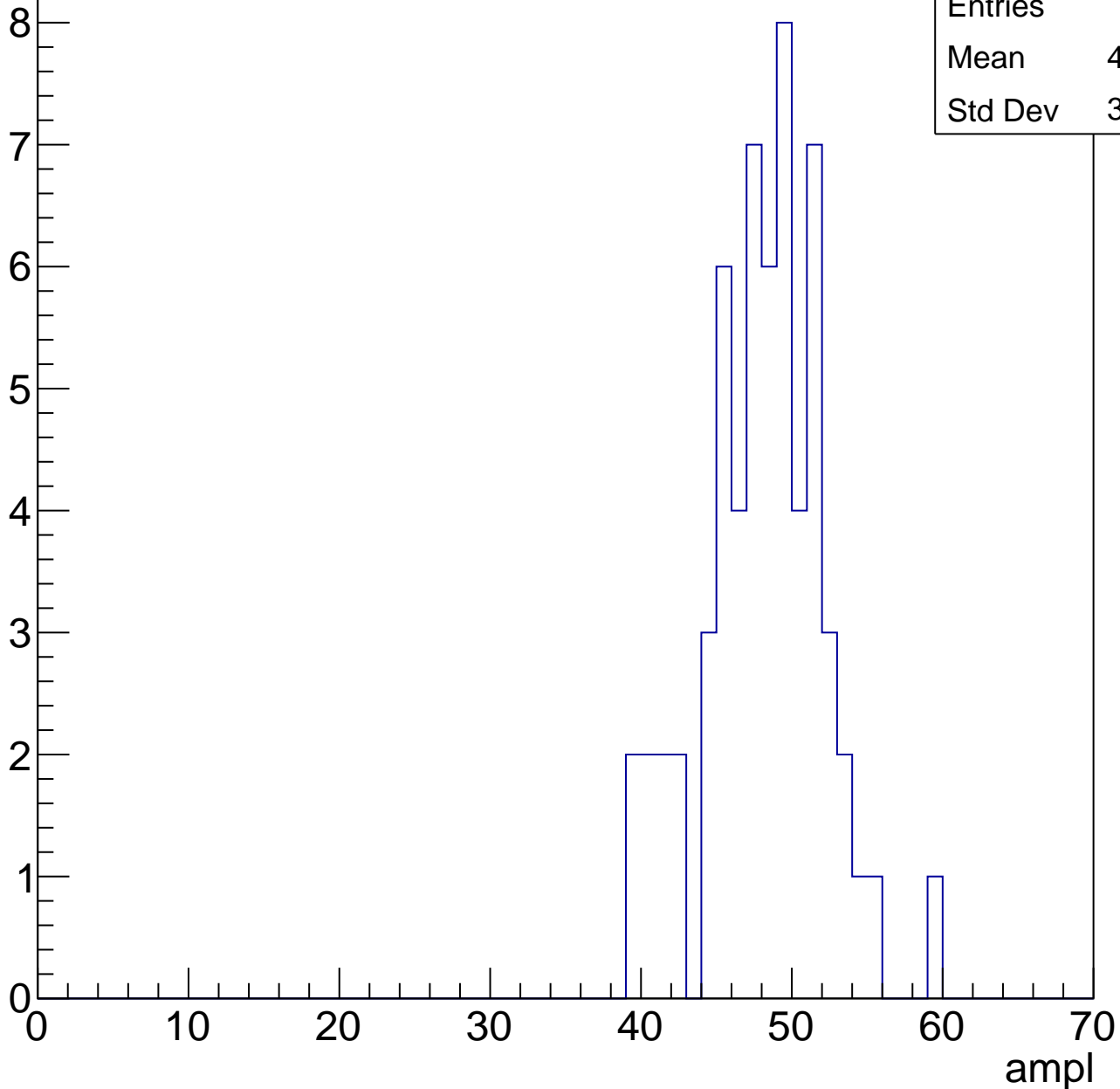


B1L103S, U10-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

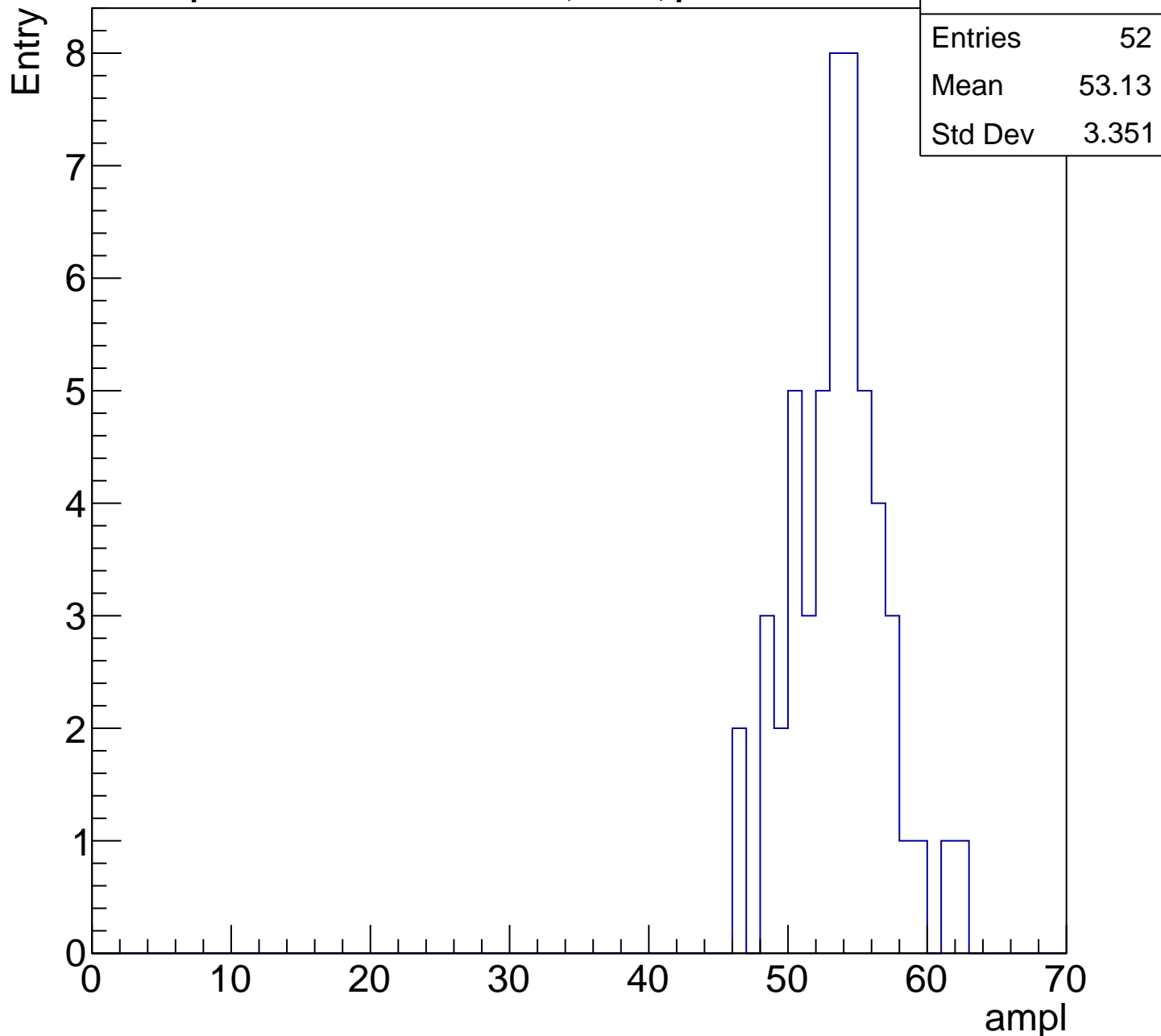
Entry

Entries	61
Mean	47.64
Std Dev	3.984



B1L103S, U10-ch28, adc4

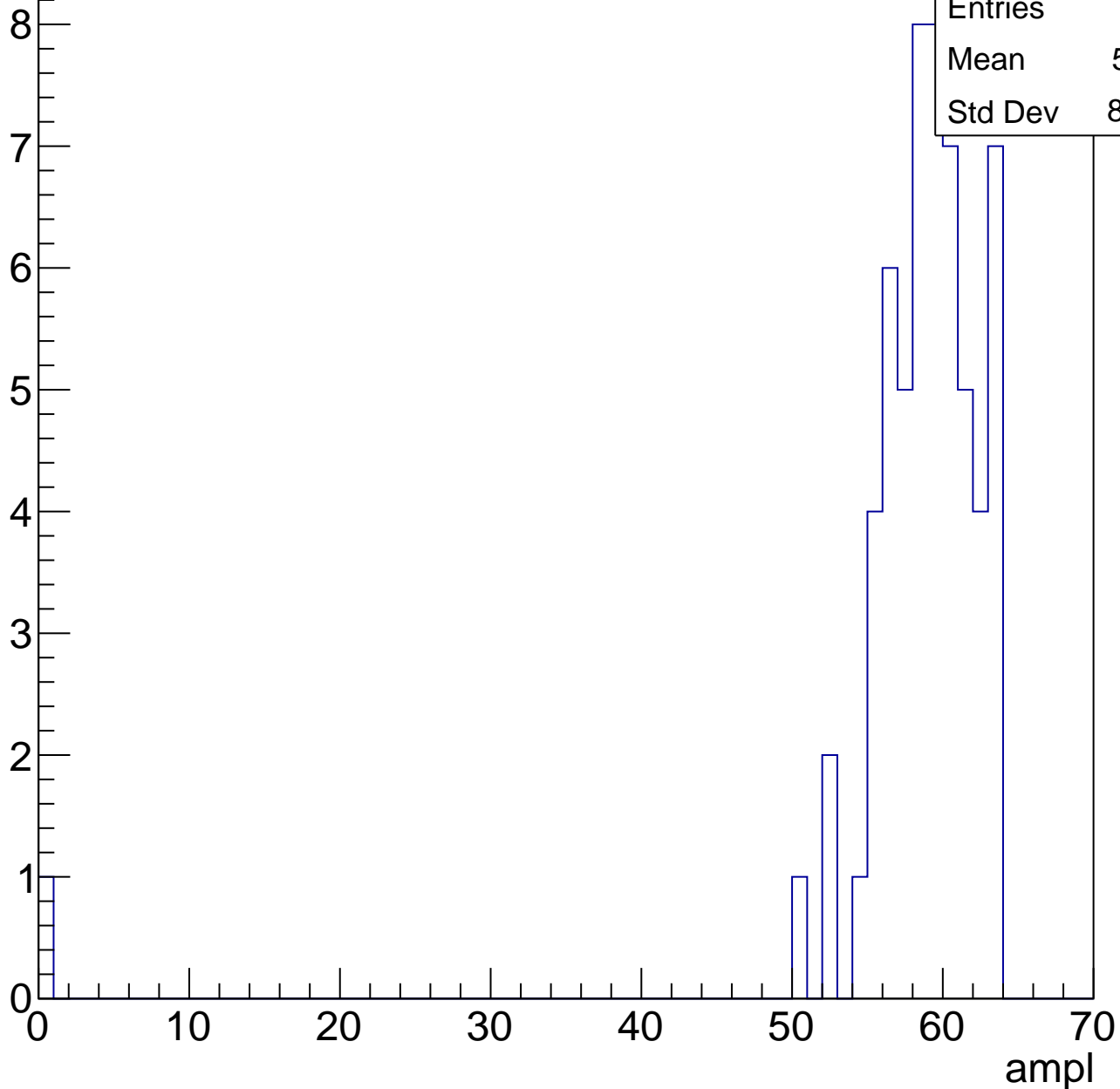
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

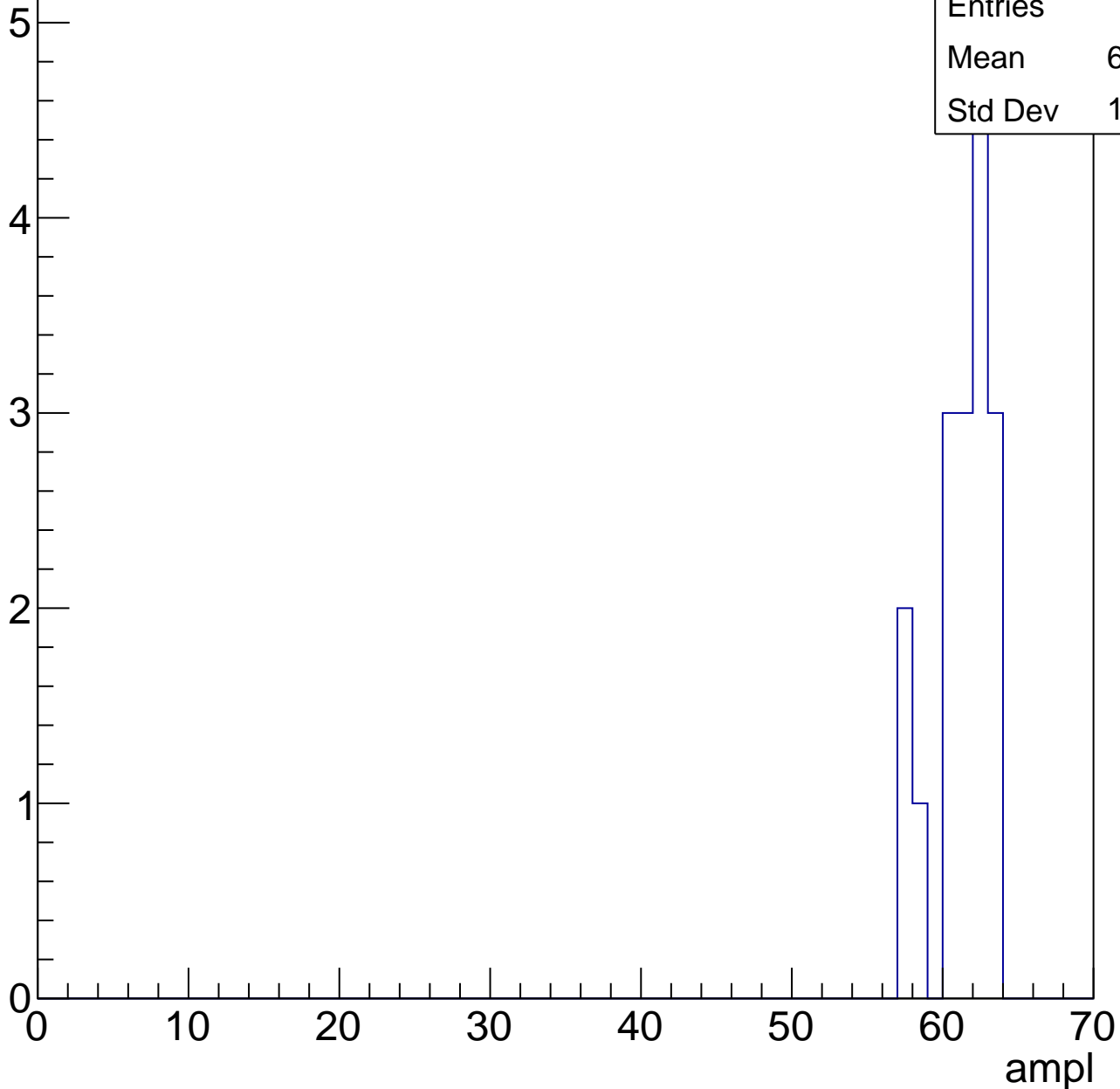


B1L103S, U10-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	60.82
Std Dev	1.886



B1L103S, U10-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U10-ch29, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	24.28
Std Dev	13.09

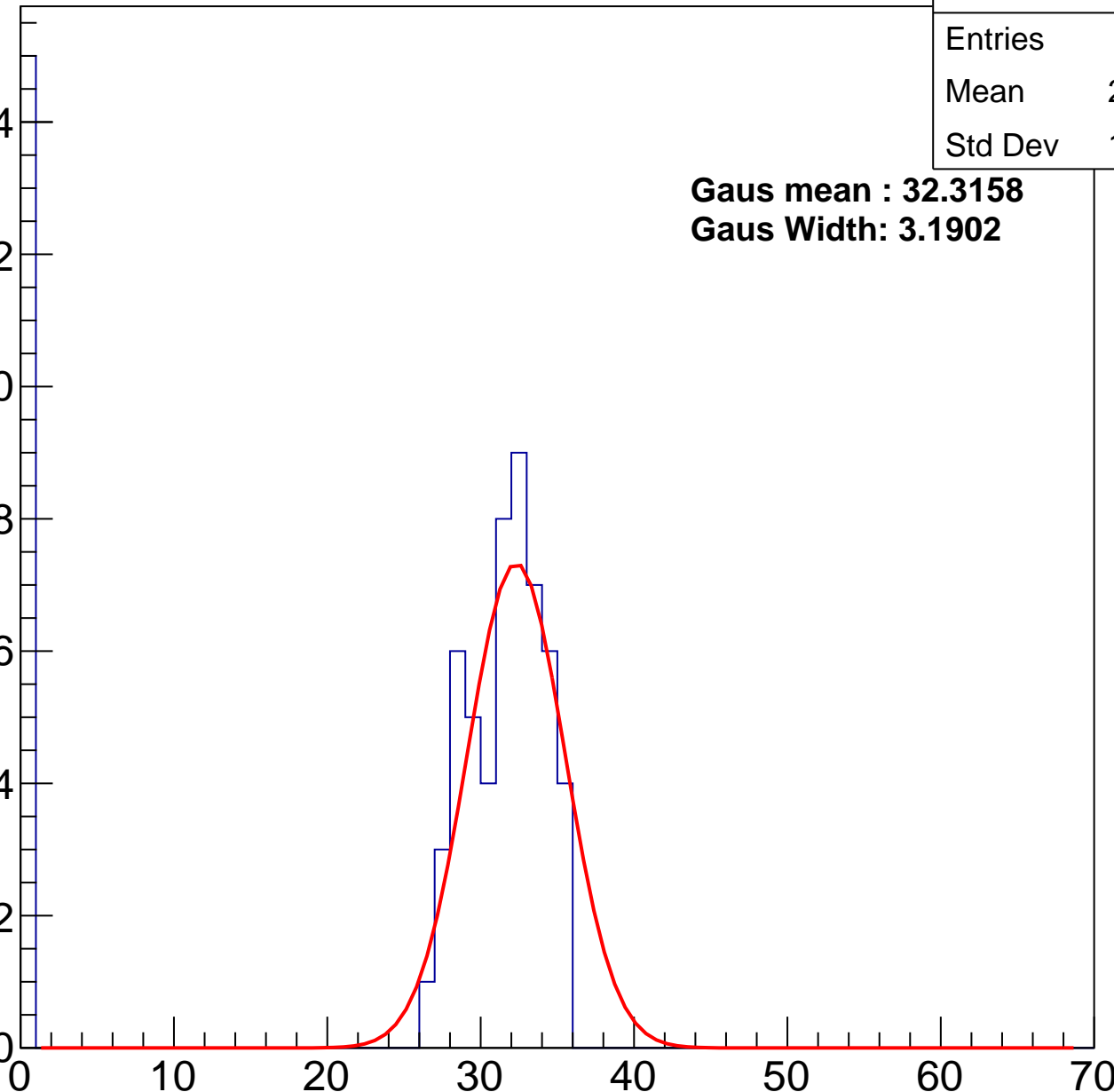
Gaus mean : 32.3158

Gaus Width: 3.1902

Entry

14
12
10
8
6
4
2
0

ampl

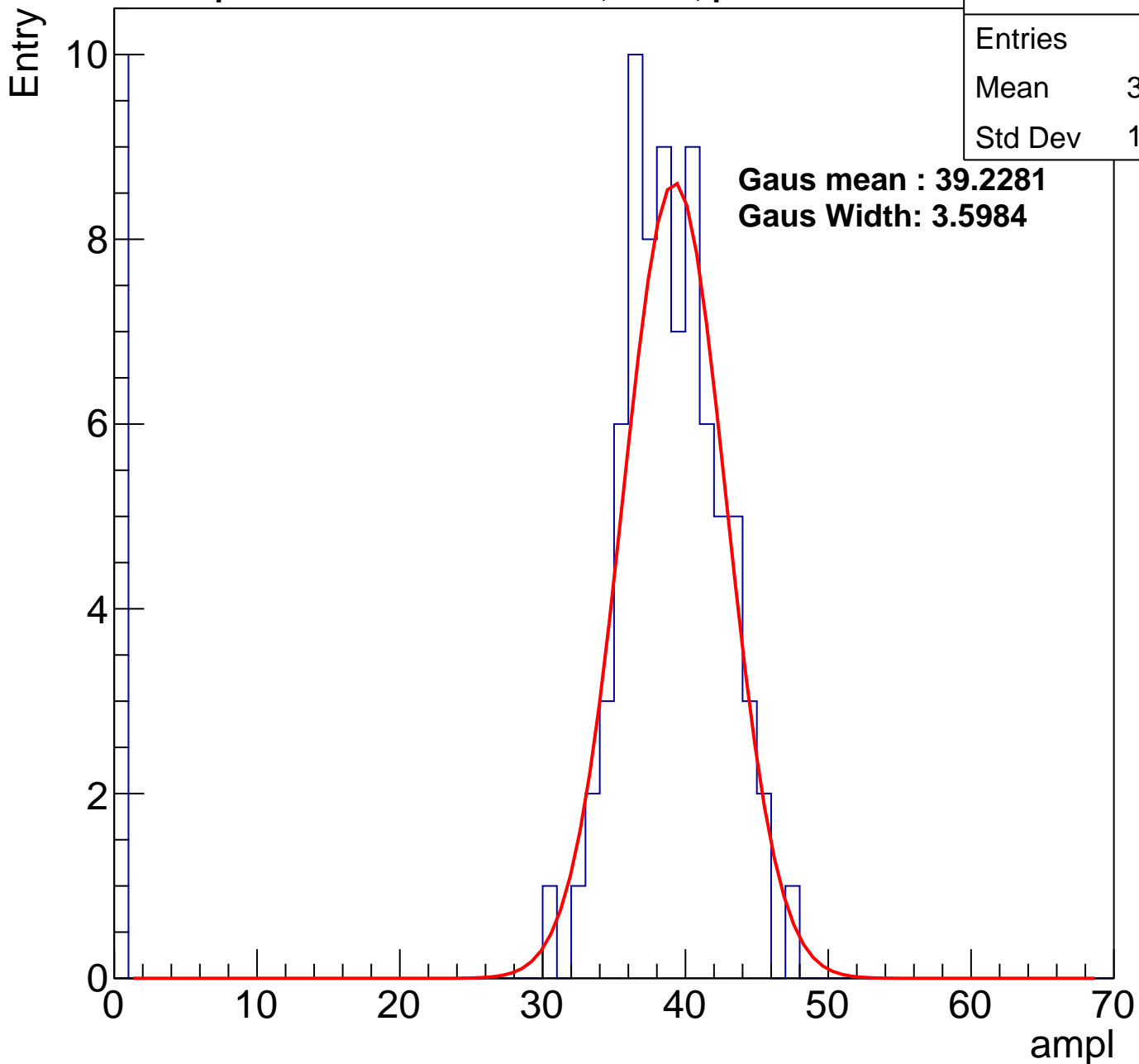


B1L103S, U10-ch29, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	34.22
Std Dev	12.64

Gaus mean : 39.2281
Gaus Width: 3.5984



B1L103S, U10-ch29, adc2

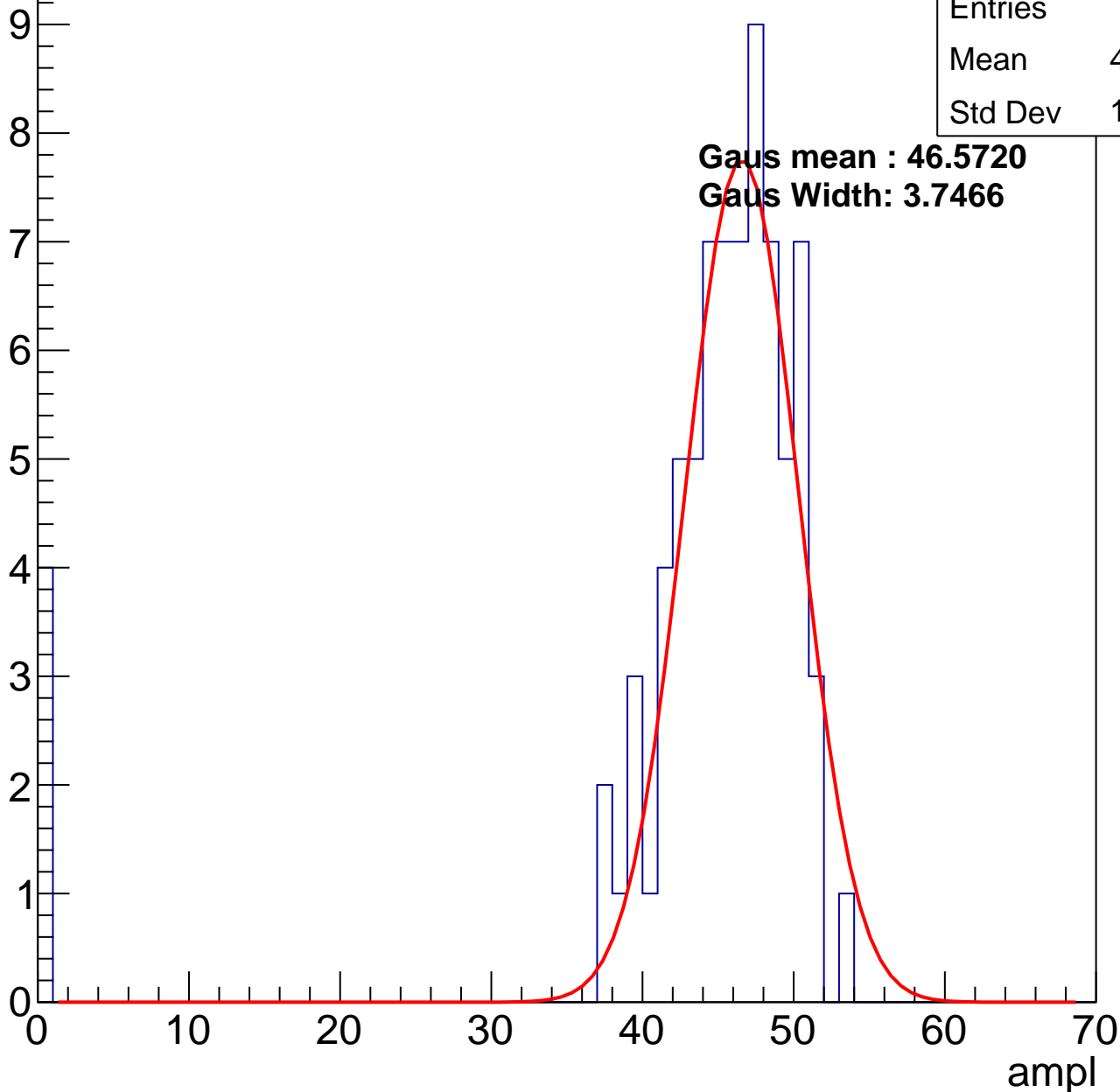
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.12
Std Dev	10.62

Gaus mean : 46.5720

Gaus Width: 3.7466

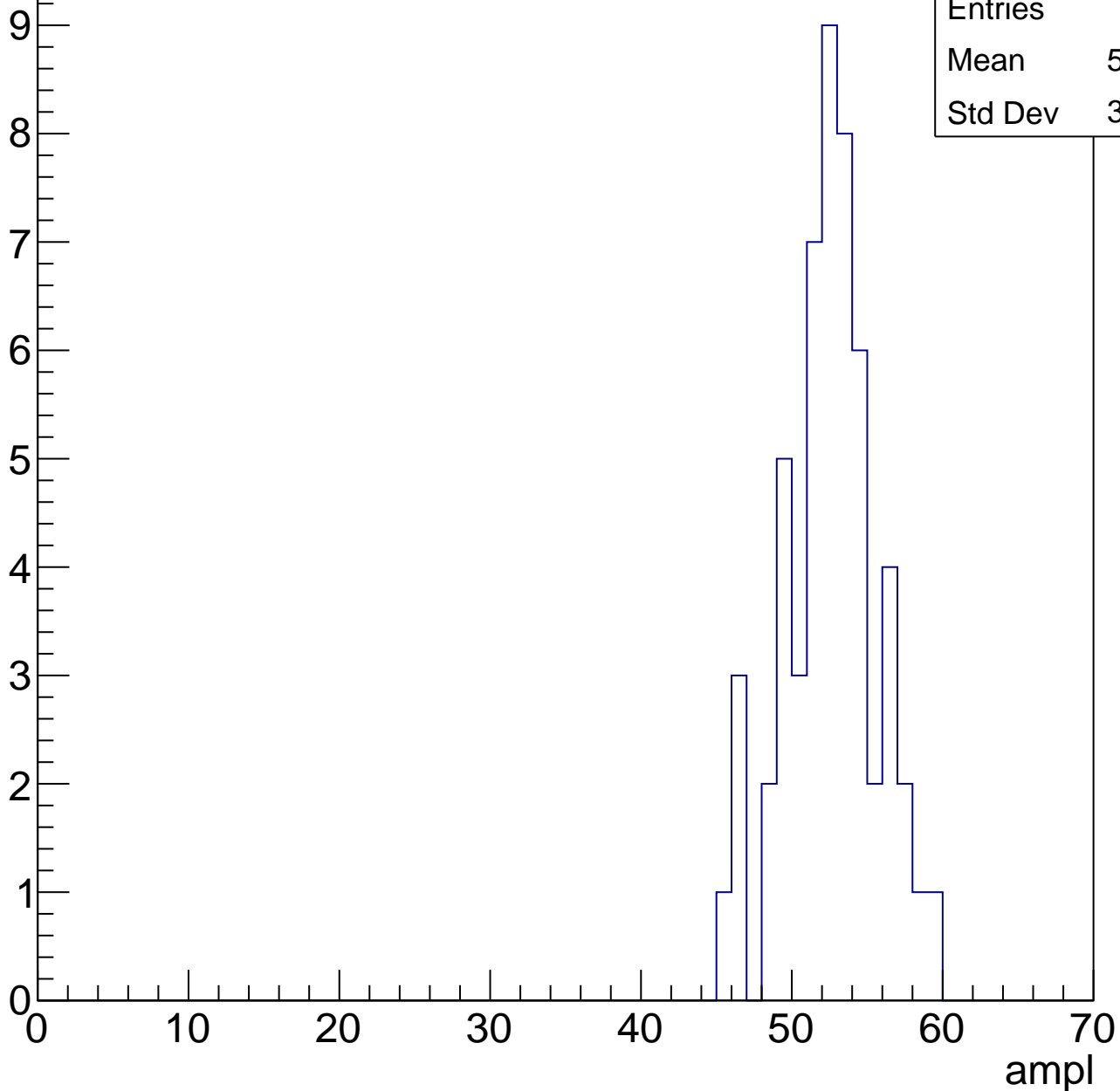


B1L103S, U10-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

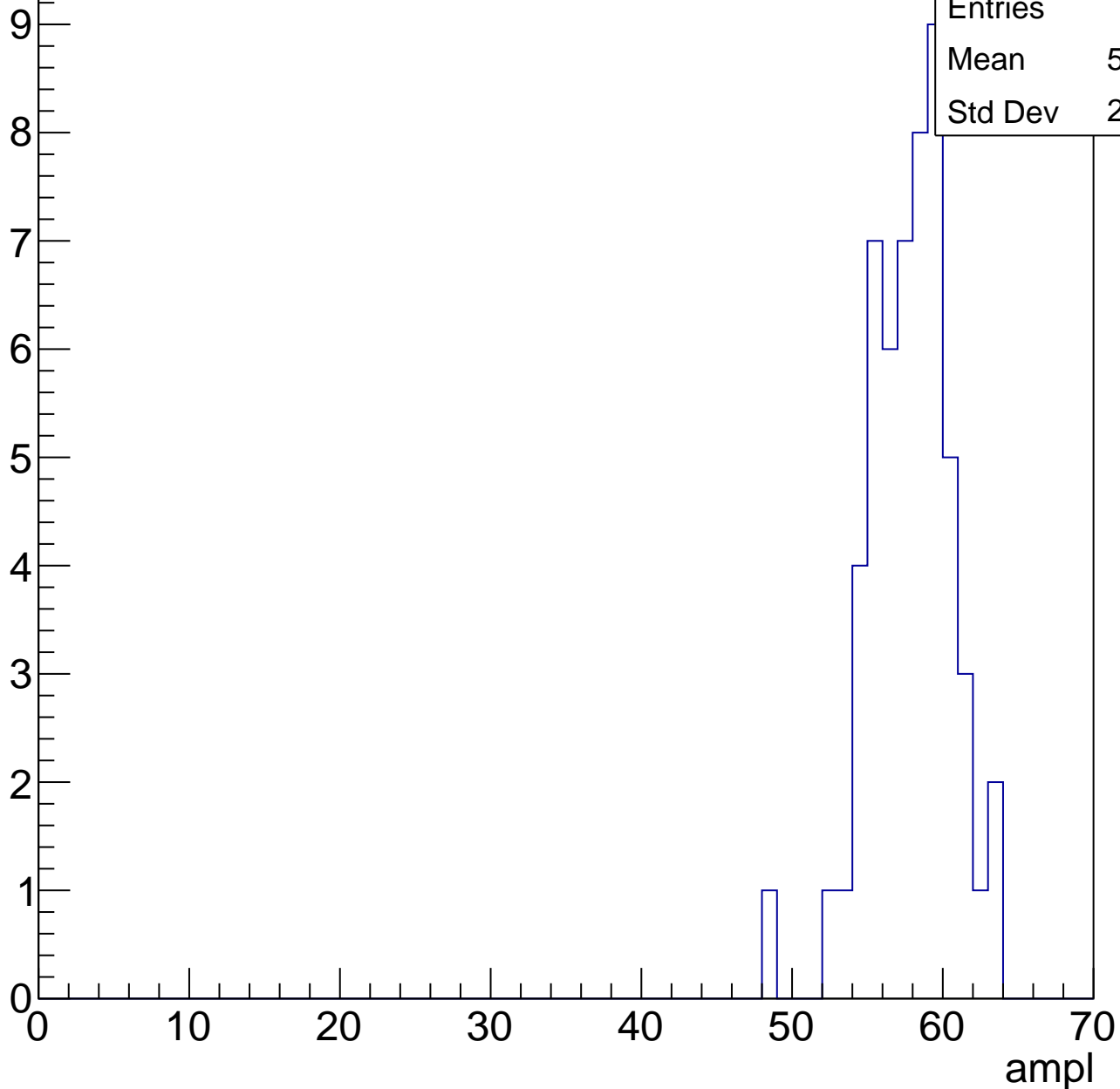
Entries	54
Mean	52.07
Std Dev	3.048



B1L103S, U10-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

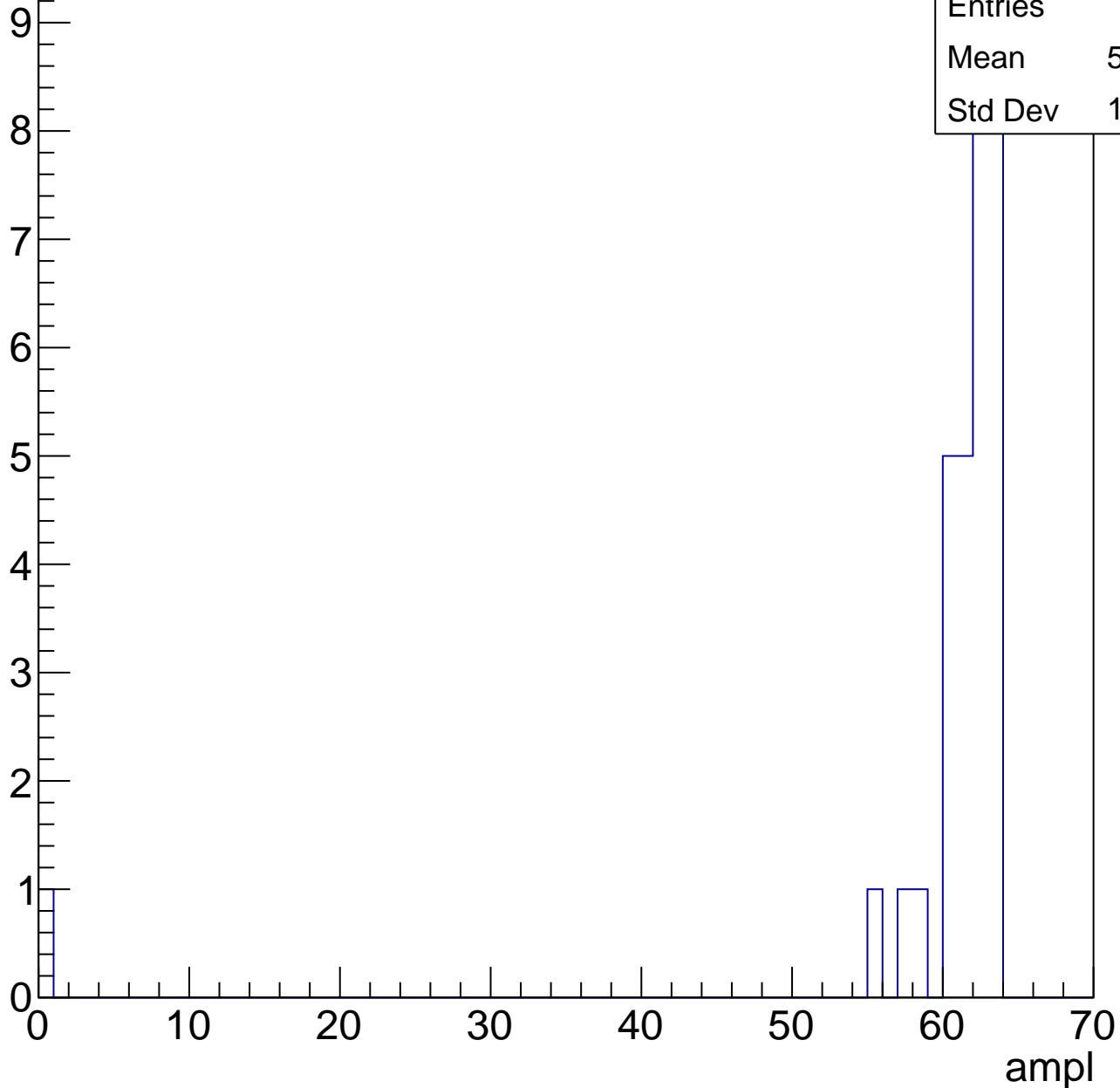
Entry



B1L103S, U10-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

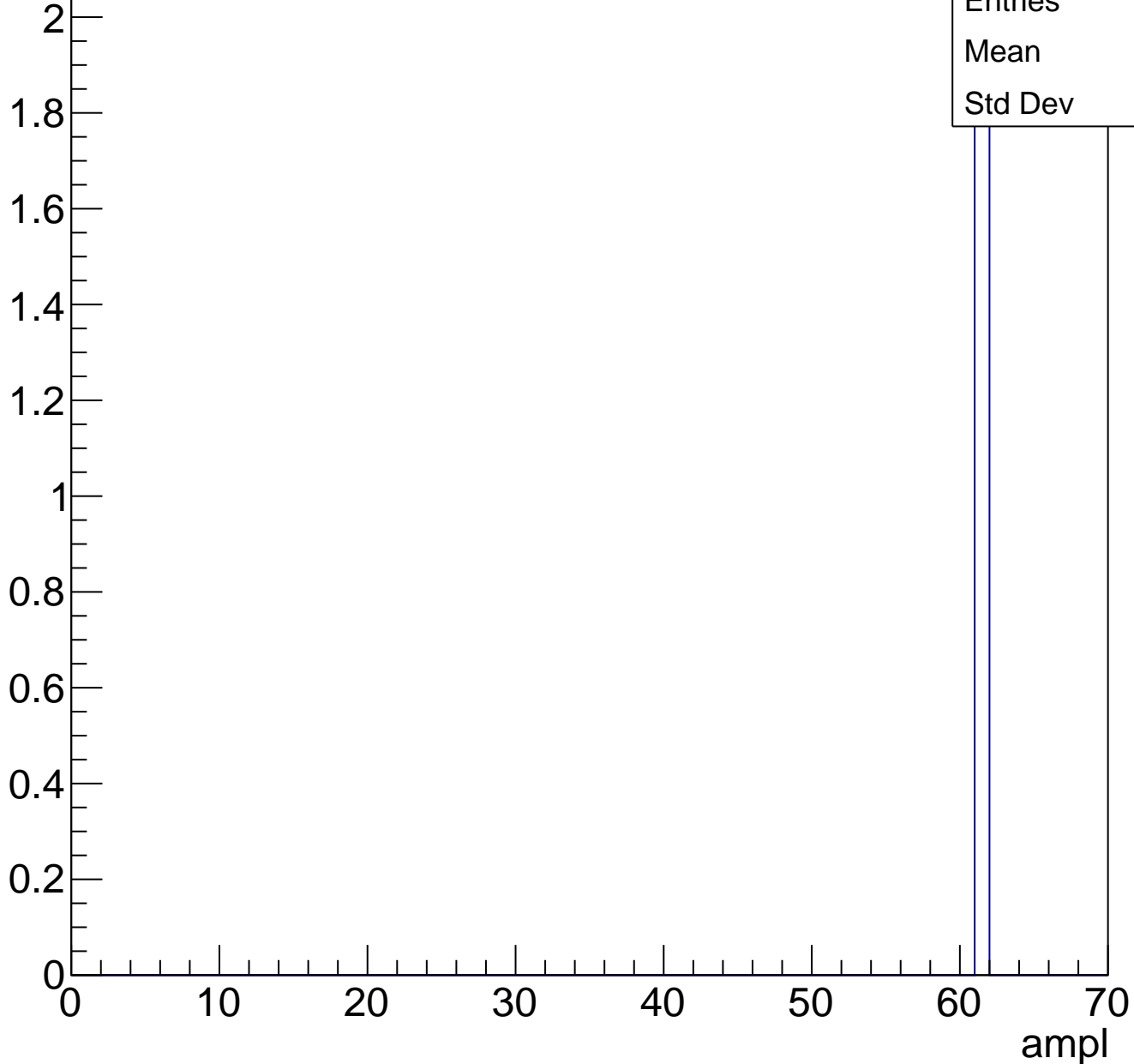
Entry



B1L103S, U10-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch30, adc0

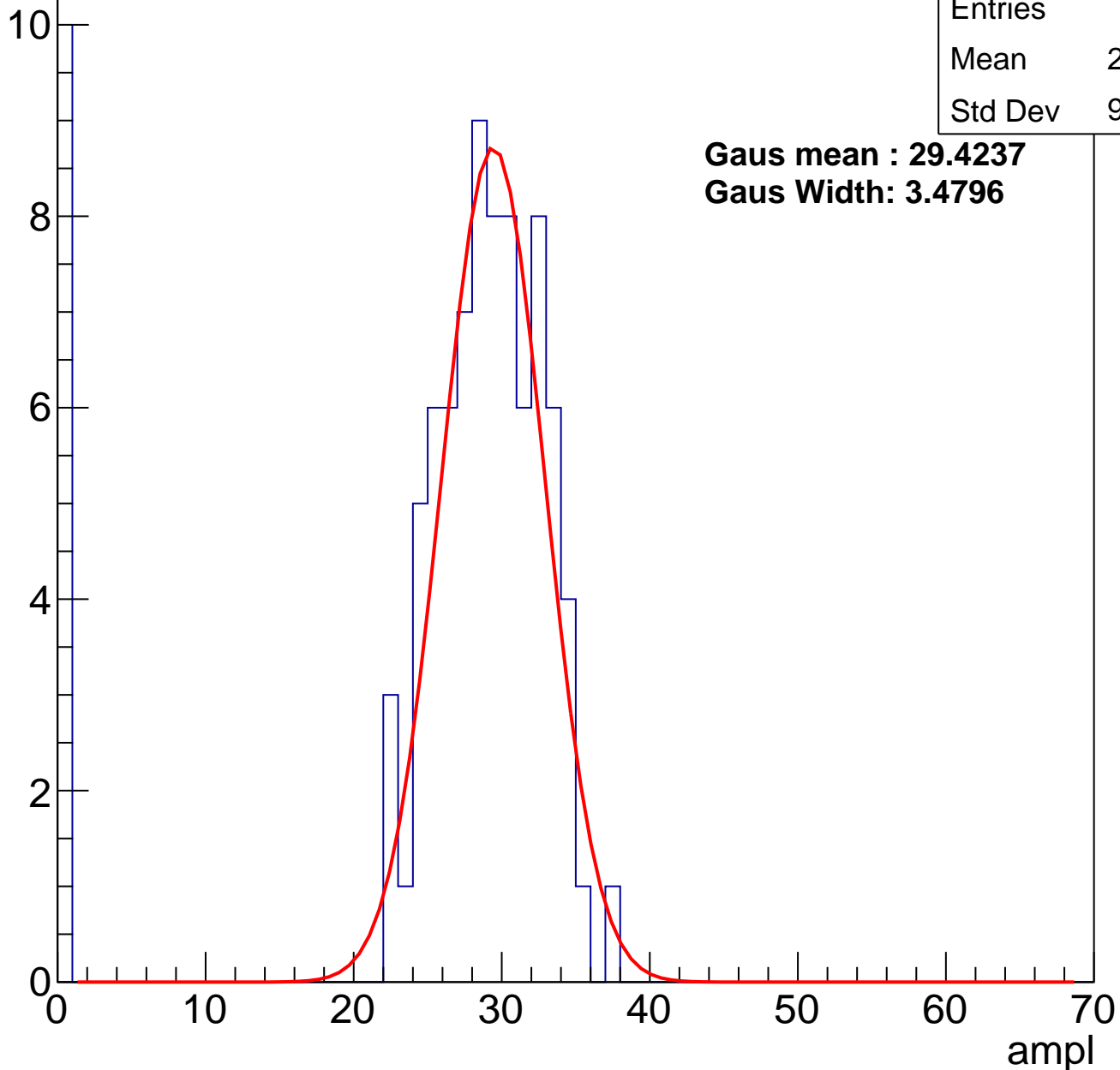
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.57
Std Dev	9.634

Gaus mean : 29.4237

Gaus Width: 3.4796

Entry



B1L103S, U10-ch30, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	31.66
Std Dev	13.34

Gaus mean : 37.1472

Gaus Width: 3.5225

Entry

12

10

8

6

4

2

0

0

10

20

30

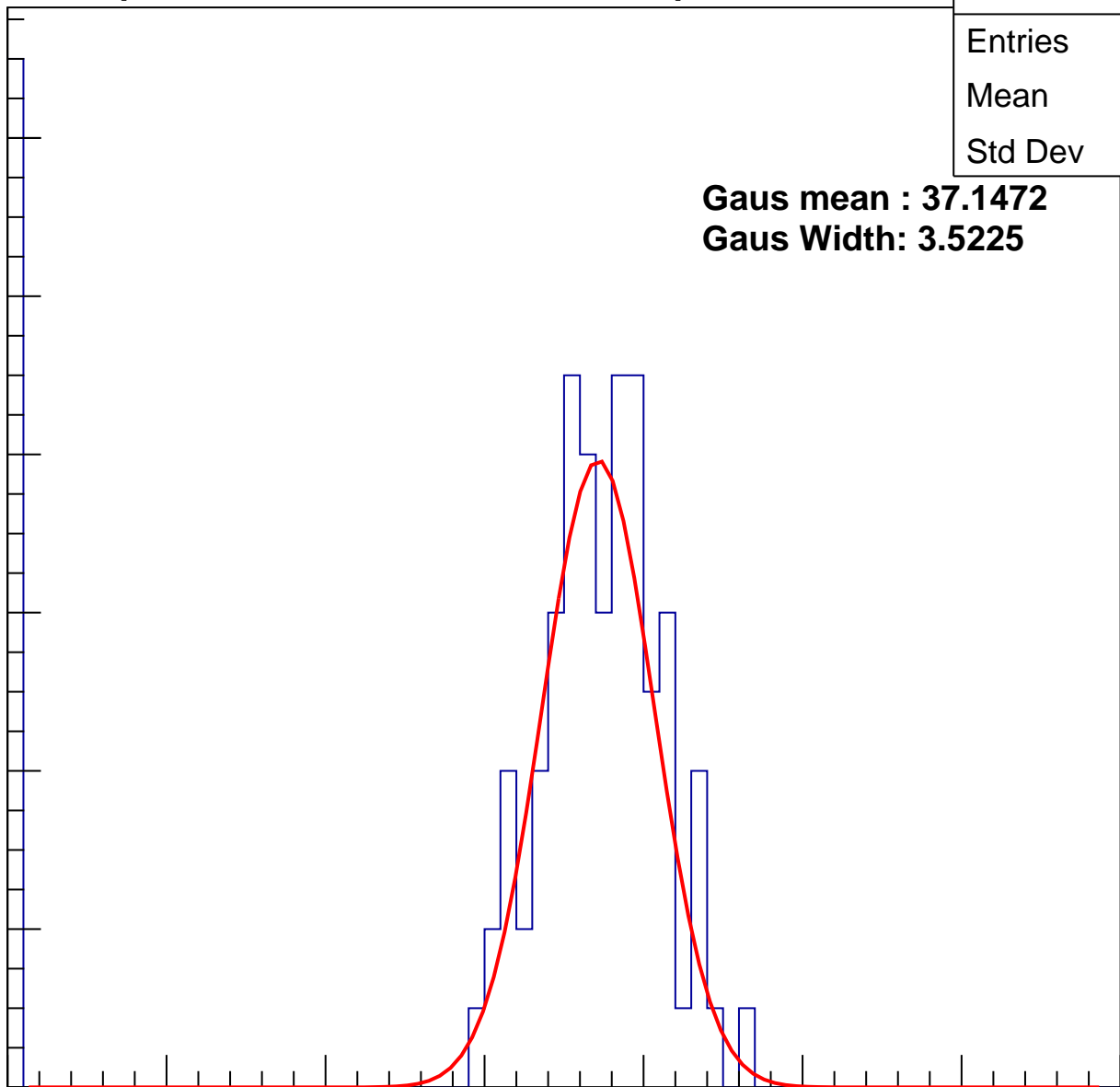
40

50

60

70

ampl



B1L103S, U10-ch30, adc2

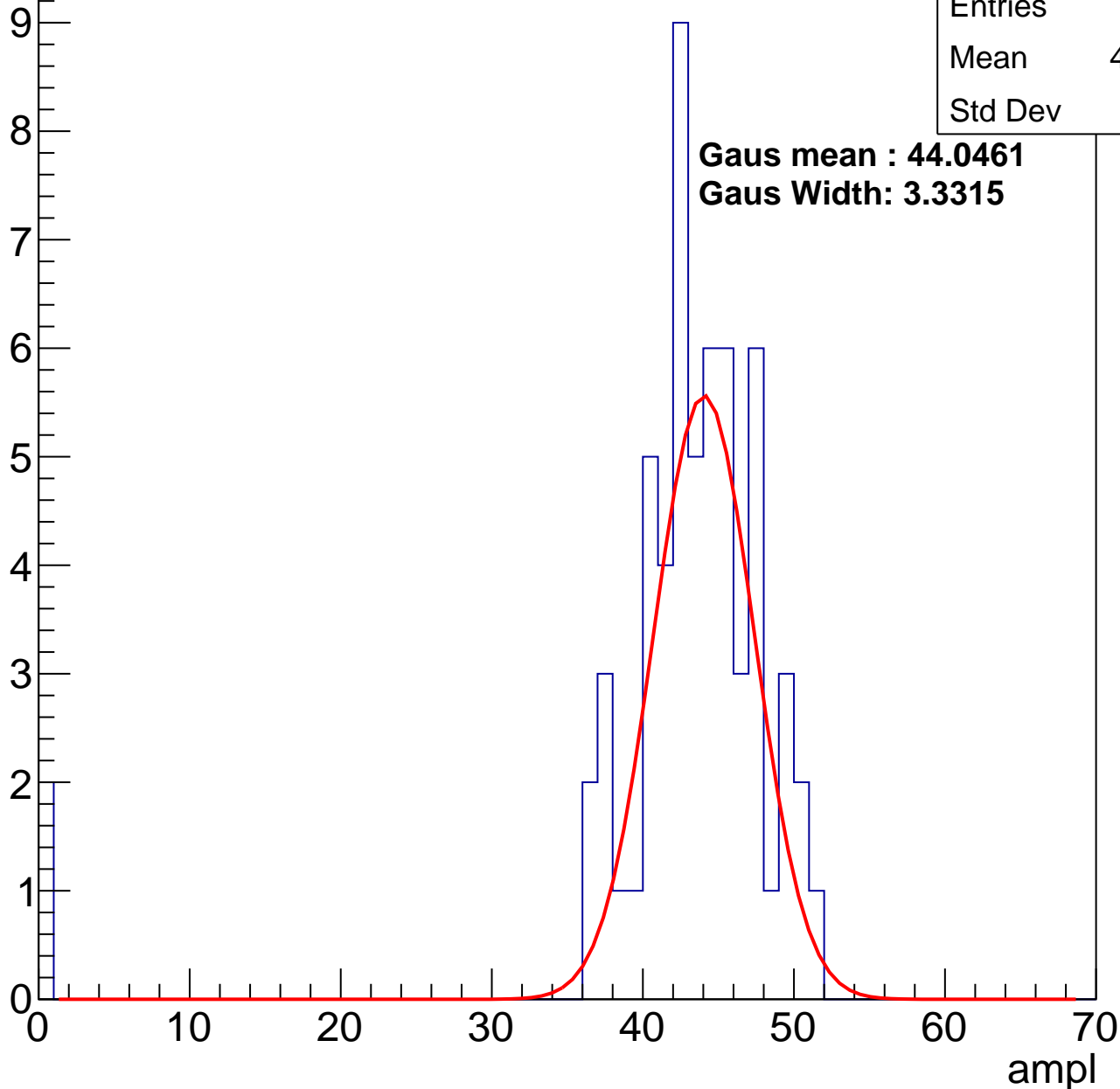
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	41.95
Std Dev	8.56

Gaus mean : 44.0461

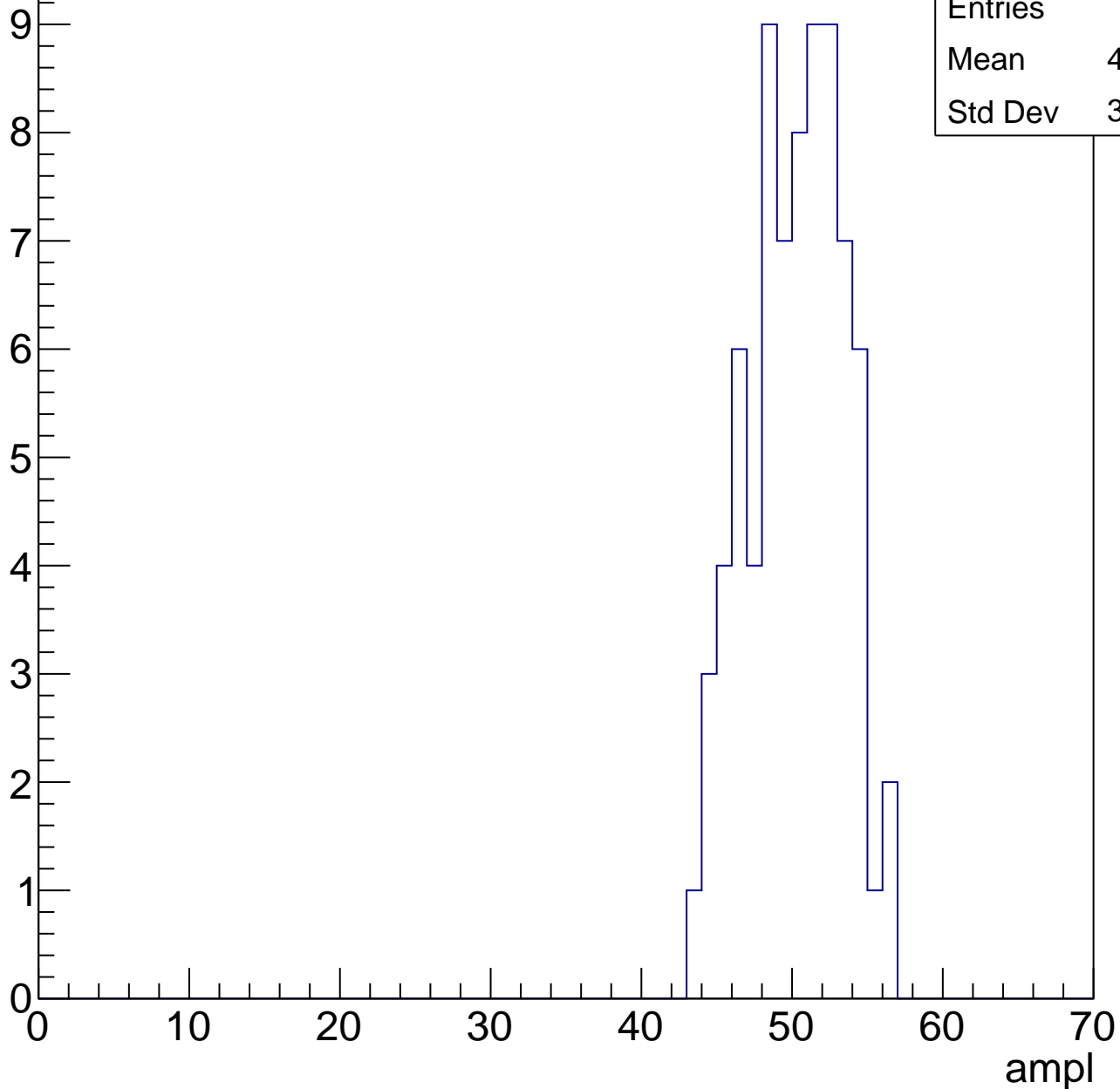
Gaus Width: 3.3315



B1L103S, U10-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

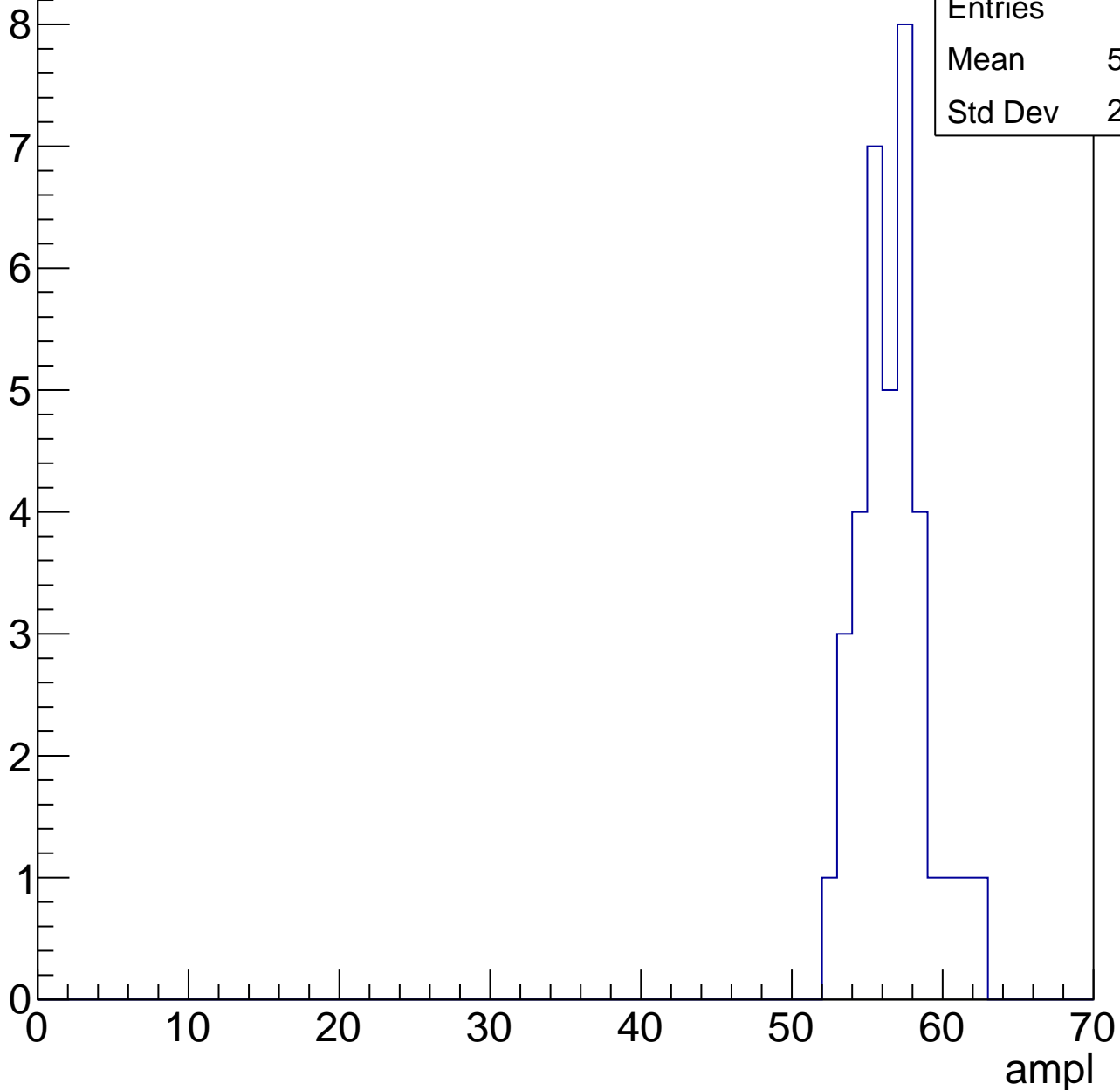


B1L103S, U10-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	56.17
Std Dev	2.205



B1L103S, U10-ch30, adc5

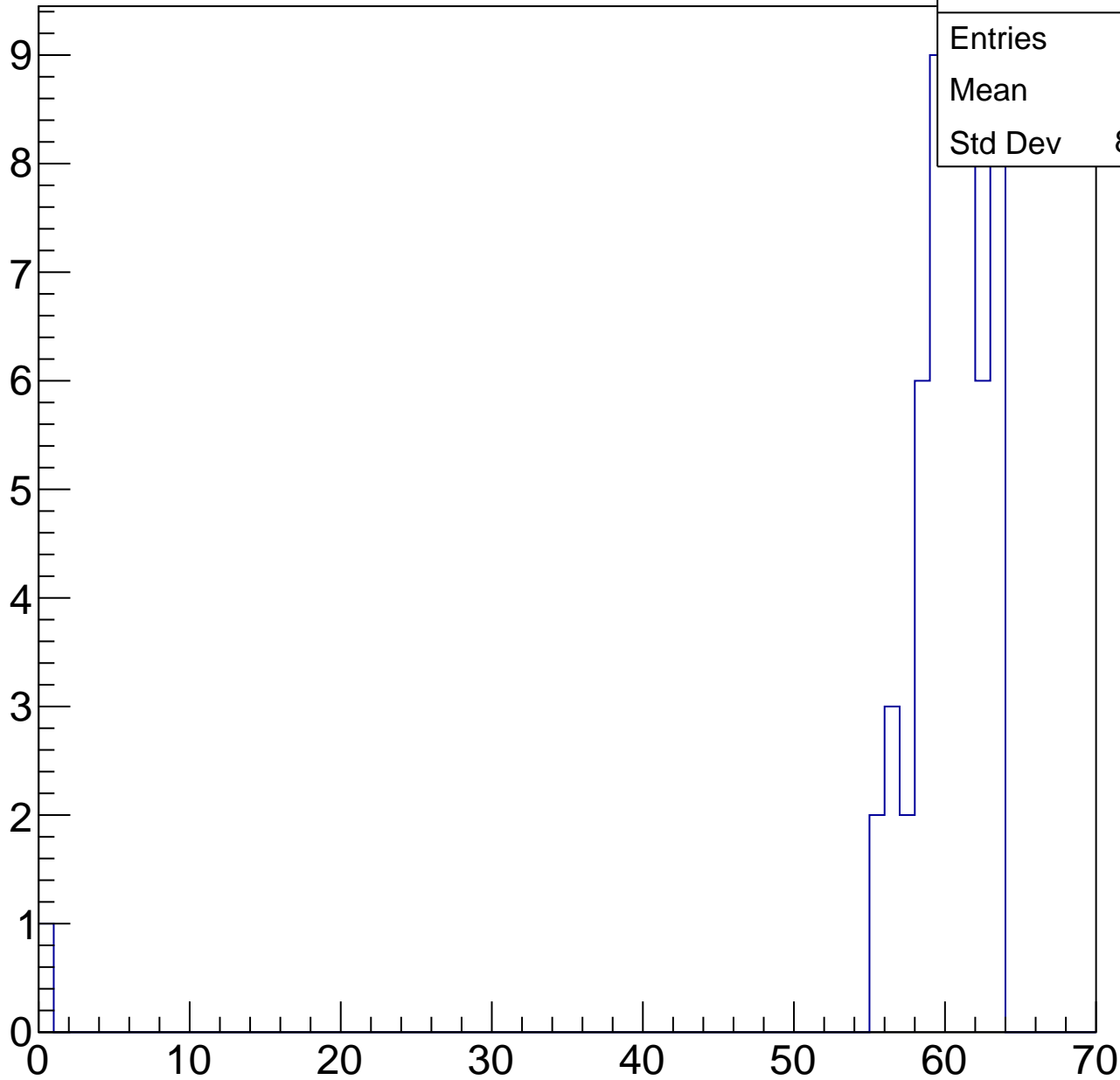
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.8
Std Dev	8.361

ampl



B1L103S, U10-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch31, adc0

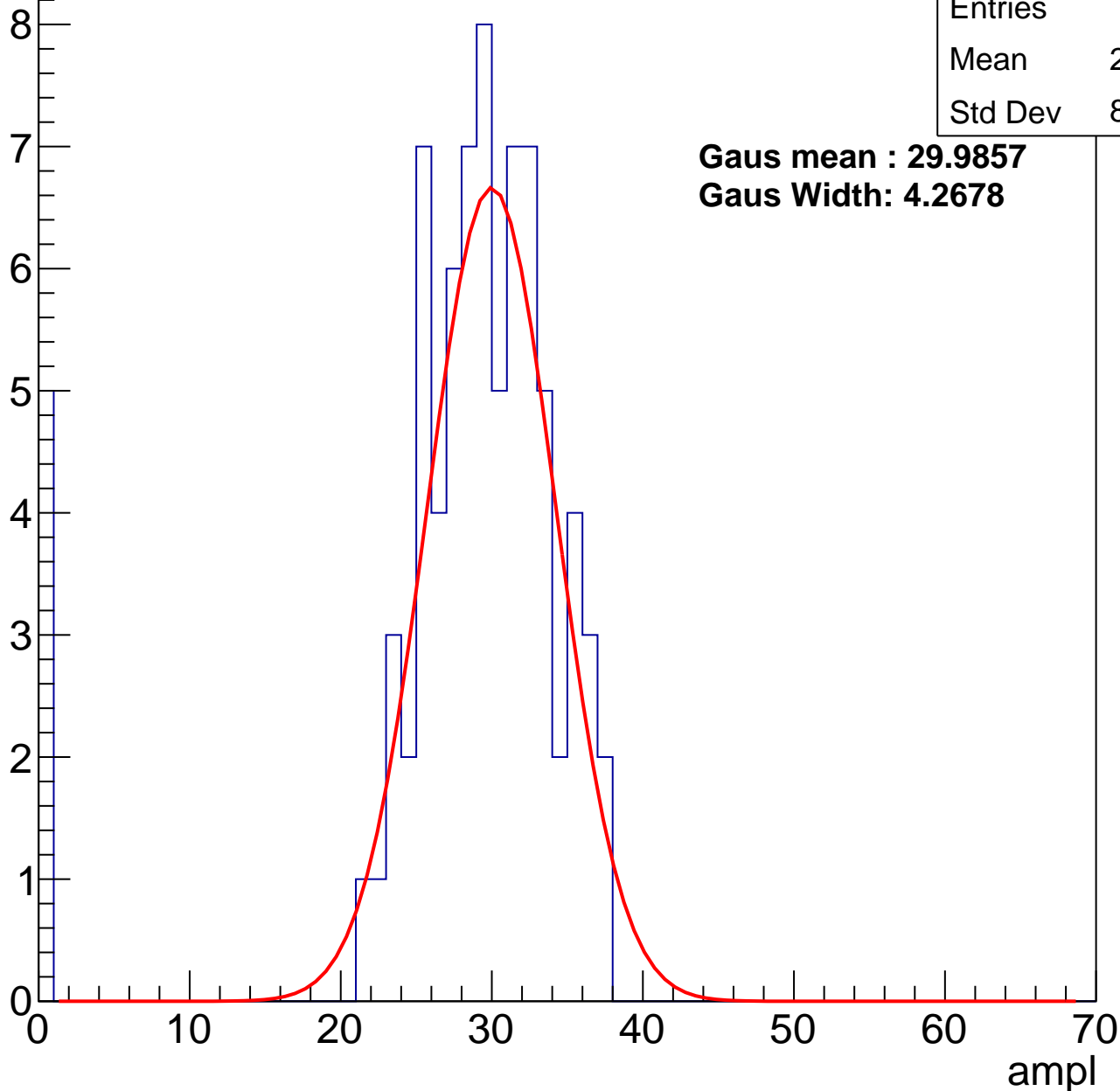
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	27.53
Std Dev	8.058

Gaus mean : 29.9857

Gaus Width: 4.2678



B1L103S, U10-ch31, adc1

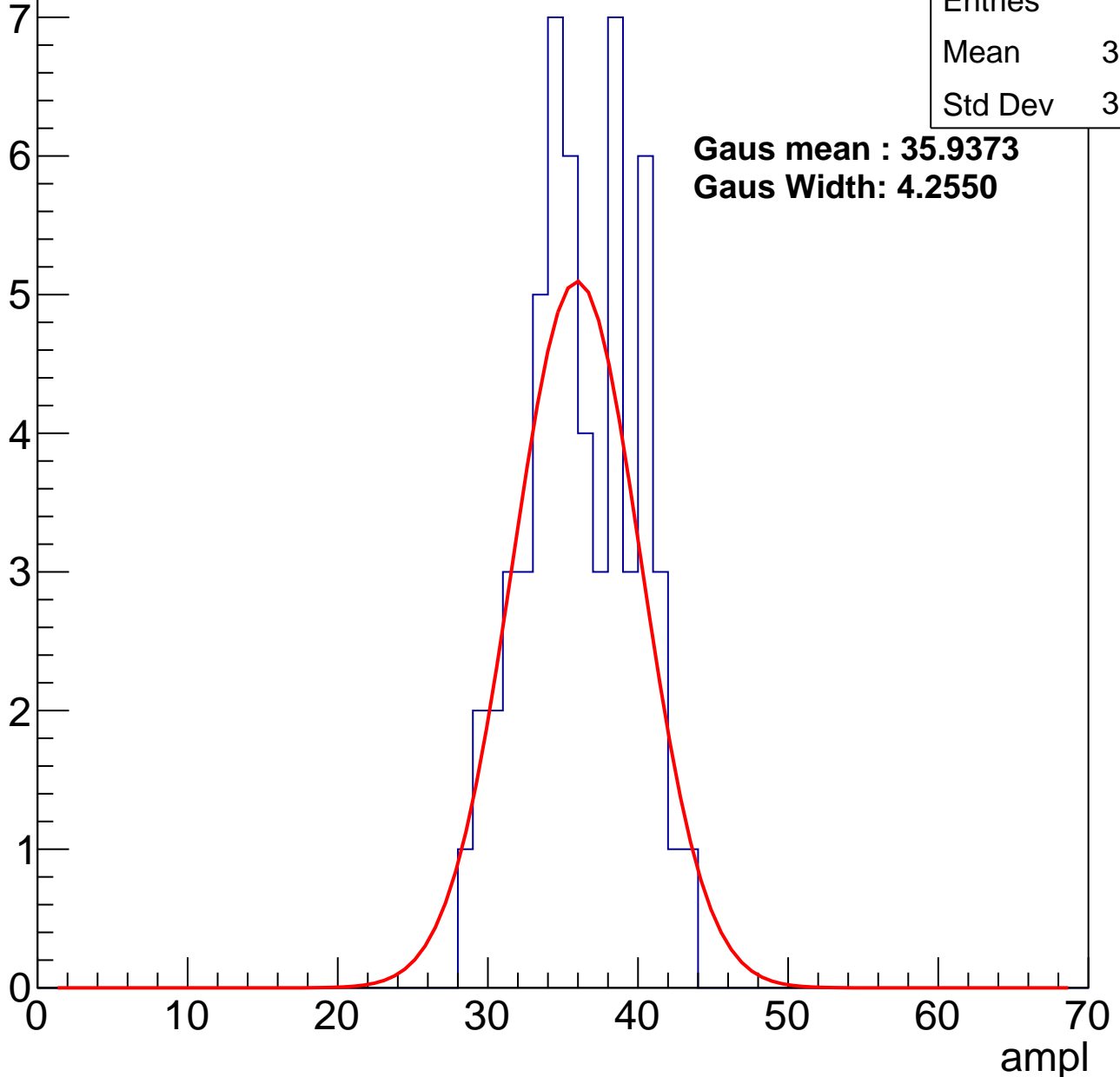
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	35.68
Std Dev	3.614

Gaus mean : 35.9373

Gaus Width: 4.2550



B1L103S, U10-ch31, adc2

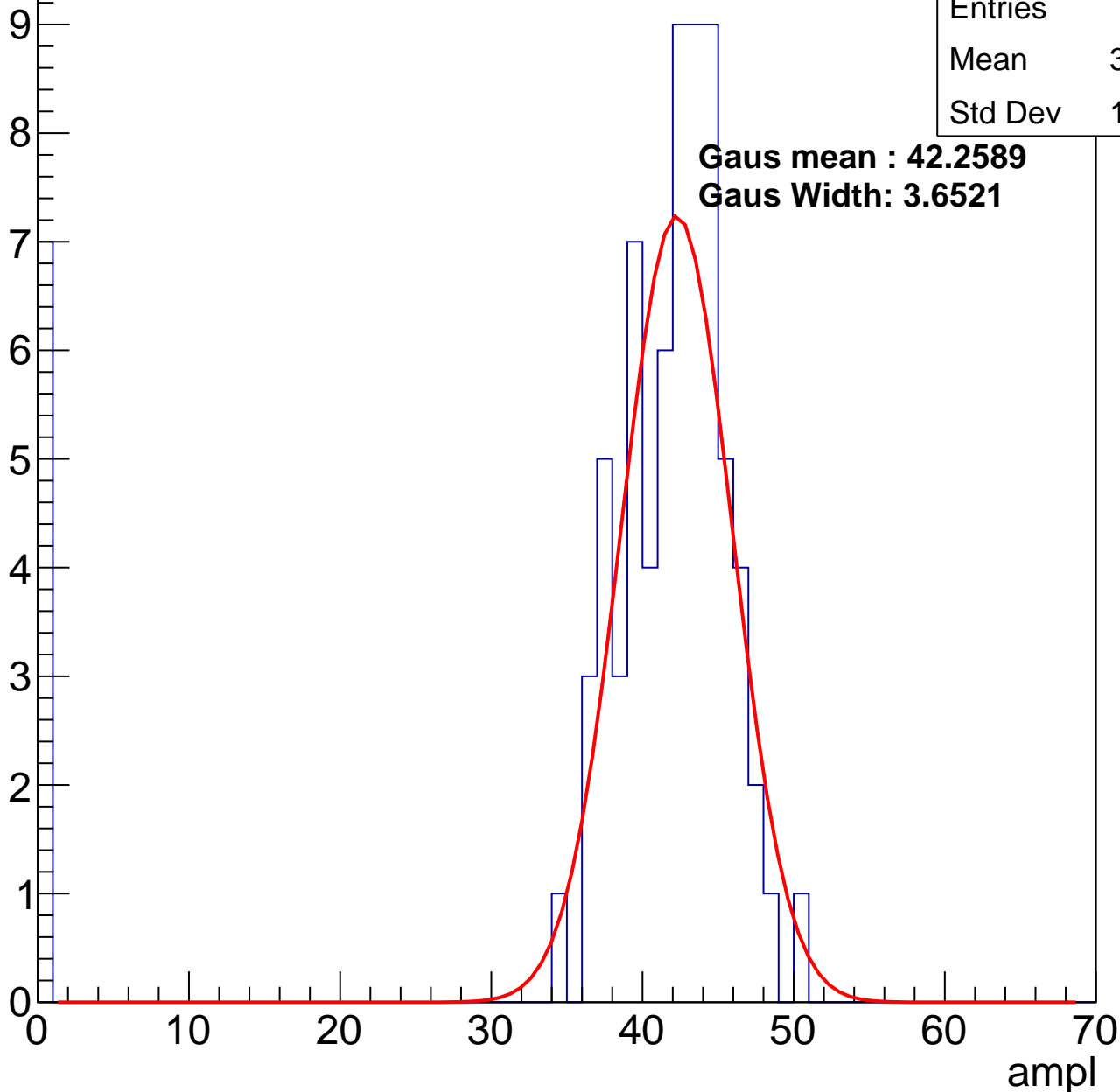
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	37.92
Std Dev	12.47

Gaus mean : 42.2589

Gaus Width: 3.6521



B1L103S, U10-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	46.51
Std Dev	10.58

Entry

10

8

6

4

2

0

ampl

0

10

20

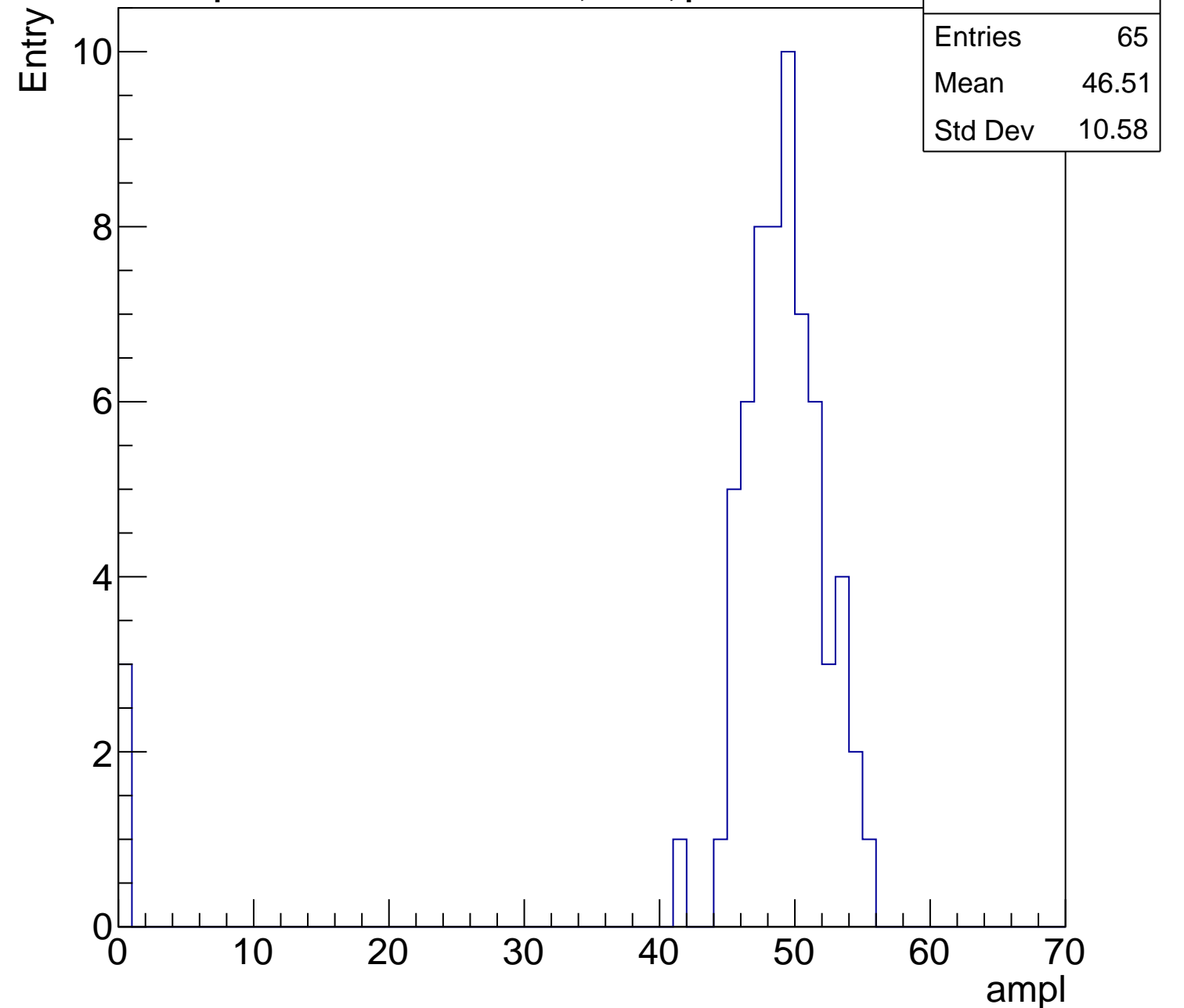
30

40

50

60

70

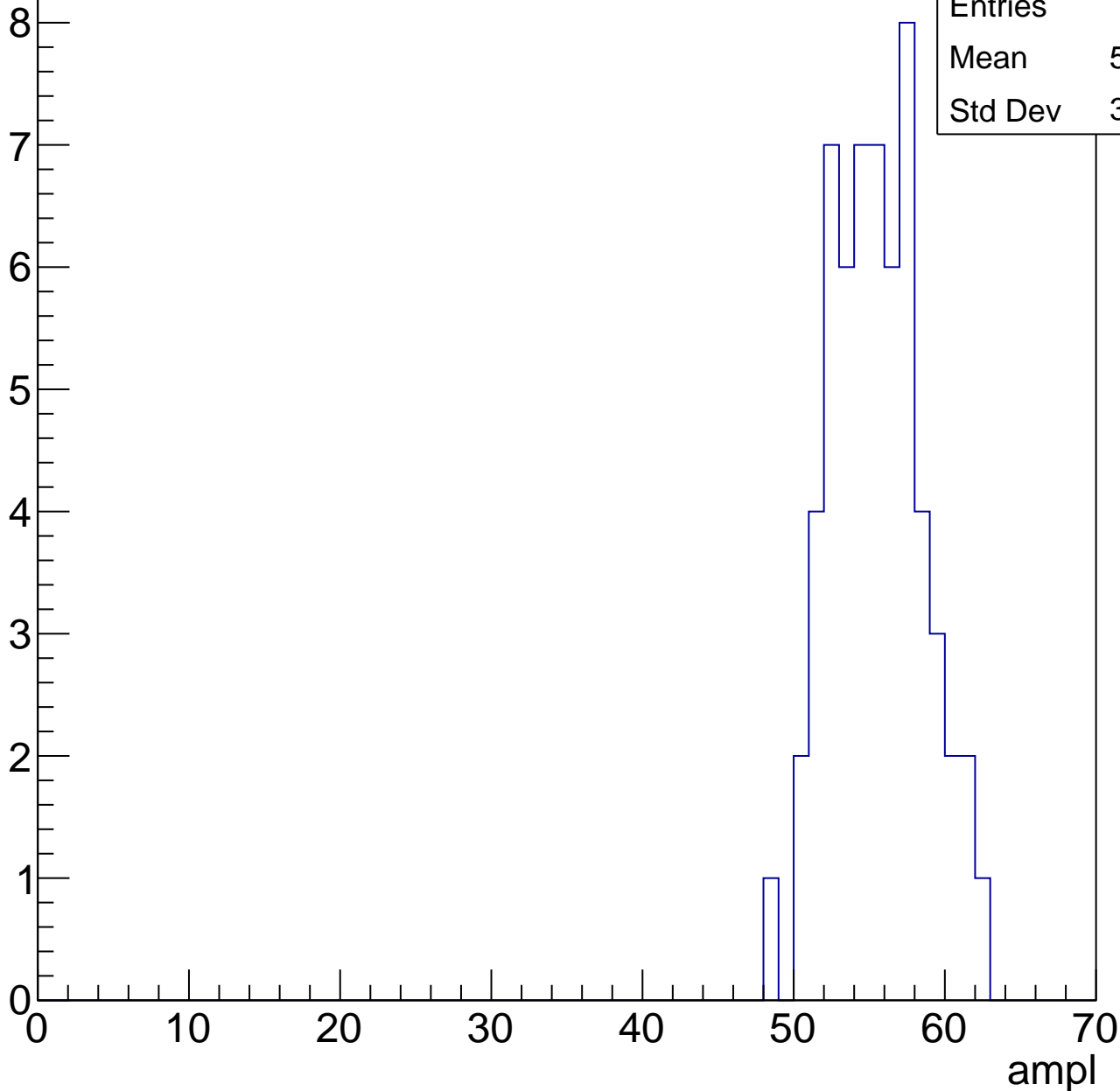


B1L103S, U10-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.03
Std Dev	3.027

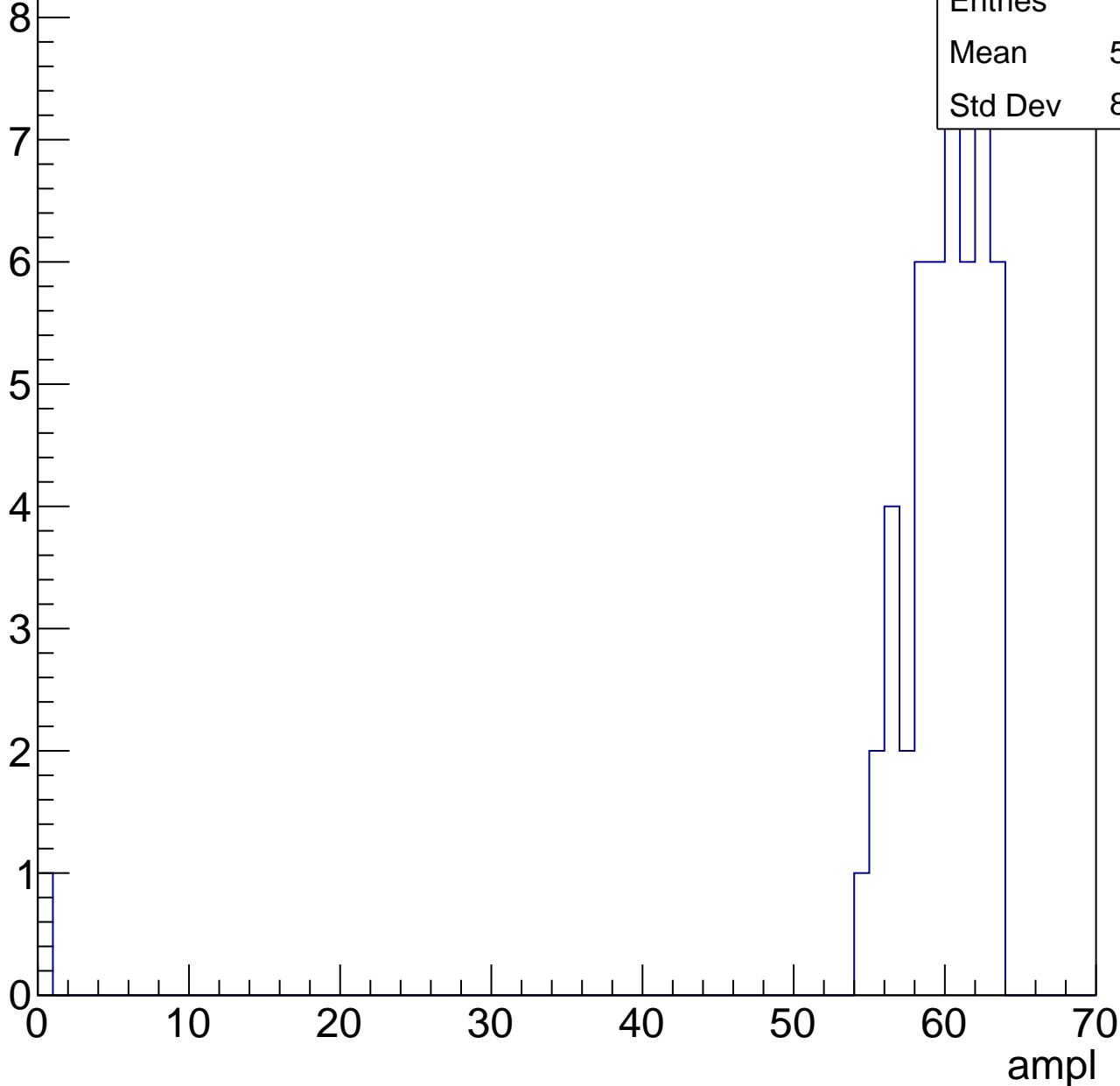


B1L103S, U10-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

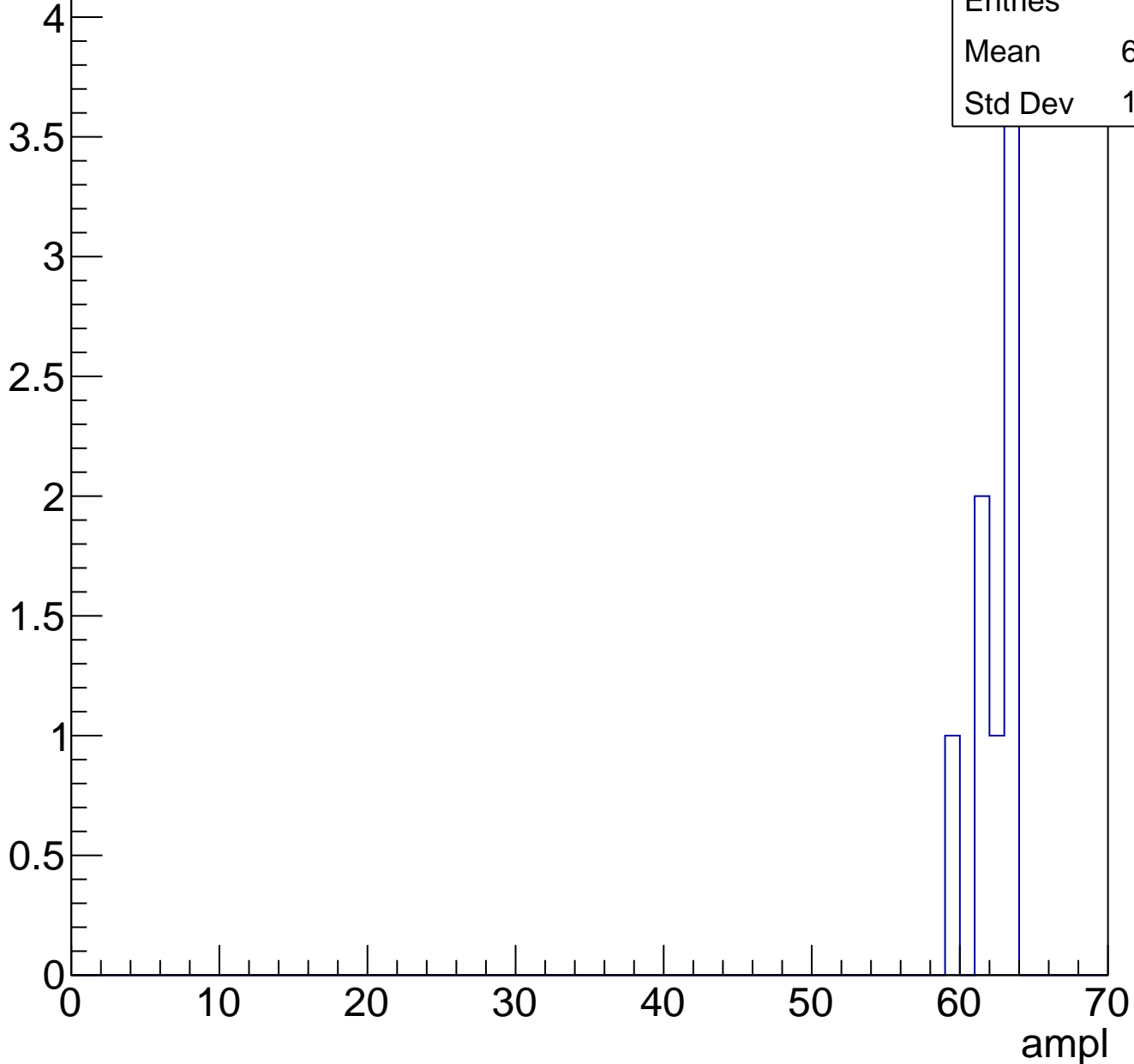
Entries	50
Mean	58.48
Std Dev	8.688



B1L103S, U10-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch32, adc0

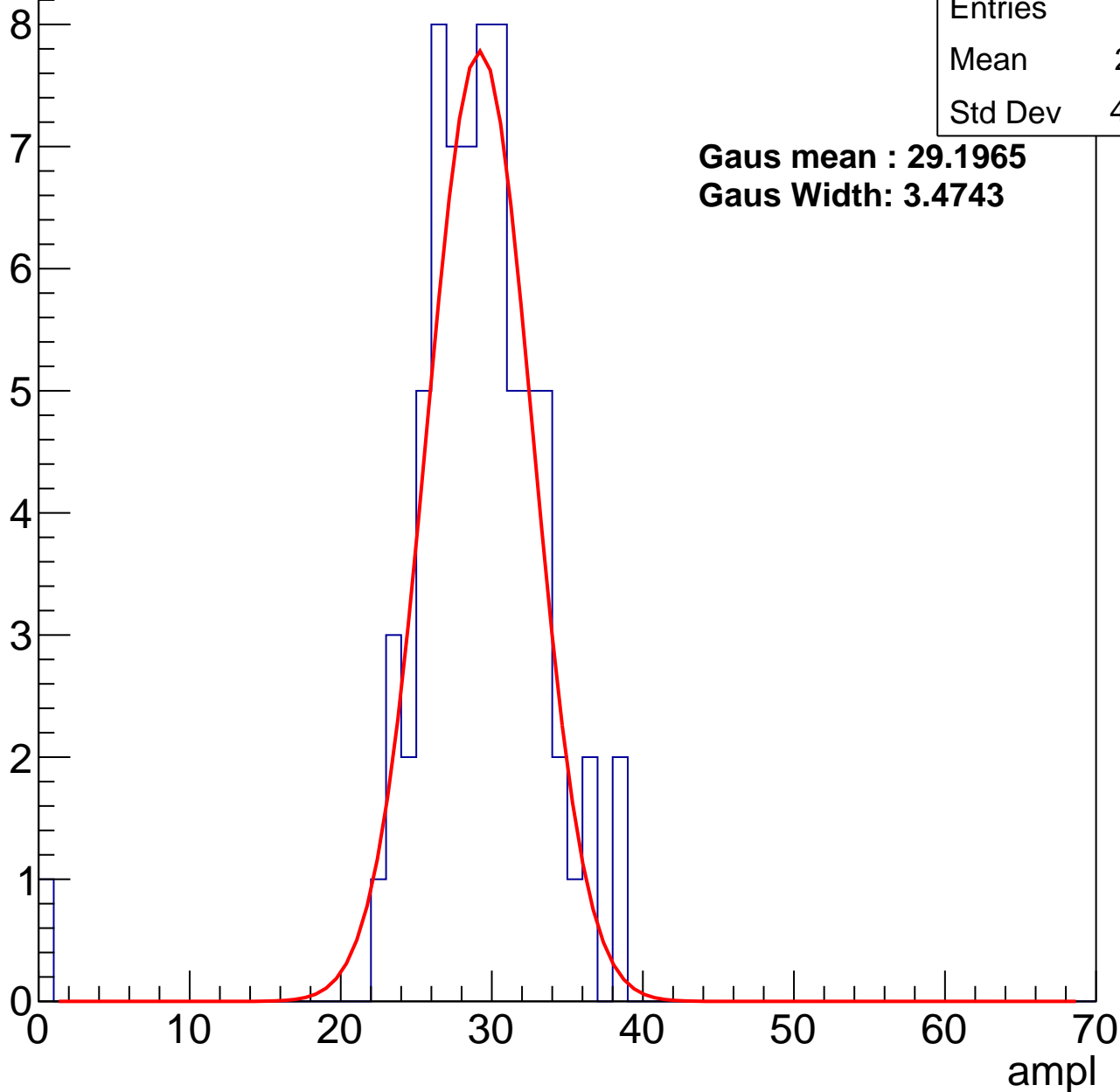
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	28.61
Std Dev	4.892

Gaus mean : 29.1665

Gaus Width: 3.4743



B1L103S, U10-ch32, adc1

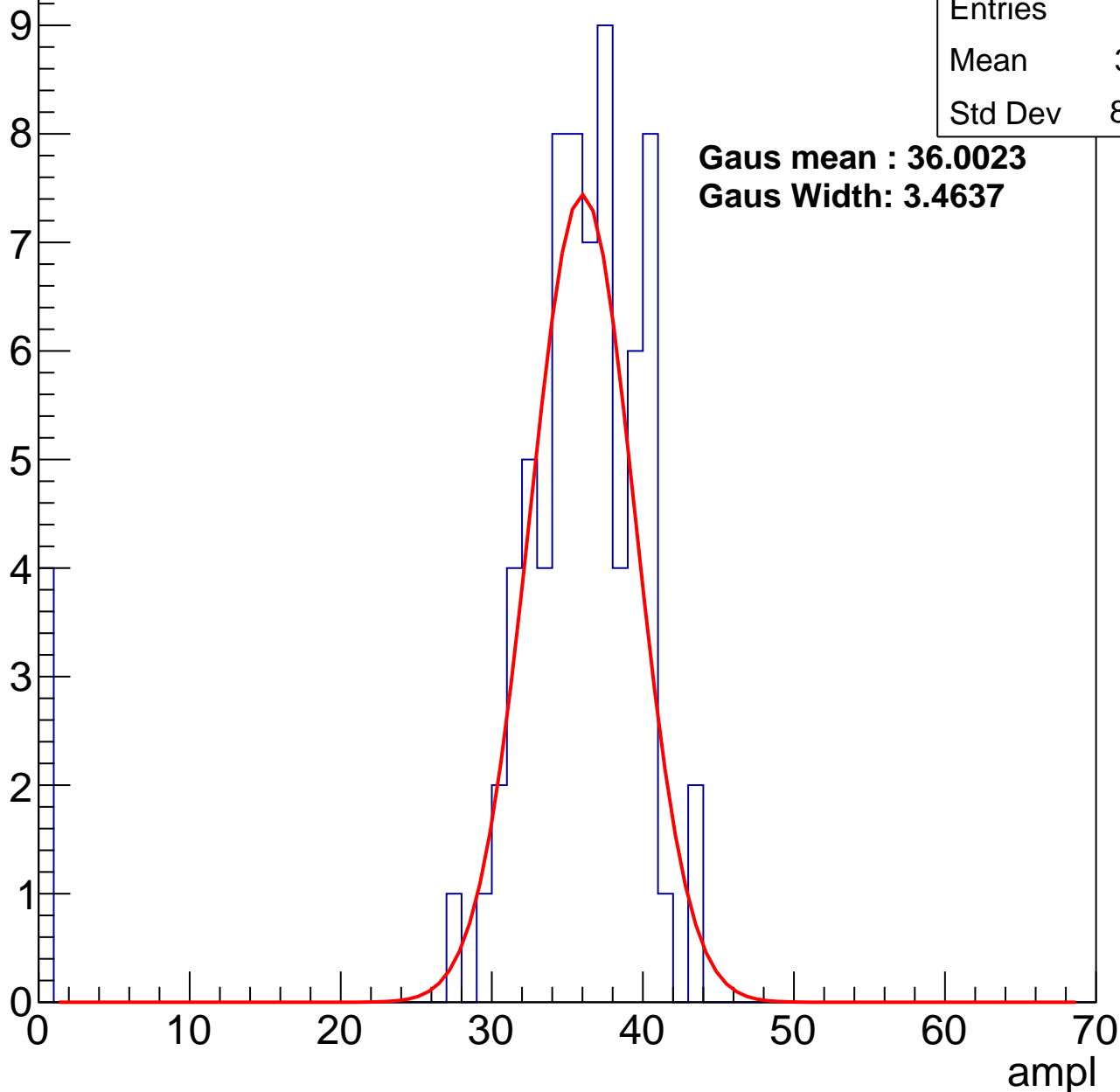
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	33.81
Std Dev	8.713

Gaus mean : 36.0023

Gaus Width: 3.4637



B1L103S, U10-ch32, adc2

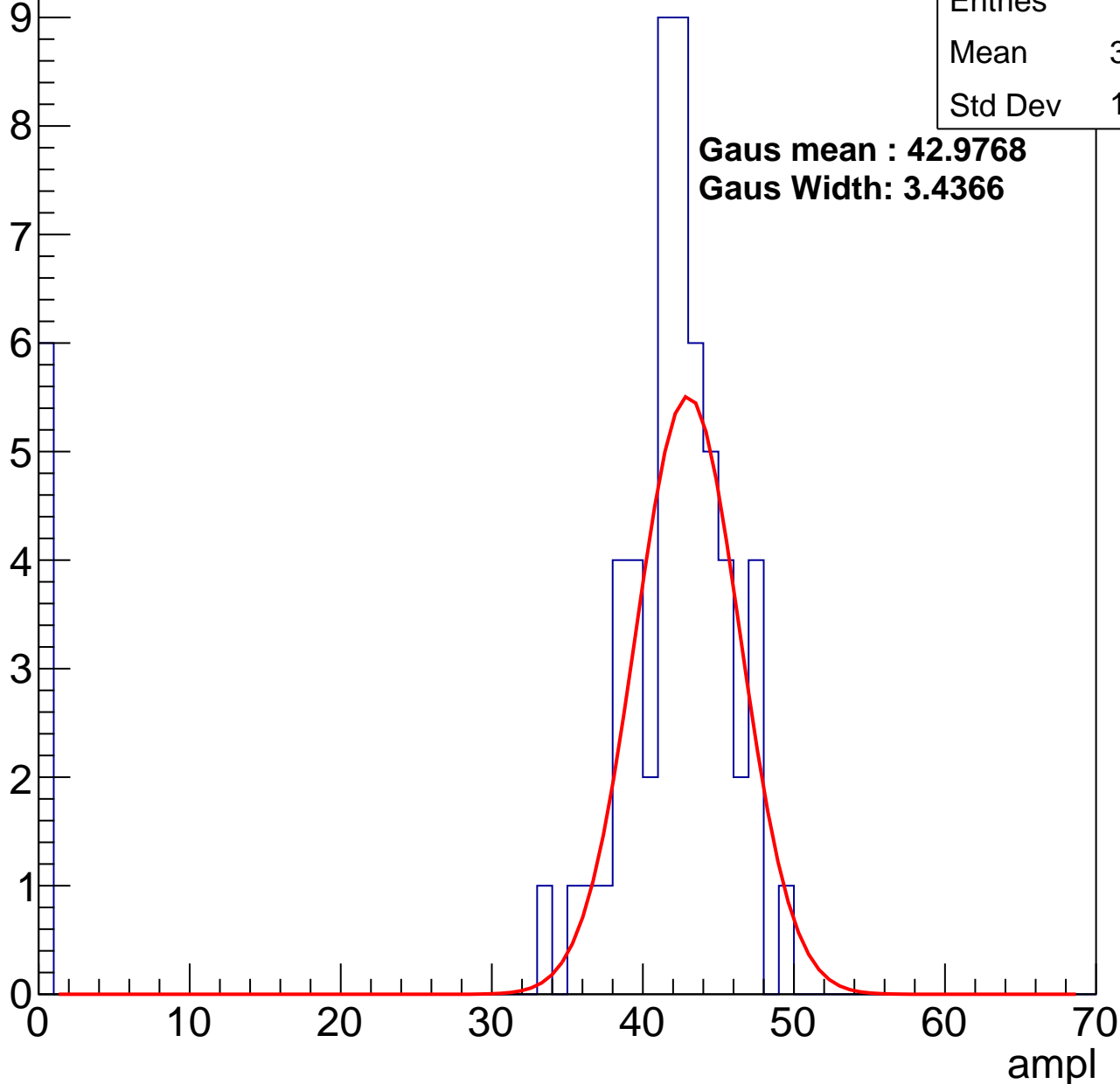
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	37.72
Std Dev	12.93

Gaus mean : 42.9768

Gaus Width: 3.4366



B1L103S, U10-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

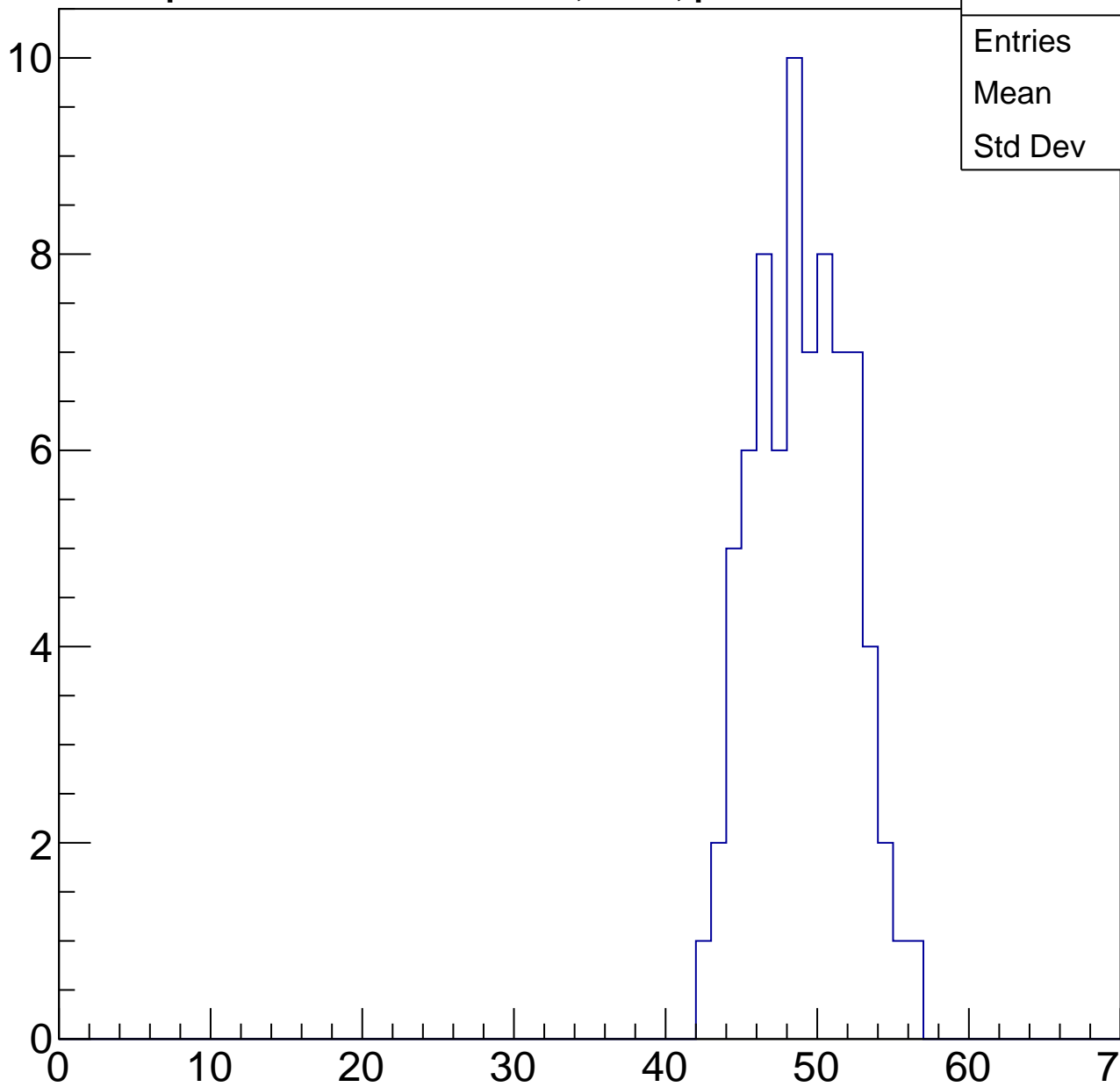
Entries	75
Mean	48.57
Std Dev	3.125

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

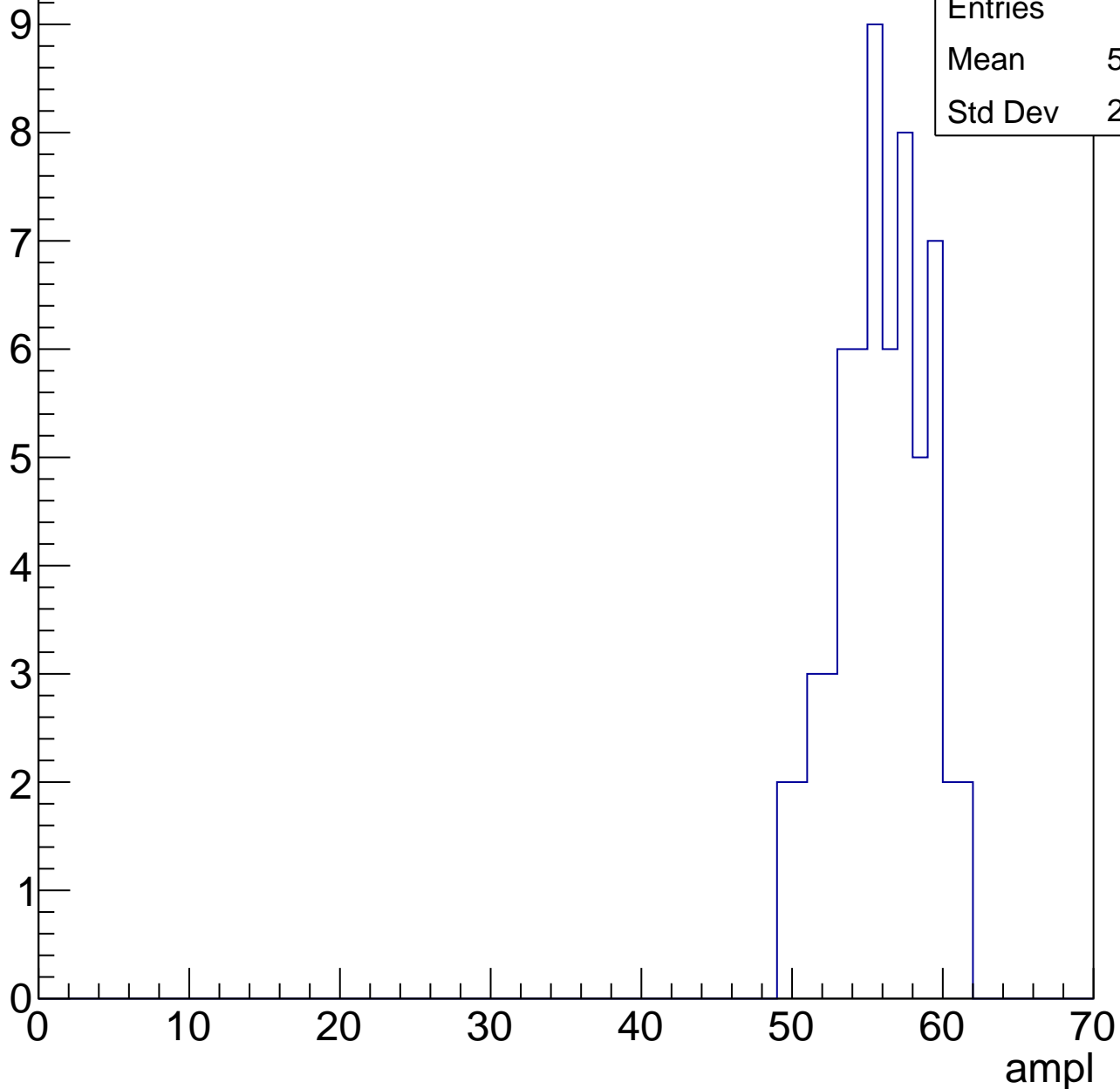
ampl



B1L103S, U10-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



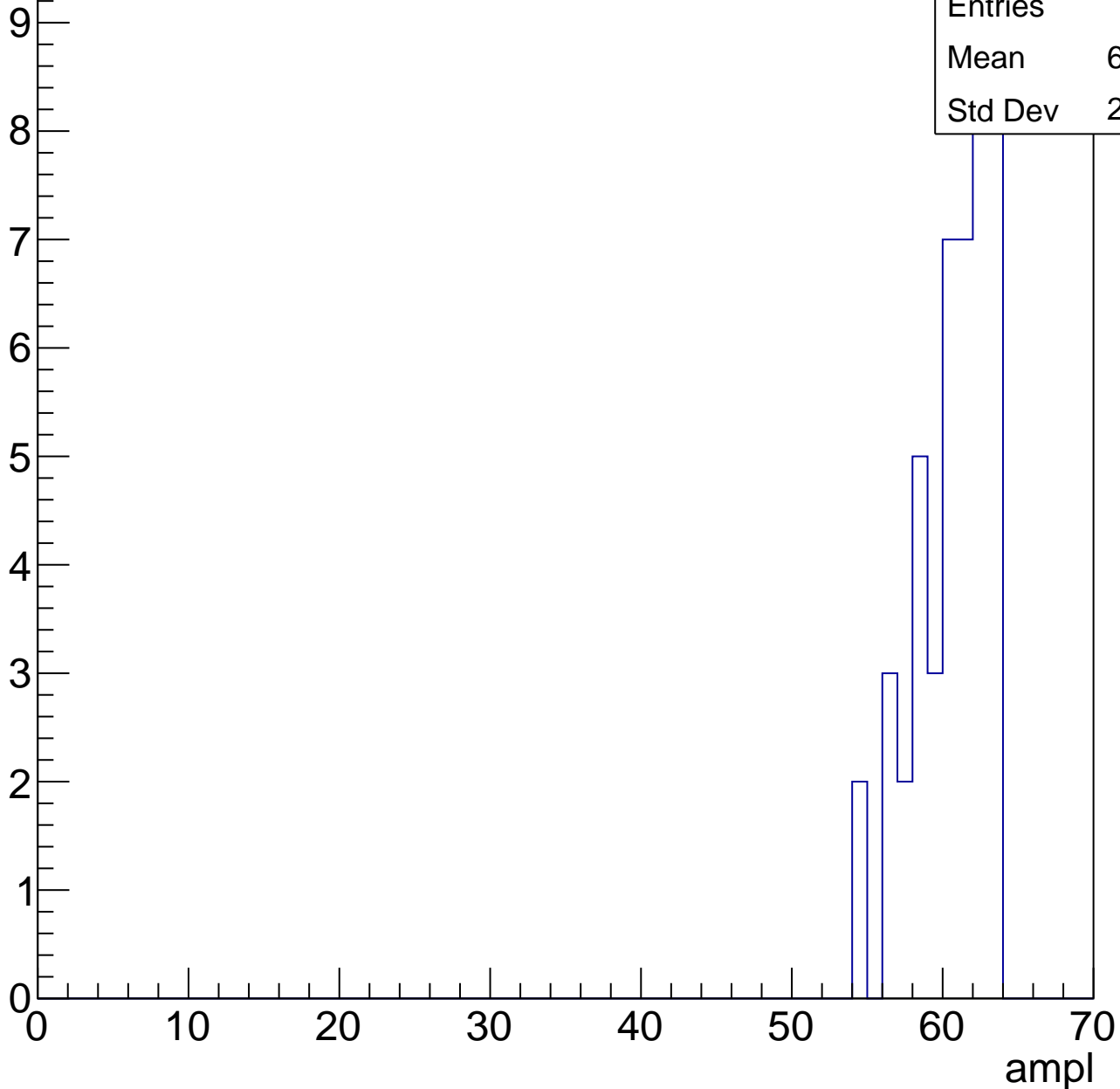
Entries	61
Mean	55.43
Std Dev	2.956

B1L103S, U10-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

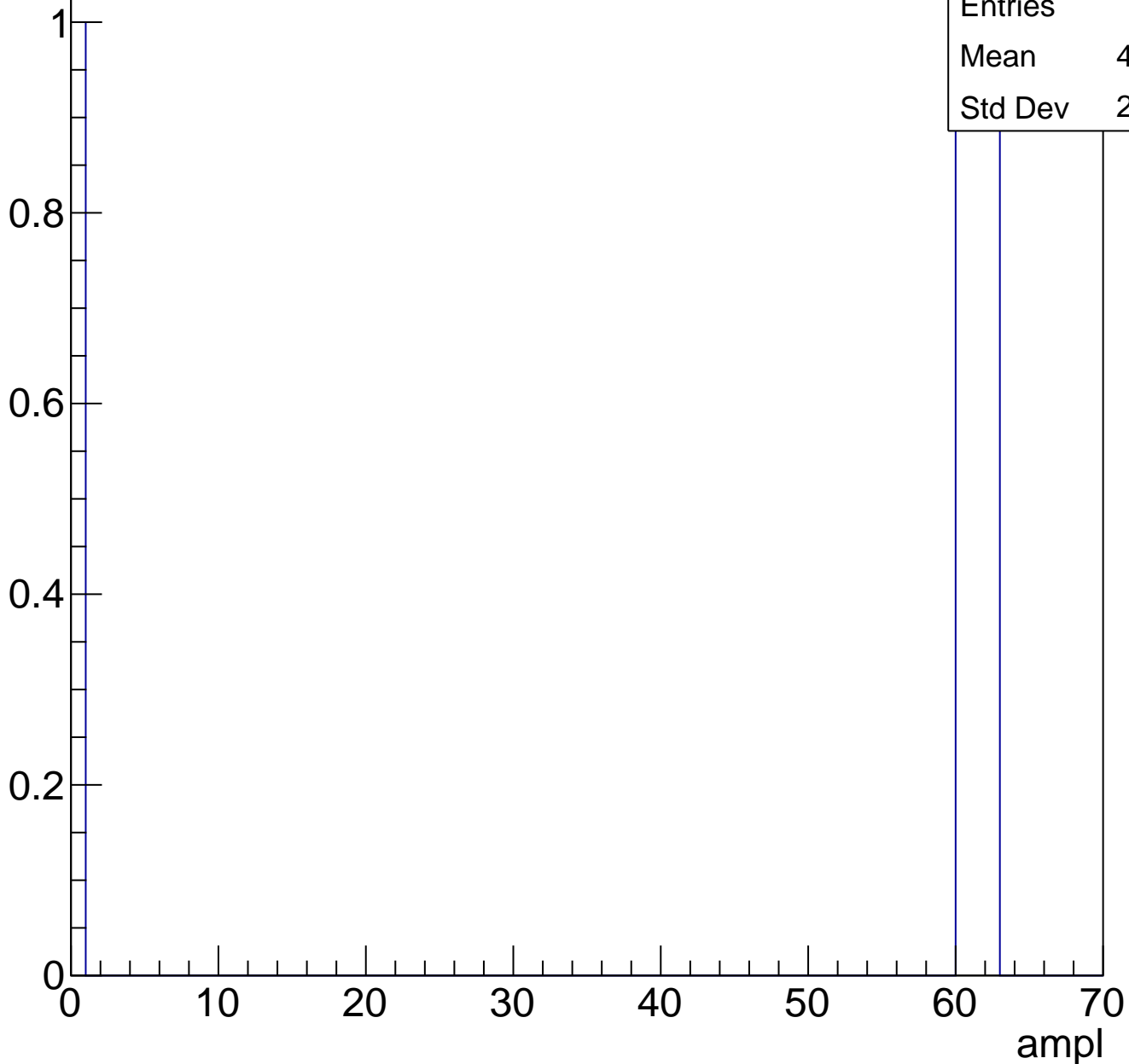
Entries	47
Mean	60.19
Std Dev	2.455



B1L103S, U10-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

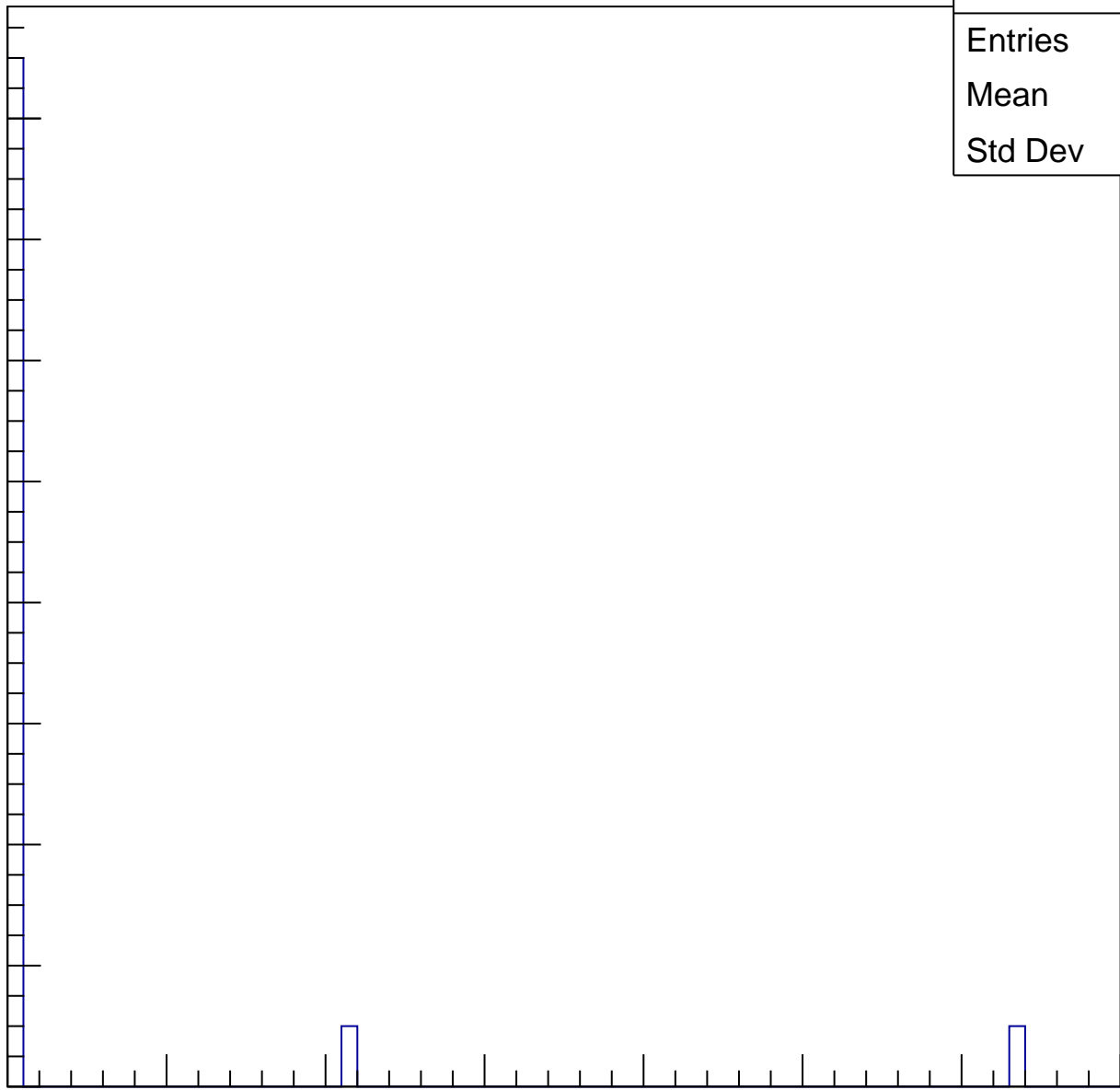
Entries	19
Mean	4.421
Std Dev	14.58

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch33, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	30.35
Std Dev	6.048

Gaus mean : 32.0591

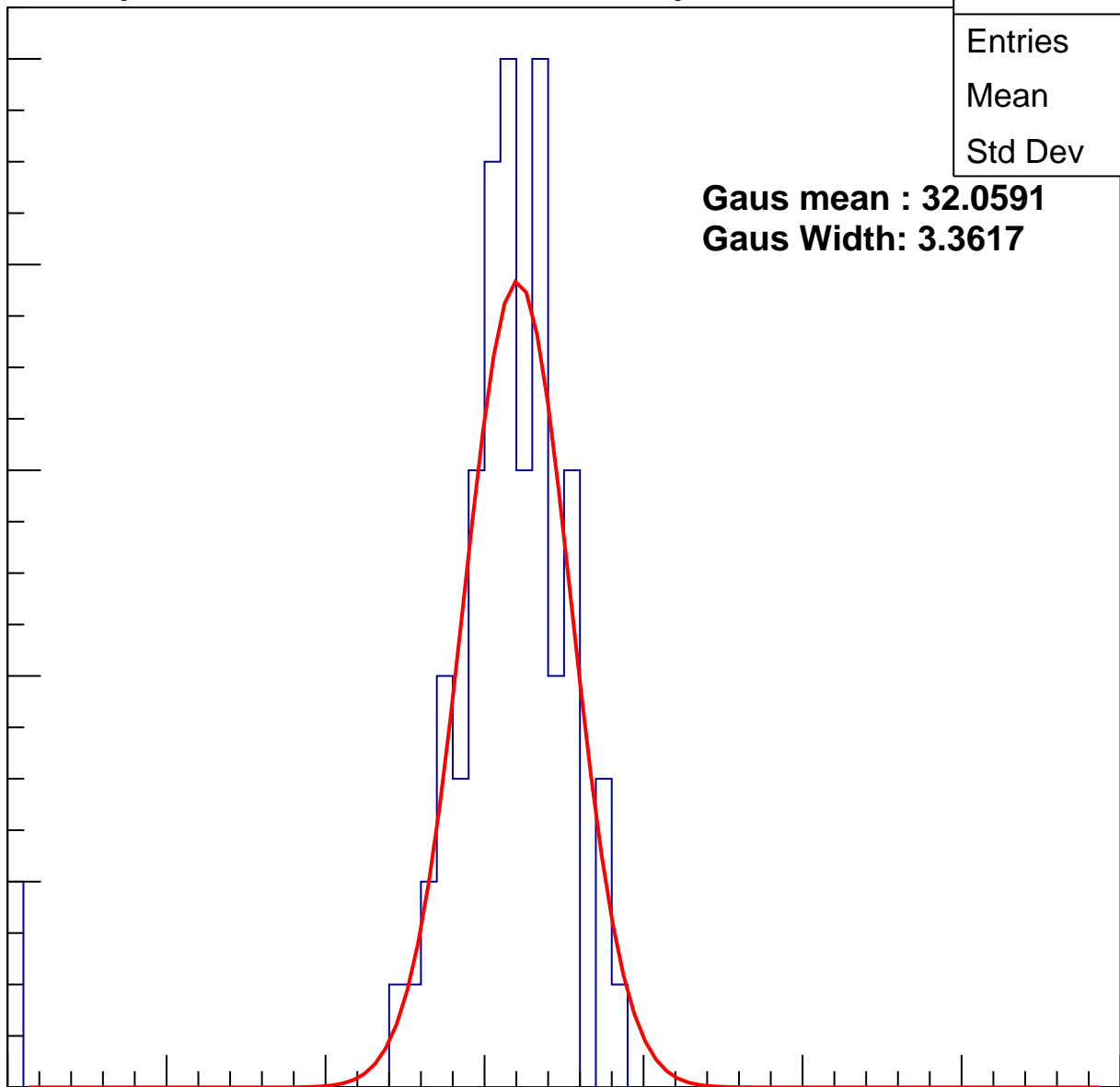
Gaus Width: 3.3617

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch33, adc1

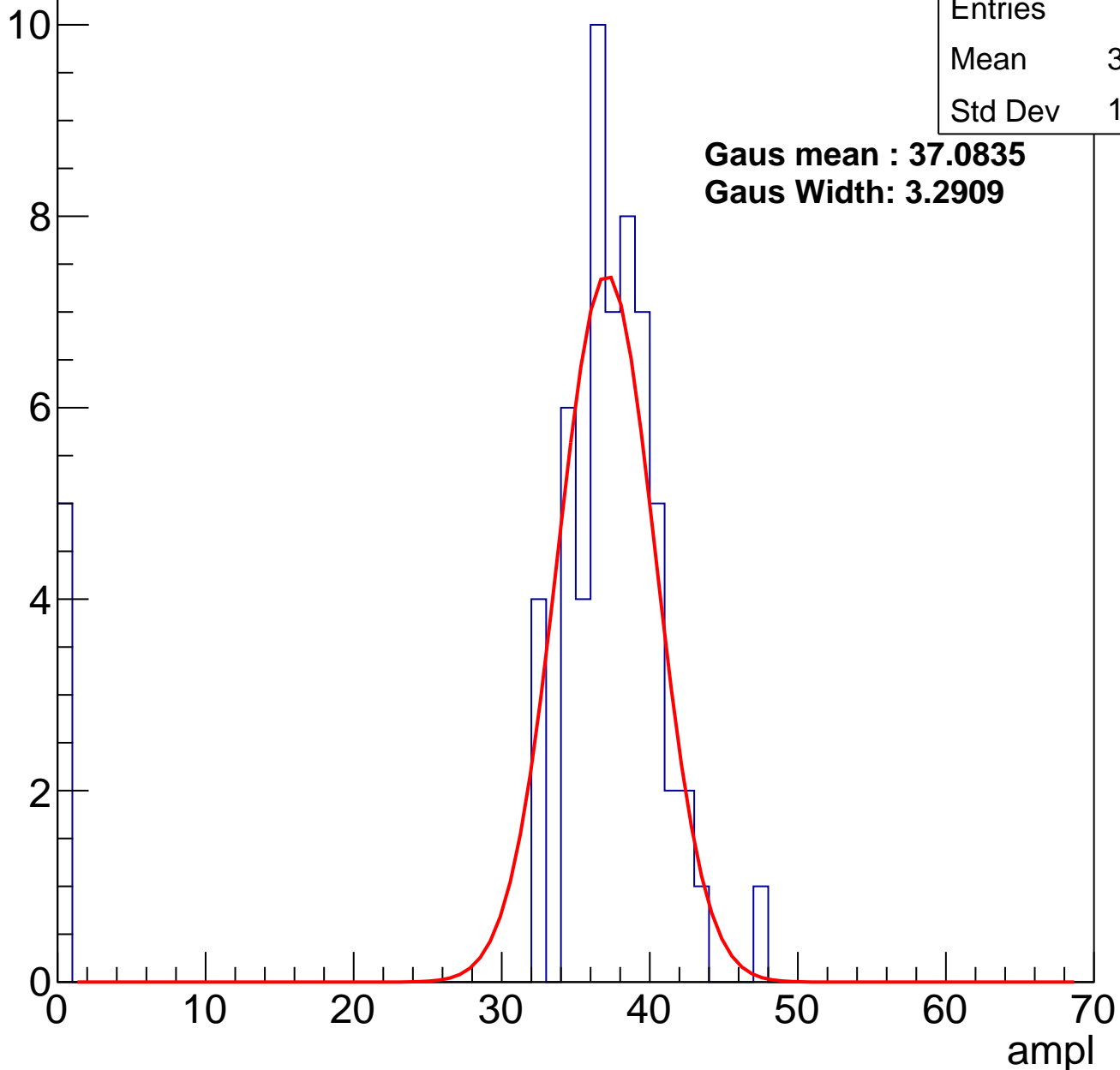
calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	34.26
Std Dev	10.52

Gaus mean : 37.0835

Gaus Width: 3.2909

Entry



B1L103S, U10-ch33, adc2

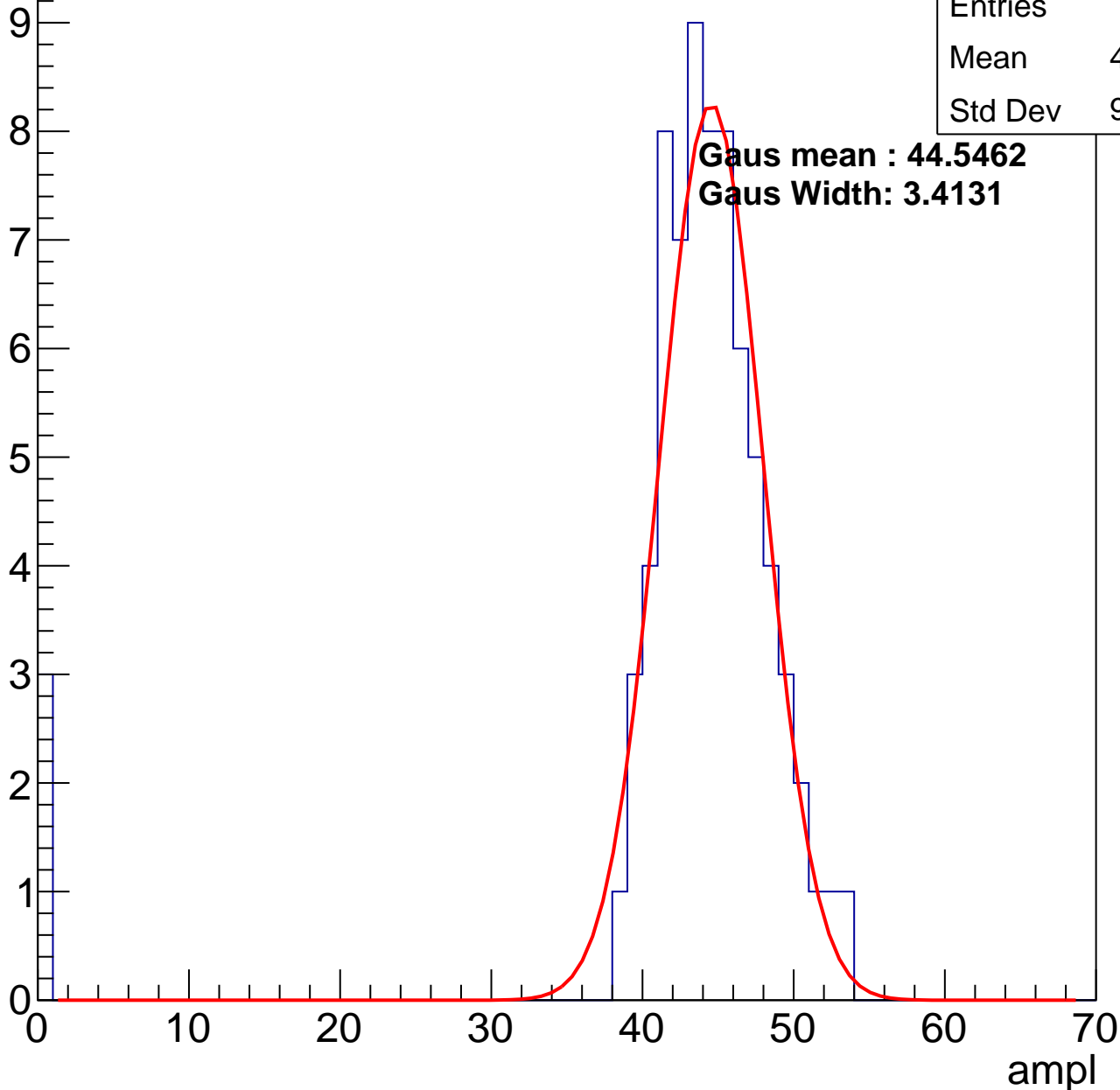
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	42.46
Std Dev	9.299

Gaus mean : 44.5462

Gaus Width: 3.4131

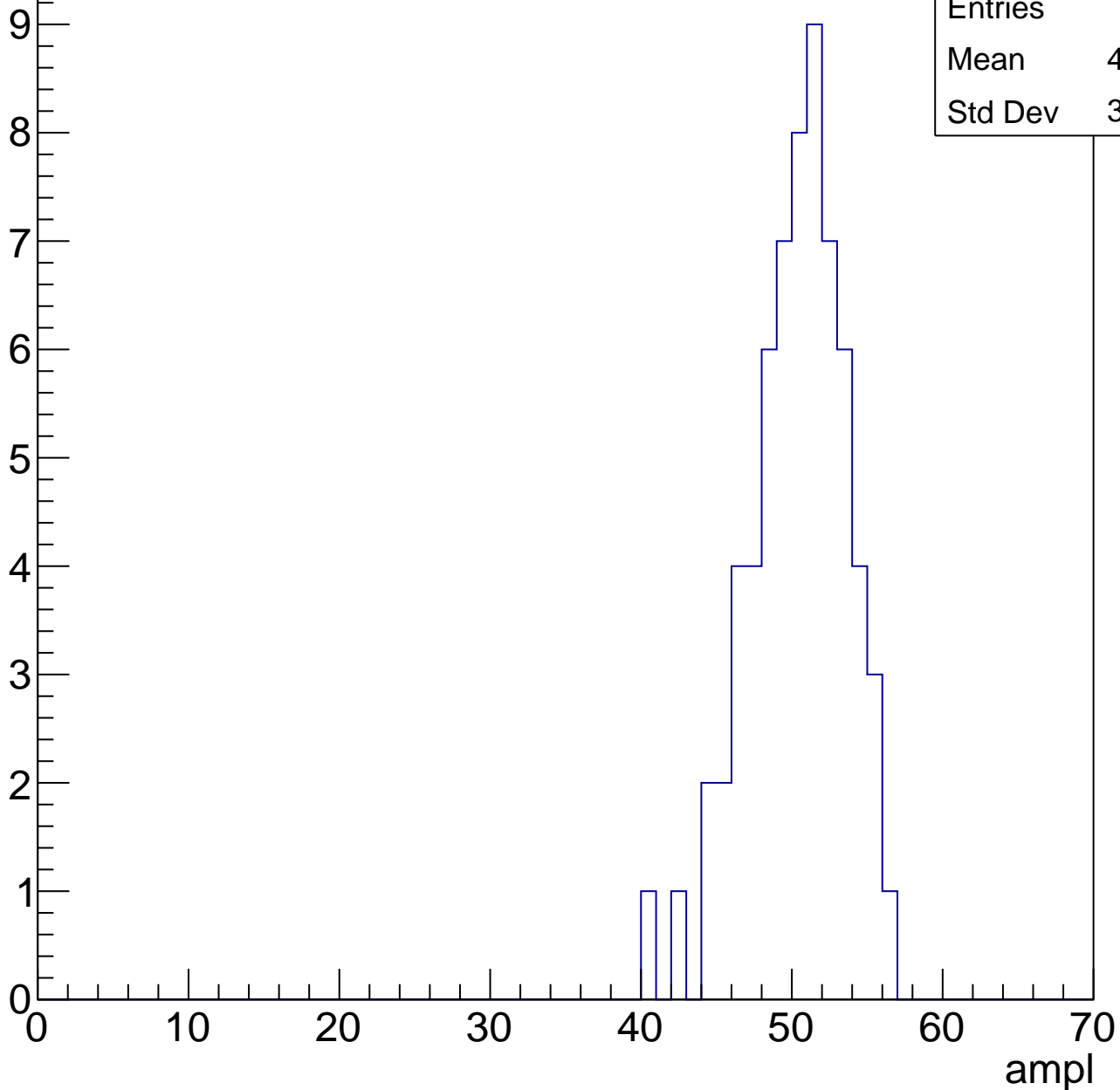


B1L103S, U10-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	49.86
Std Dev	3.239

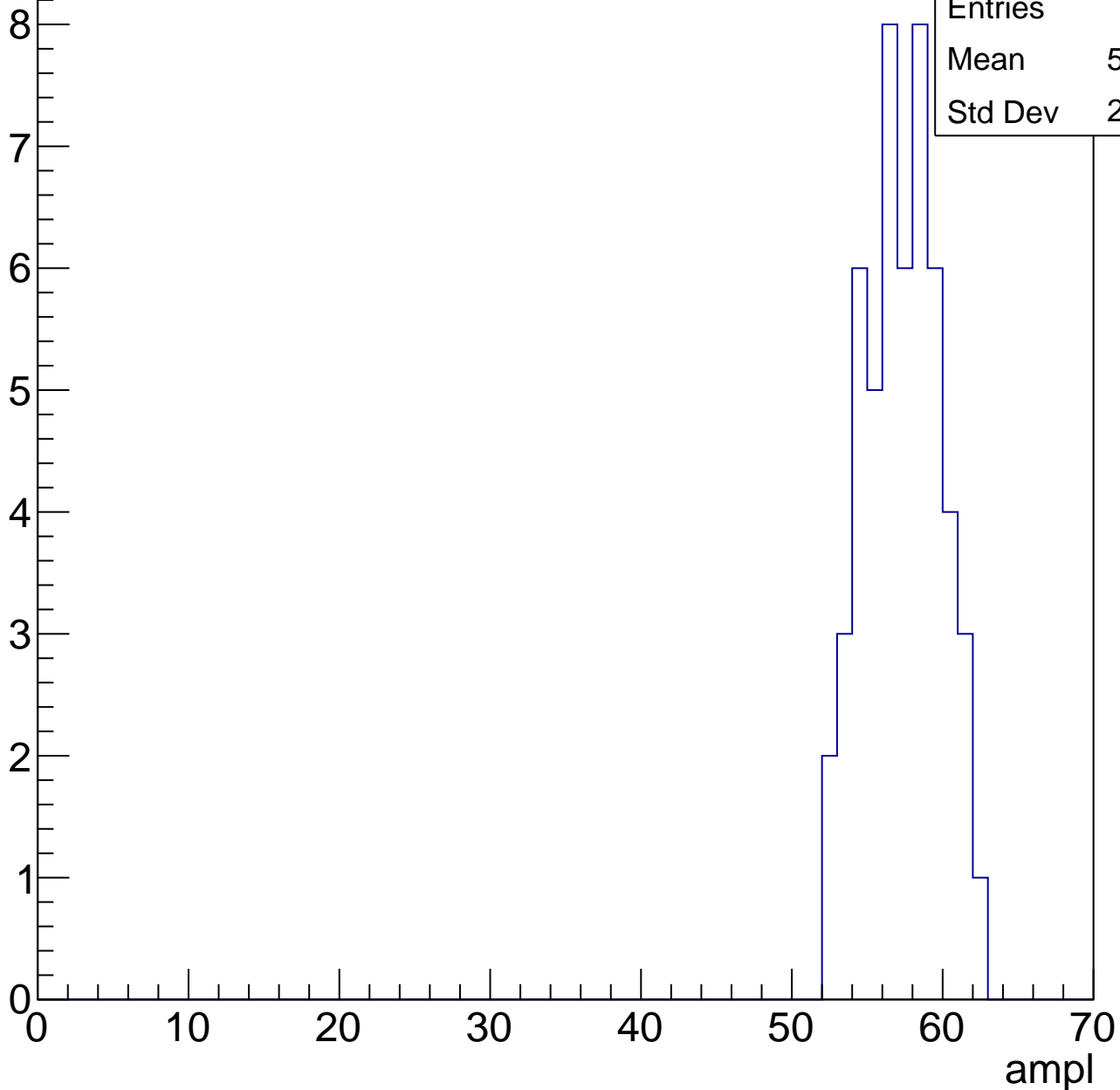


B1L103S, U10-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56.83
Std Dev	2.479

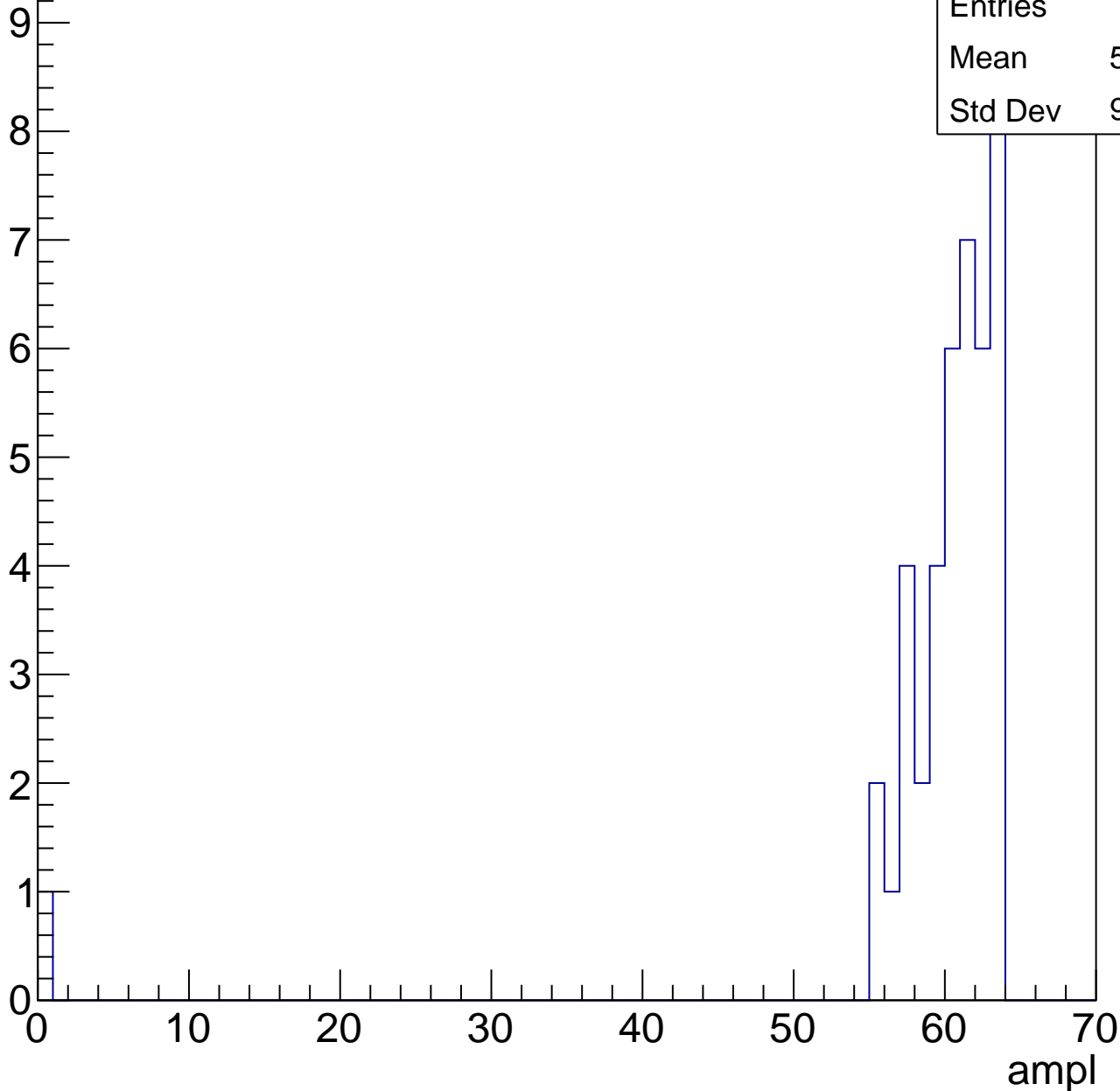


B1L103S, U10-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

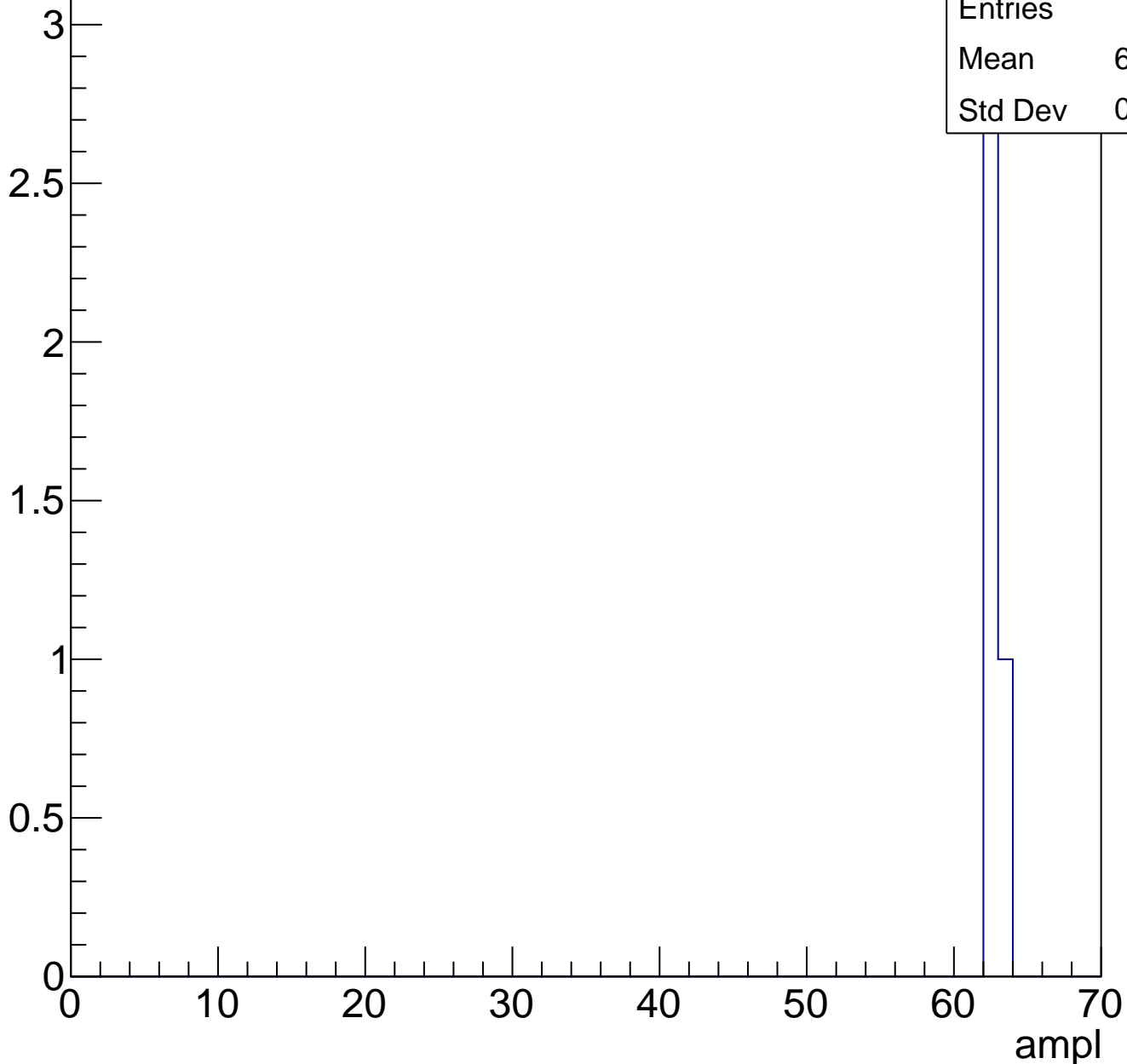
Entries	42
Mean	58.86
Std Dev	9.476



B1L103S, U10-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch34, adc0

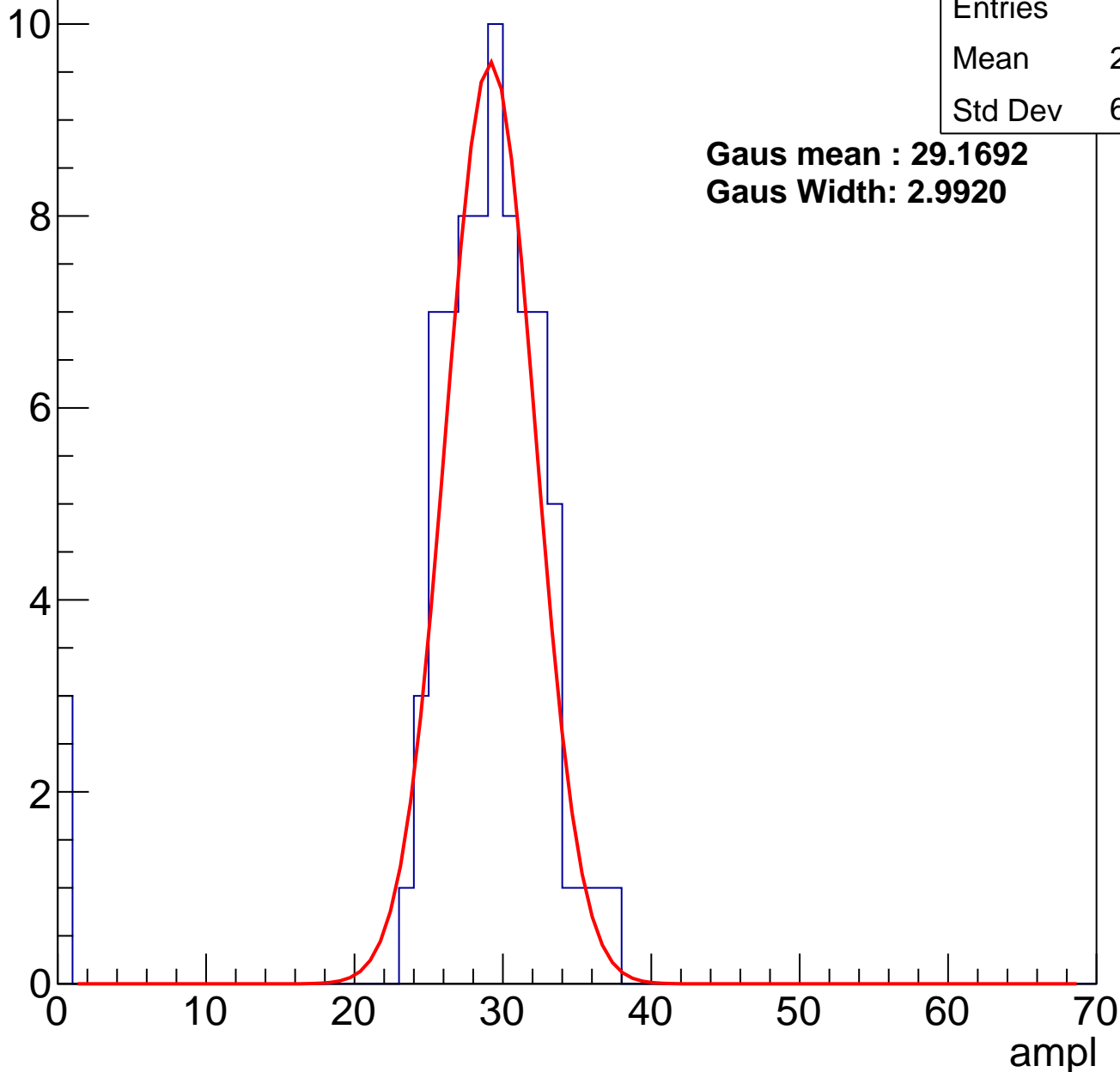
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	27.82
Std Dev	6.298

Gaus mean : 29.1692

Gaus Width: 2.9920

Entry



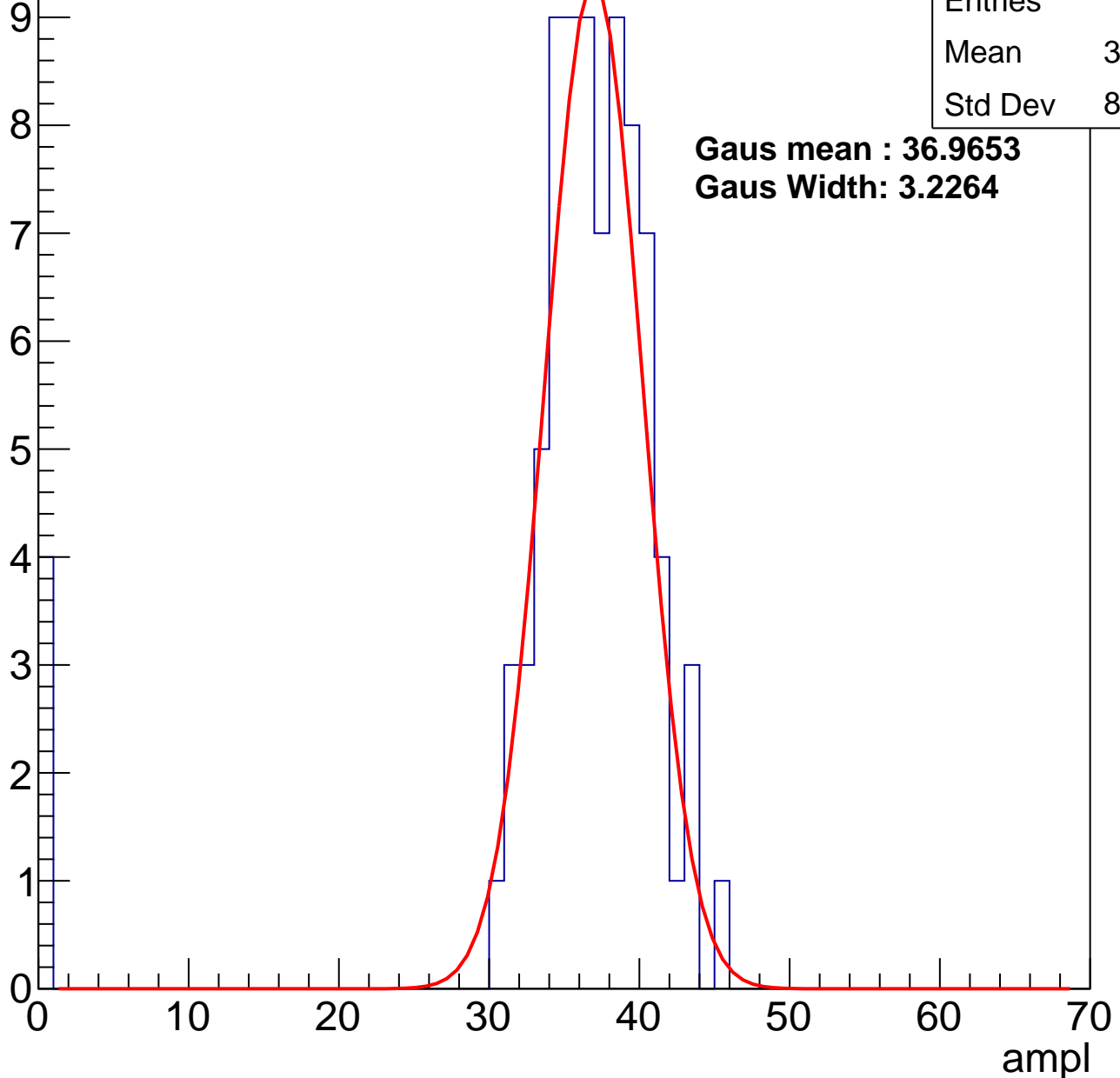
B1L103S, U10-ch34, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

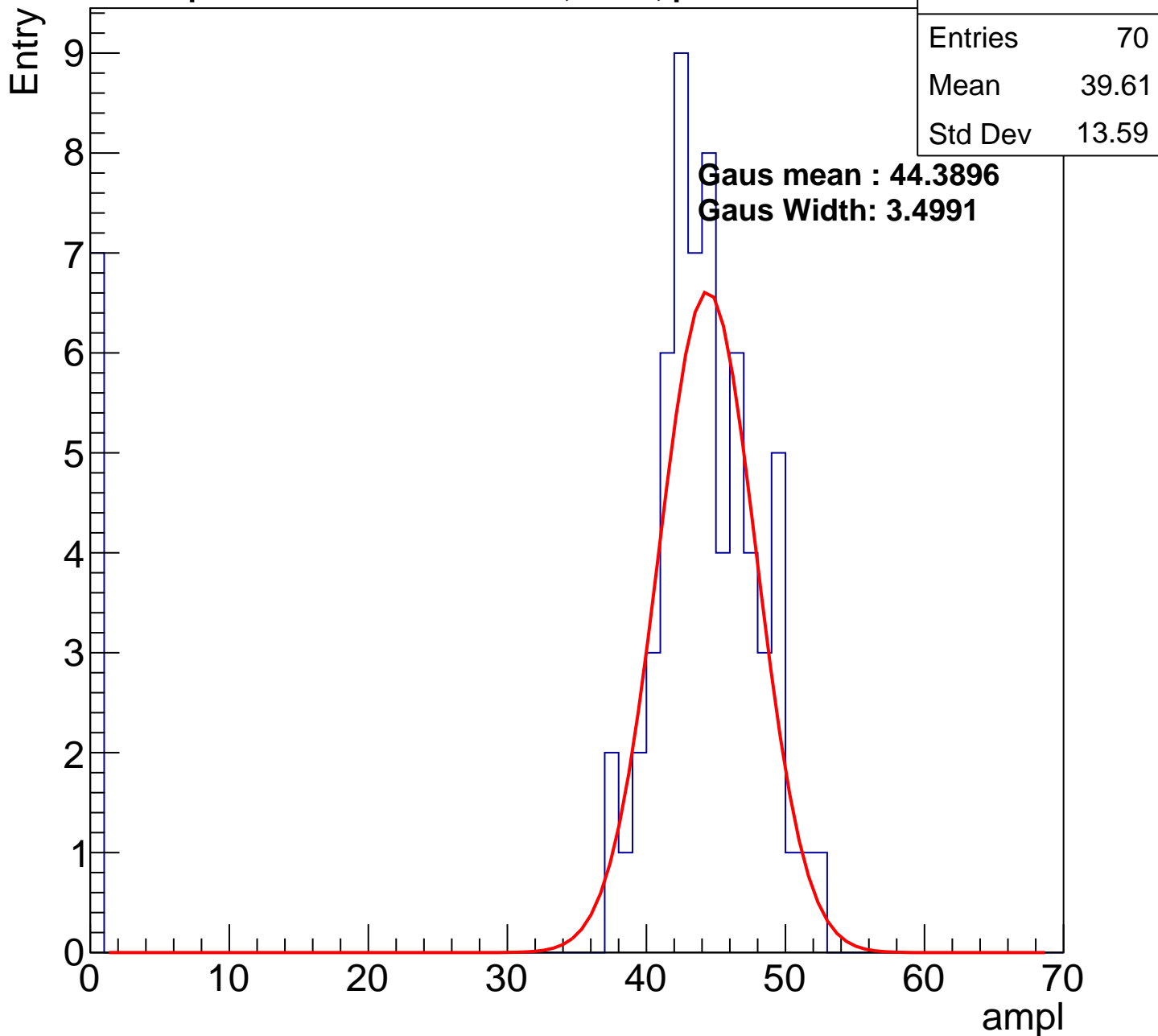
Entries	83
Mean	34.96
Std Dev	8.456

Gaus mean : 36.9653
Gaus Width: 3.2264



B1L103S, U10-ch34, adc2

calib_packv5_041523_1651.root, FC#0, port C2

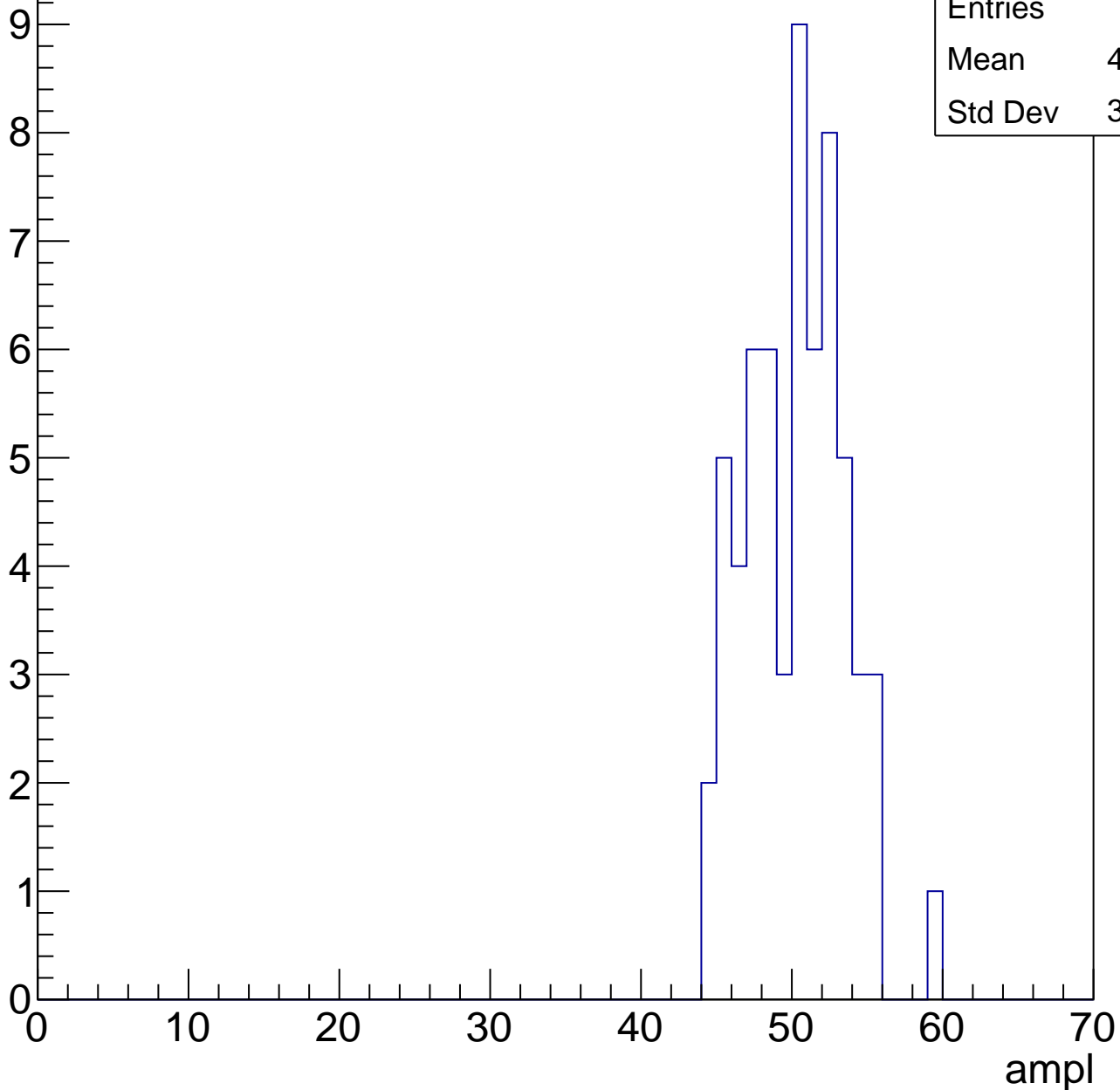


B1L103S, U10-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.79
Std Dev	3.204

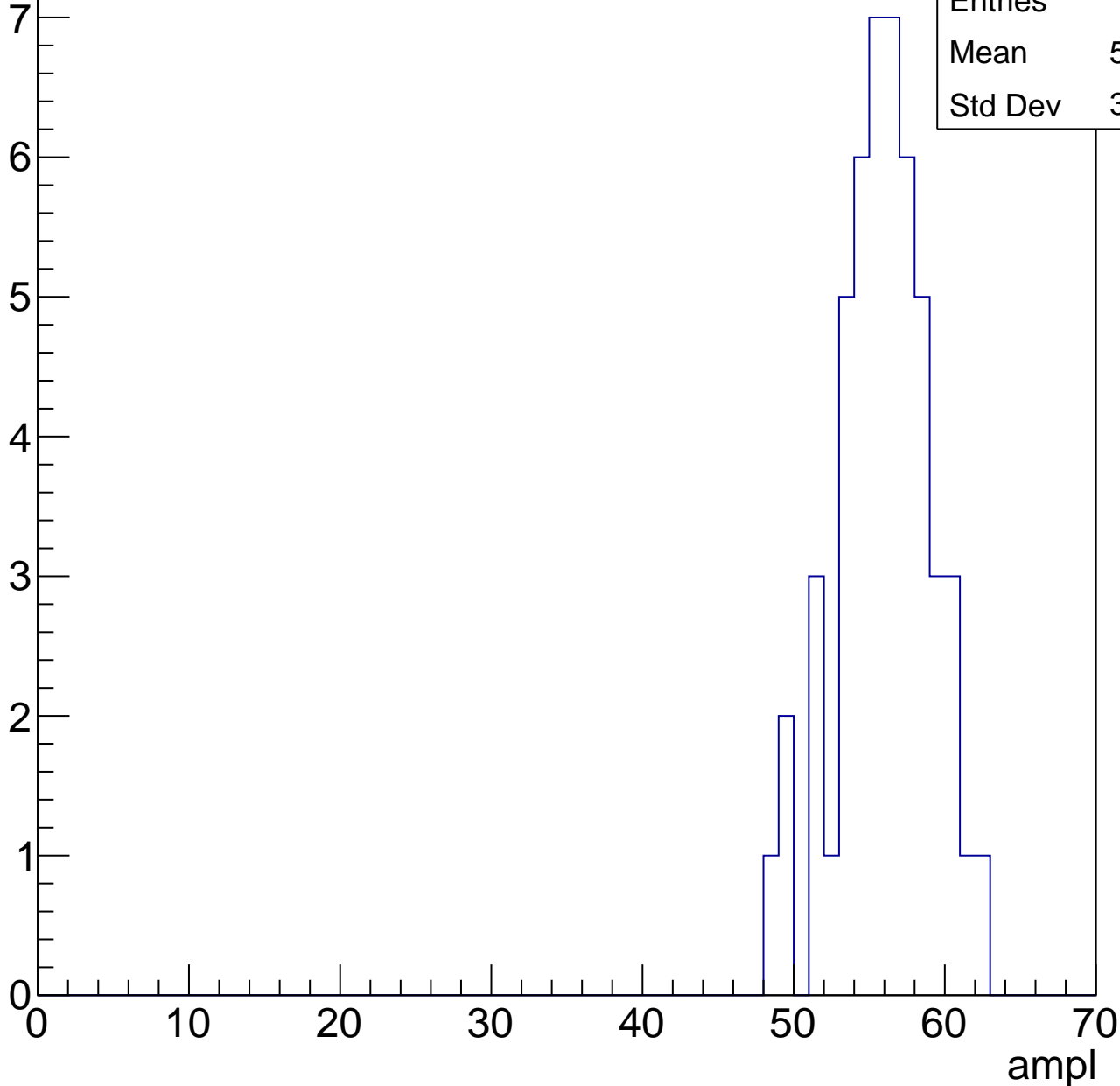


B1L103S, U10-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	55.47
Std Dev	3.057

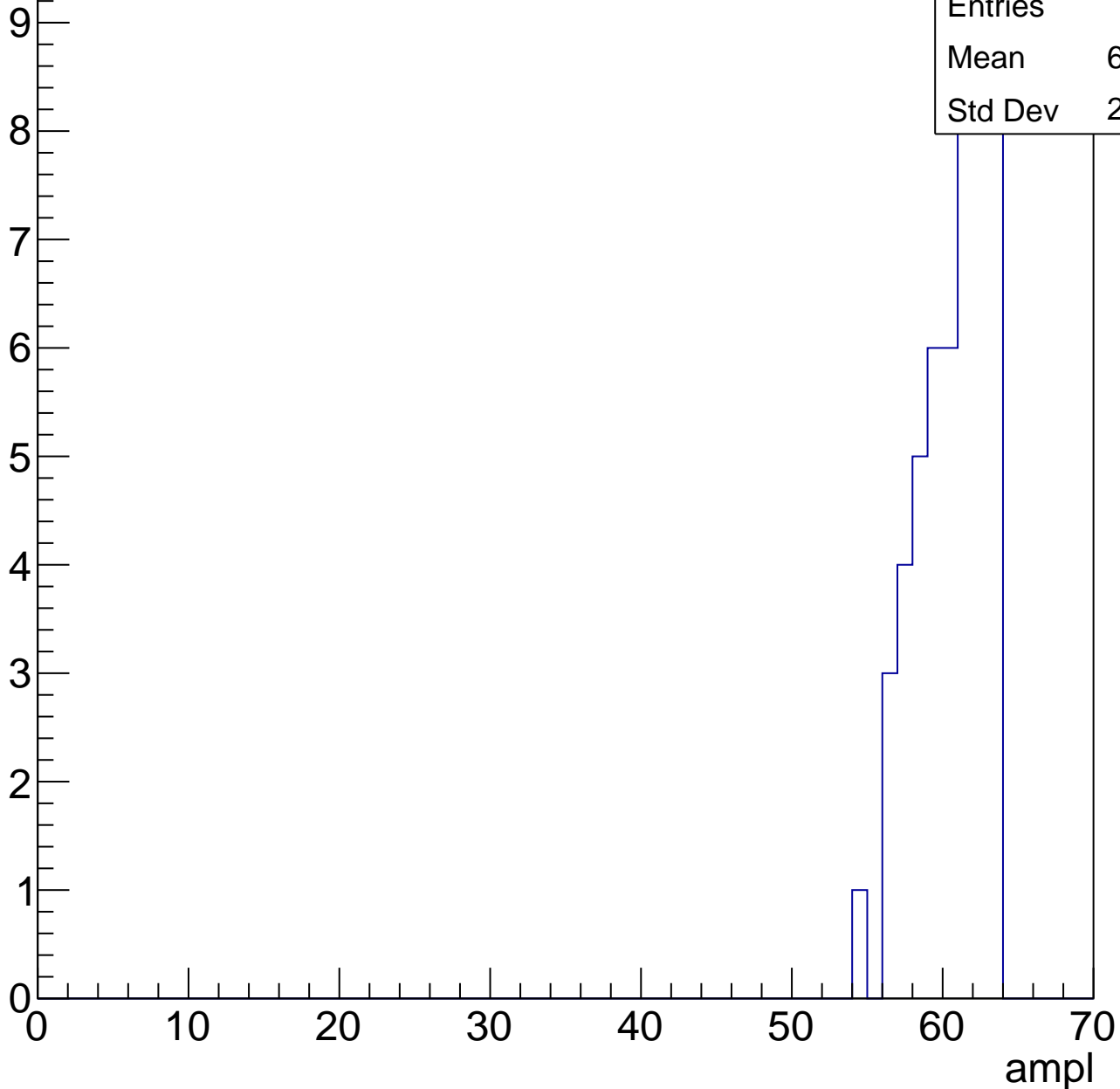


B1L103S, U10-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

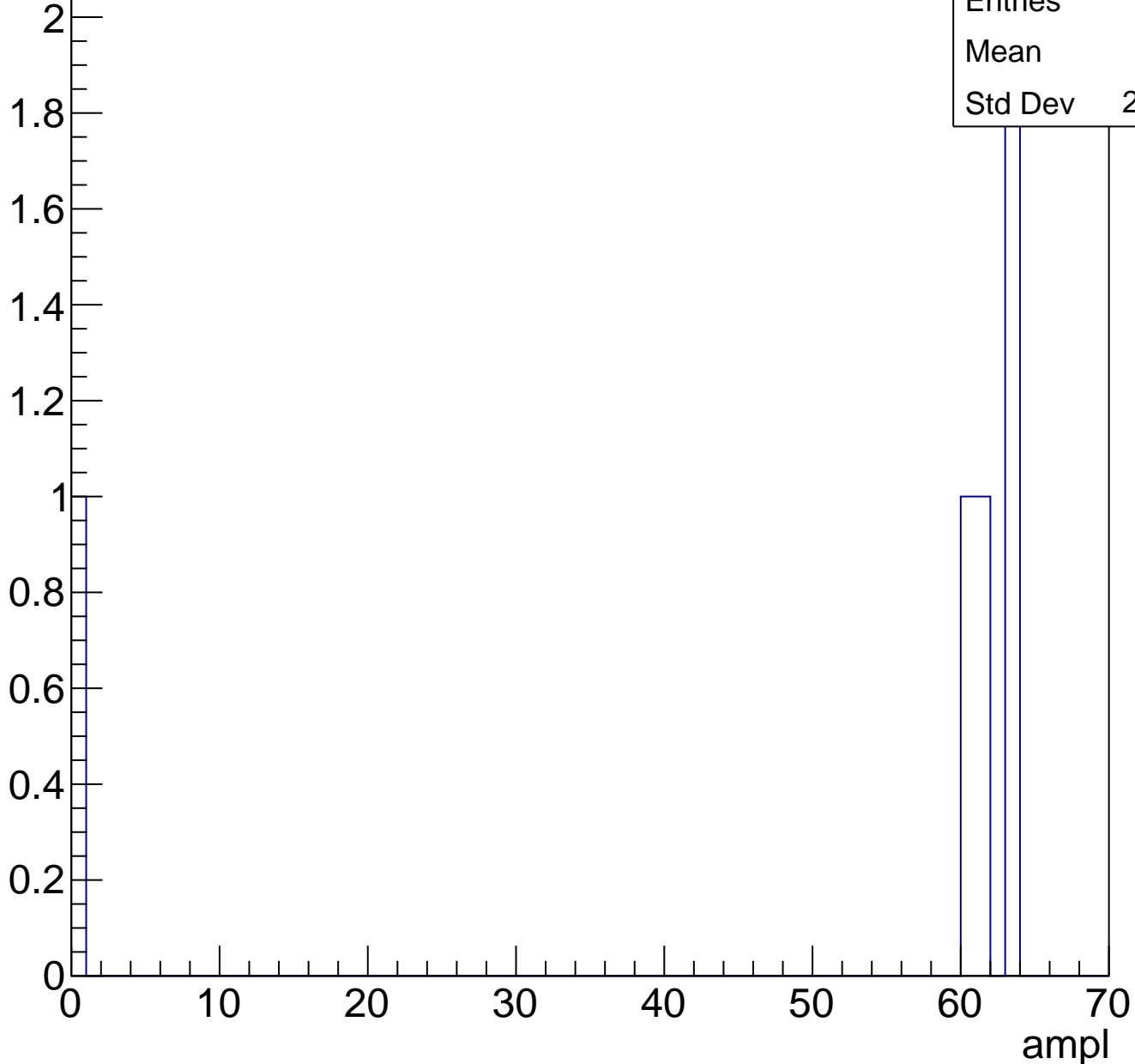
Entries	50
Mean	60.08
Std Dev	2.288



B1L103S, U10-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch35, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	24.2
Std Dev	10.78

Gaus mean : 29.1910

Gaus Width: 3.5866

Entry

10

8

6

4

2

0

0

10

20

30

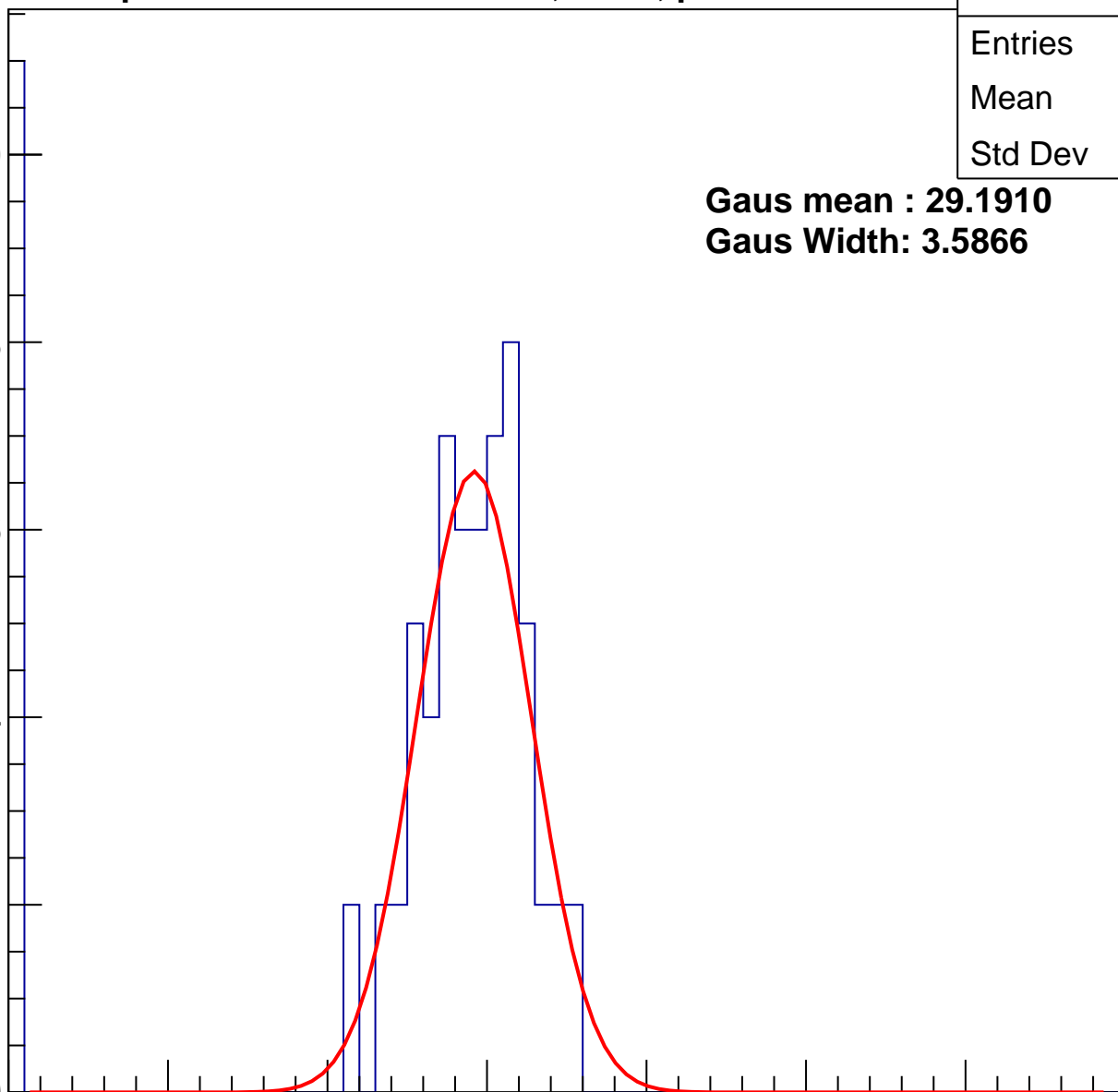
40

50

60

70

ampl



B1L103S, U10-ch35, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	33.55
Std Dev	8.655

Gaus mean : 36.3868

Gaus Width: 3.0979

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U10-ch35, adc2

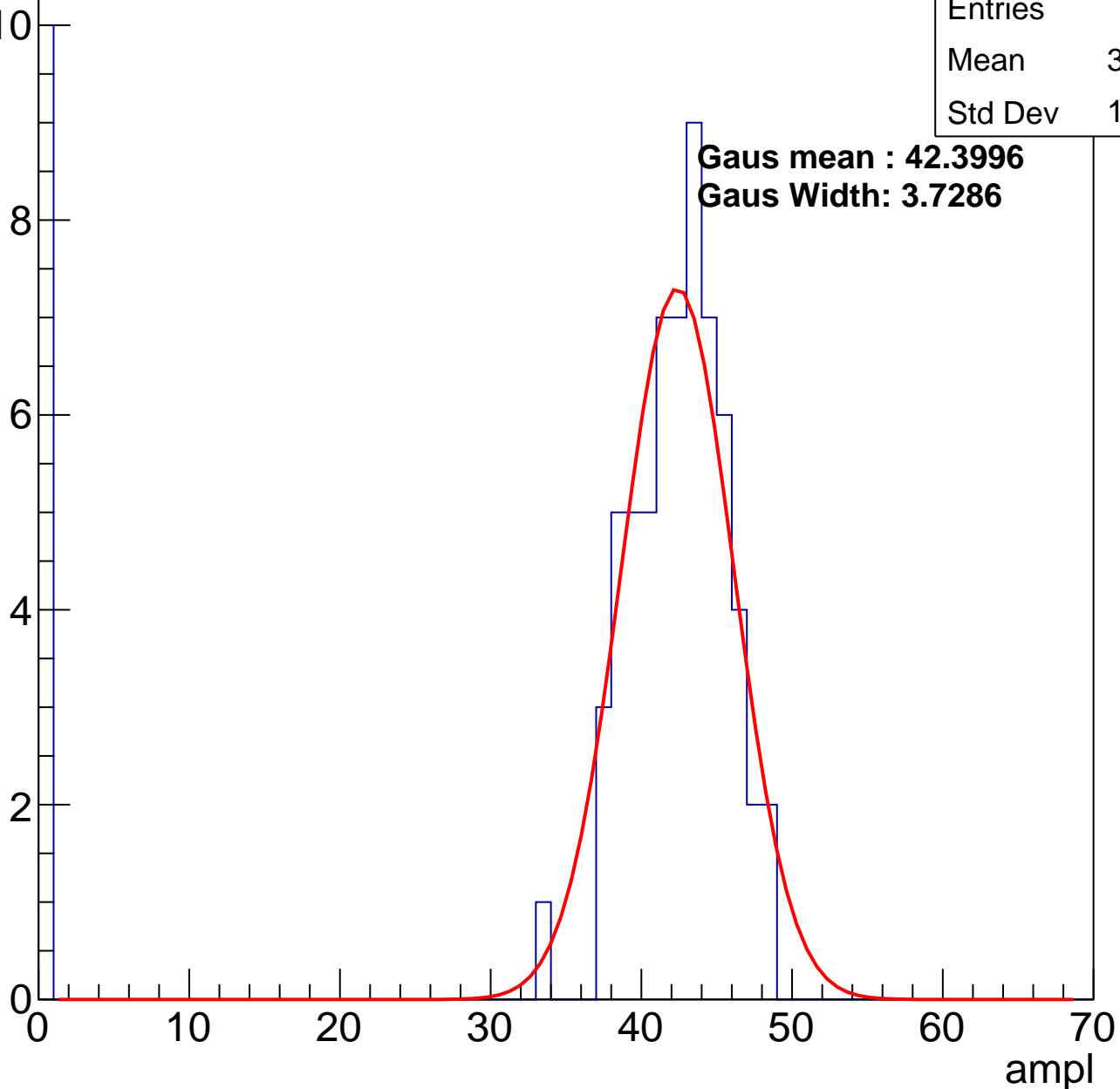
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.29
Std Dev	14.73

Gaus mean : 42.3996

Gaus Width: 3.7286



B1L103S, U10-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

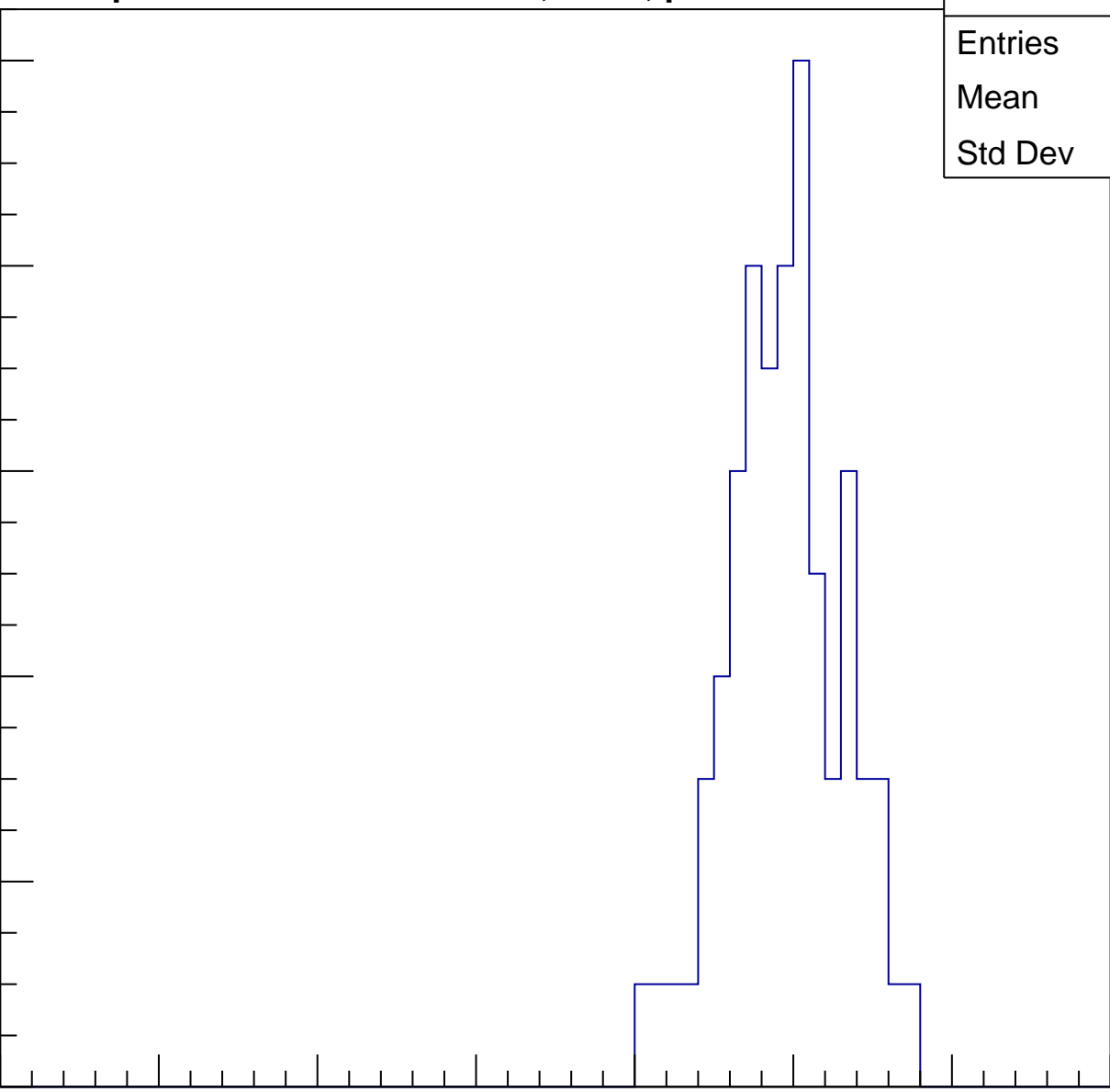
Entries	72
Mean	48.99
Std Dev	3.557

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

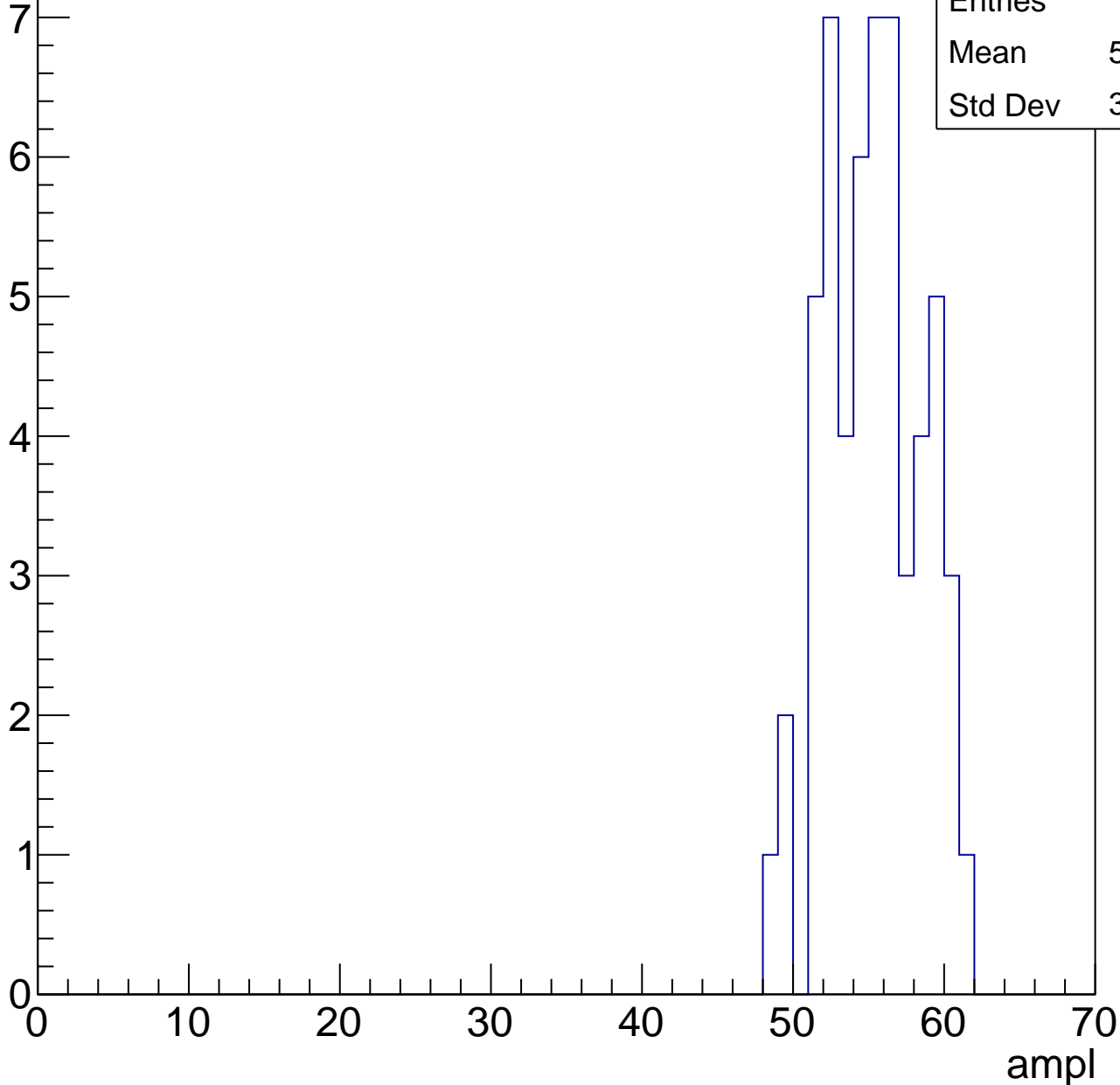


B1L103S, U10-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.85
Std Dev	3.107

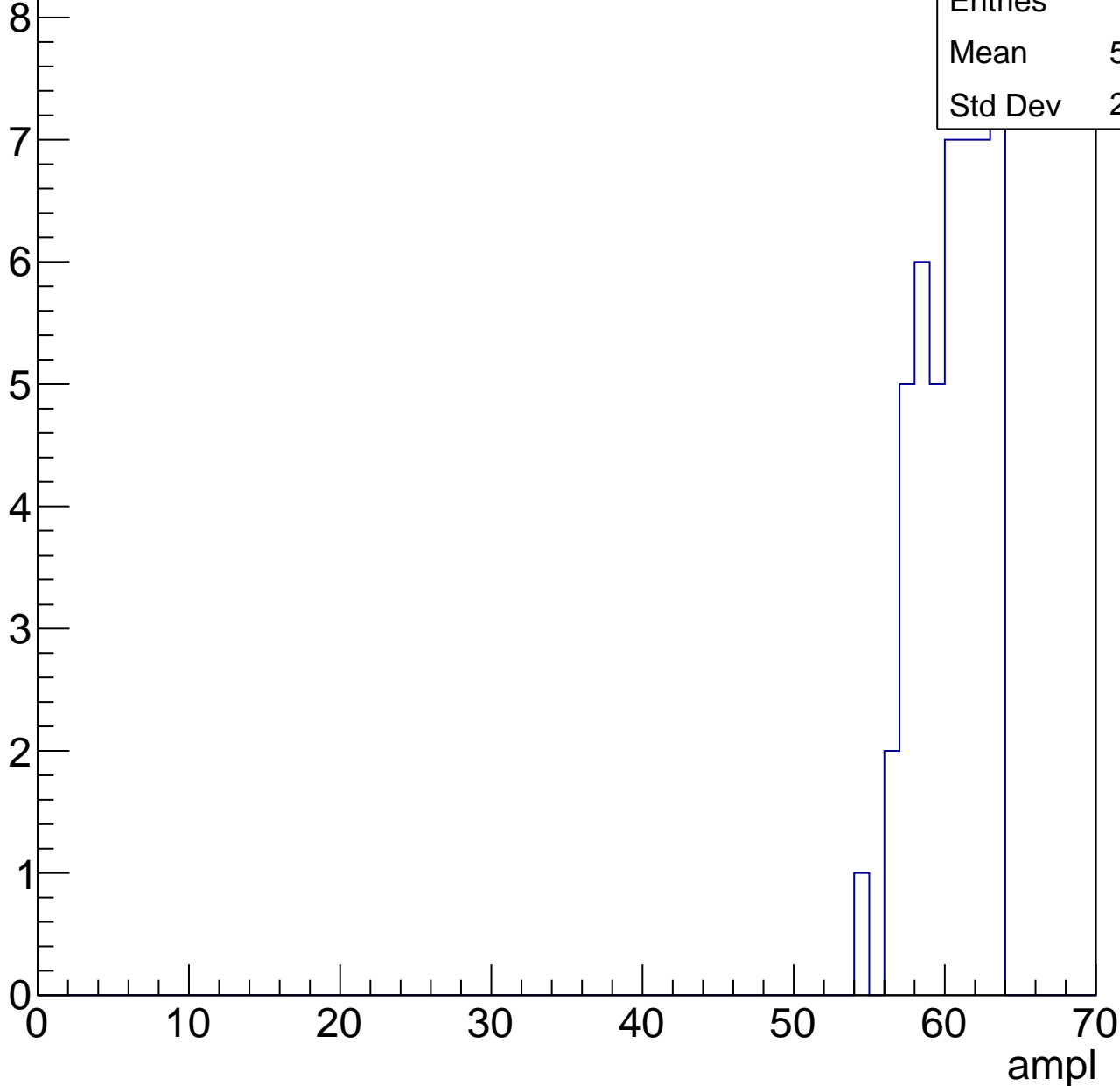


B1L103S, U10-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

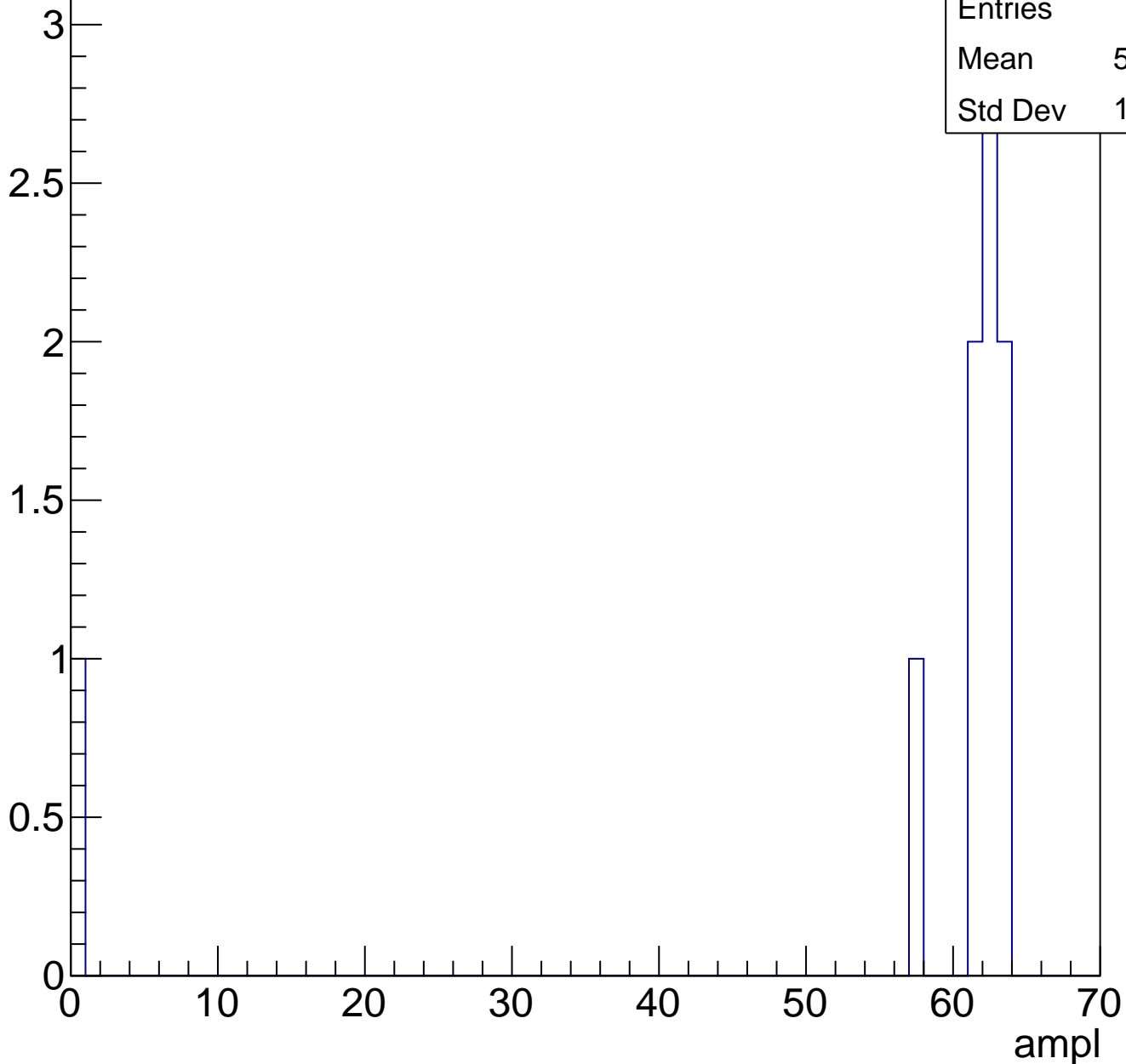
Entries	48
Mean	59.98
Std Dev	2.278



B1L103S, U10-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch36, adc0

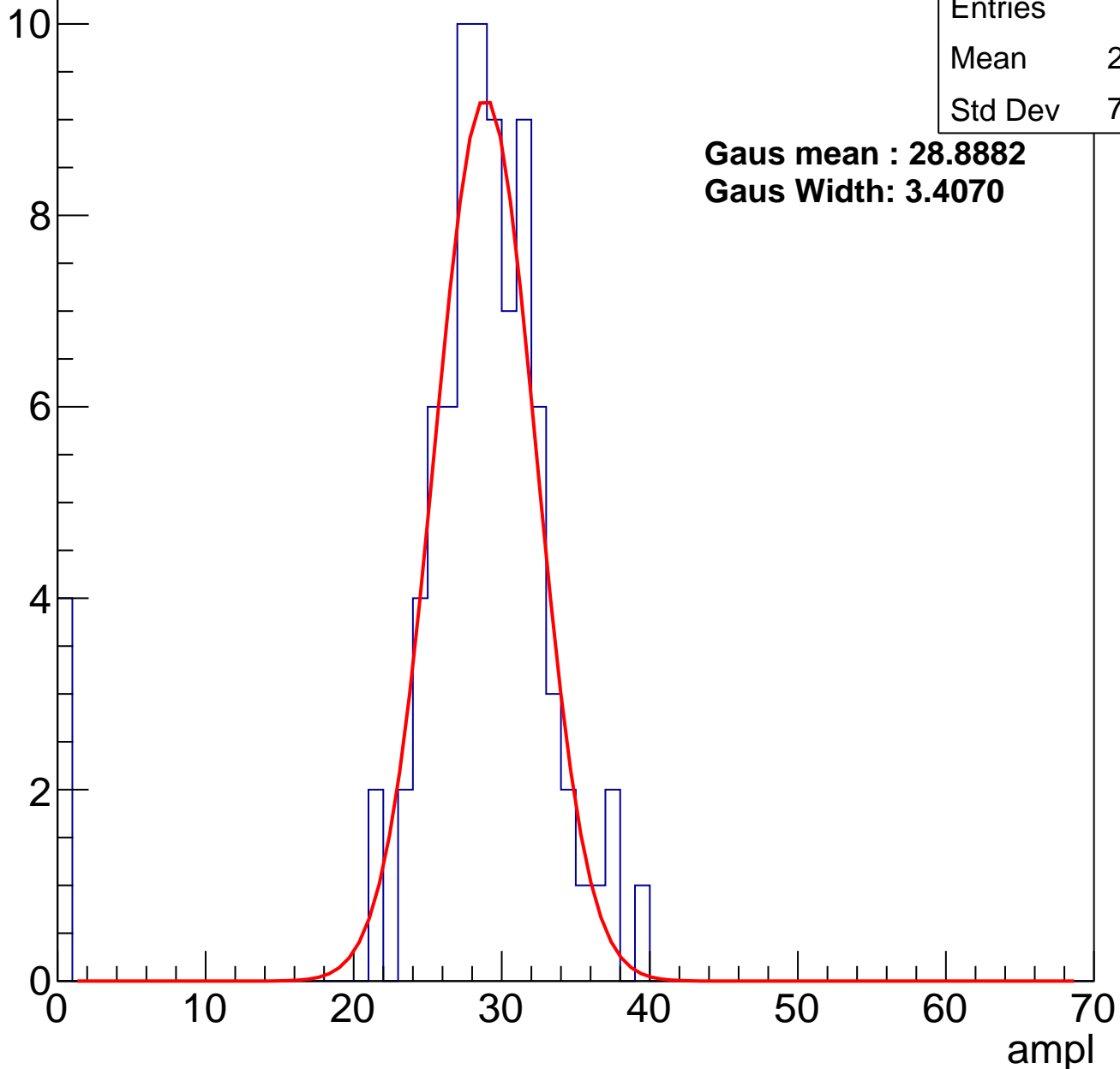
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	27.45
Std Dev	7.002

Gaus mean : 28.8882

Gaus Width: 3.4070

Entry



B1L103S, U10-ch36, adc1

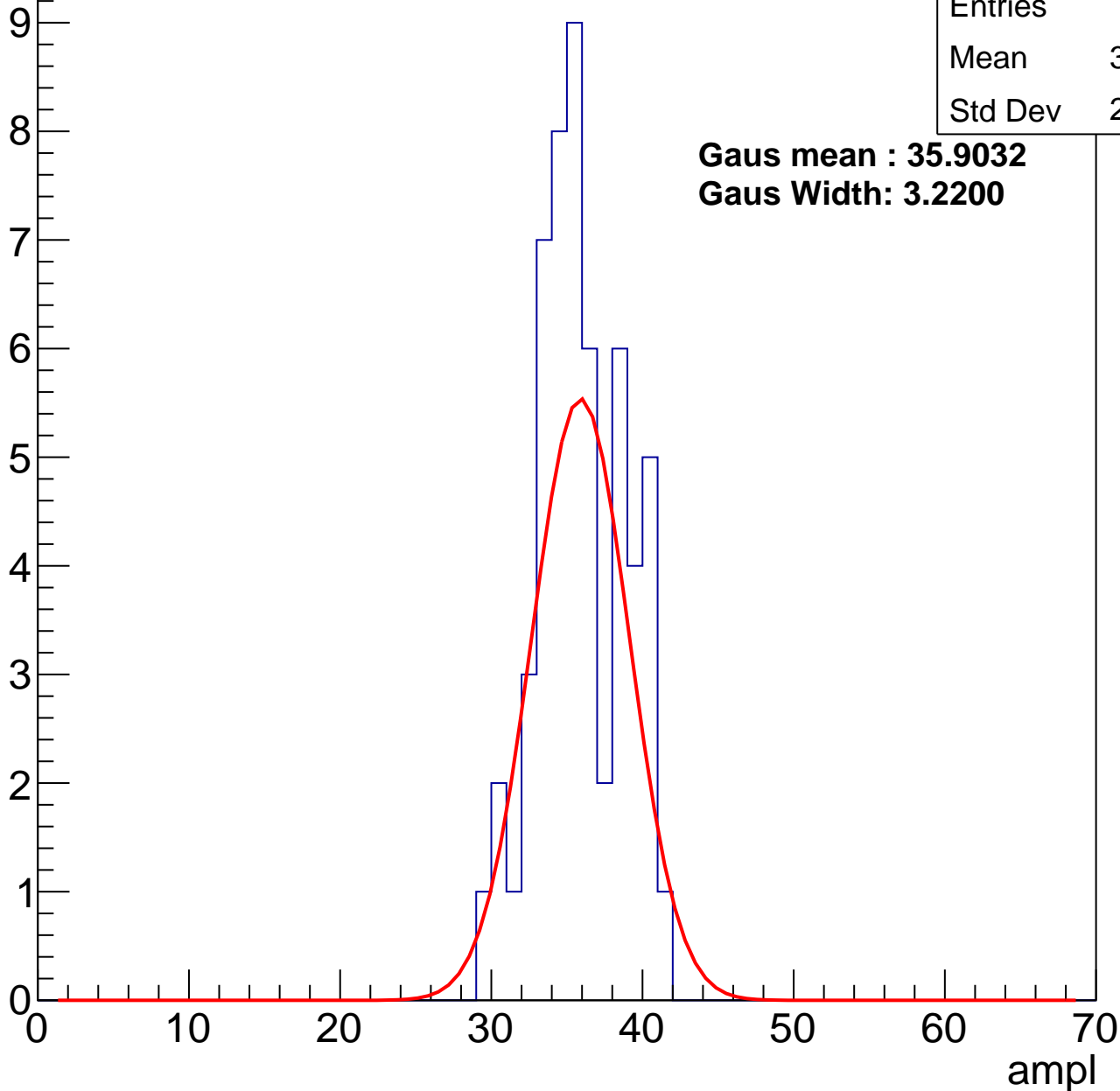
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.44
Std Dev	2.853

Gaus mean : 35.9032

Gaus Width: 3.2200



B1L103S, U10-ch36, adc2

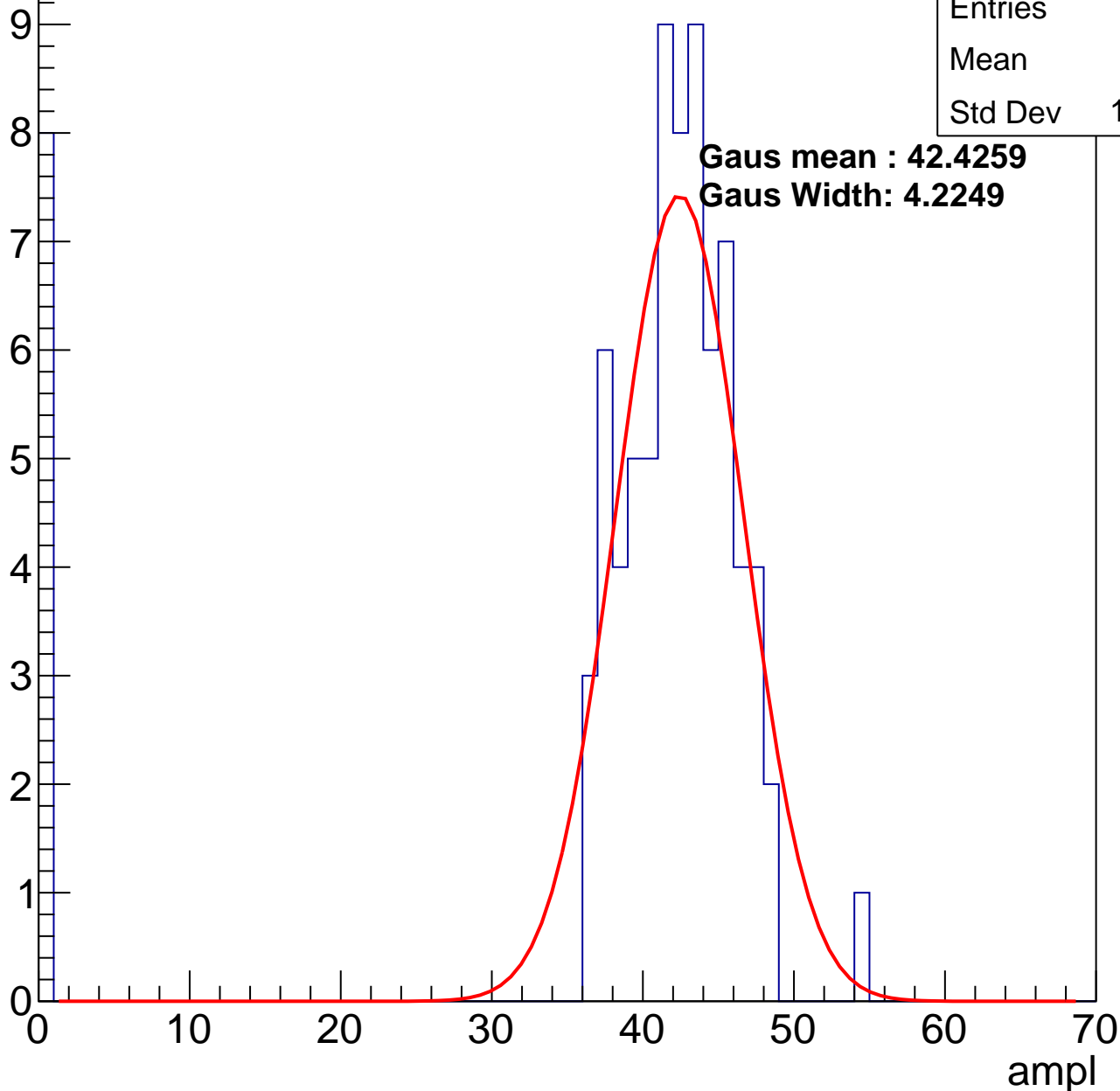
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	37.9
Std Dev	12.97

Gaus mean : 42.4259

Gaus Width: 4.2249

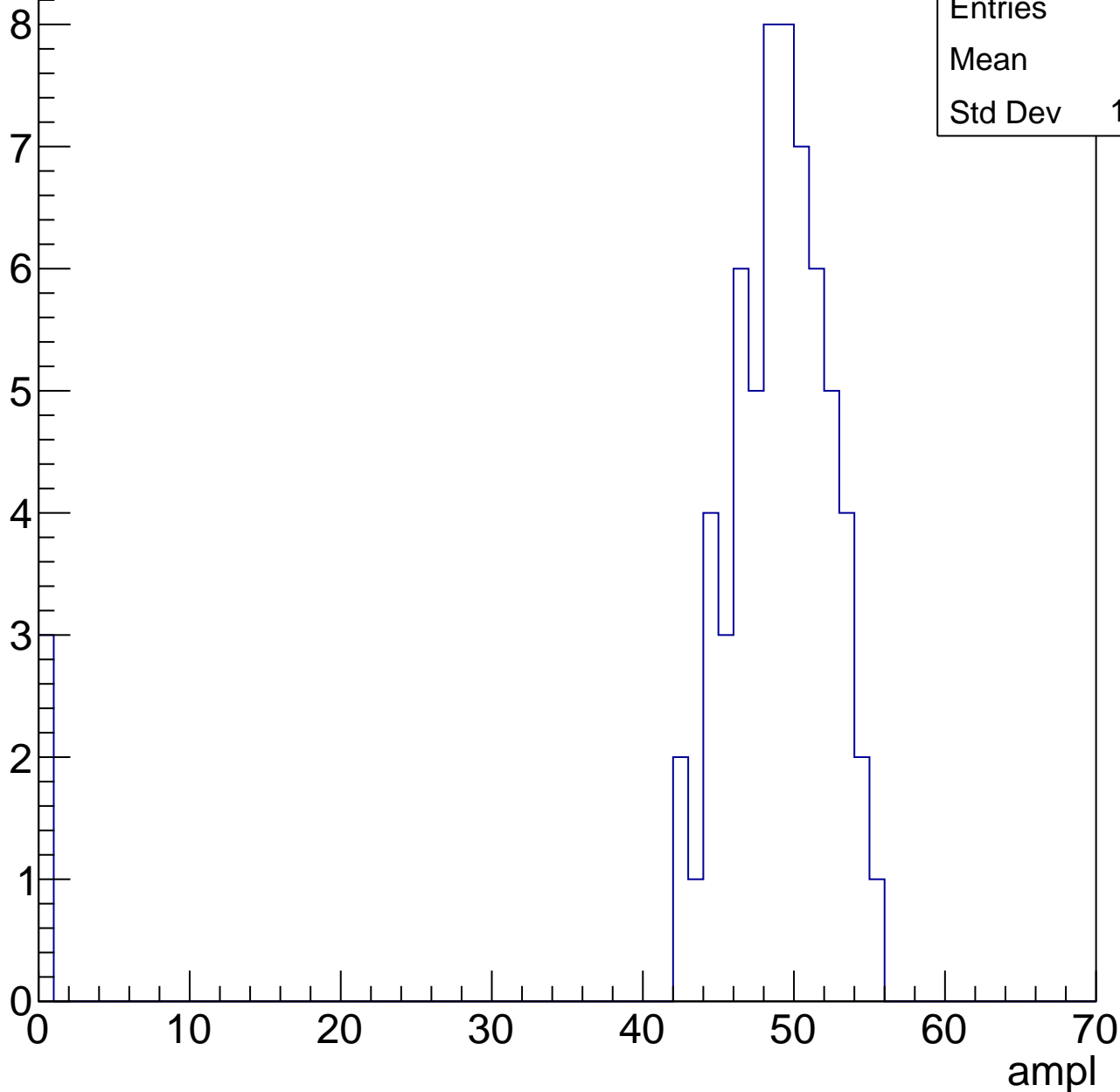


B1L103S, U10-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	46.4
Std Dev	10.64

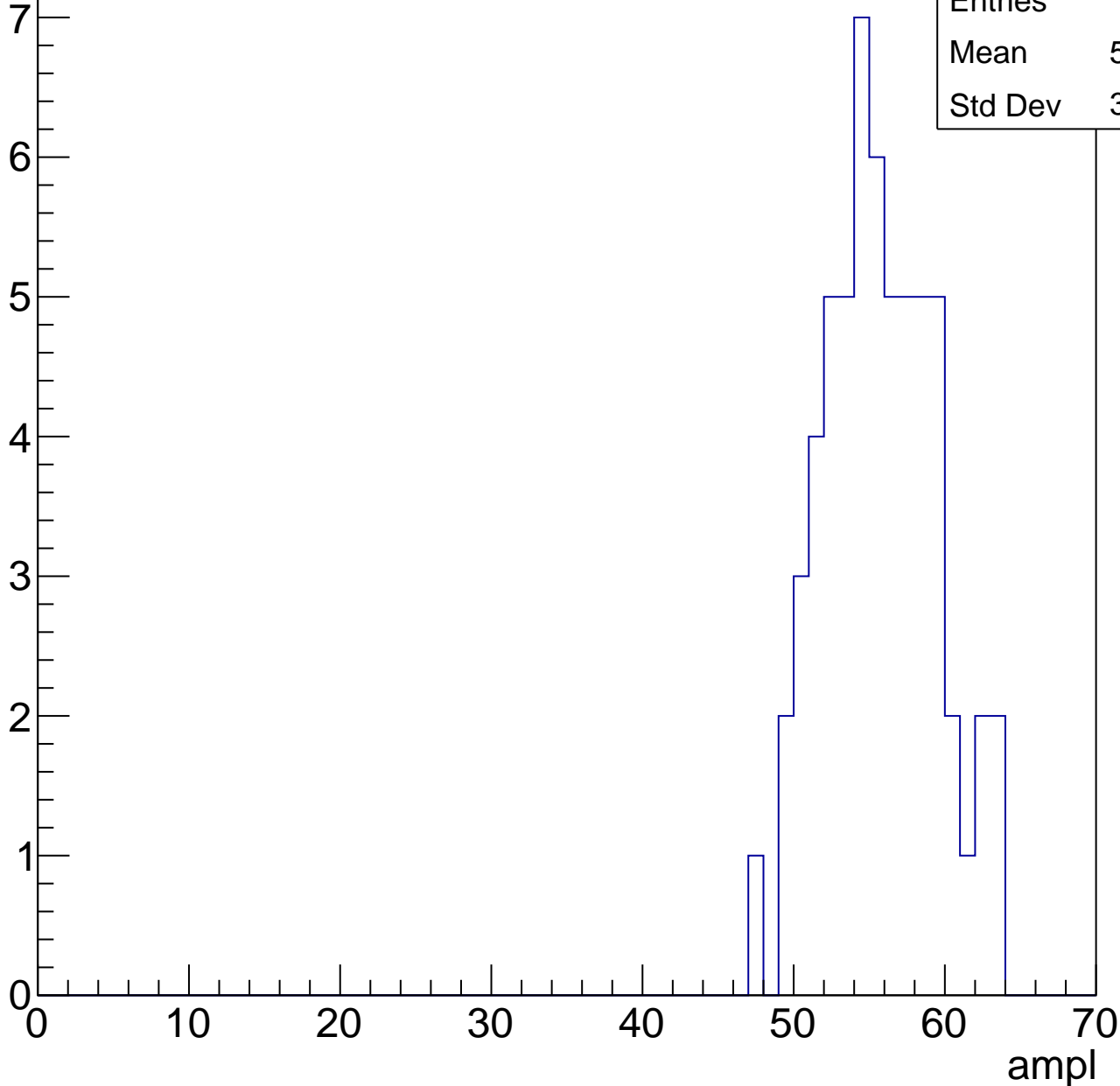


B1L103S, U10-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.22
Std Dev	3.666

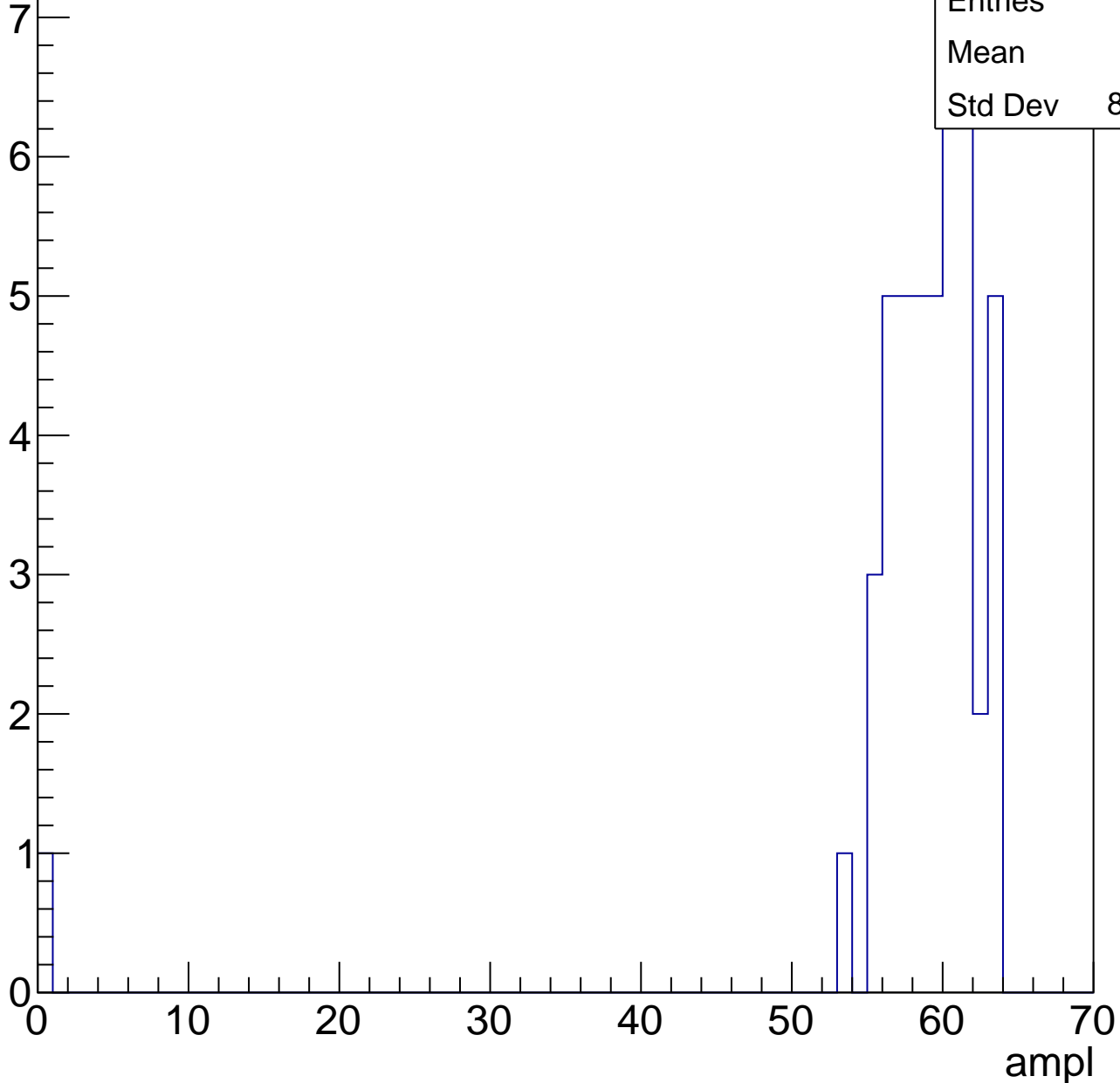


B1L103S, U10-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	57.7
Std Dev	8.956

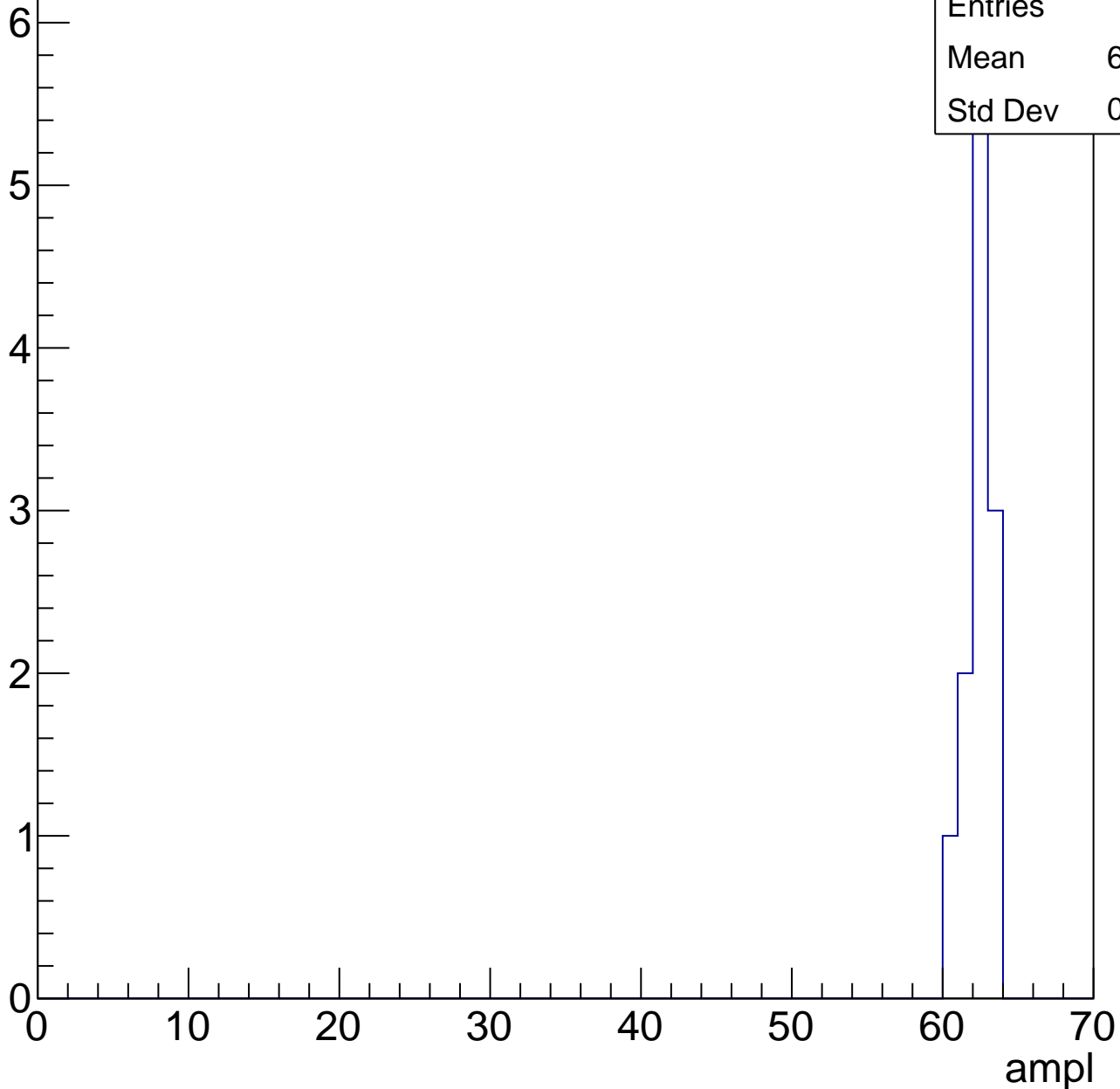


B1L103S, U10-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.92
Std Dev	0.862



B1L103S, U10-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch37, adc0

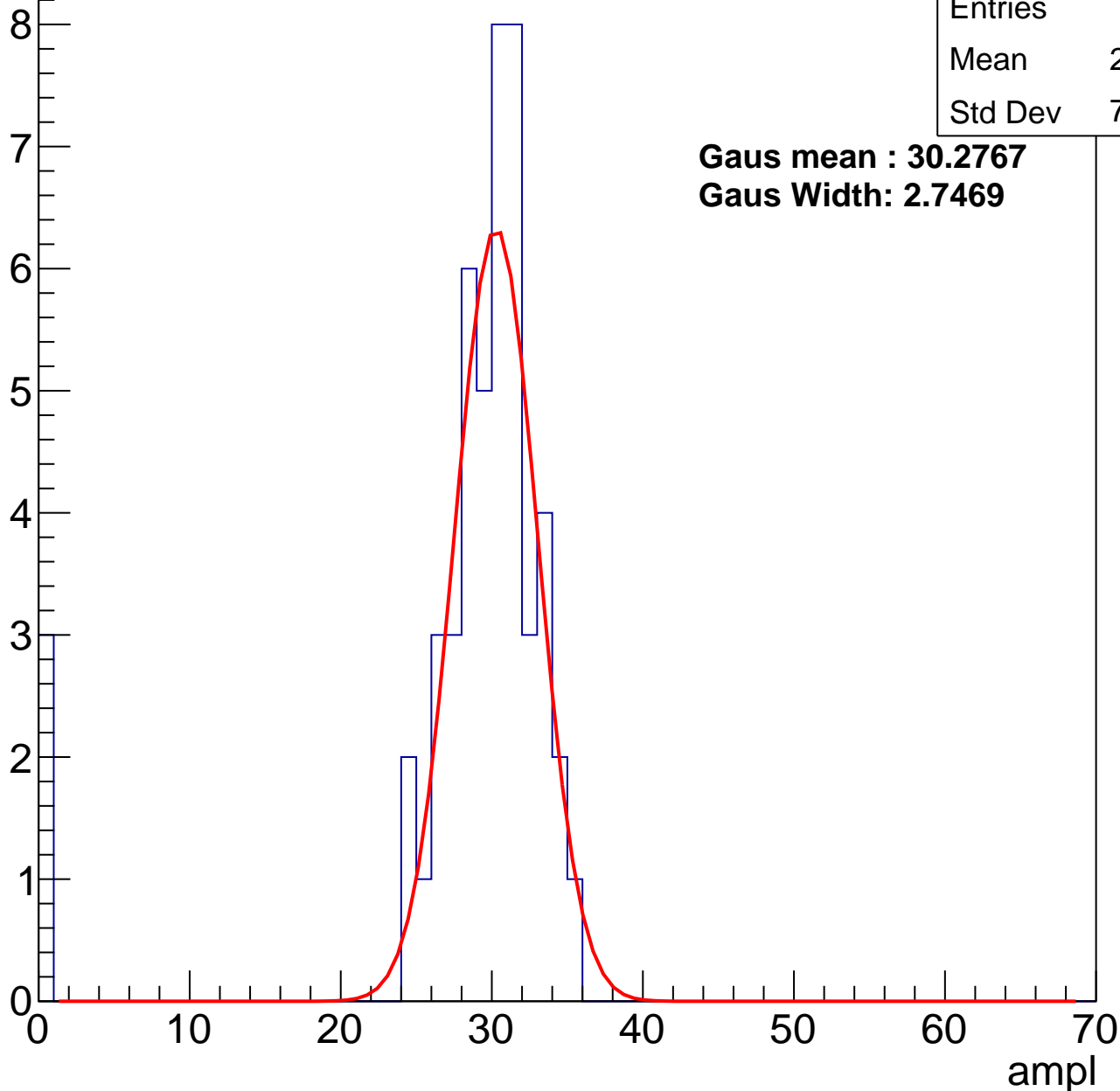
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	27.84
Std Dev	7.539

Gaus mean : 30.2767

Gaus Width: 2.7469



B1L103S, U10-ch37, adc1

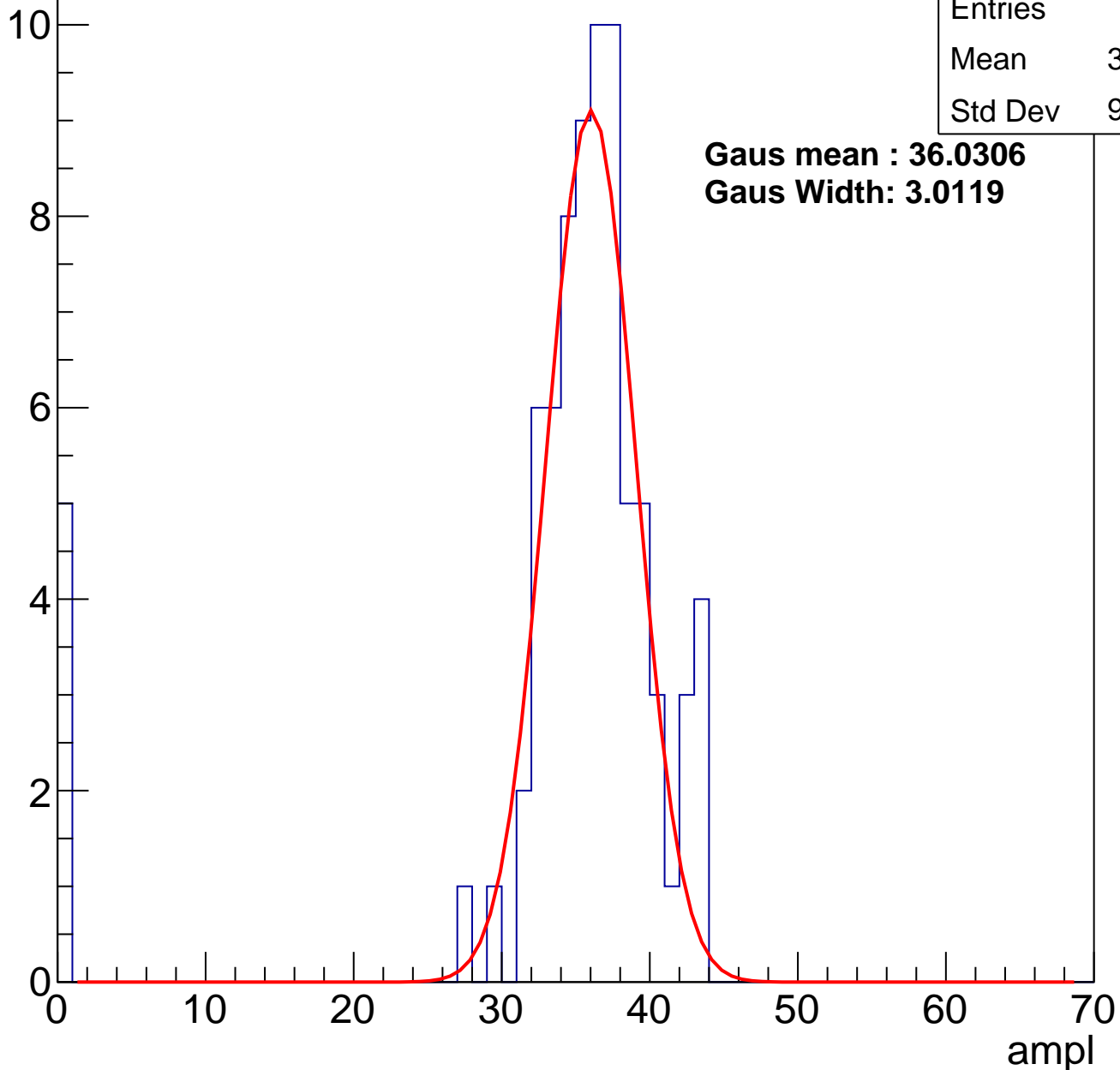
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	33.78
Std Dev	9.356

Gaus mean : 36.0306

Gaus Width: 3.0119

Entry



B1L103S, U10-ch37, adc2

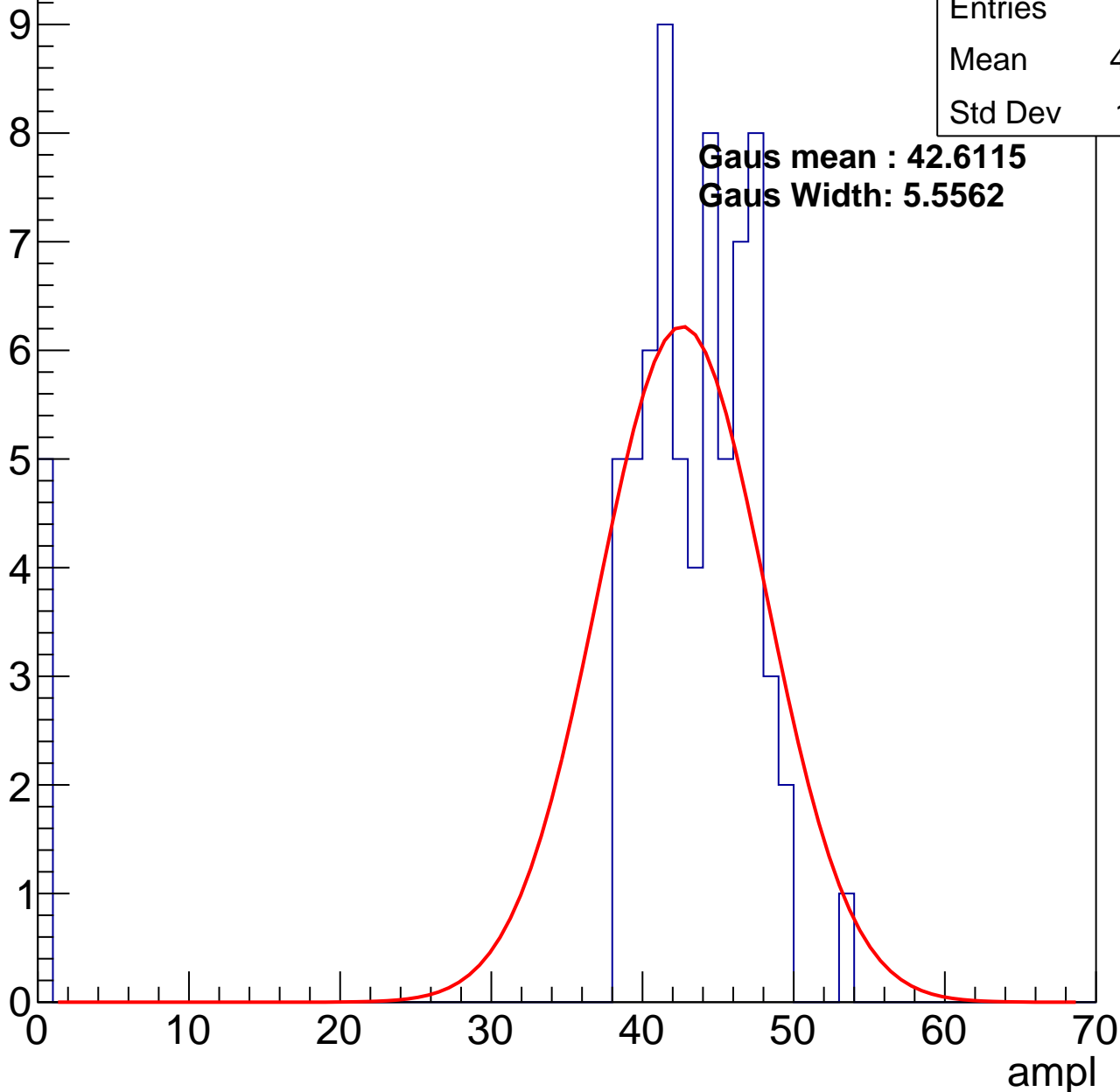
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.36
Std Dev	11.41

Gaus mean : 42.6115

Gaus Width: 5.5562

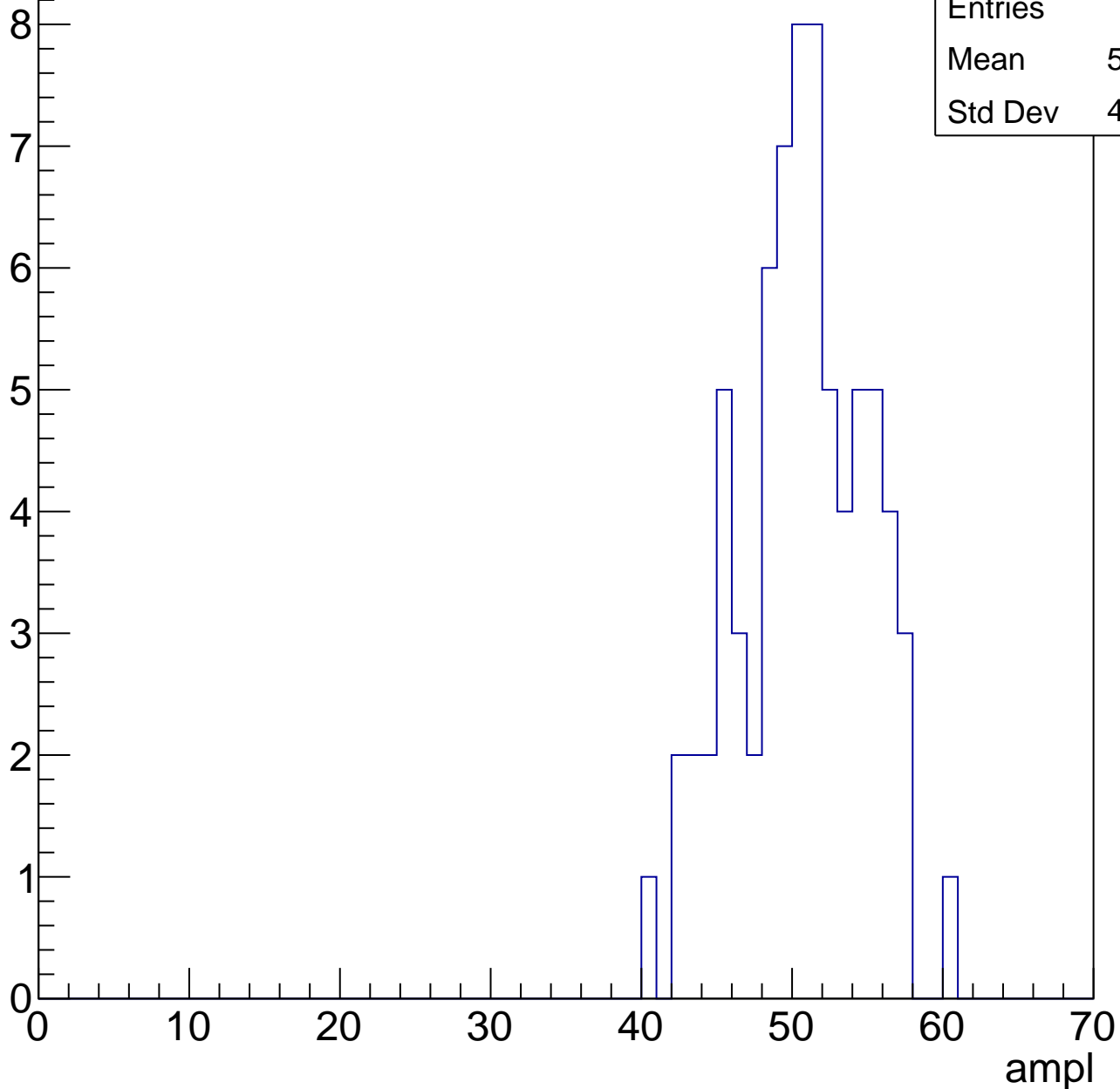


B1L103S, U10-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	50.22
Std Dev	4.182

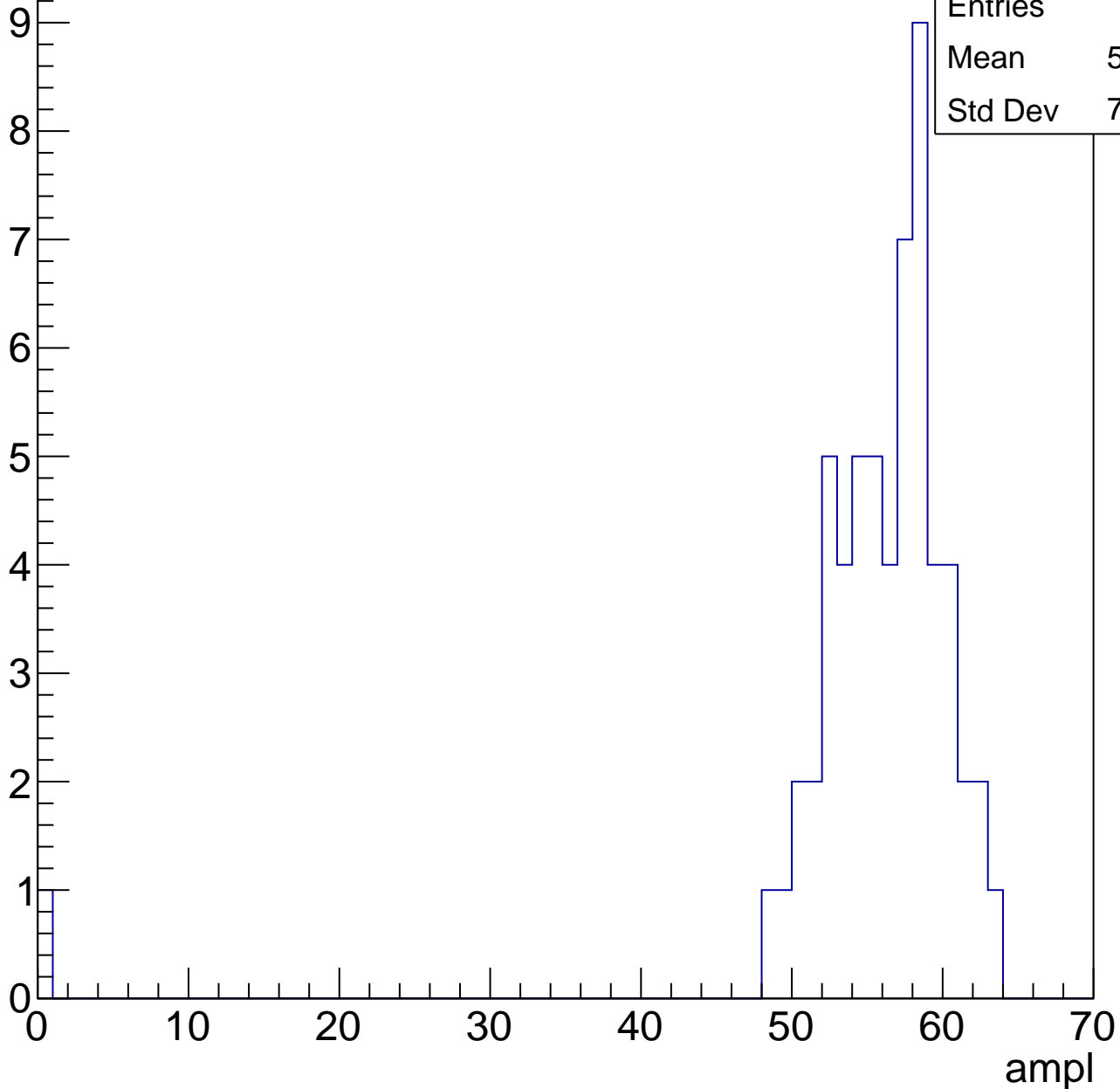


B1L103S, U10-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	55.02
Std Dev	7.993

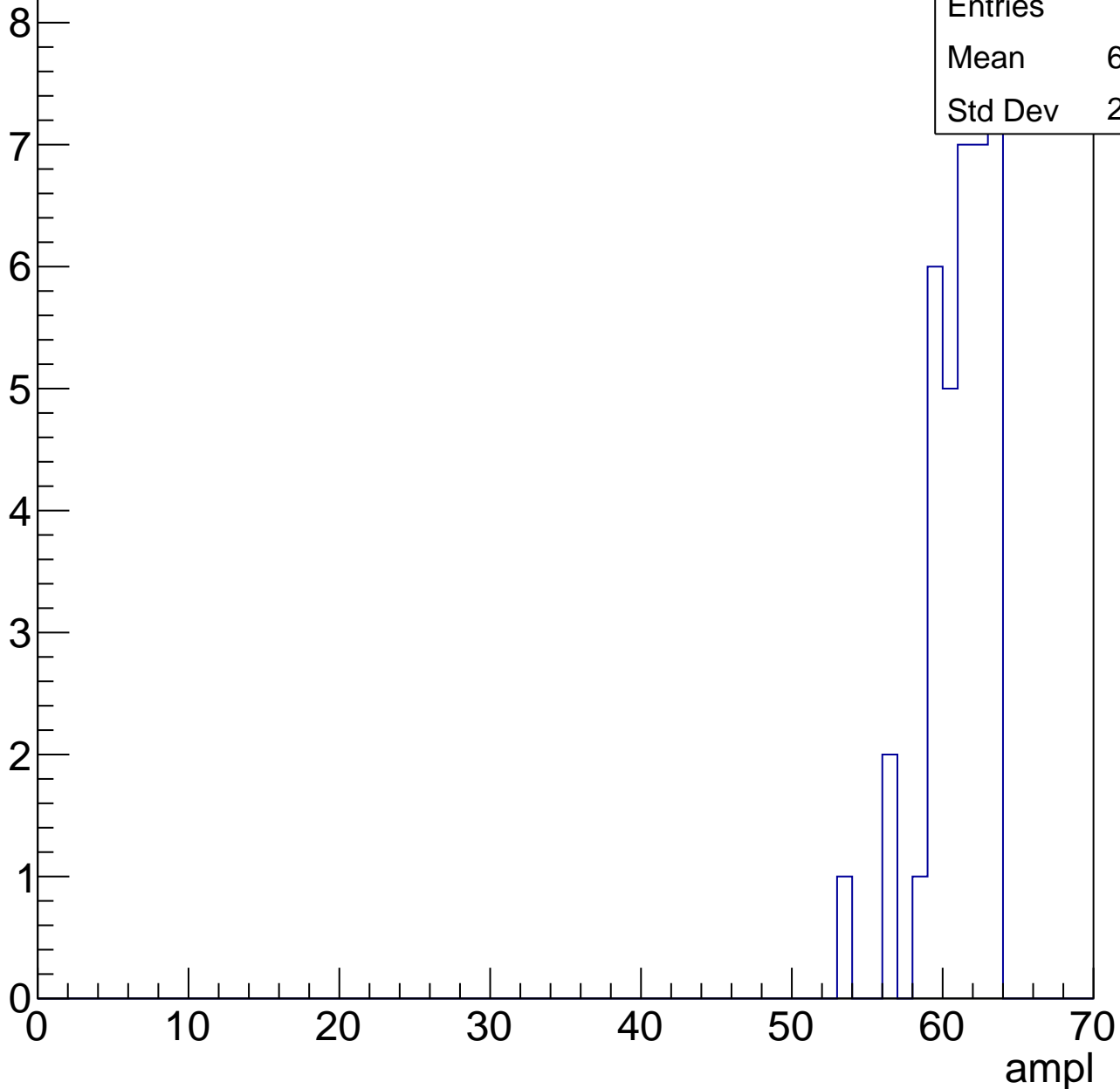


B1L103S, U10-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	60.59
Std Dev	2.236



B1L103S, U10-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch38, adc0

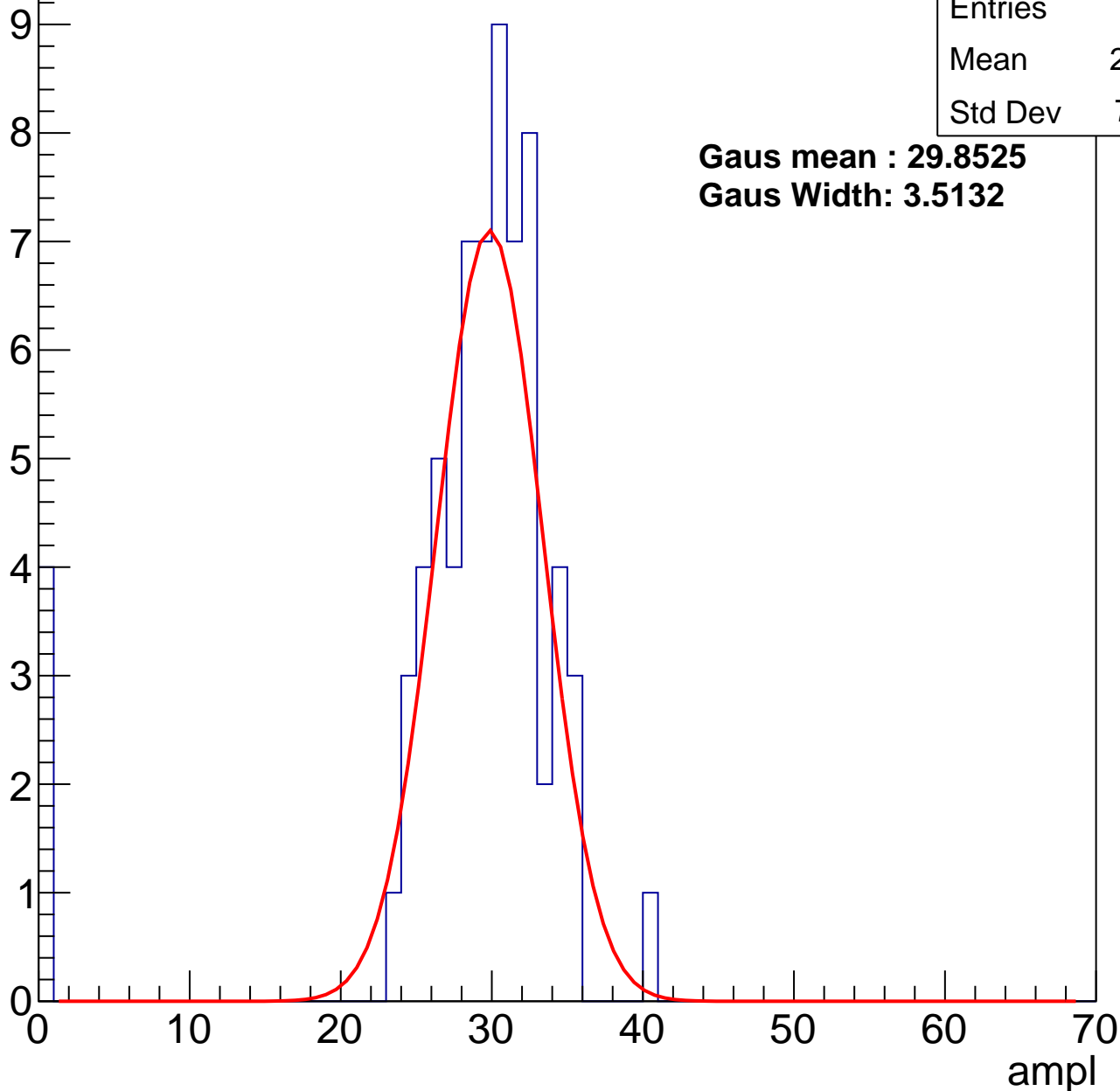
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	27.86
Std Dev	7.601

Gaus mean : 29.8525

Gaus Width: 3.5132



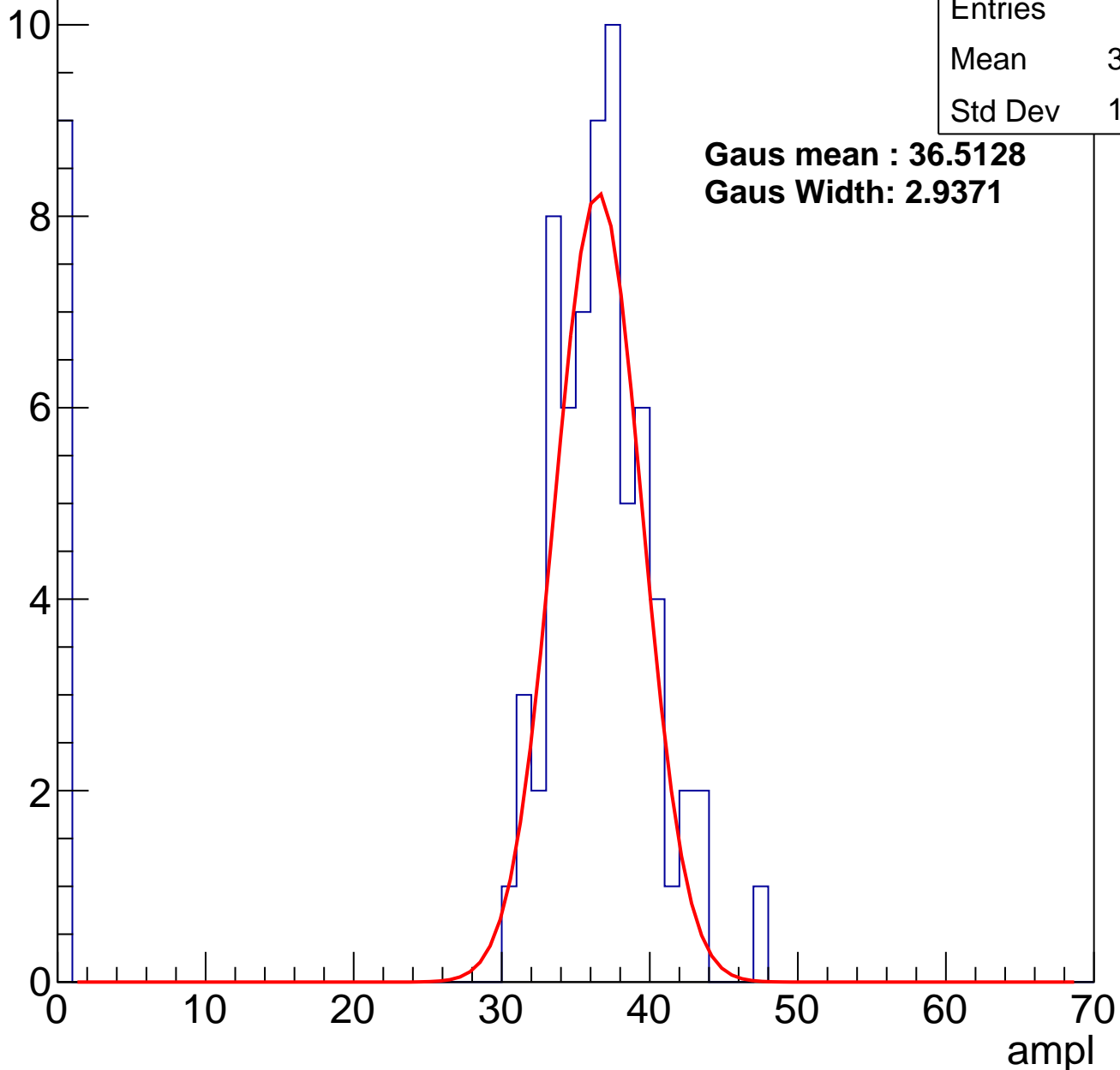
B1L103S, U10-ch38, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	32.05
Std Dev	12.13

Gaus mean : 36.5128
Gaus Width: 2.9371

Entry



B1L103S, U10-ch38, adc2

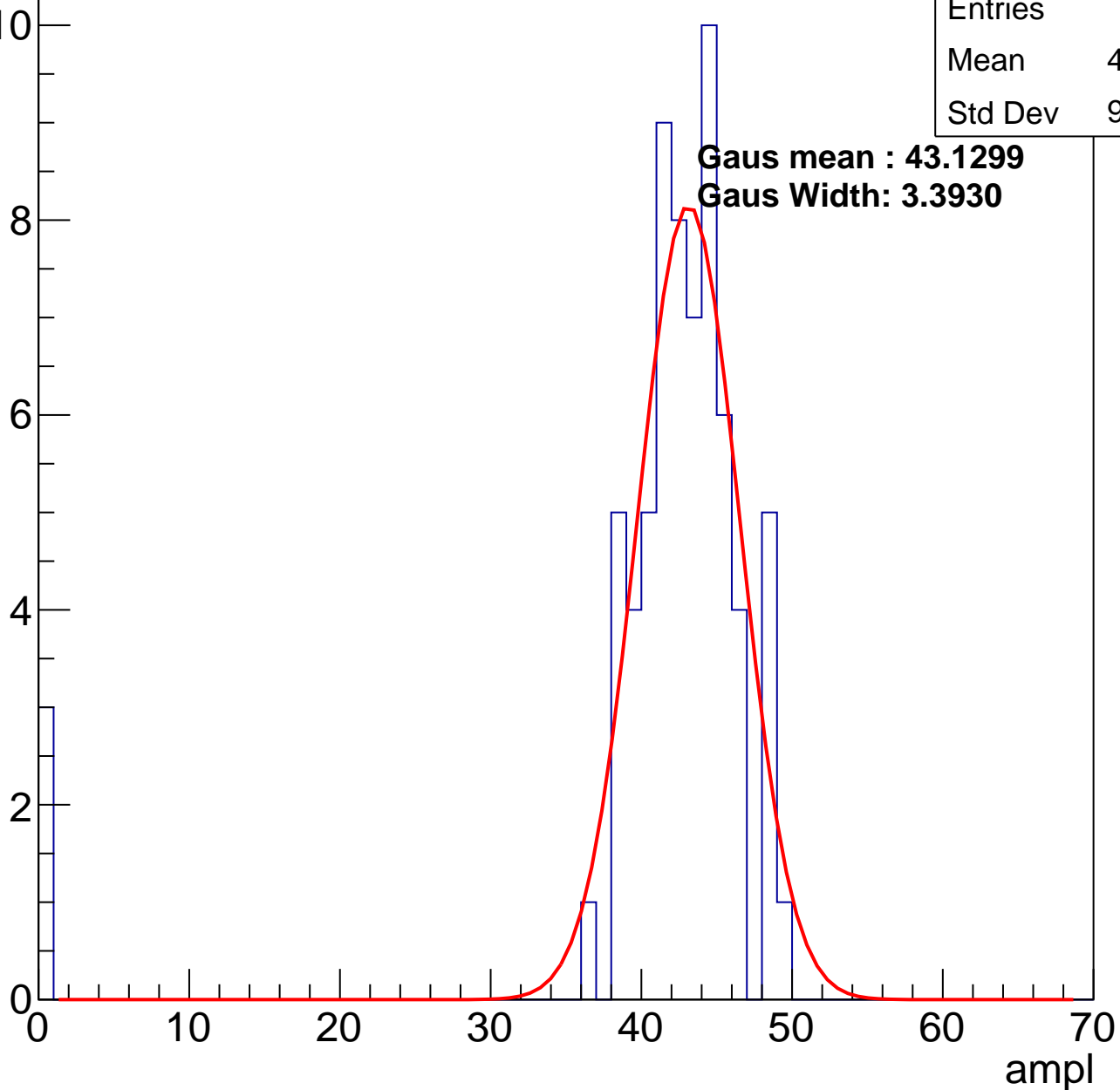
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.75
Std Dev	9.204

Gaus mean : 43.1299

Gaus Width: 3.3930

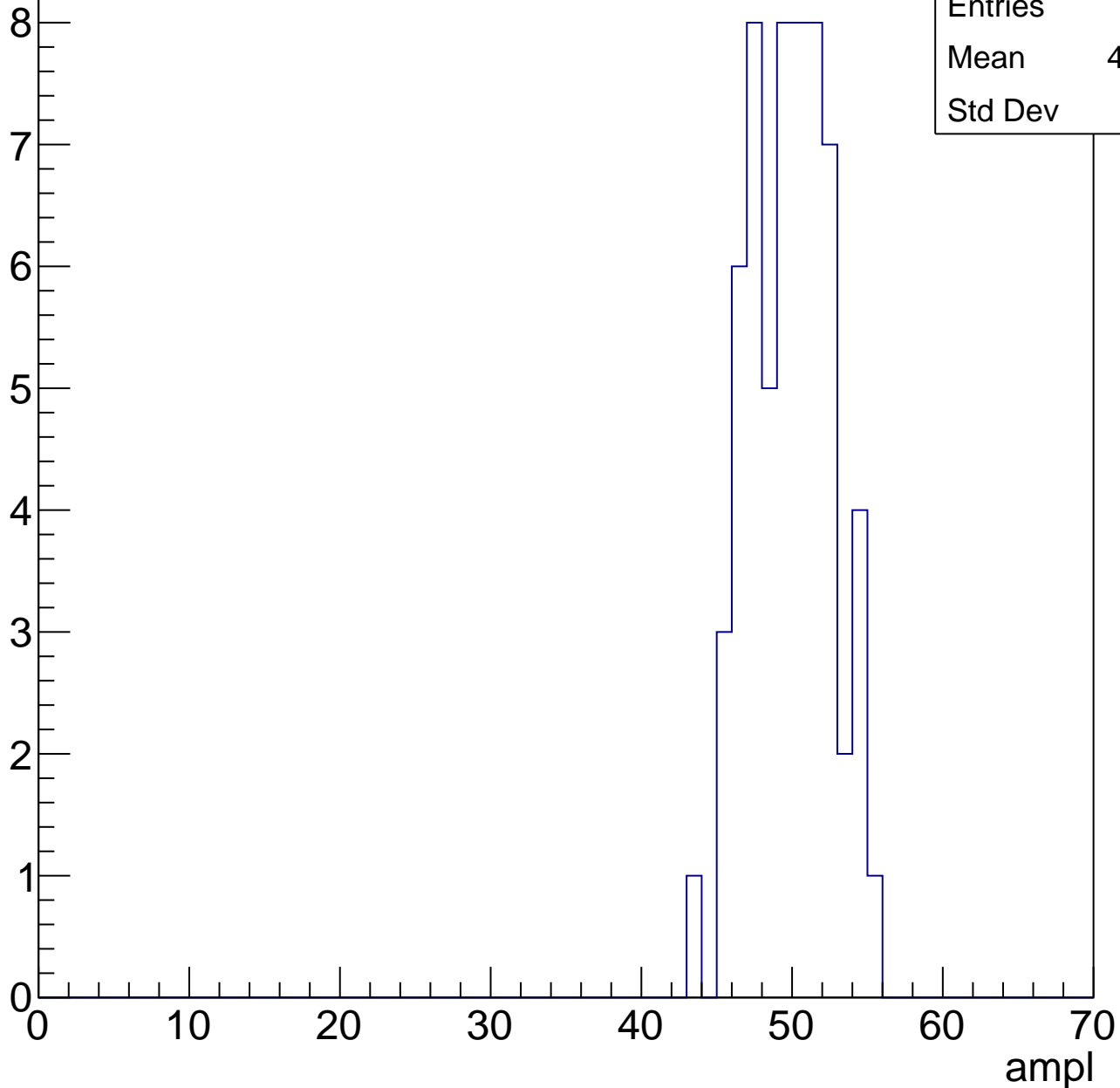


B1L103S, U10-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.36
Std Dev	2.68

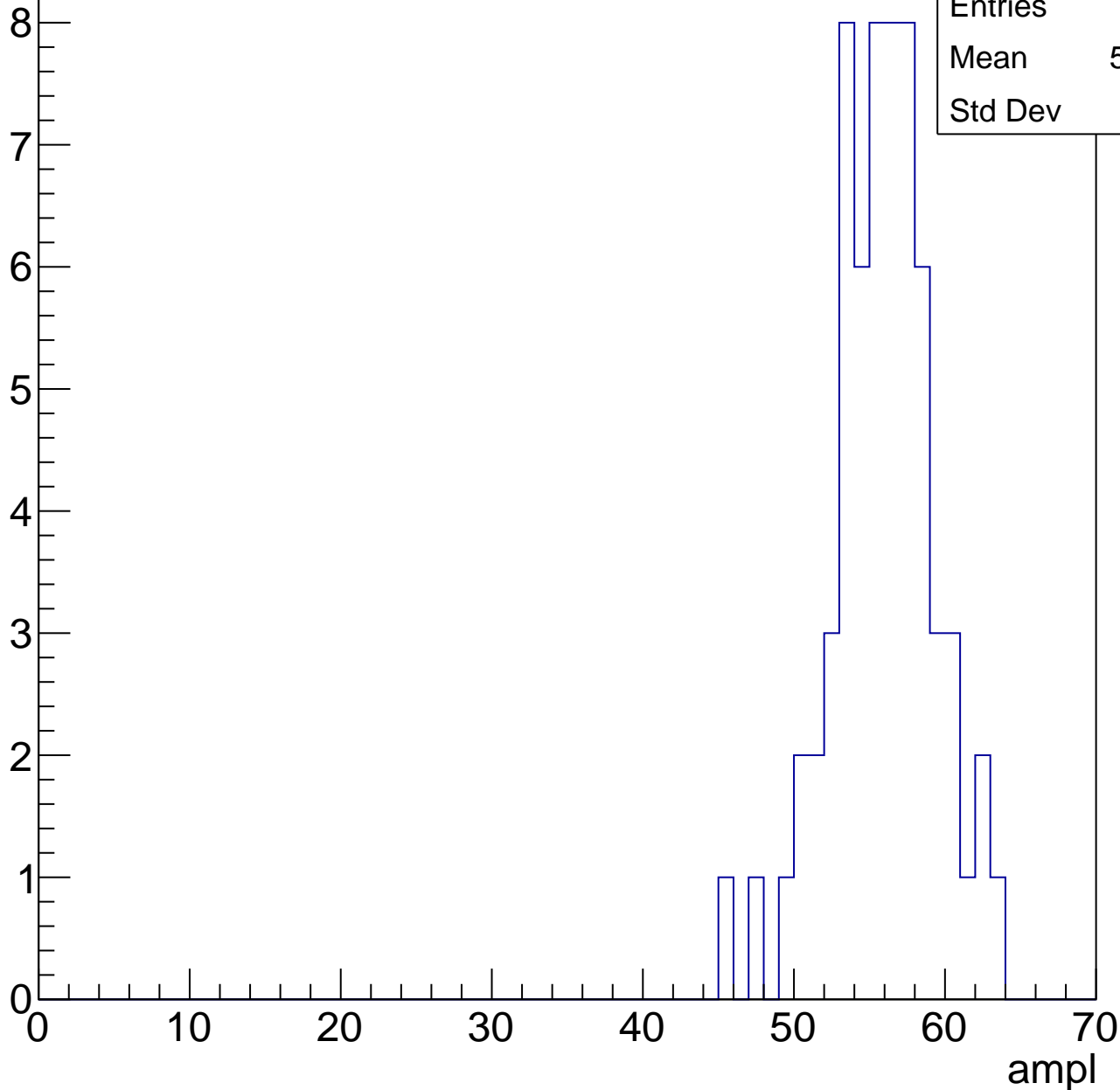


B1L103S, U10-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	55.38
Std Dev	3.43

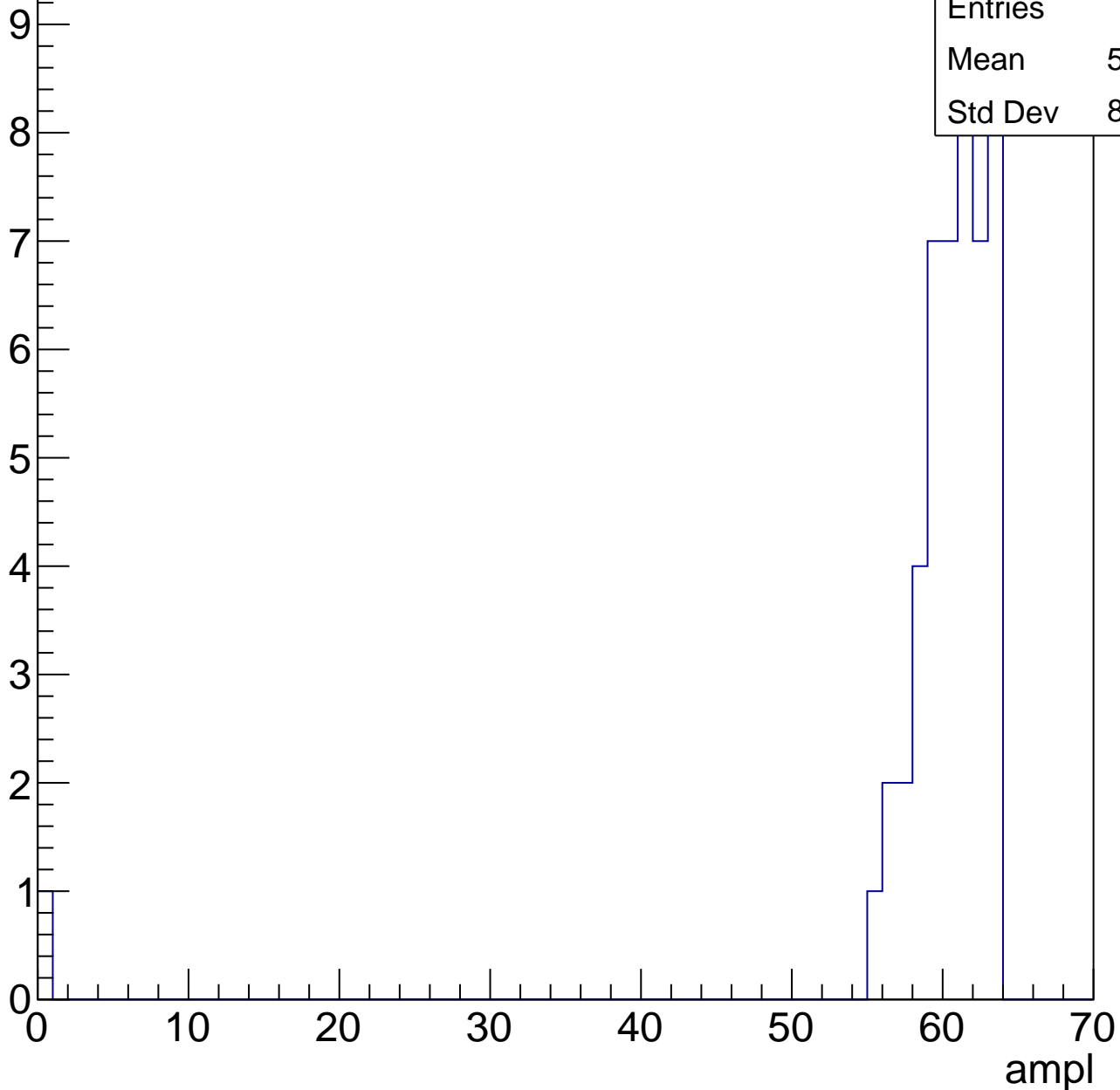


B1L103S, U10-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	59.02
Std Dev	8.852



B1L103S, U10-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch39, adc0

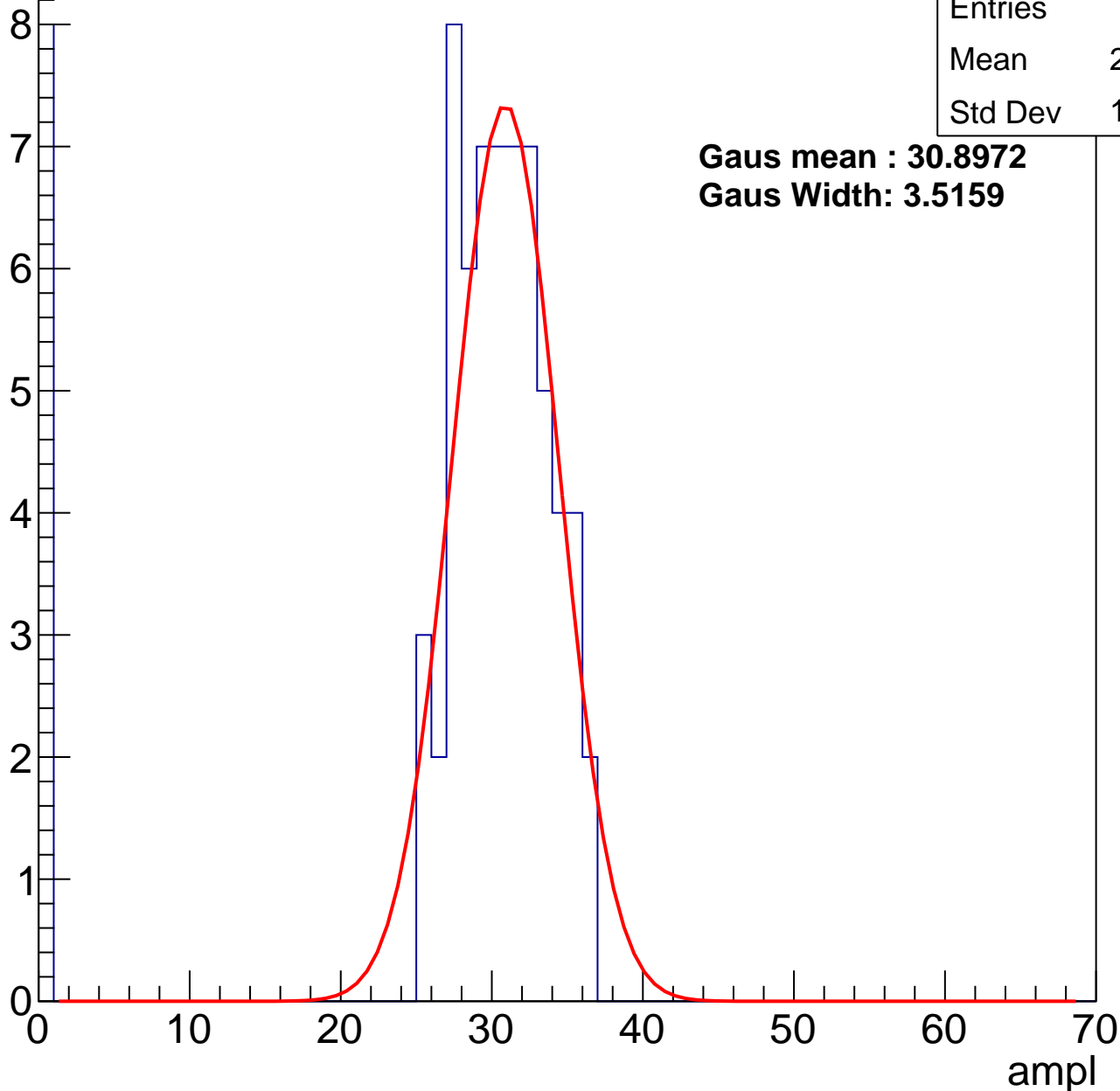
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	26.83
Std Dev	10.02

Gaus mean : 30.8972

Gaus Width: 3.5159



B1L103S, U10-ch39, adc1

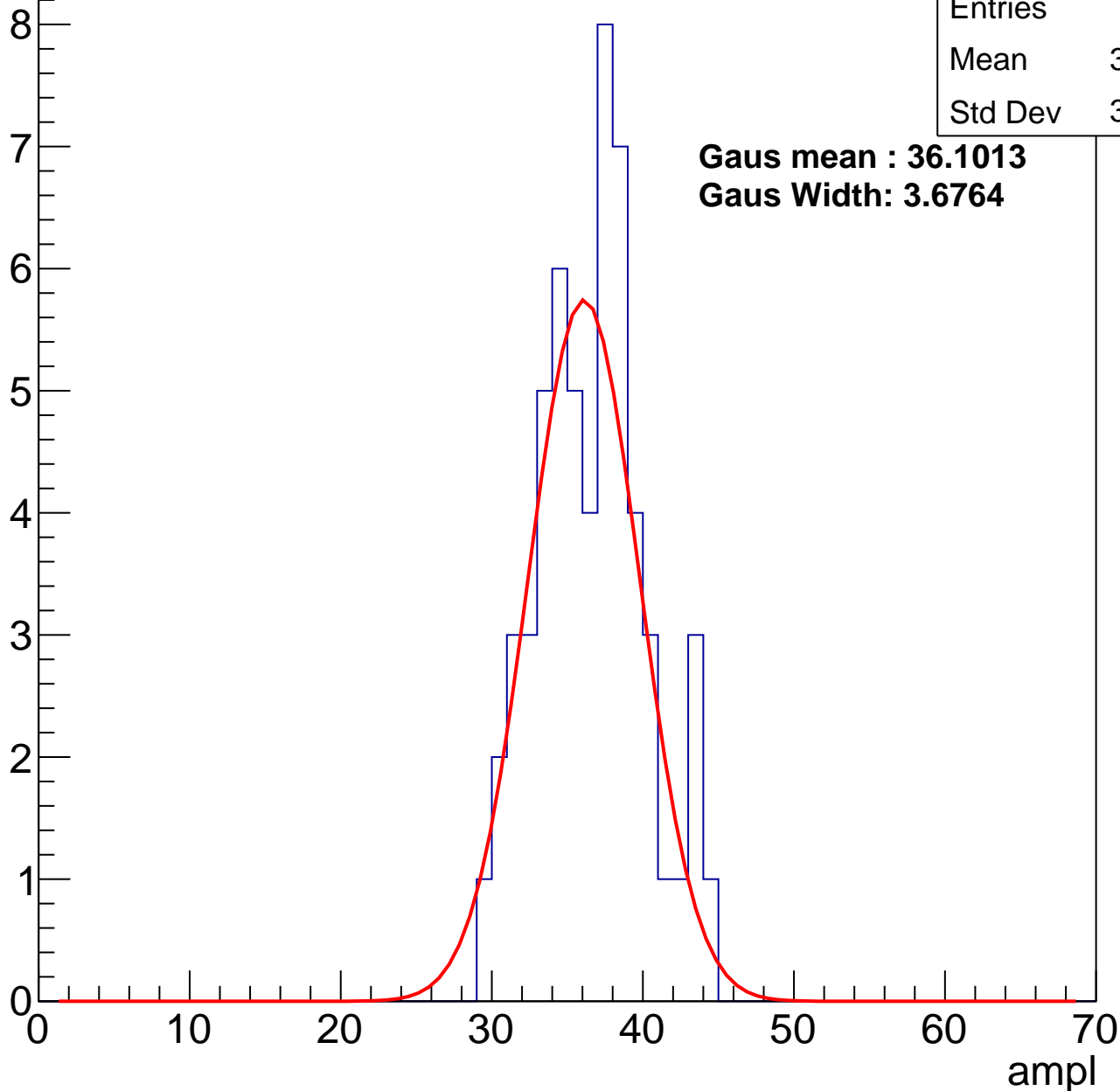
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	36.14
Std Dev	3.527

Gaus mean : 36.1013

Gaus Width: 3.6764



B1L103S, U10-ch39, adc2

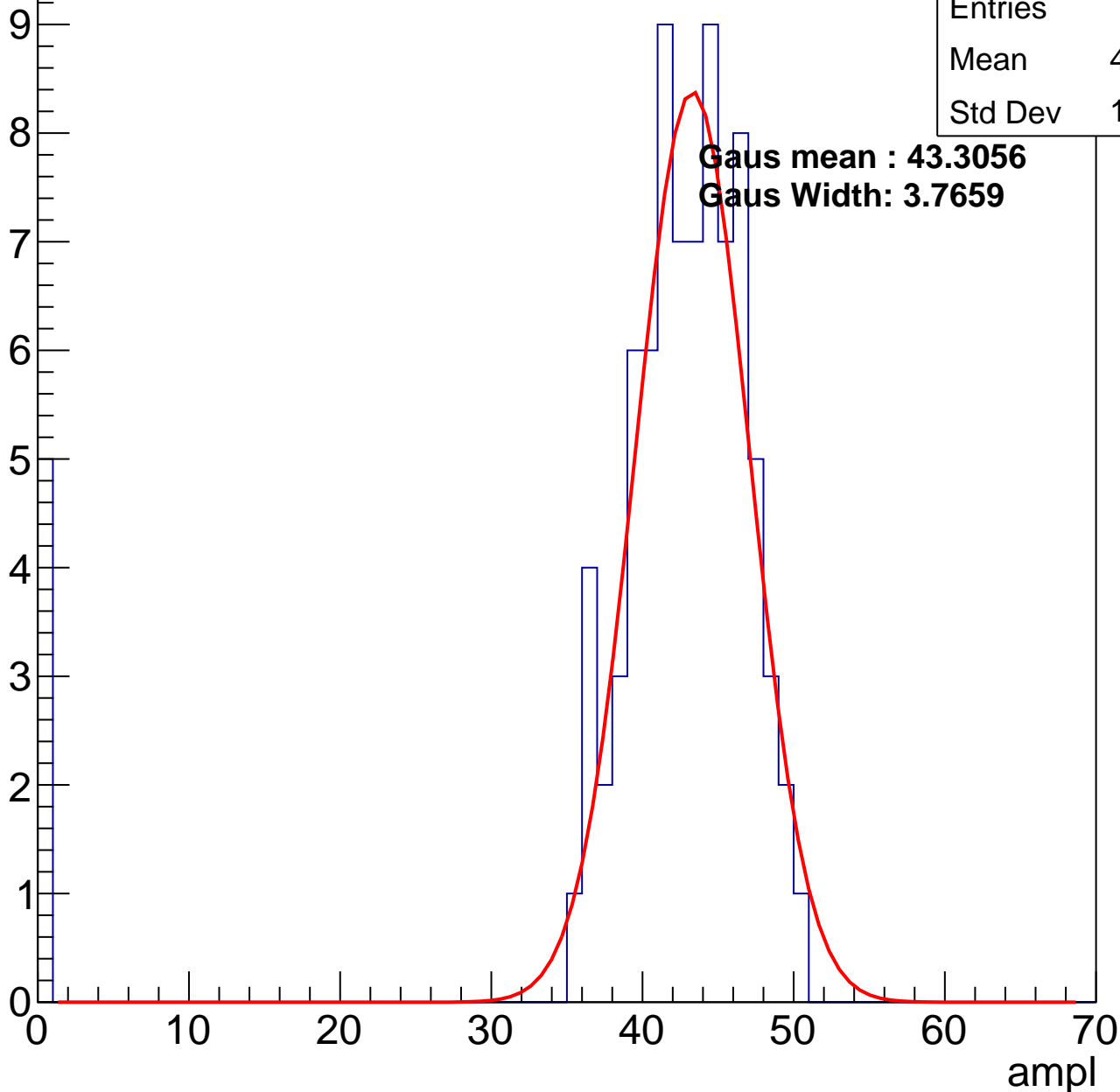
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	40.13
Std Dev	10.59

Gaus mean : 43.3056

Gaus Width: 3.7659

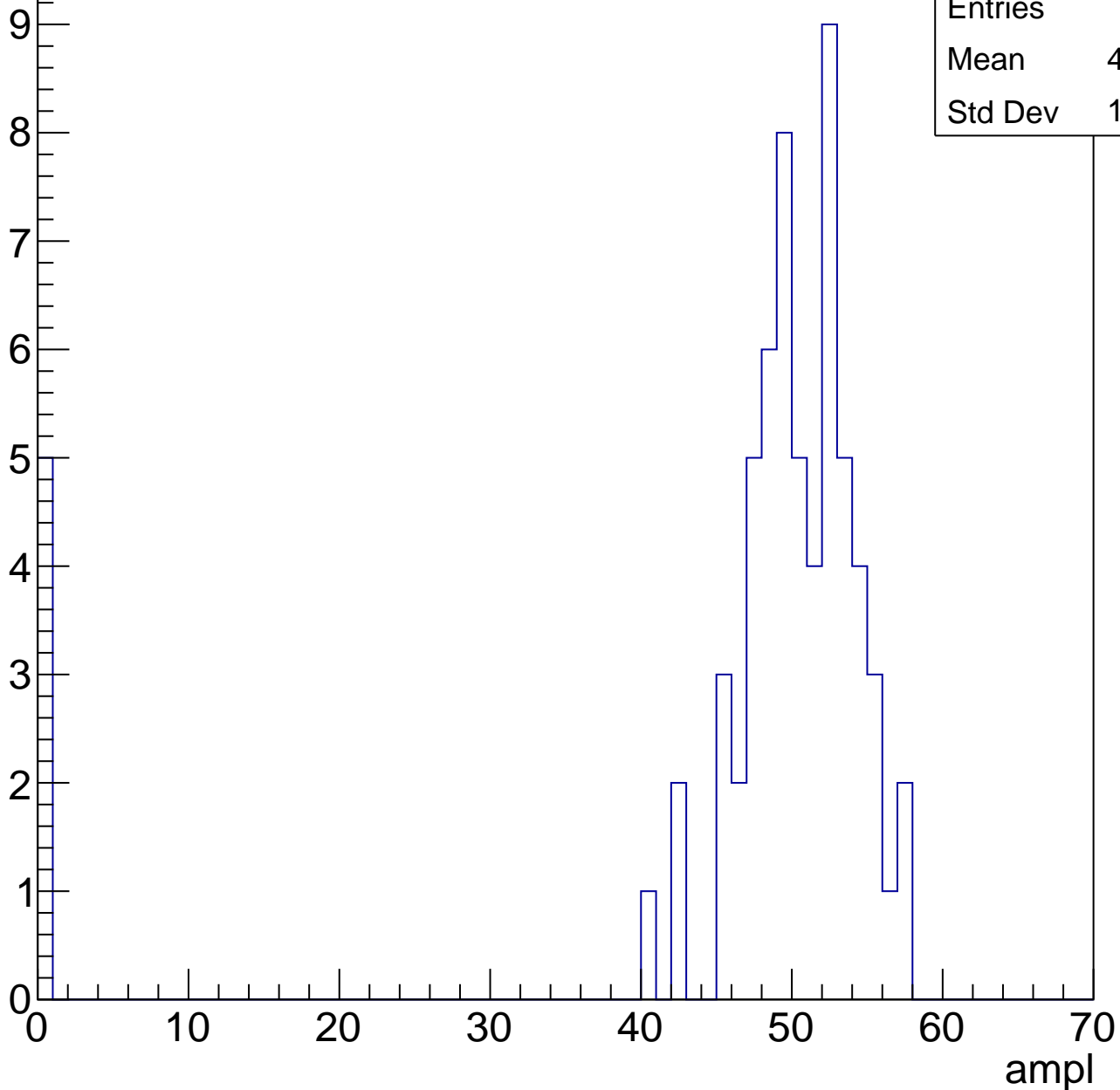


B1L103S, U10-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	46.22
Std Dev	13.78

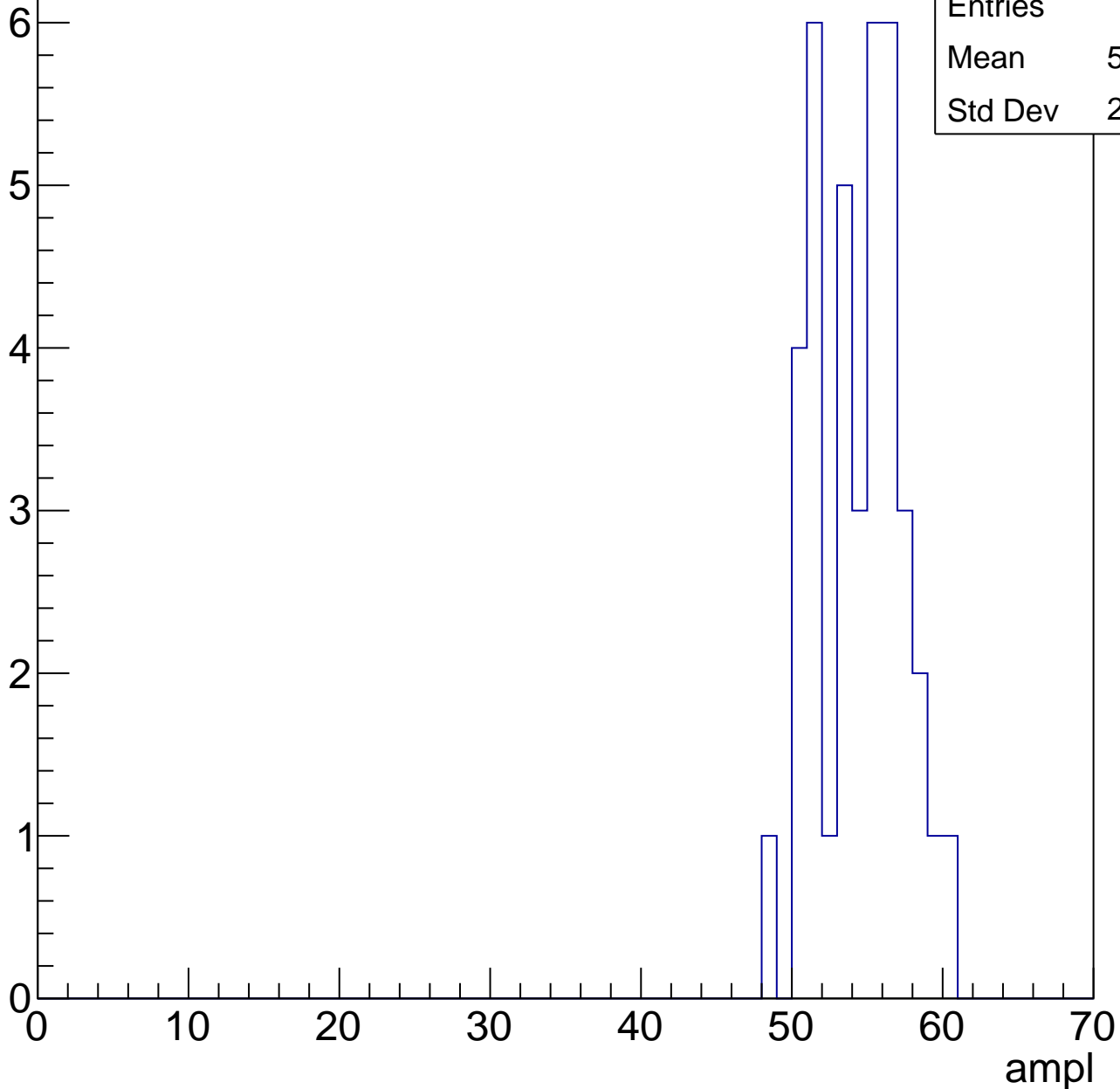


B1L103S, U10-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	53.97
Std Dev	2.833



B1L103S, U10-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 65

Mean 58.69

Std Dev 7.716

8

6

4

2

0

0

10

20

30

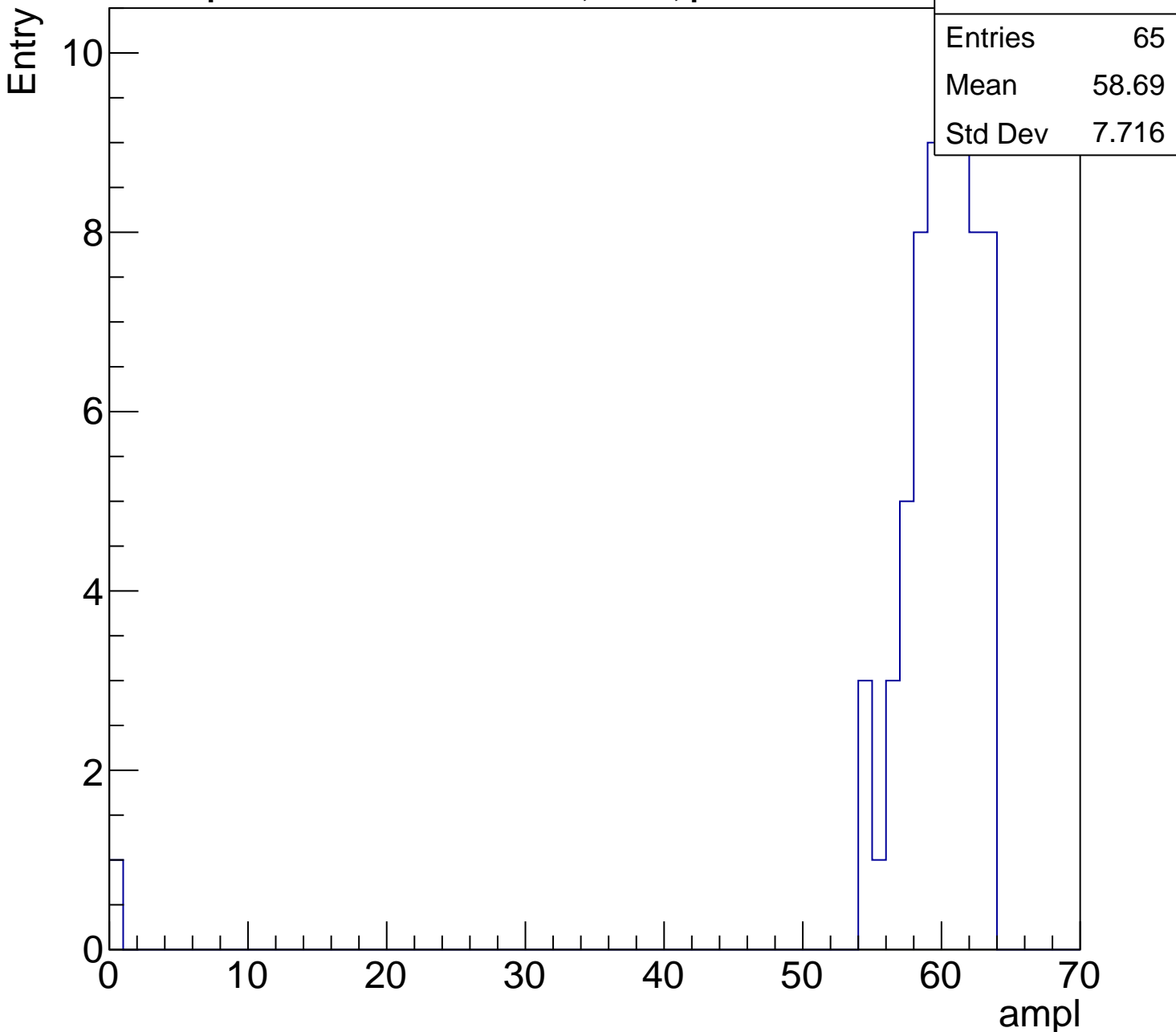
40

50

60

ampl

70



B1L103S, U10-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L103S, U10-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch40, adc0

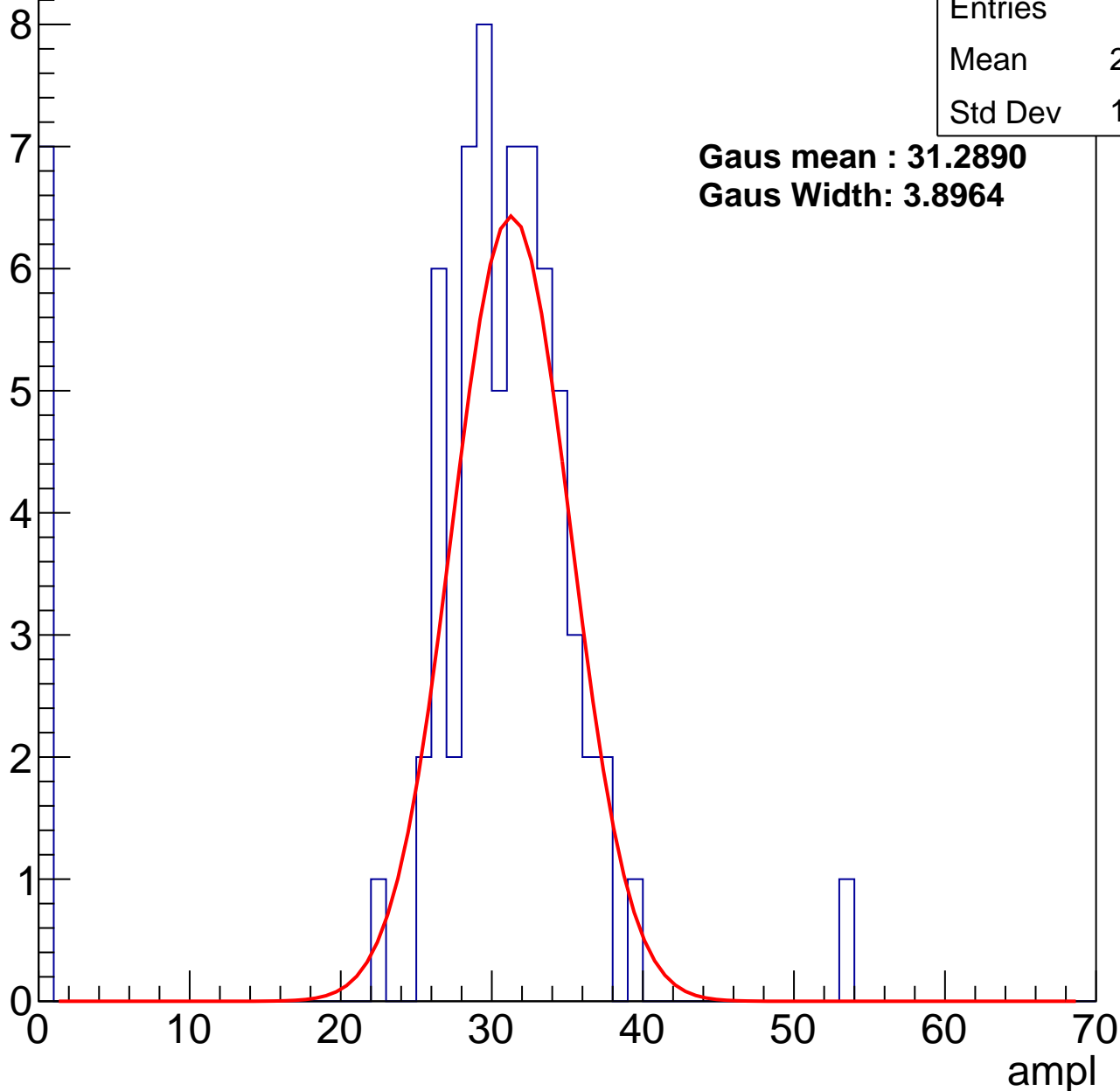
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	27.94
Std Dev	10.06

Gaus mean : 31.2890

Gaus Width: 3.8964



B1L103S, U10-ch40, adc1

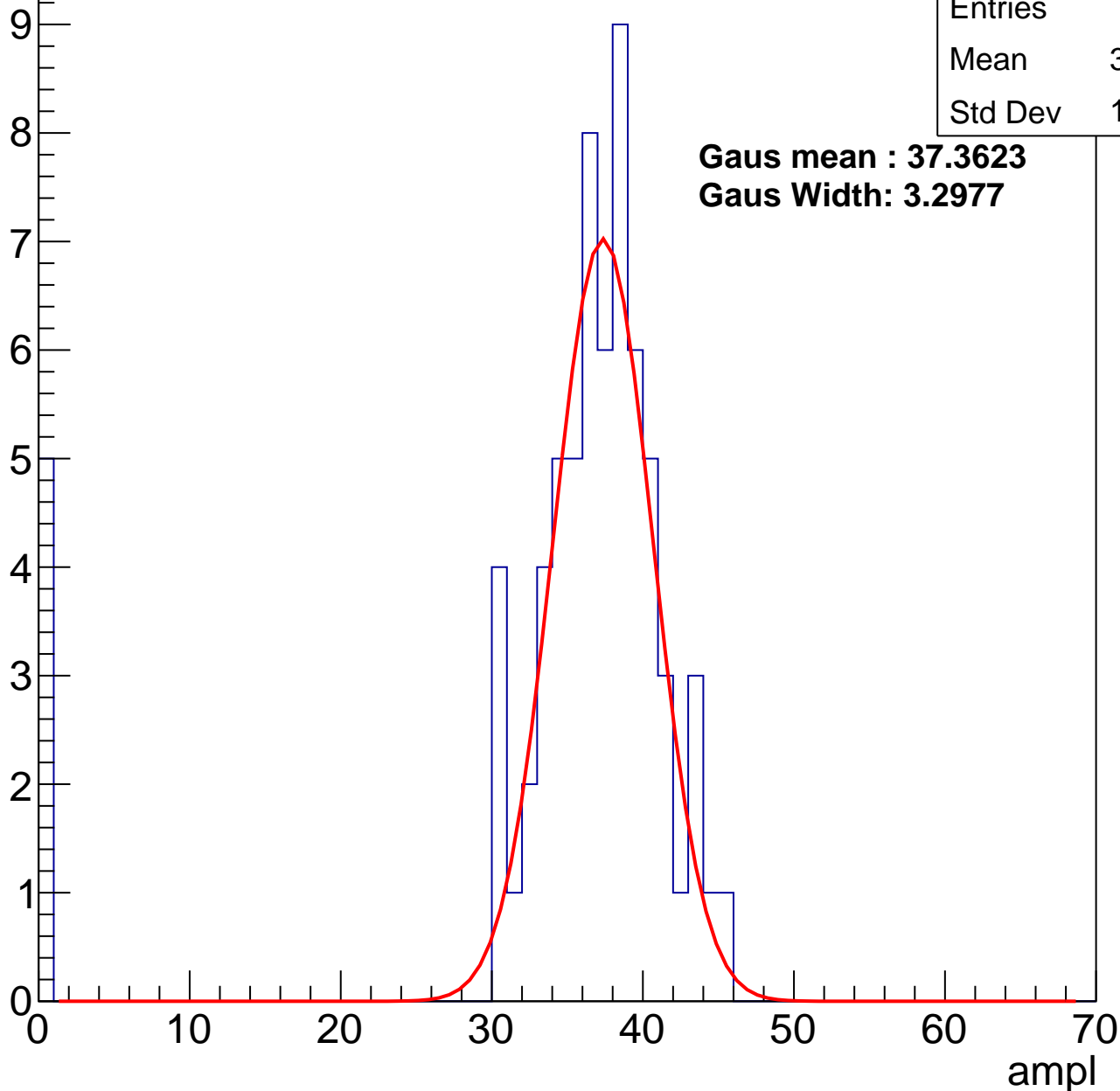
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	34.22
Std Dev	10.15

Gaus mean : 37.3623

Gaus Width: 3.2977



B1L103S, U10-ch40, adc2

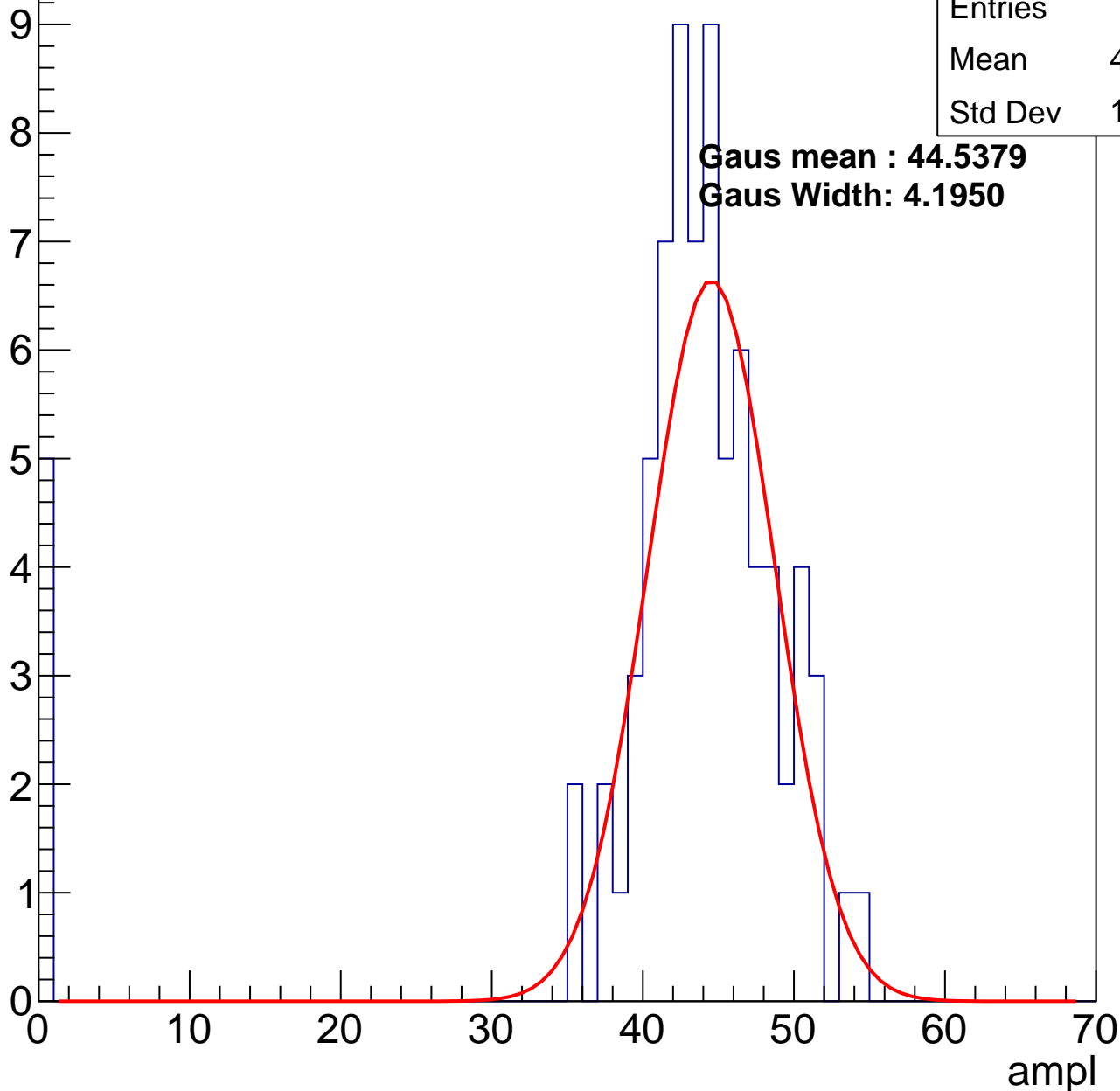
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	41.25
Std Dev	11.34

Gaus mean : 44.5379

Gaus Width: 4.1950

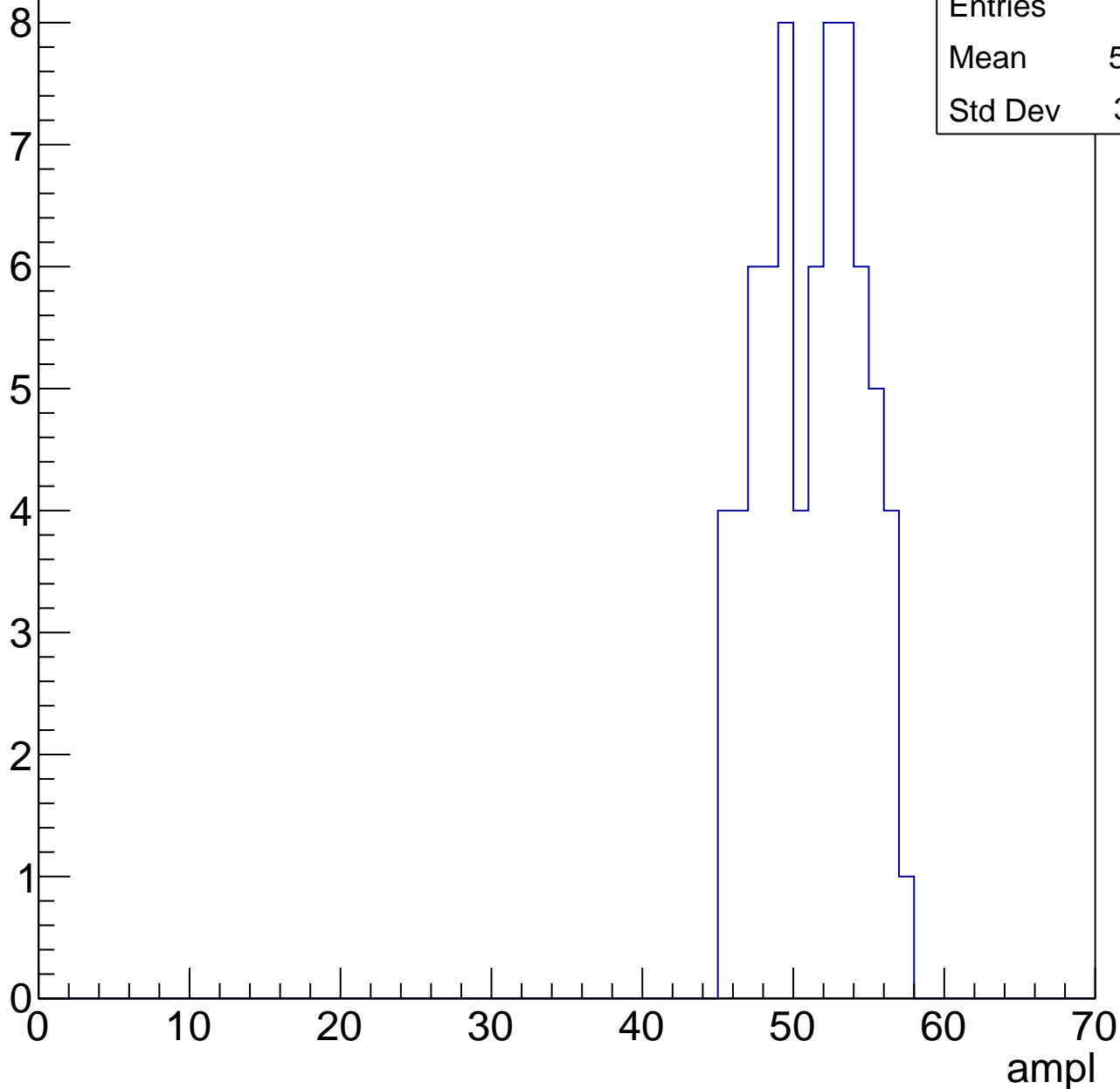


B1L103S, U10-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	50.74
Std Dev	3.241

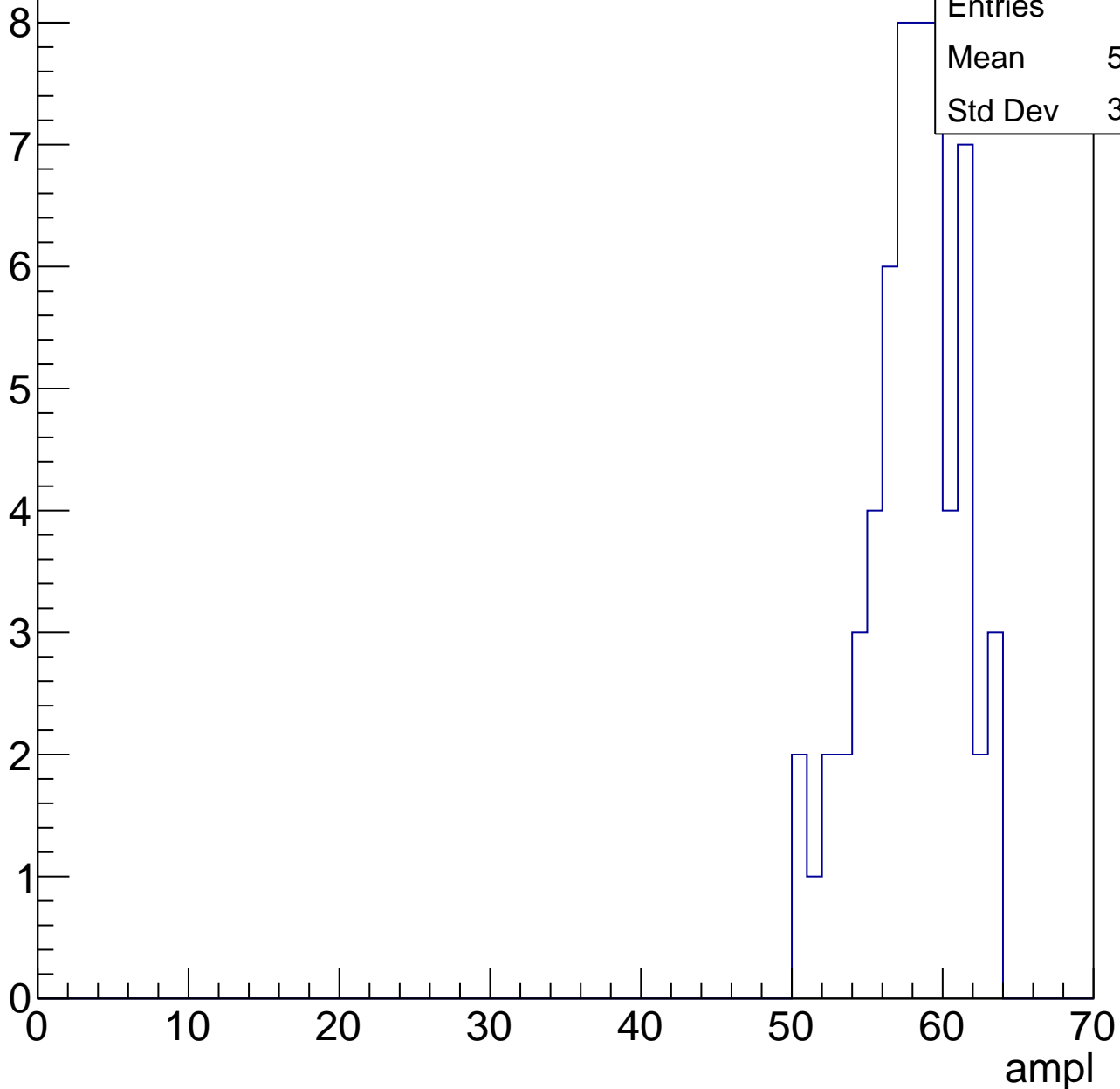


B1L103S, U10-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.52
Std Dev	3.149

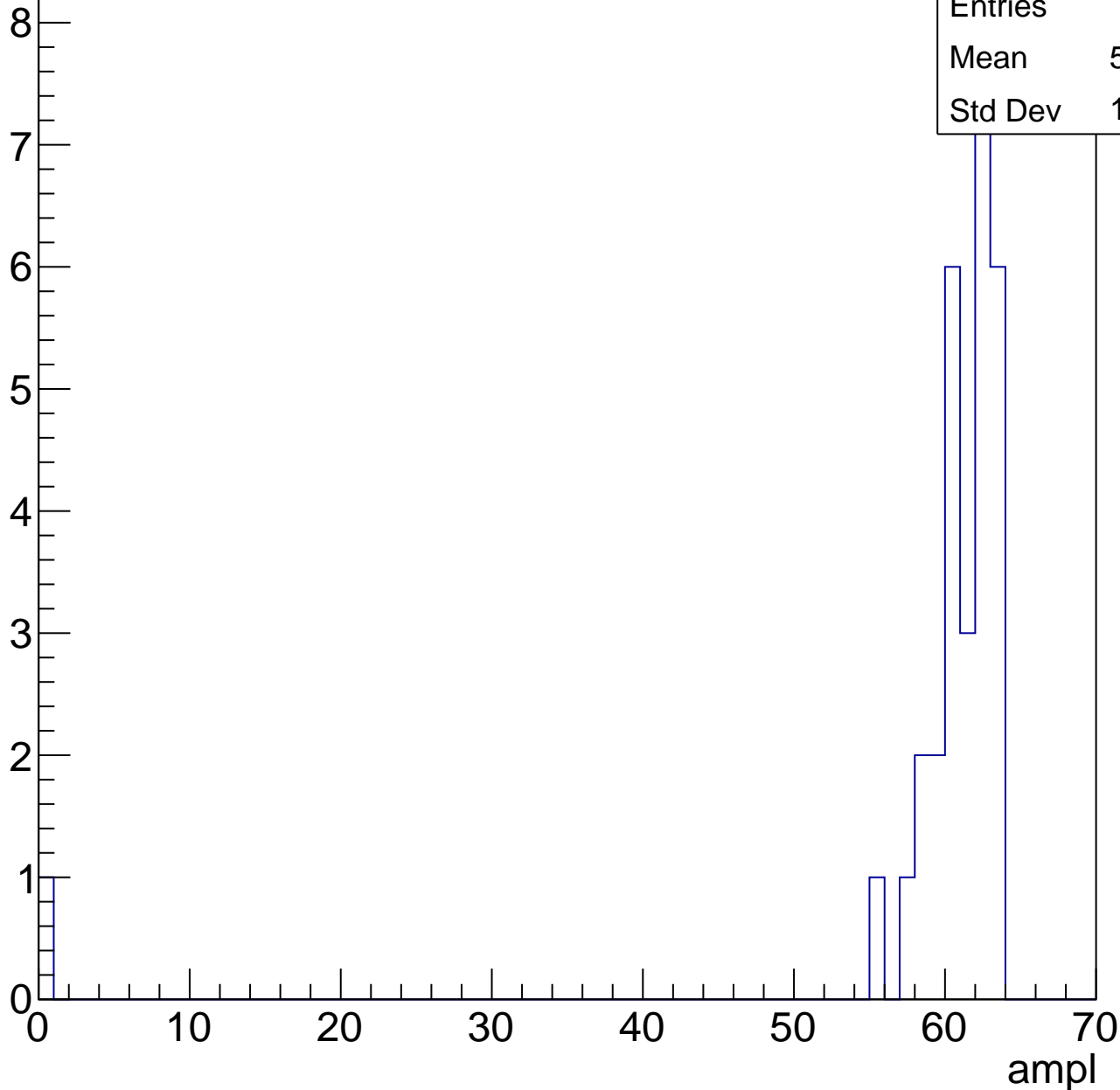


B1L103S, U10-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	58.77
Std Dev	11.09



B1L103S, U10-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L103S, U10-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.167
Std Dev	4.81

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch41, adc0

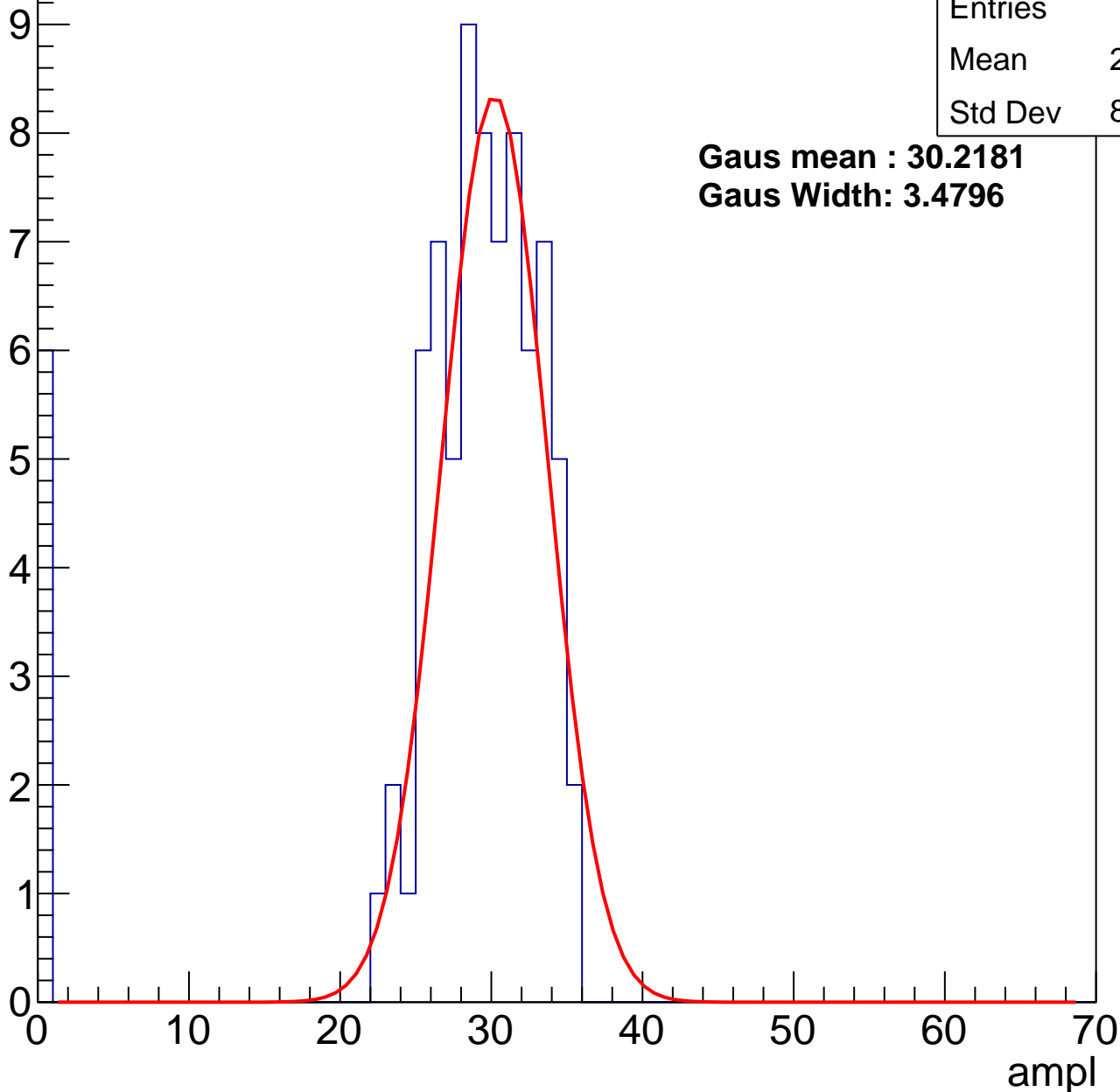
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	27.05
Std Dev	8.275

Gaus mean : 30.2181

Gaus Width: 3.4796



B1L103S, U10-ch41, adc1

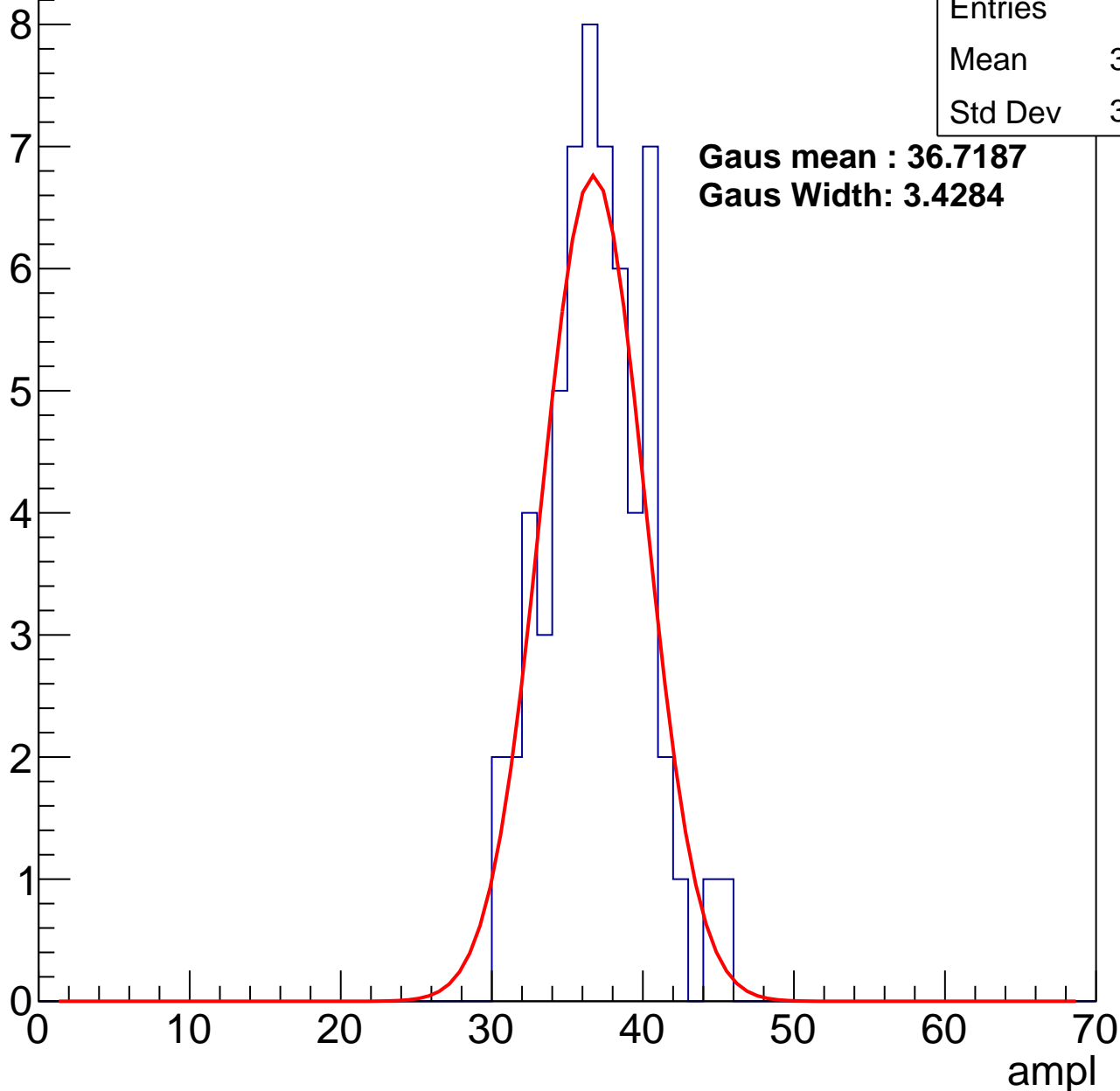
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	36.47
Std Dev	3.258

Gaus mean : 36.7187

Gaus Width: 3.4284



B1L103S, U10-ch41, adc2

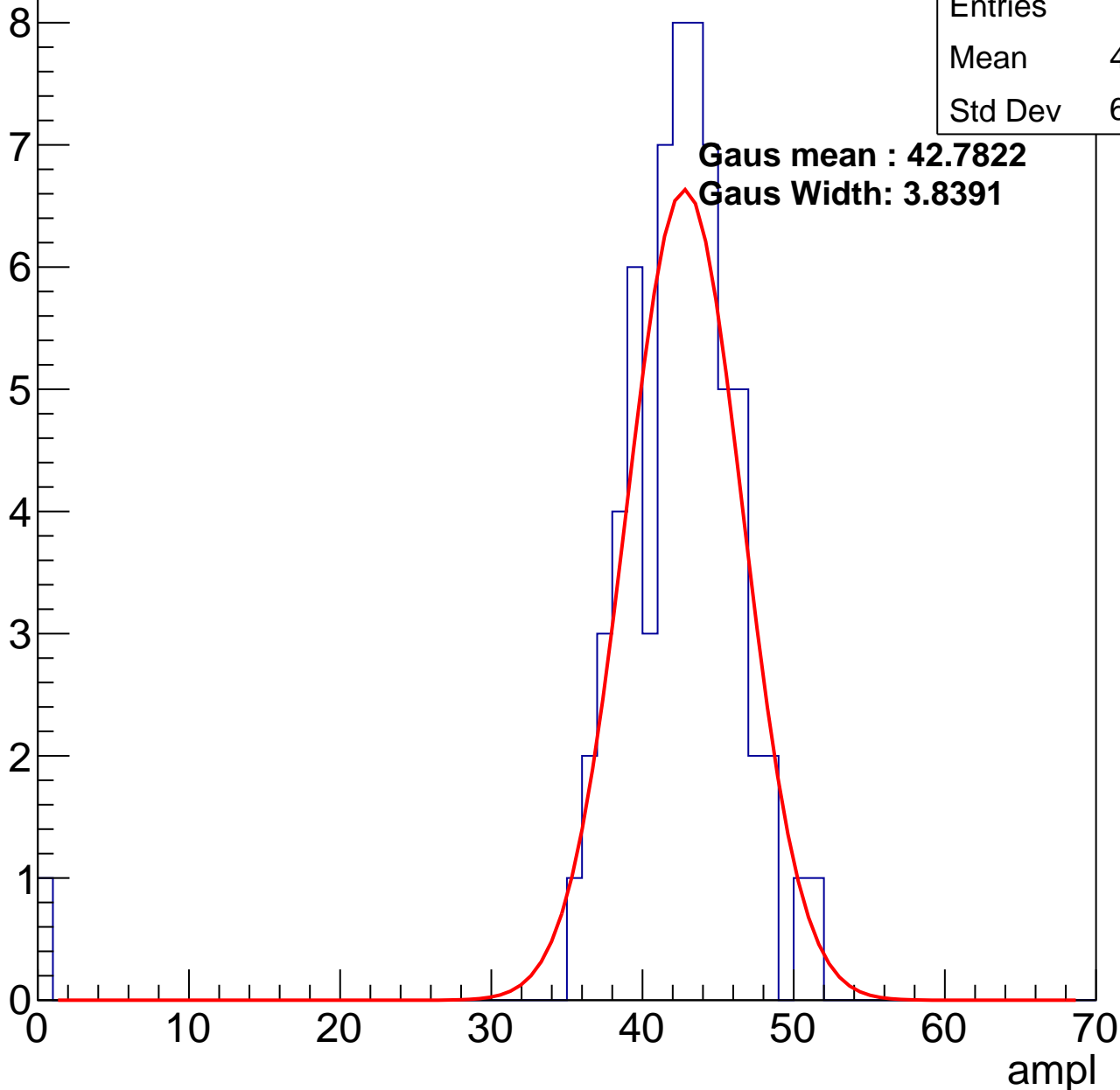
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.59
Std Dev	6.172

Gaus mean : 42.7822

Gaus Width: 3.8391

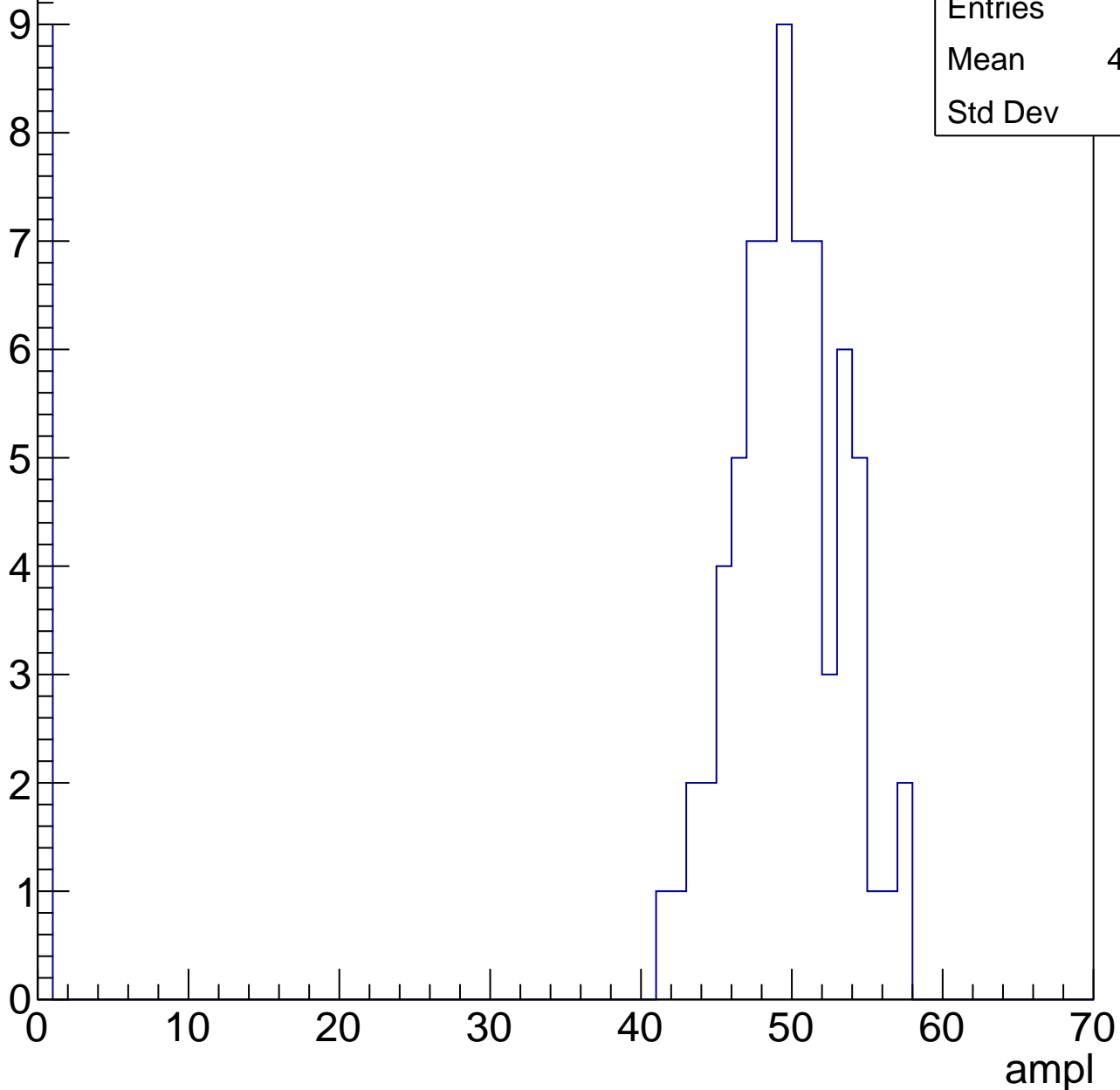


B1L103S, U10-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	43.66
Std Dev	16

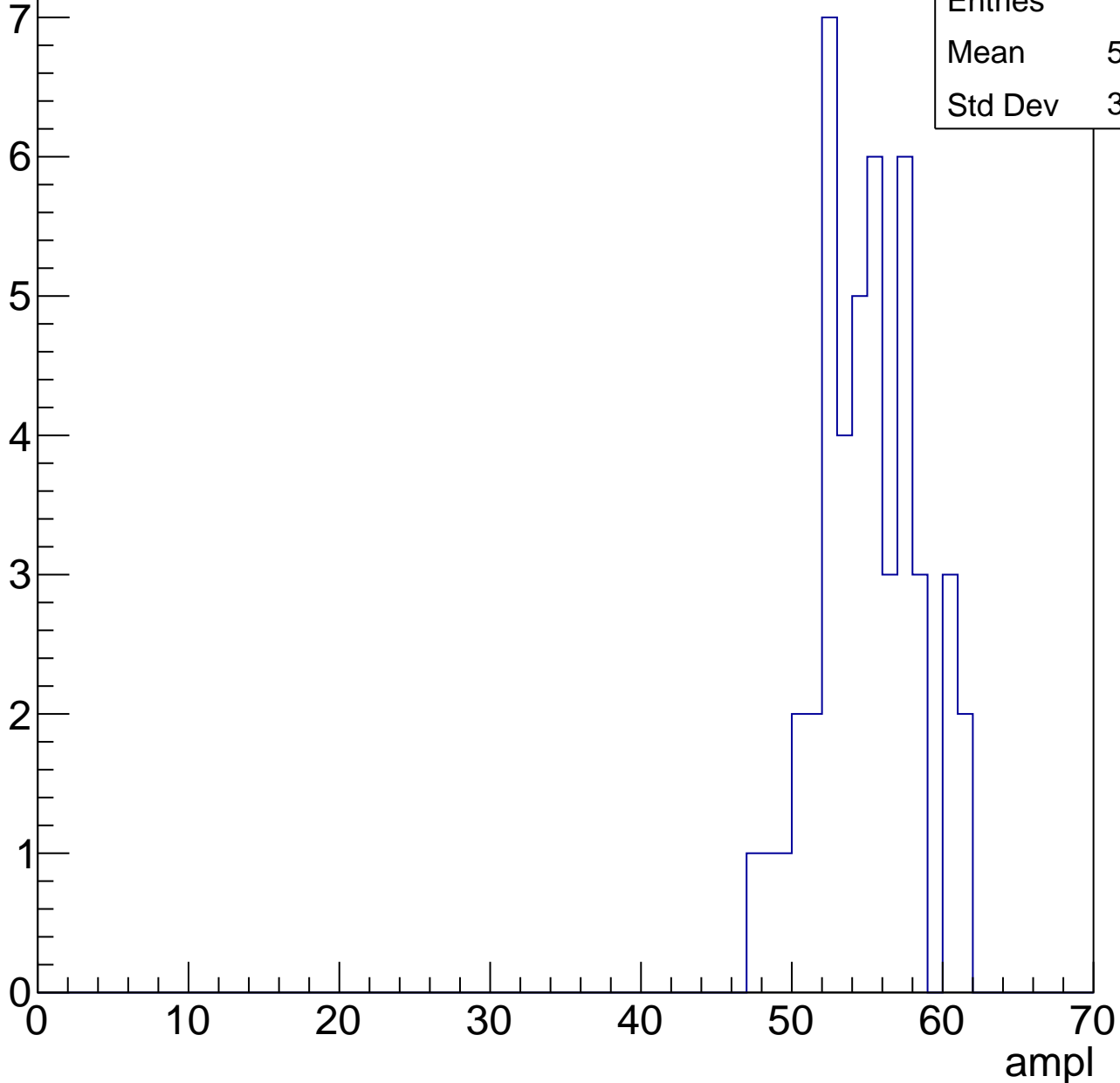


B1L103S, U10-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.52
Std Dev	3.315



B1L103S, U10-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

57

Mean

58.47

Std Dev

8.157

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

57

Mean

58.47

Std Dev

8.157

B1L103S, U10-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch42, adc0

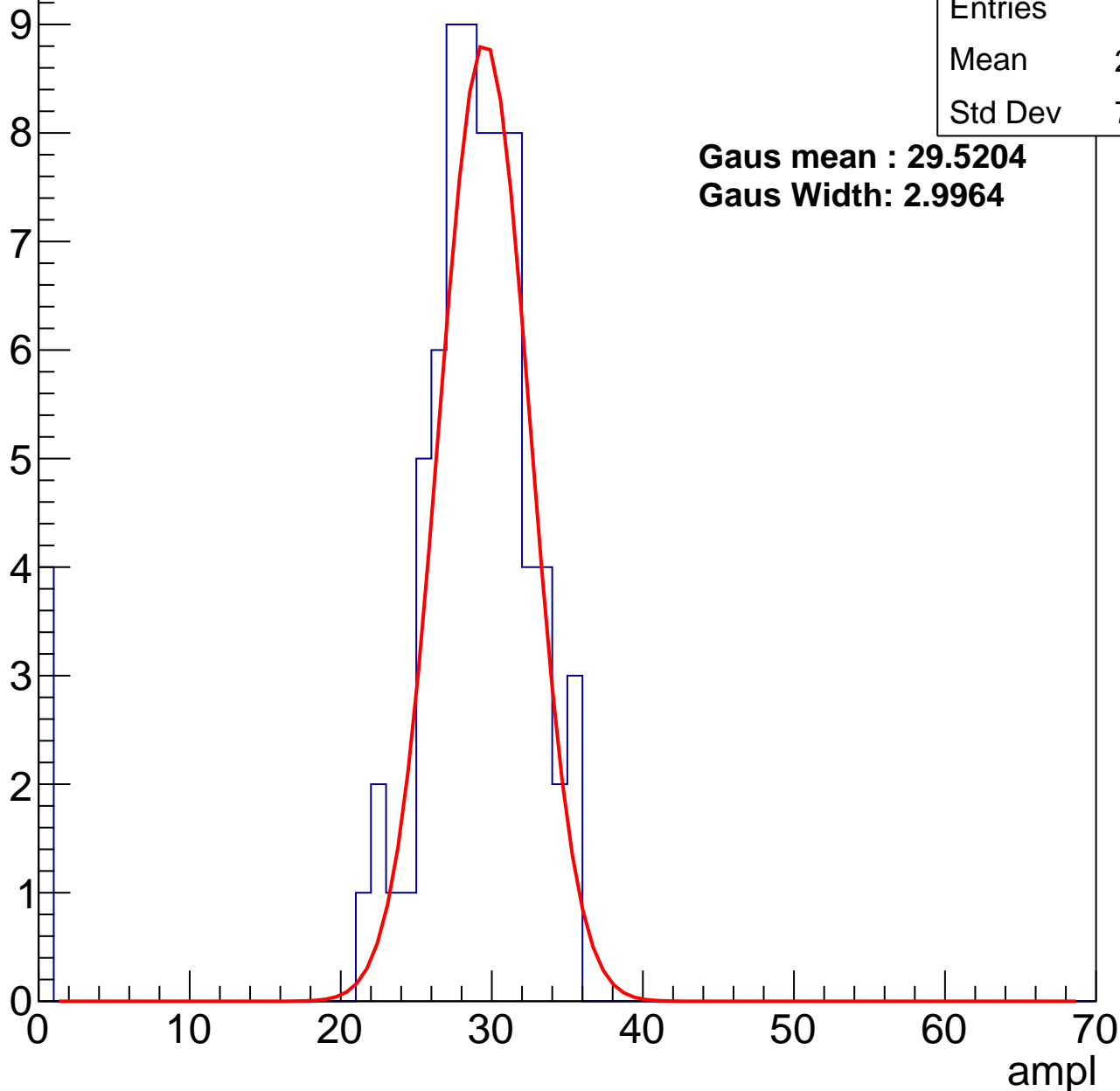
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	27.21
Std Dev	7.141

Gaus mean : 29.5204

Gaus Width: 2.9964



B1L103S, U10-ch42, adc1

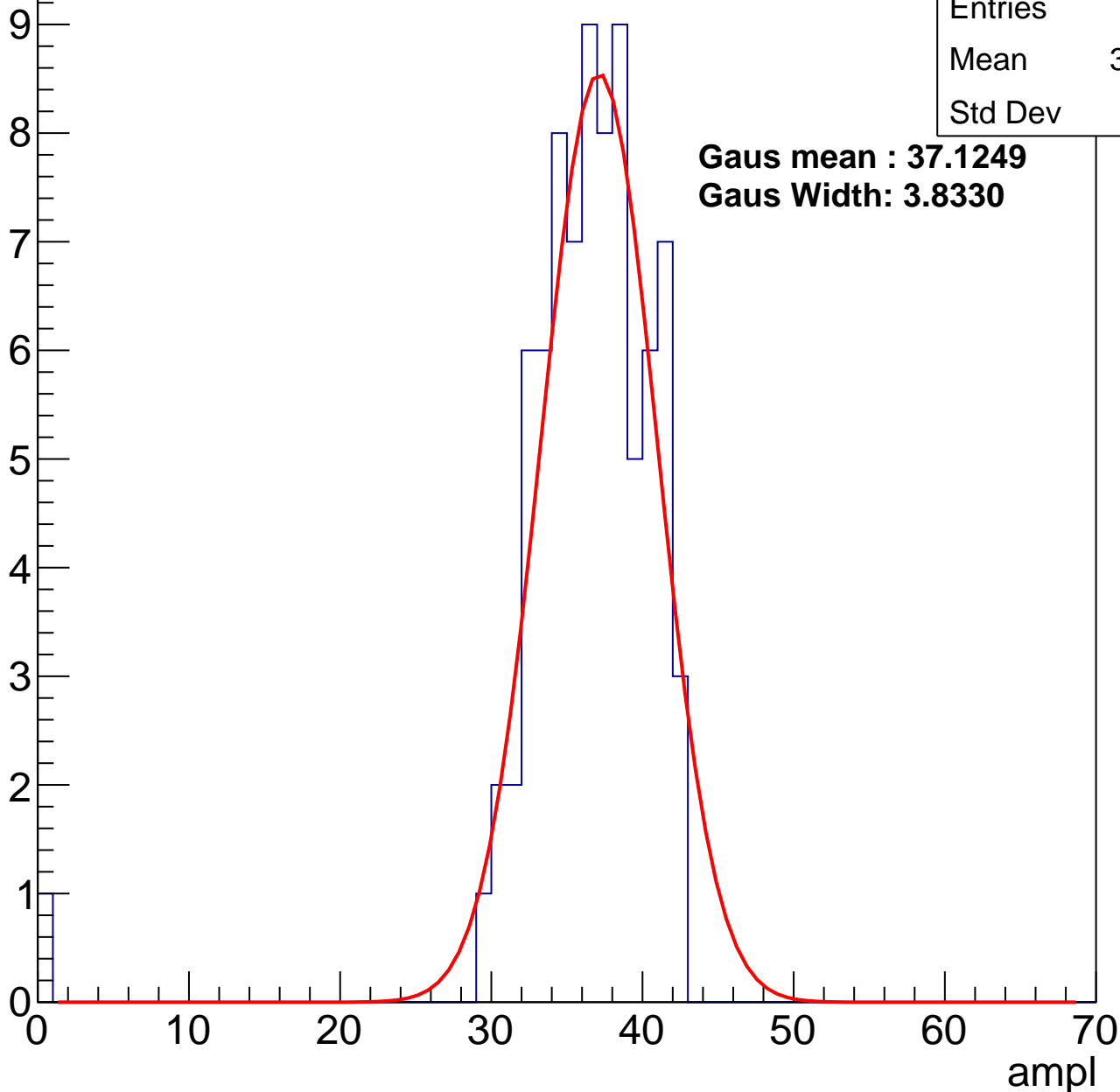
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	35.85
Std Dev	5.15

Gaus mean : 37.1249

Gaus Width: 3.8330



B1L103S, U10-ch42, adc2

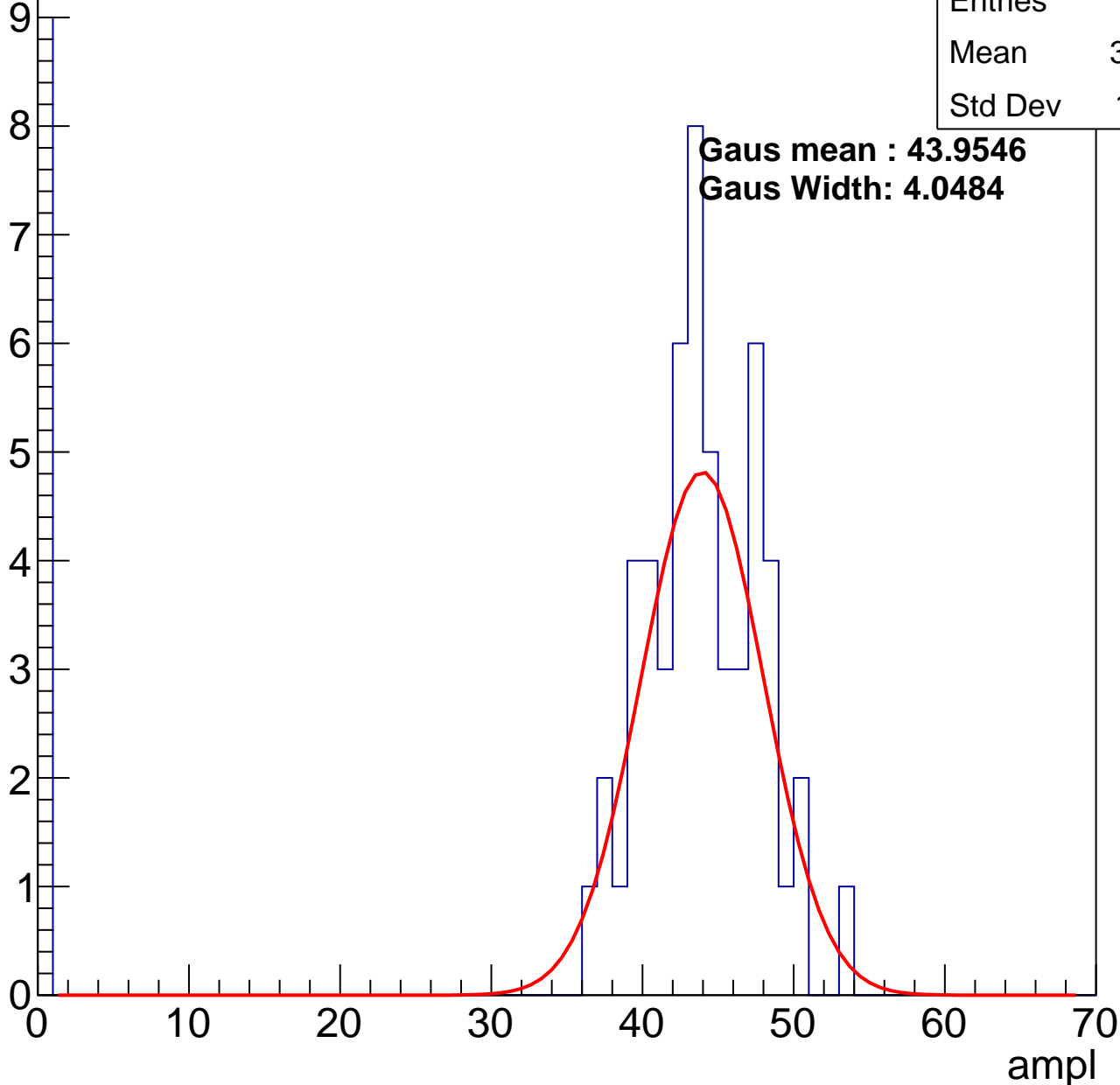
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	37.33
Std Dev	15.61

Gaus mean : 43.9546

Gaus Width: 4.0484

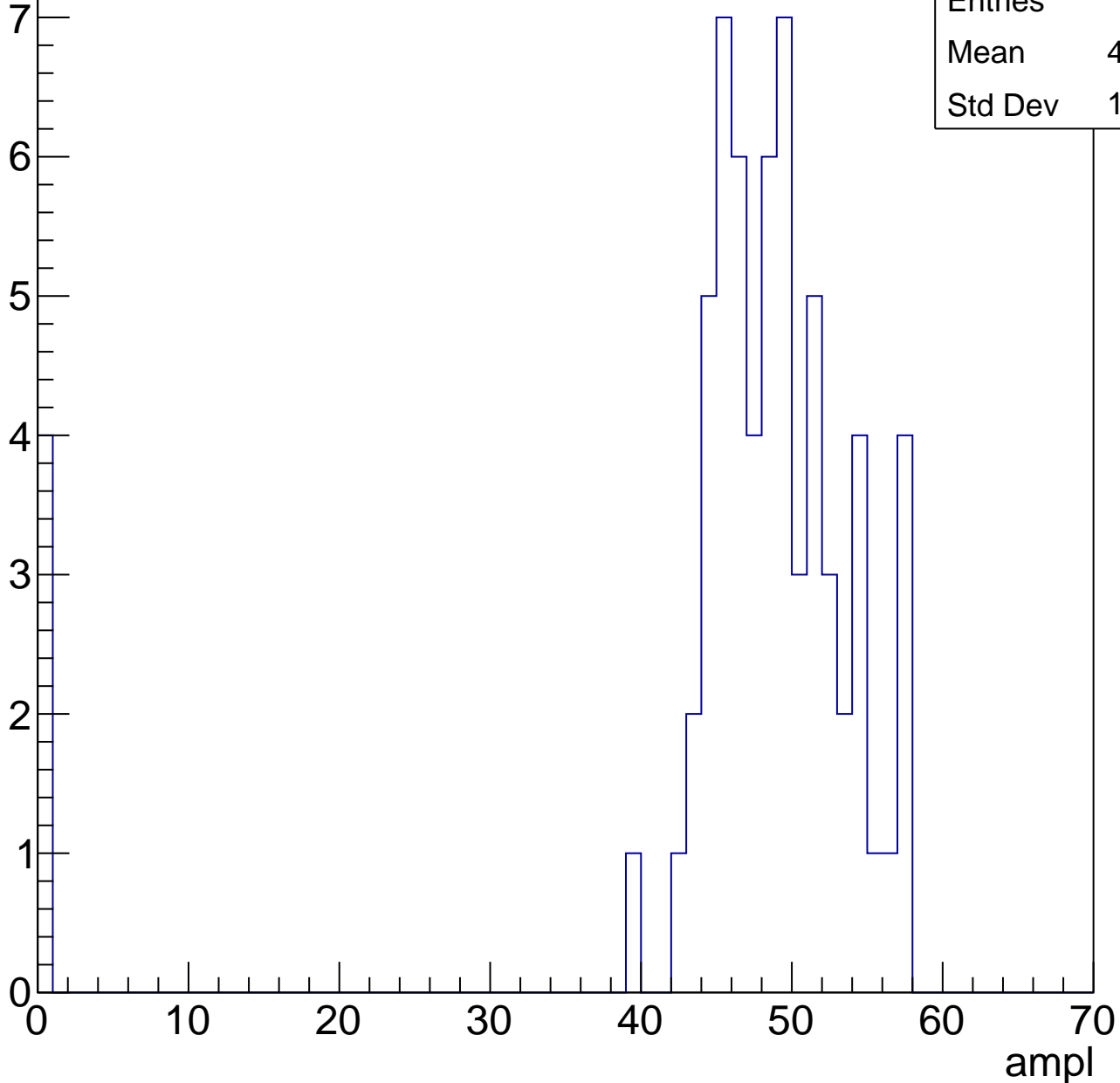


B1L103S, U10-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	45.74
Std Dev	12.28

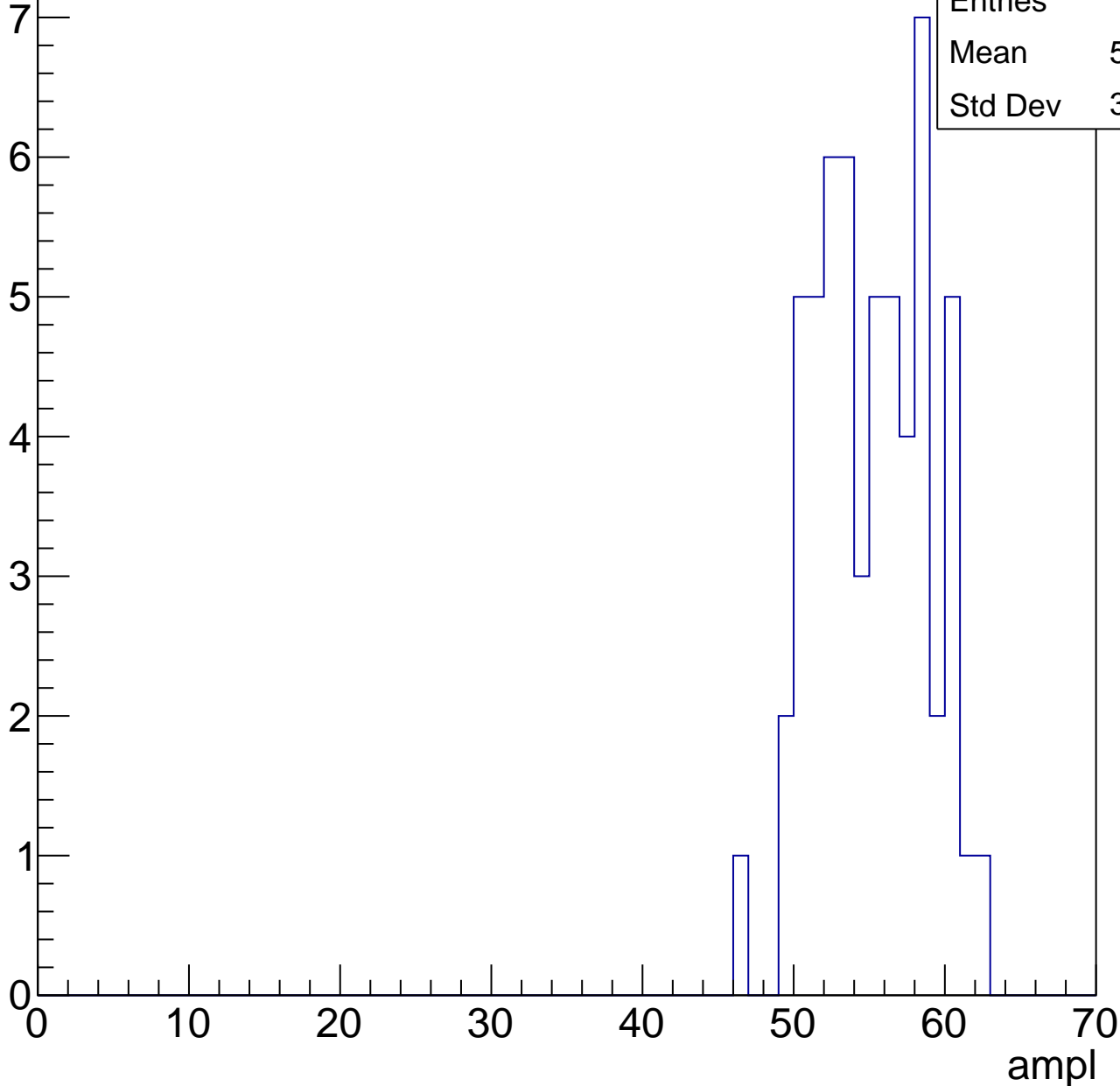


B1L103S, U10-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	54.67
Std Dev	3.617

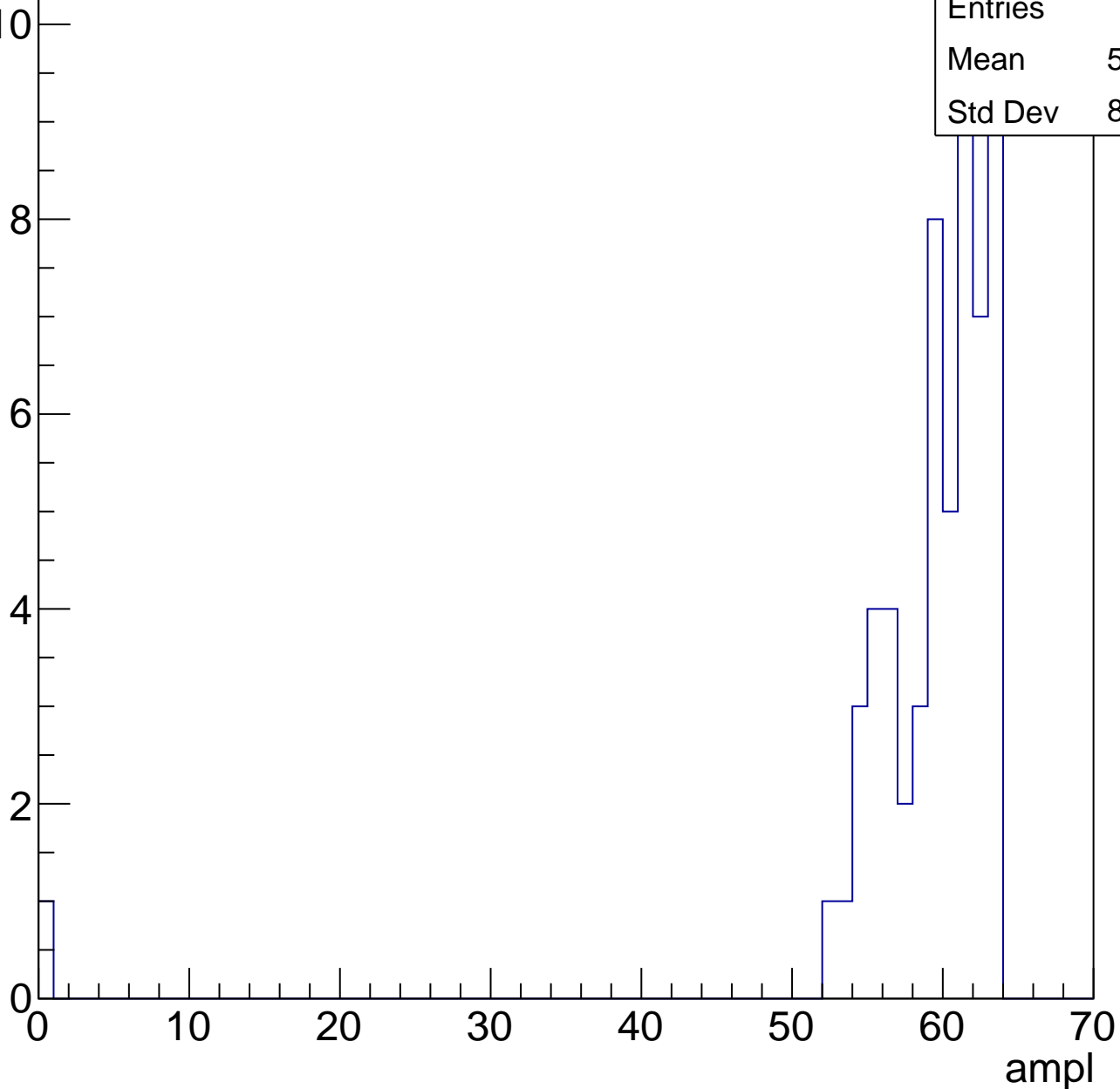


B1L103S, U10-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	58.34
Std Dev	8.295



B1L103S, U10-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch43, adc0

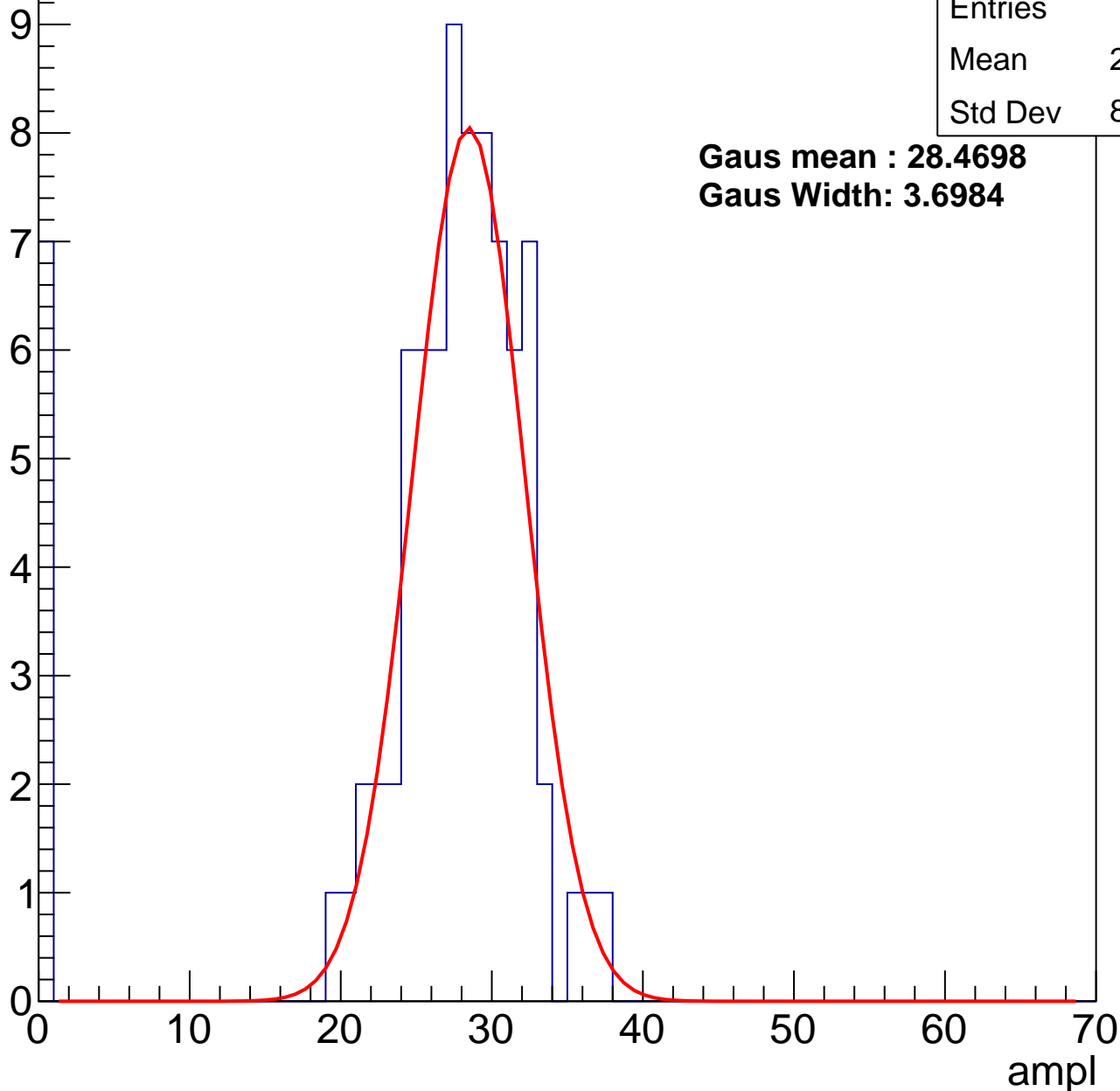
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	25.47
Std Dev	8.467

Gaus mean : 28.4698

Gaus Width: 3.6984



B1L103S, U10-ch43, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	31.1
Std Dev	11.32

Gaus mean : 35.6804

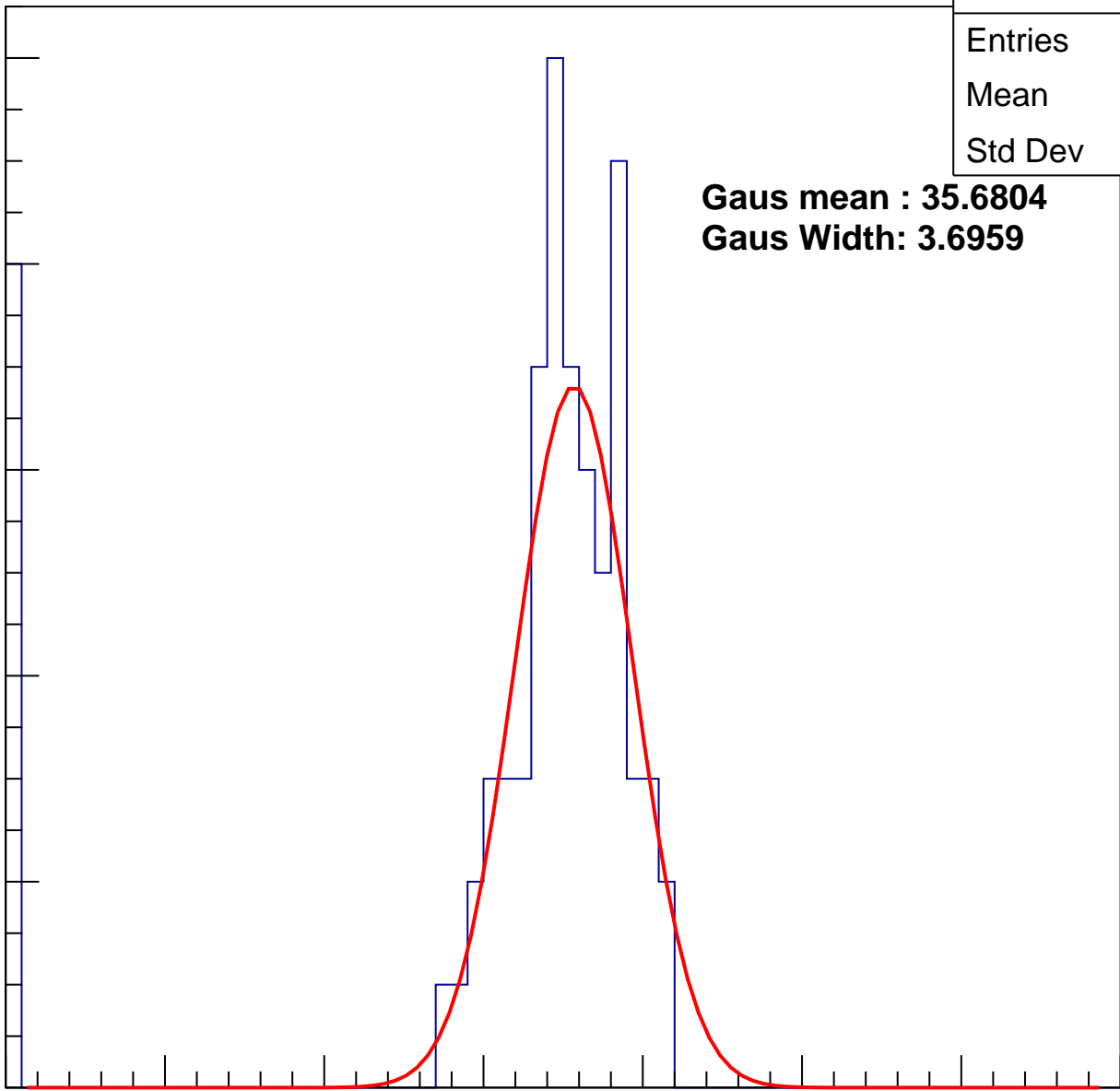
Gaus Width: 3.6959

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch43, adc2

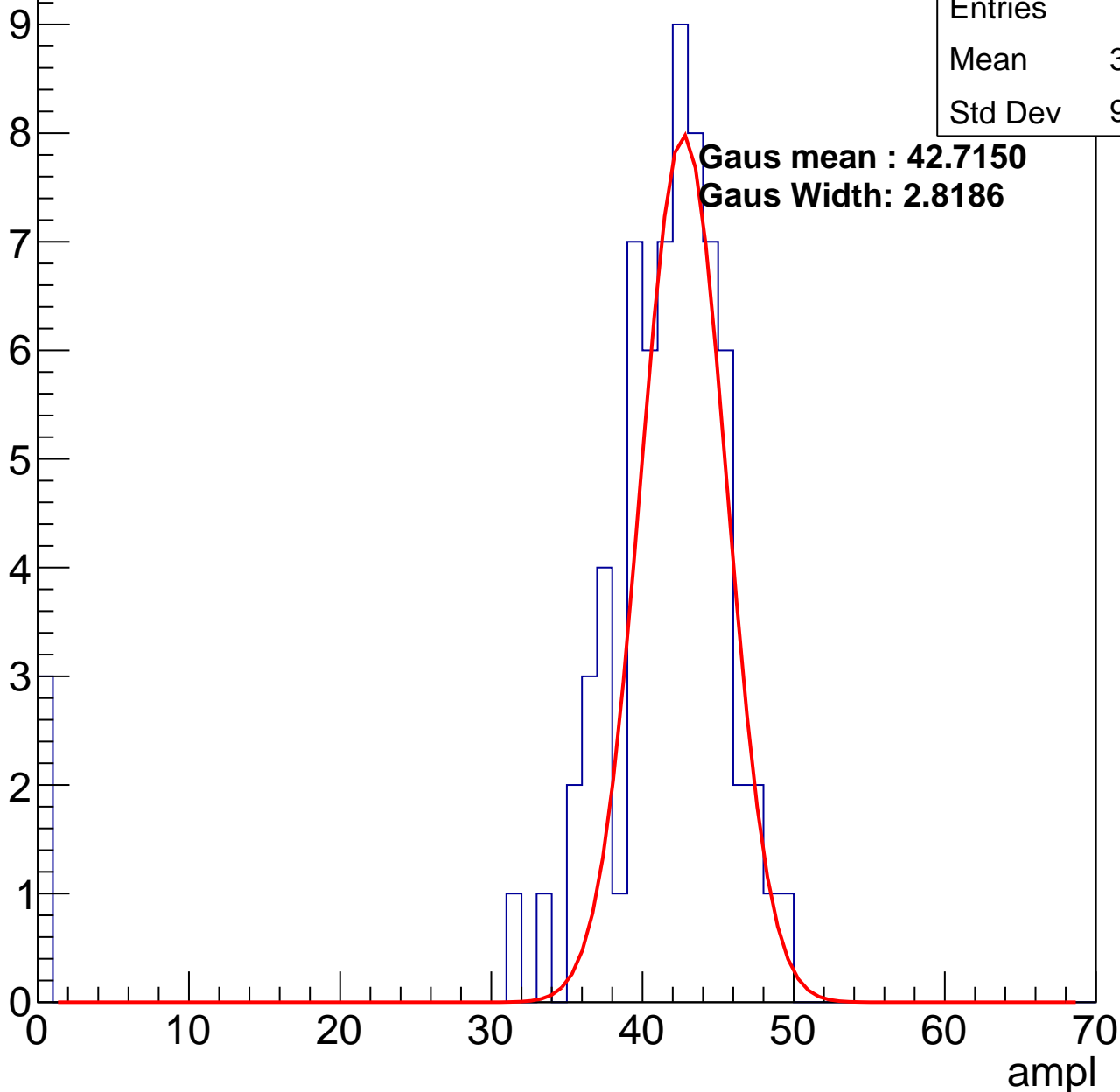
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	39.59
Std Dev	9.003

Gaus mean : 42.7150

Gaus Width: 2.8186

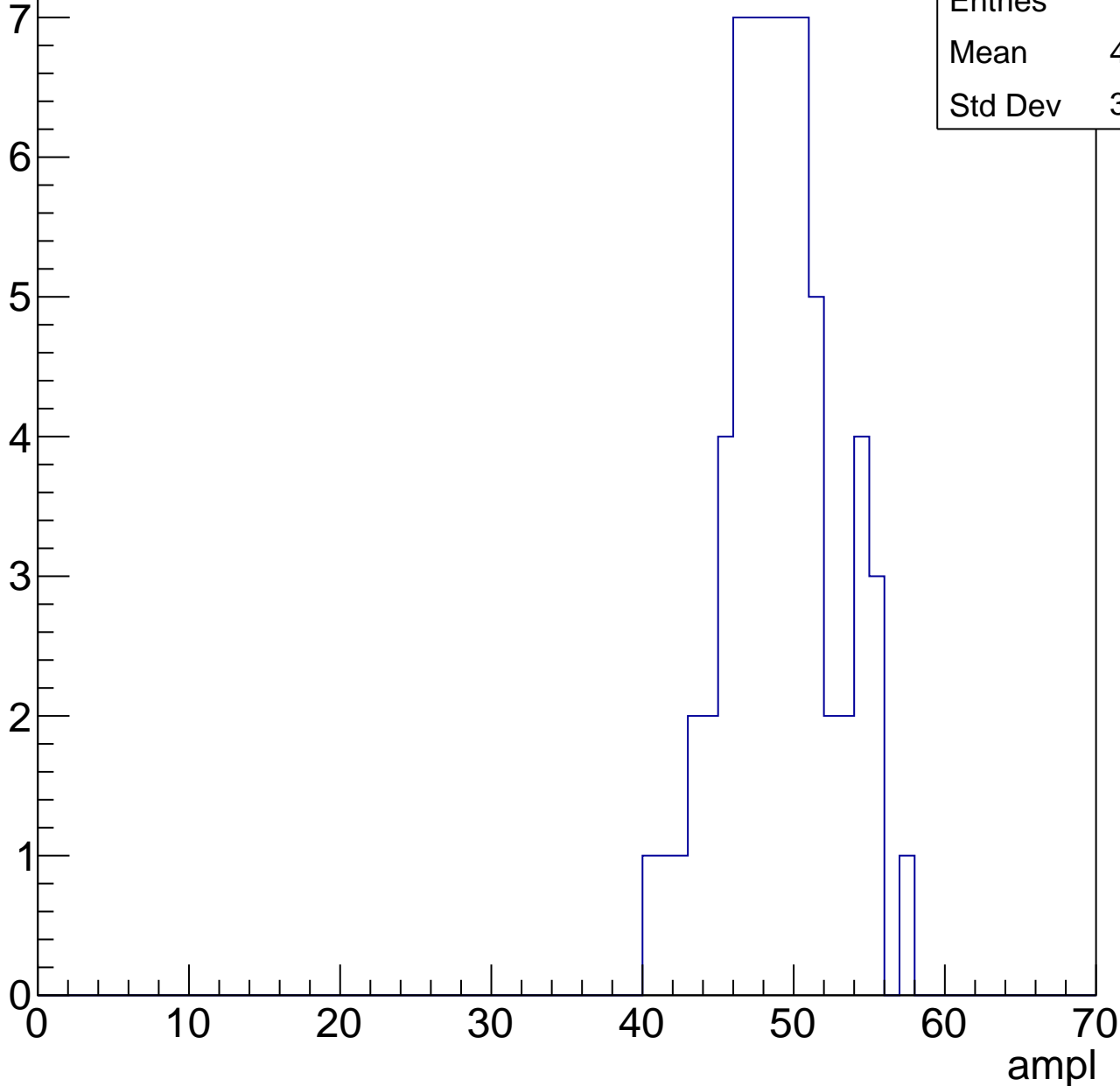


B1L103S, U10-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.57
Std Dev	3.598

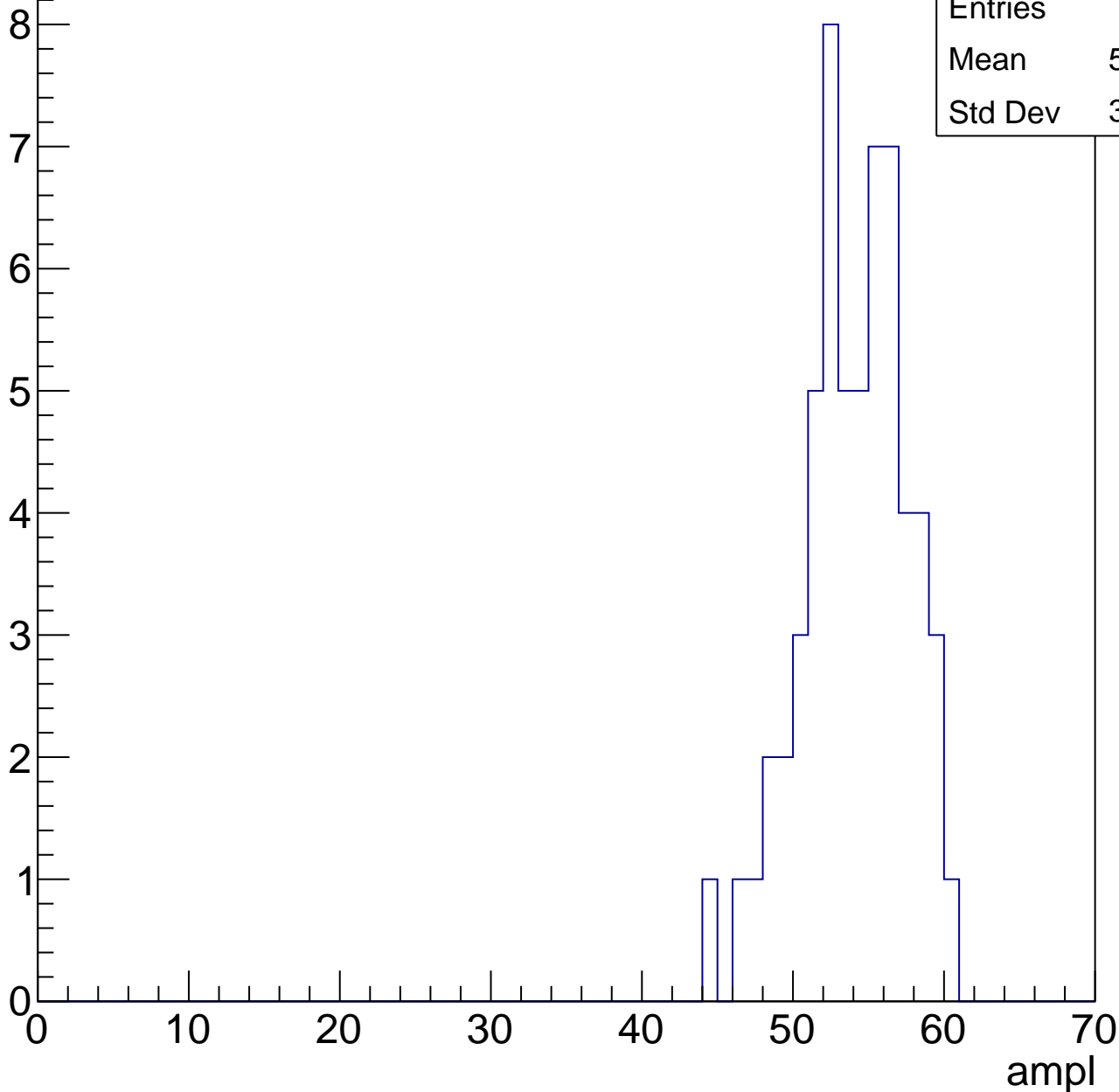


B1L103S, U10-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	53.58
Std Dev	3.445



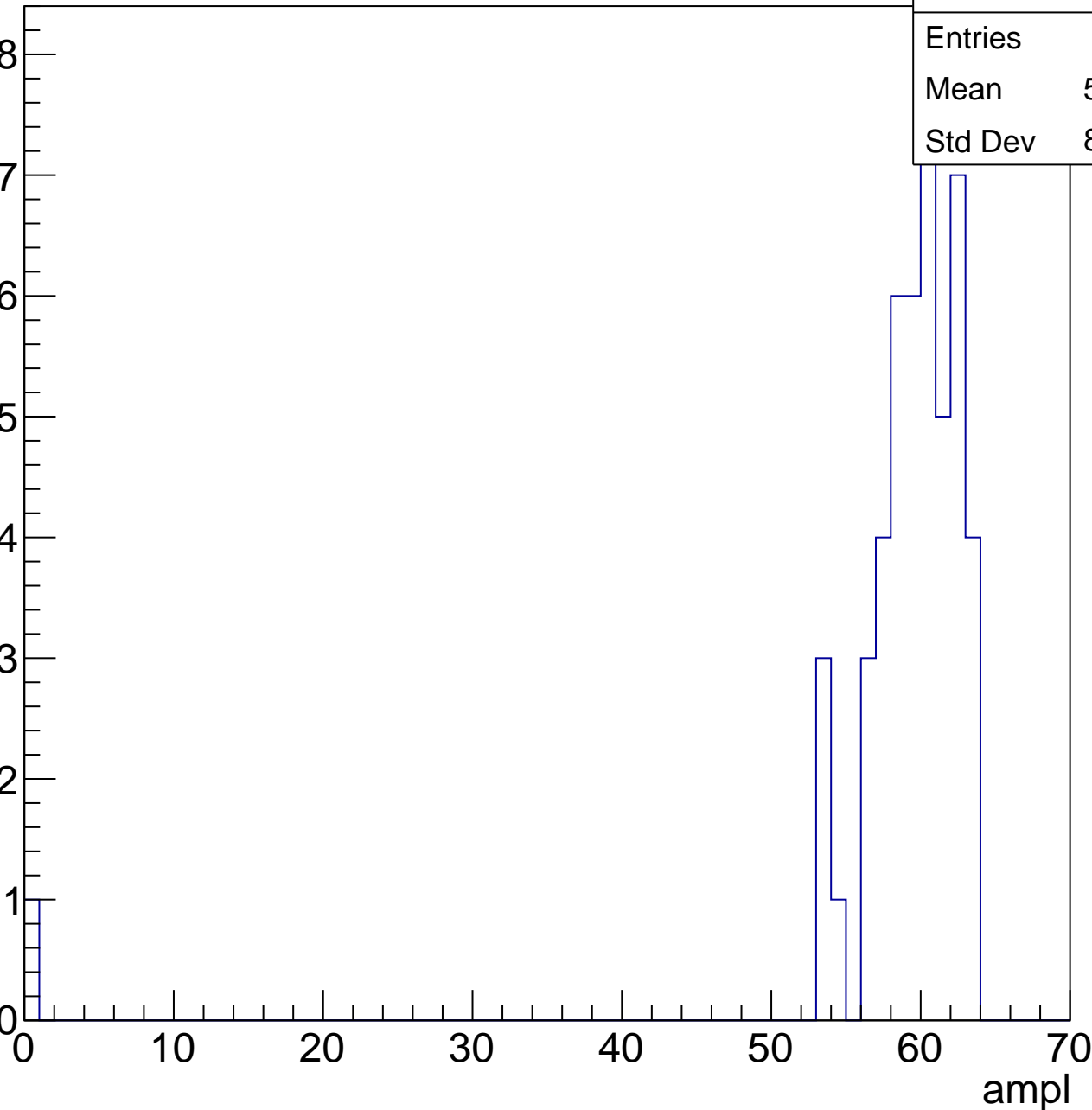
B1L103S, U10-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	57.96
Std Dev	8.858

8
7
6
5
4
3
2
1
0

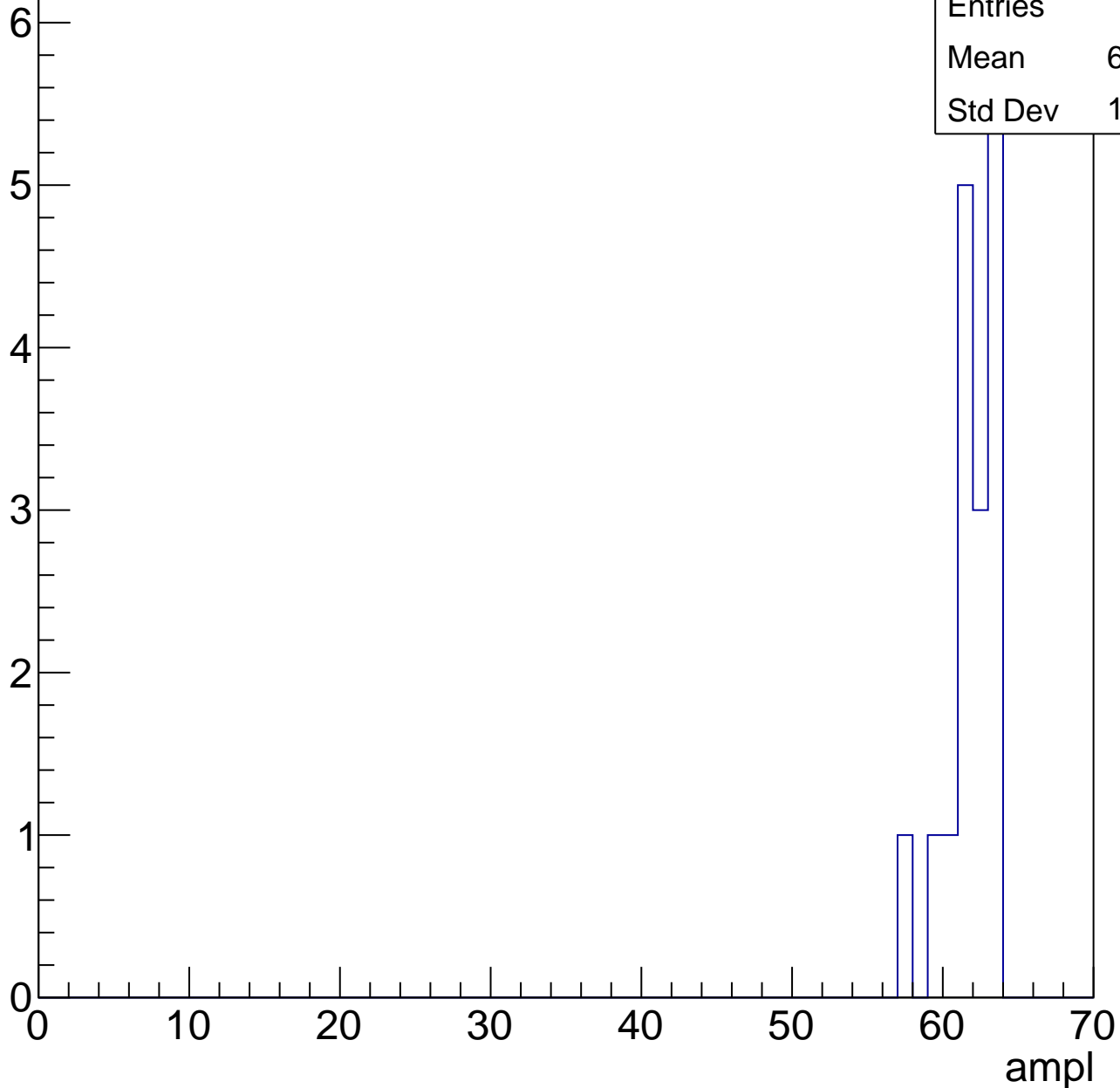


B1L103S, U10-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.47
Std Dev	1.613



B1L103S, U10-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	25.09
Std Dev	10.79

Gaus mean : 29.8975

Gaus Width: 3.7163

Entry

10

8

6

4

2

0

0

10

20

30

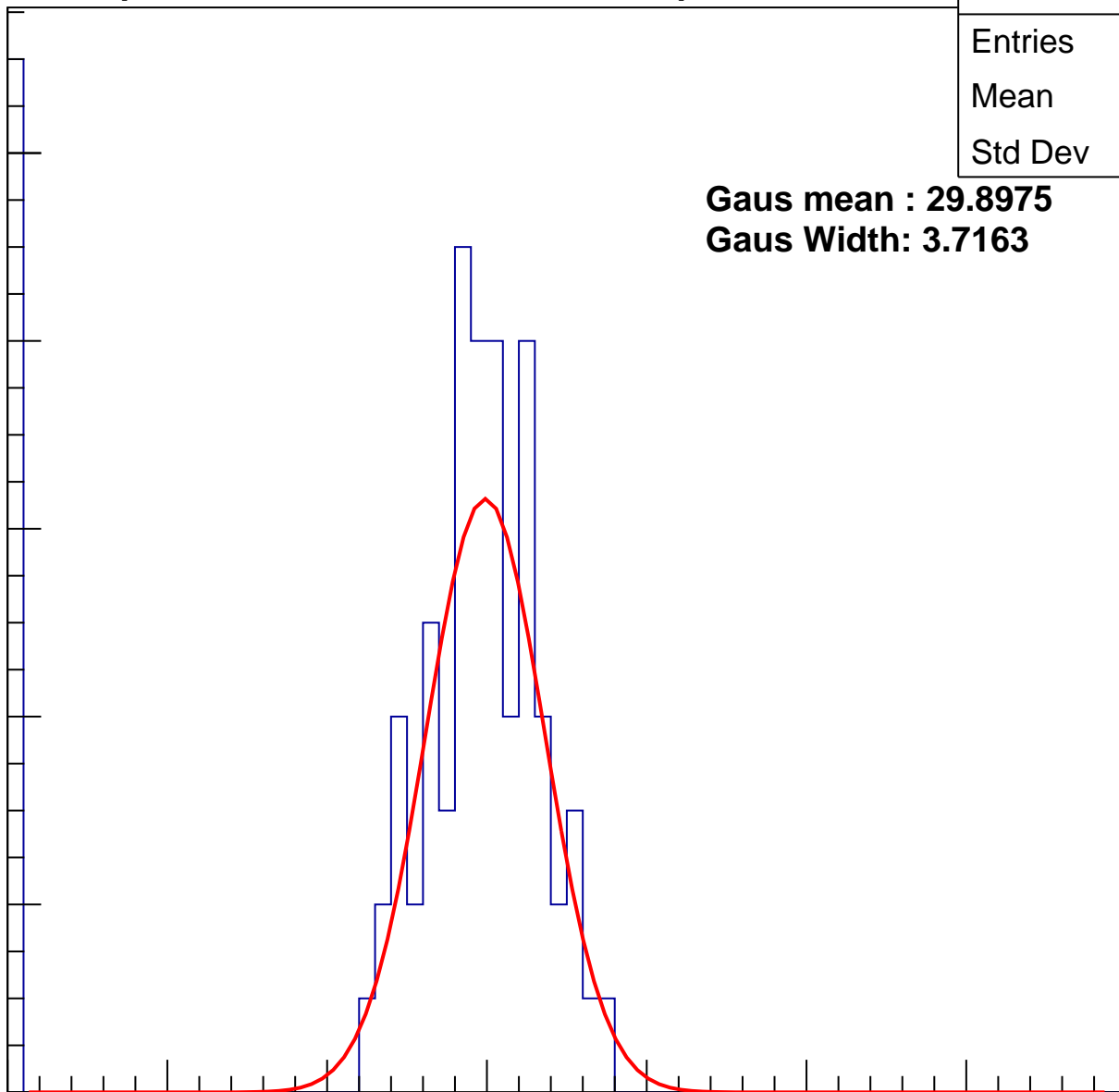
40

50

60

70

ampl



B1L103S, U10-ch44, adc1

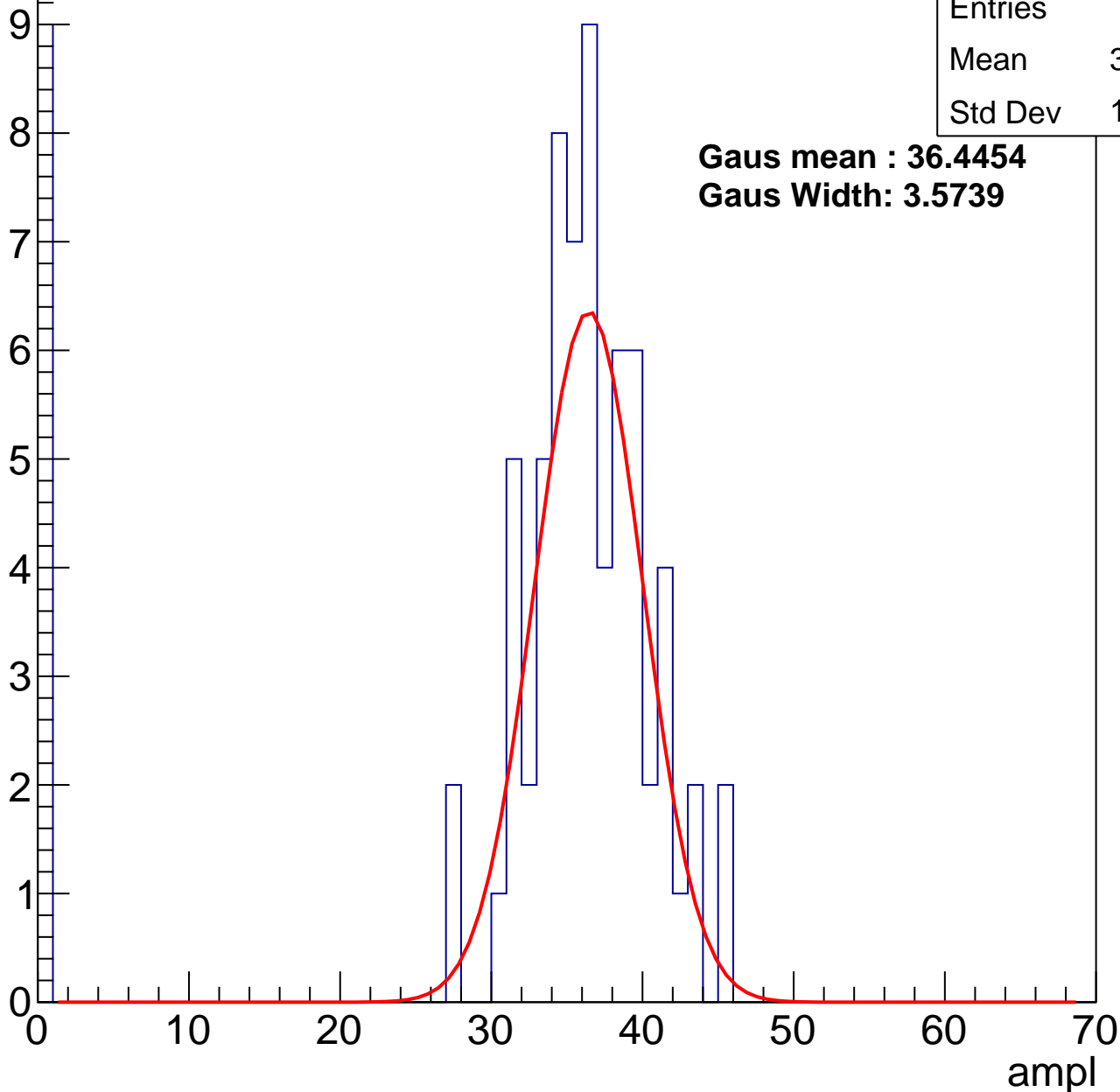
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.75
Std Dev	12.25

Gaus mean : 36.4454

Gaus Width: 3.5739



B1L103S, U10-ch44, adc2

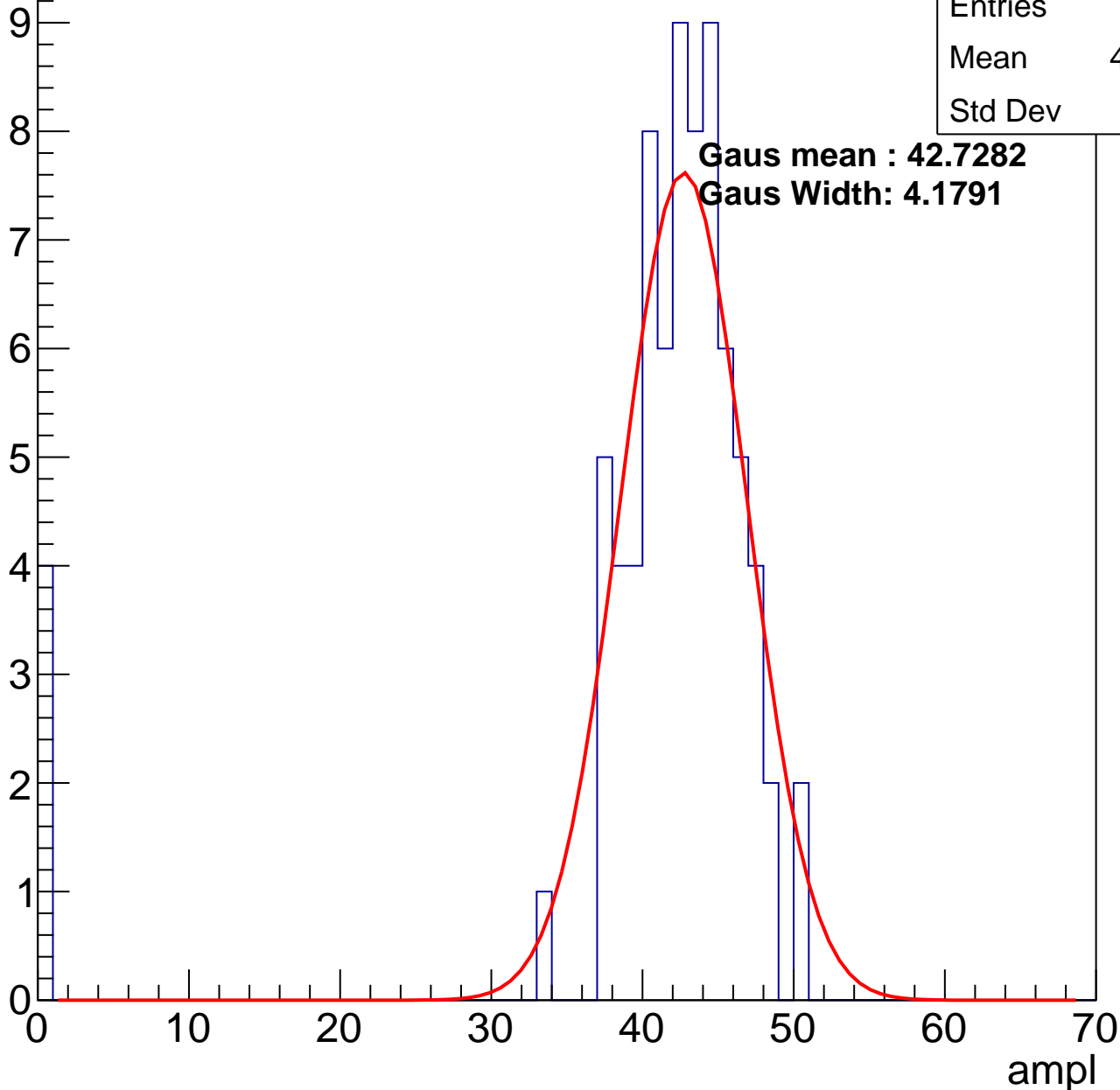
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	40.18
Std Dev	9.95

Gaus mean : 42.7282

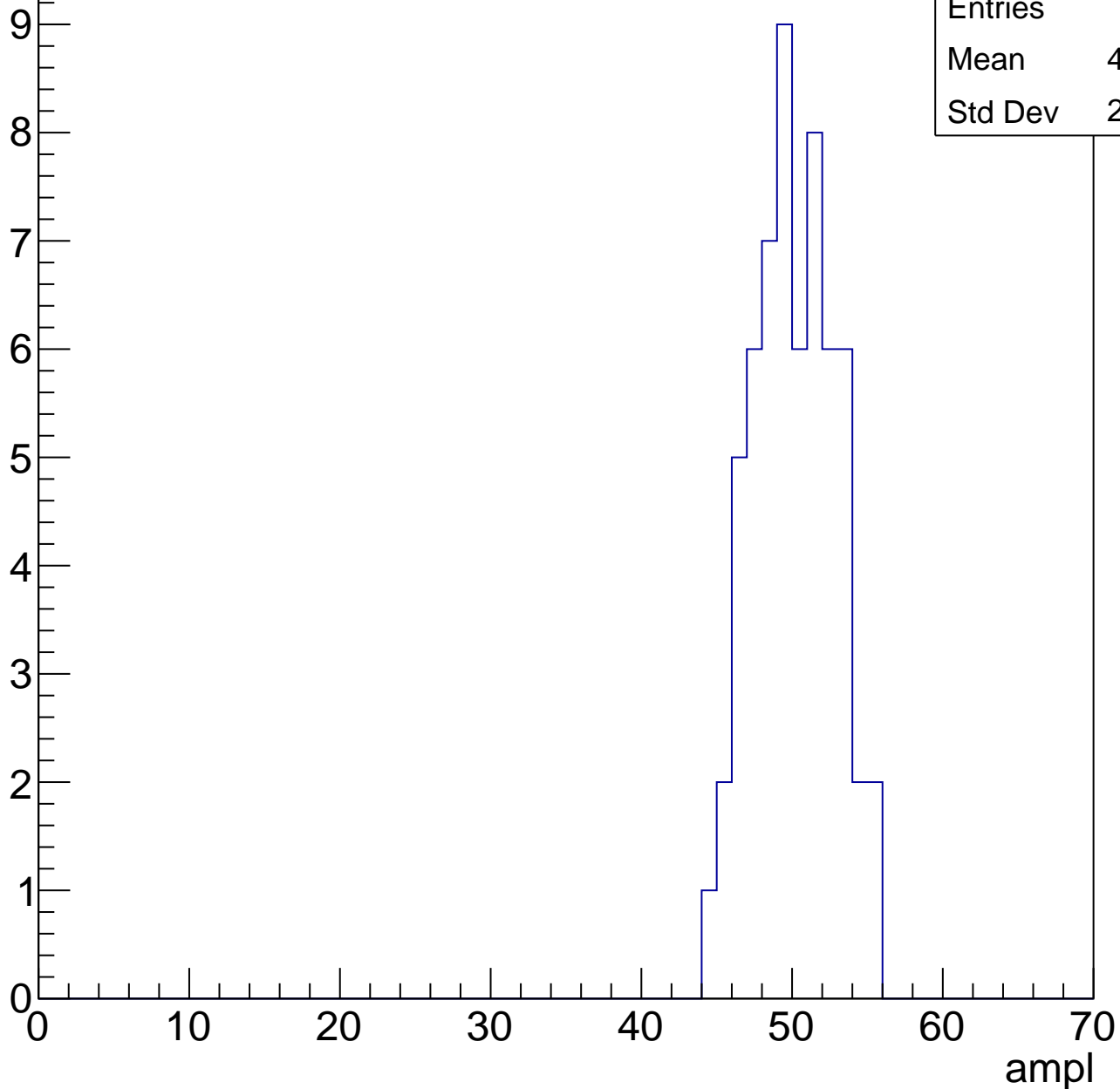
Gaus Width: 4.1791



B1L103S, U10-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

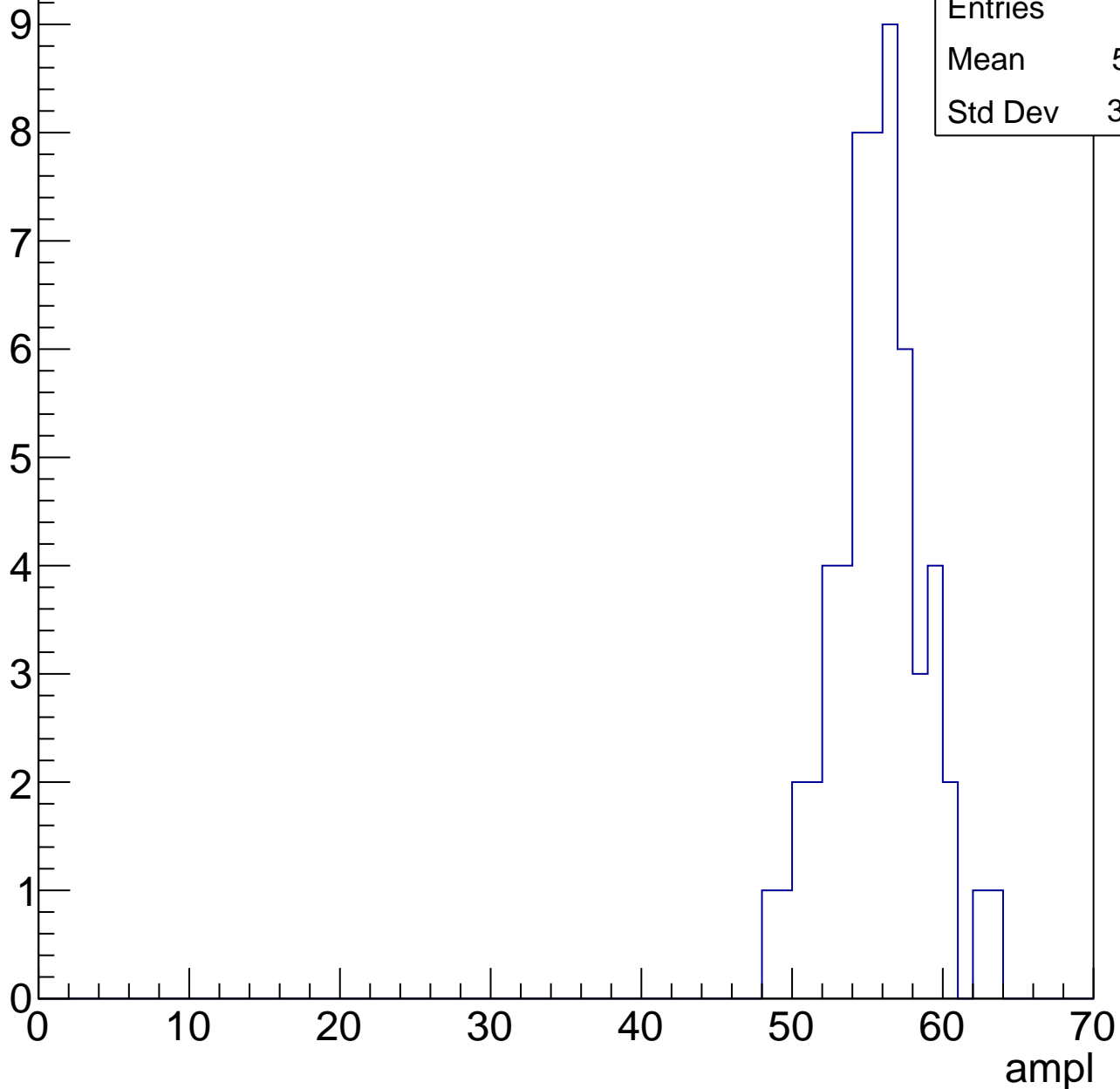
Entry



B1L103S, U10-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

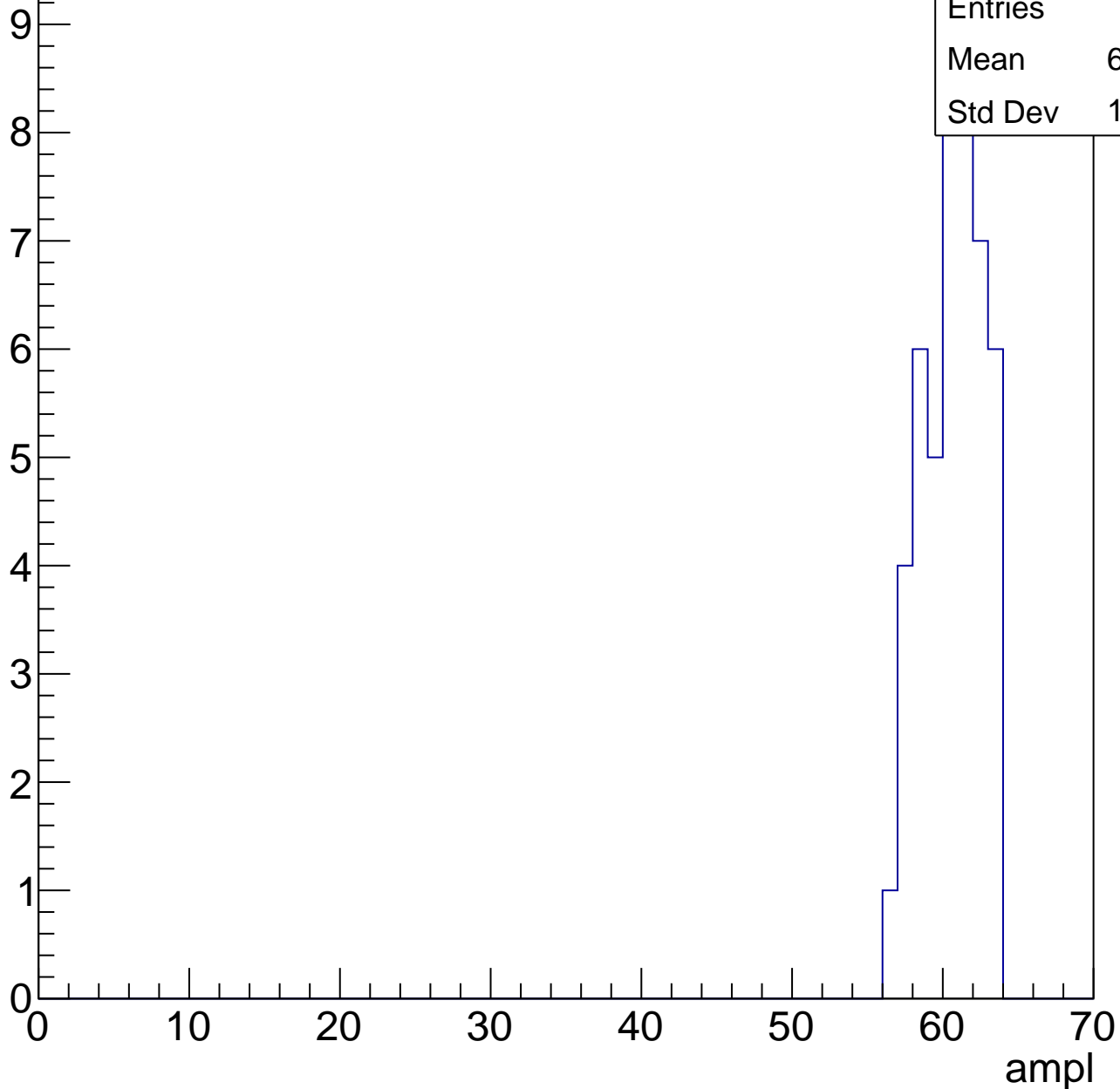
Entry



B1L103S, U10-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

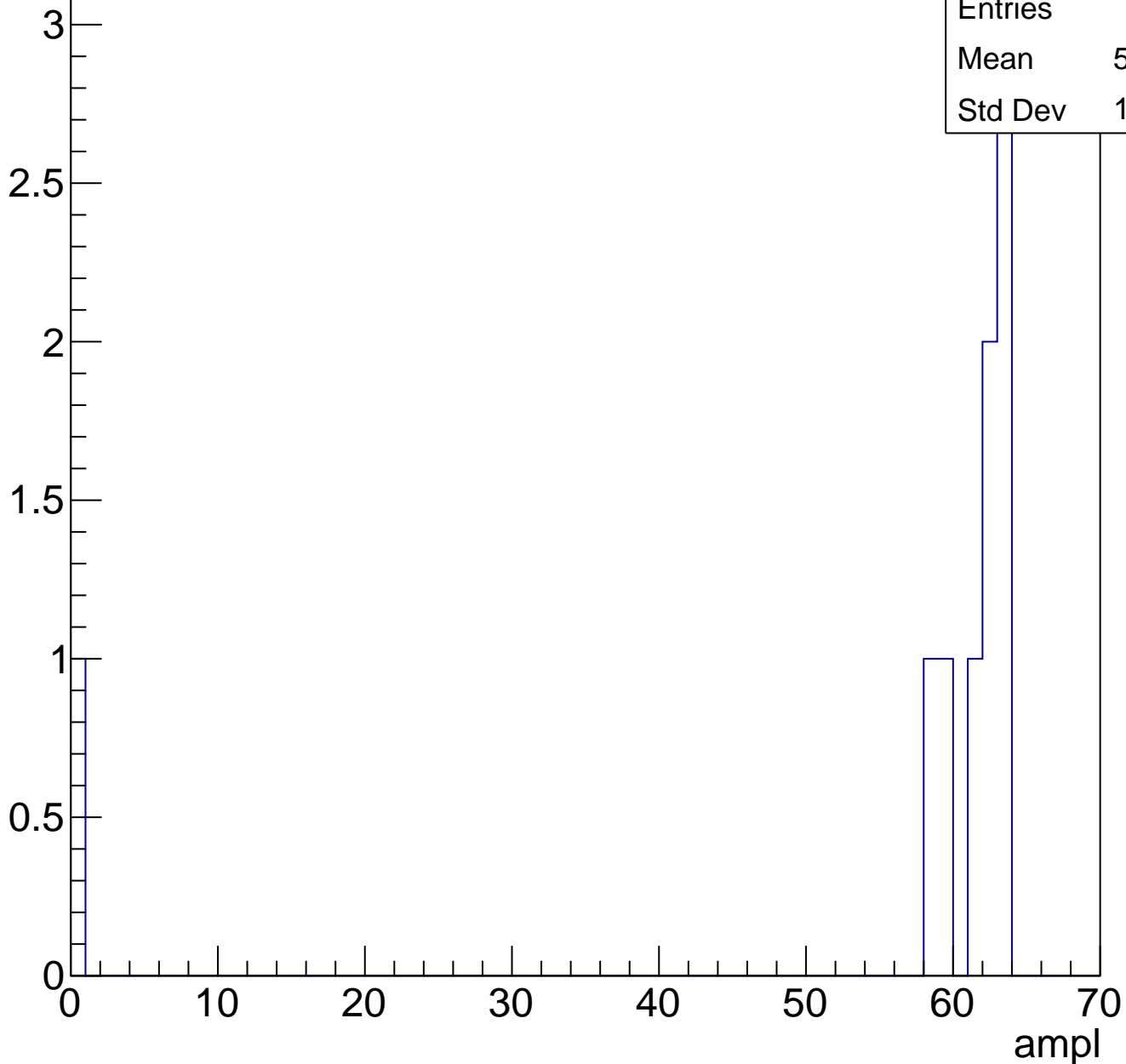


Entries	46
Mean	60.17
Std Dev	1.926

B1L103S, U10-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch45, adc0

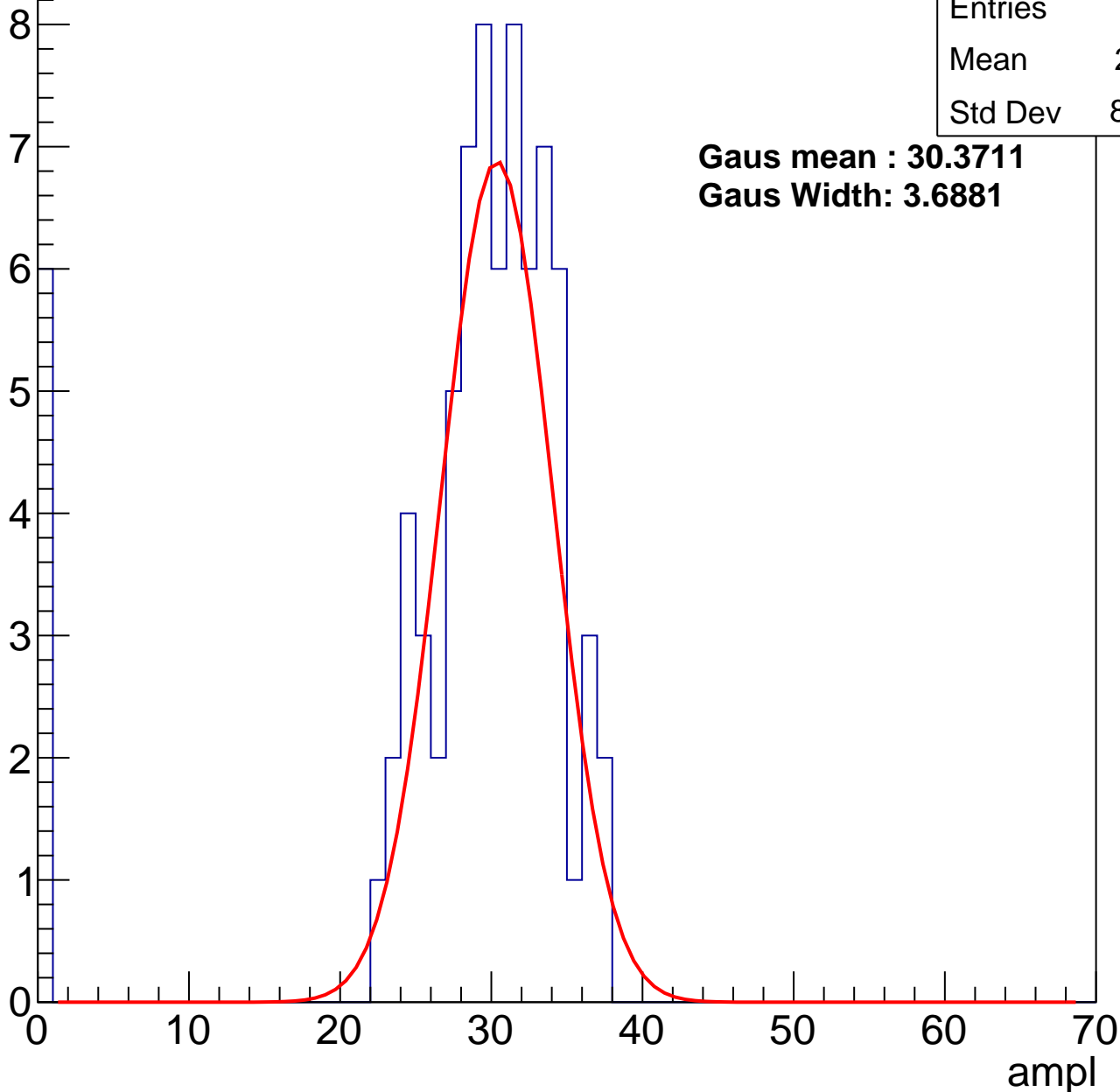
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	27.61
Std Dev	8.736

Gaus mean : 30.3711

Gaus Width: 3.6881



B1L103S, U10-ch45, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	33.16
Std Dev	11.72

Gaus mean : 37.3919

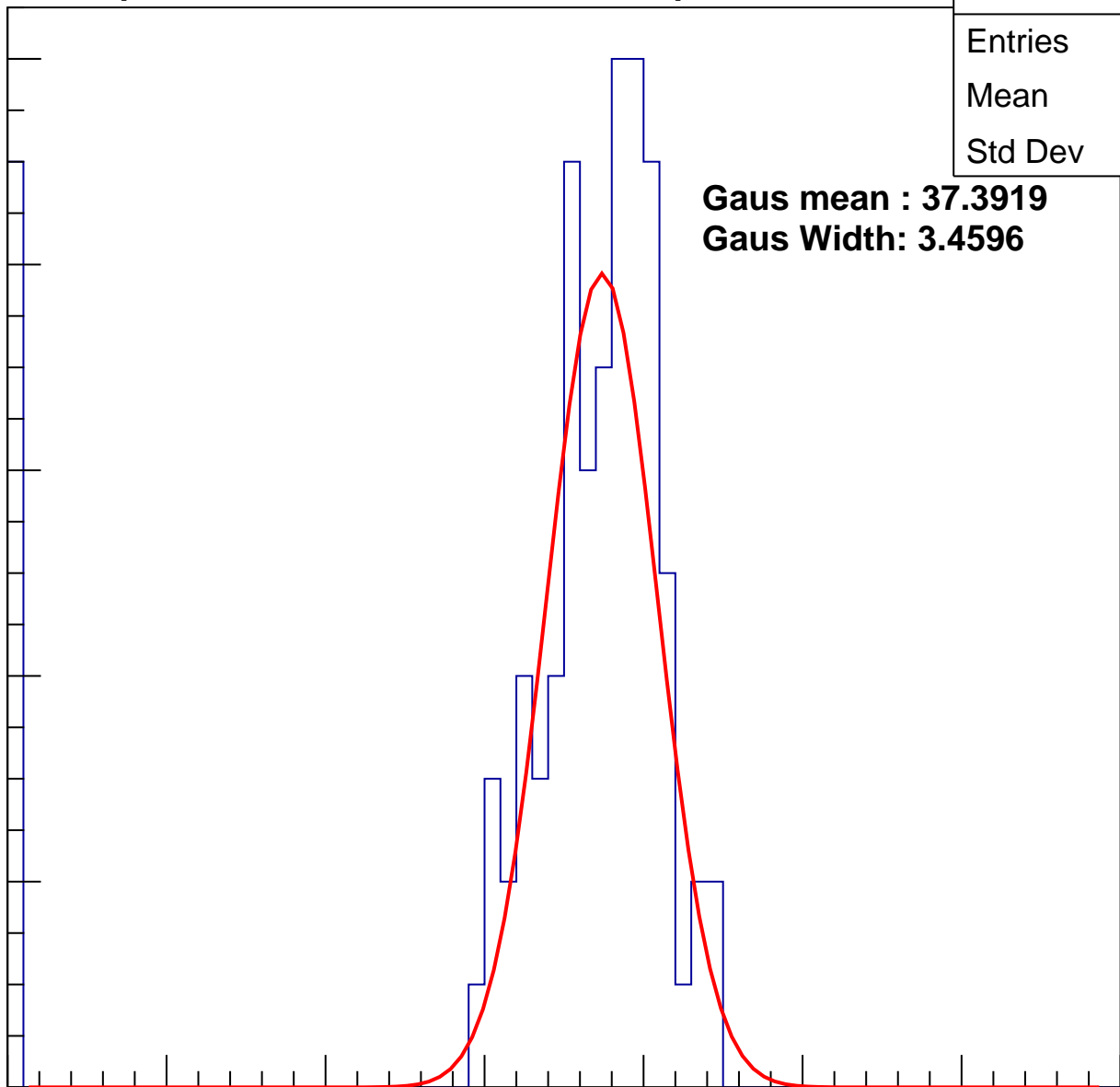
Gaus Width: 3.4596

Entry

10
8
6
4
2
0

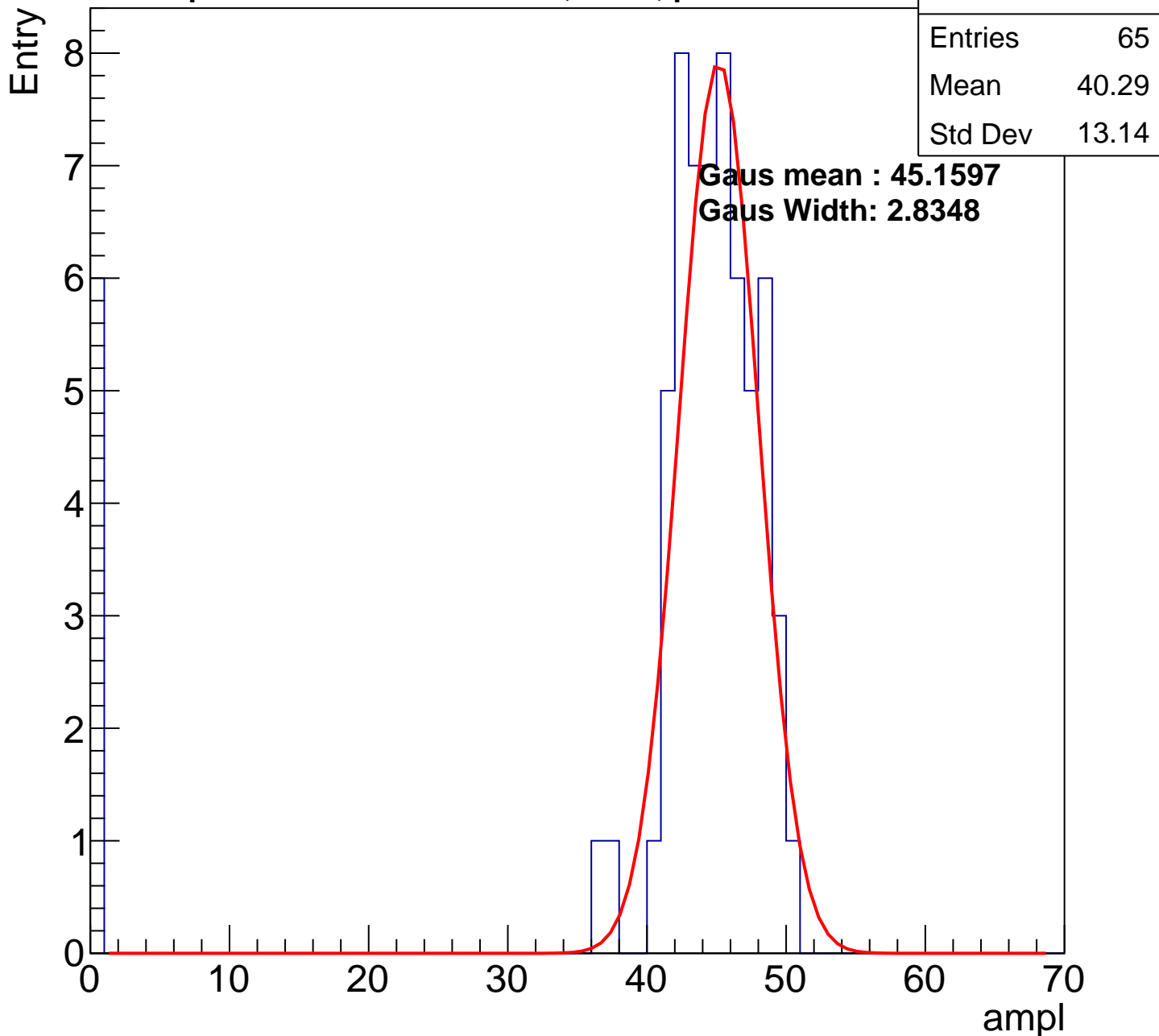
ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch45, adc2

calib_packv5_041523_1651.root, FC#0, port C2

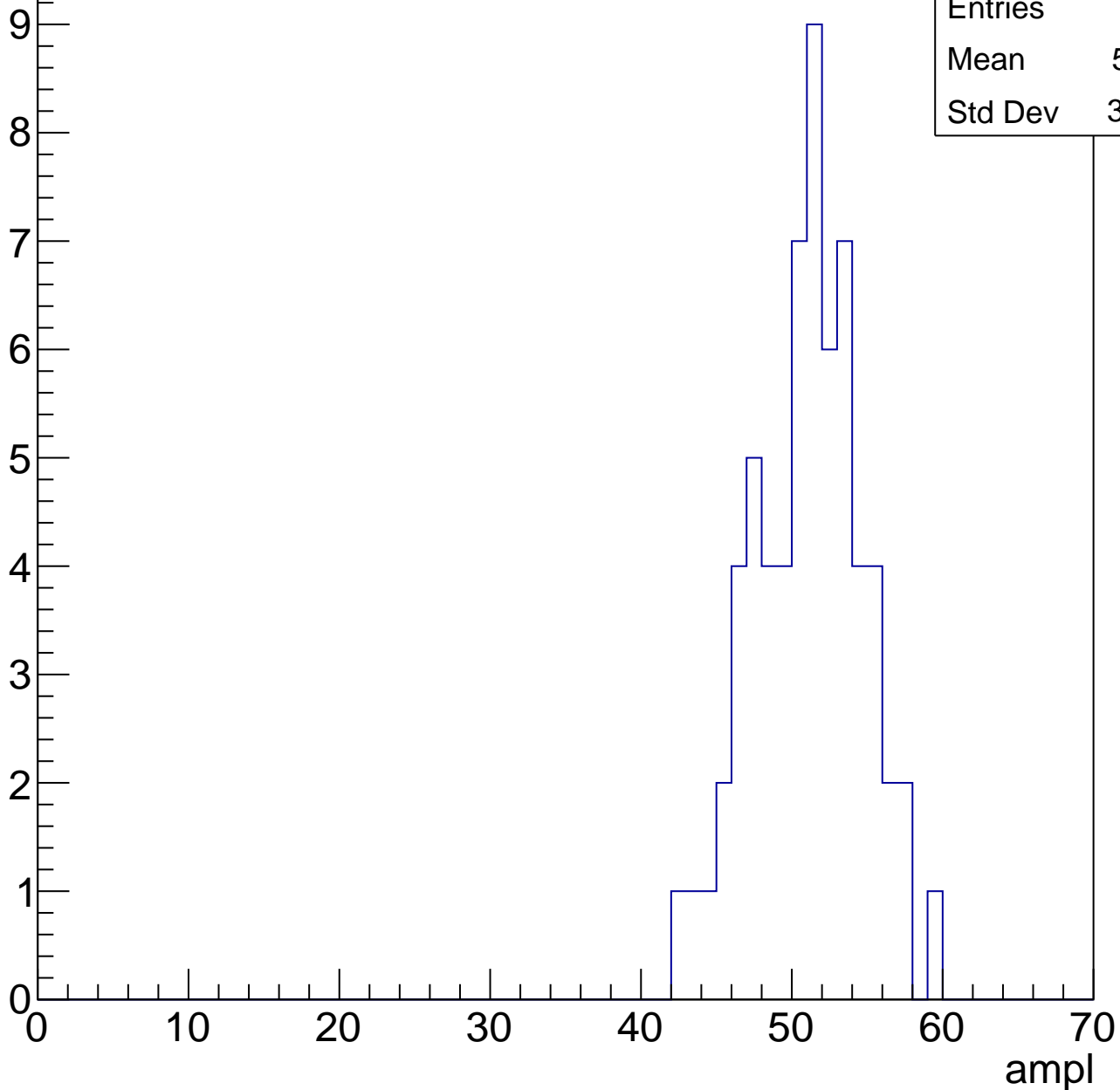


B1L103S, U10-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	50.61
Std Dev	3.569

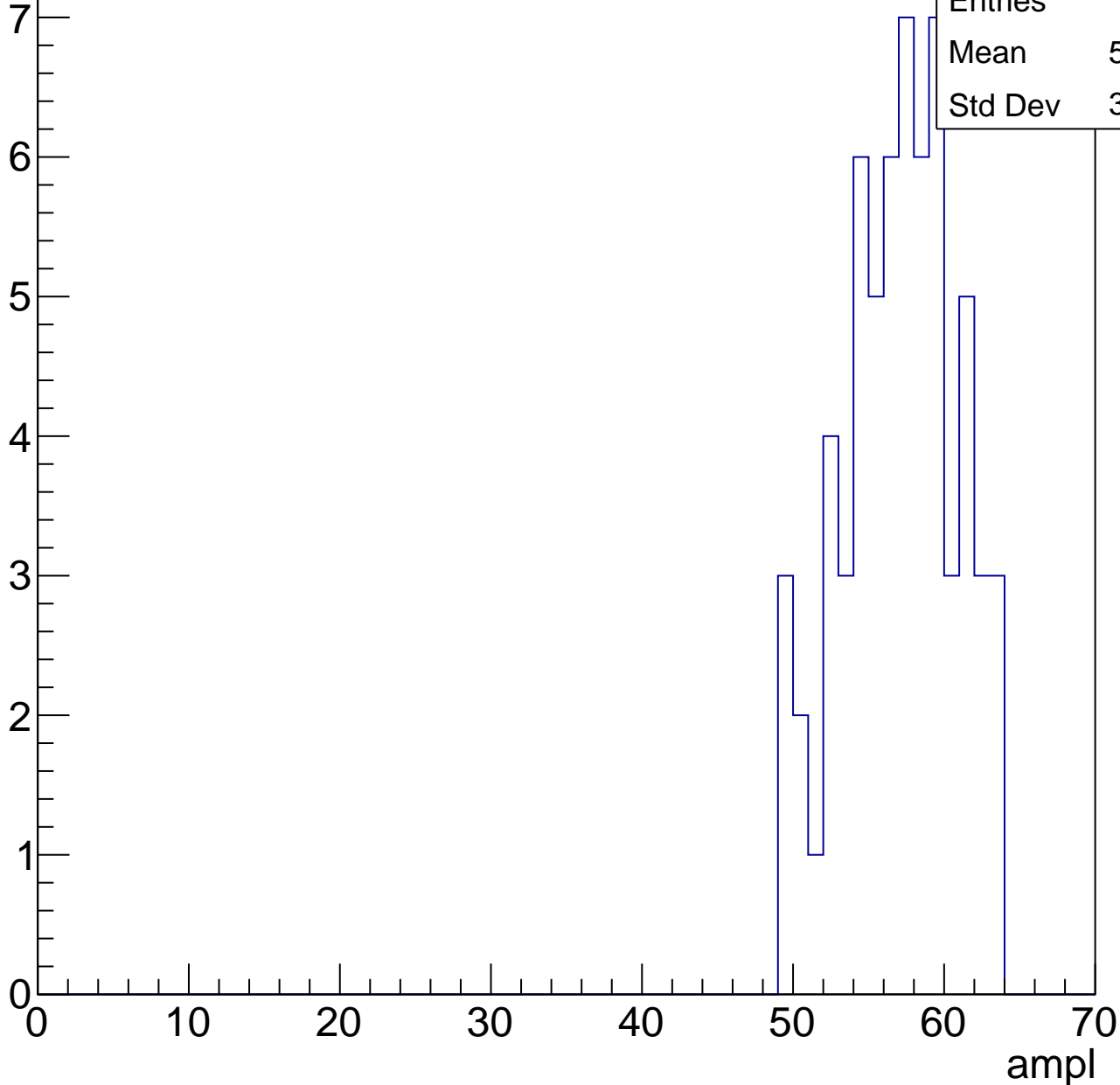


B1L103S, U10-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

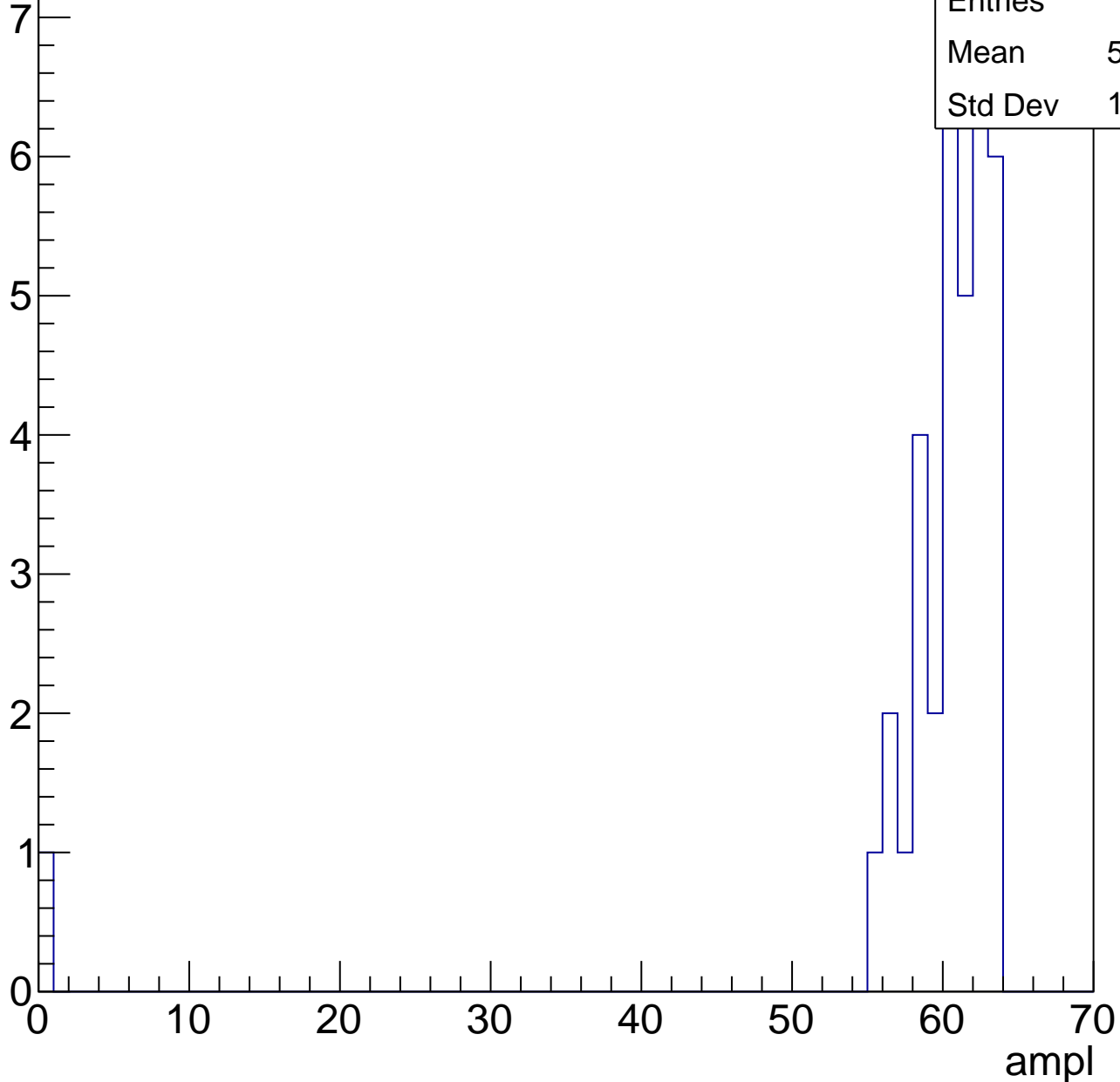
Entries	64
Mean	56.56
Std Dev	3.678



B1L103S, U10-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	36
Mean	58.64
Std Dev	10.14

B1L103S, U10-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch46, adc0

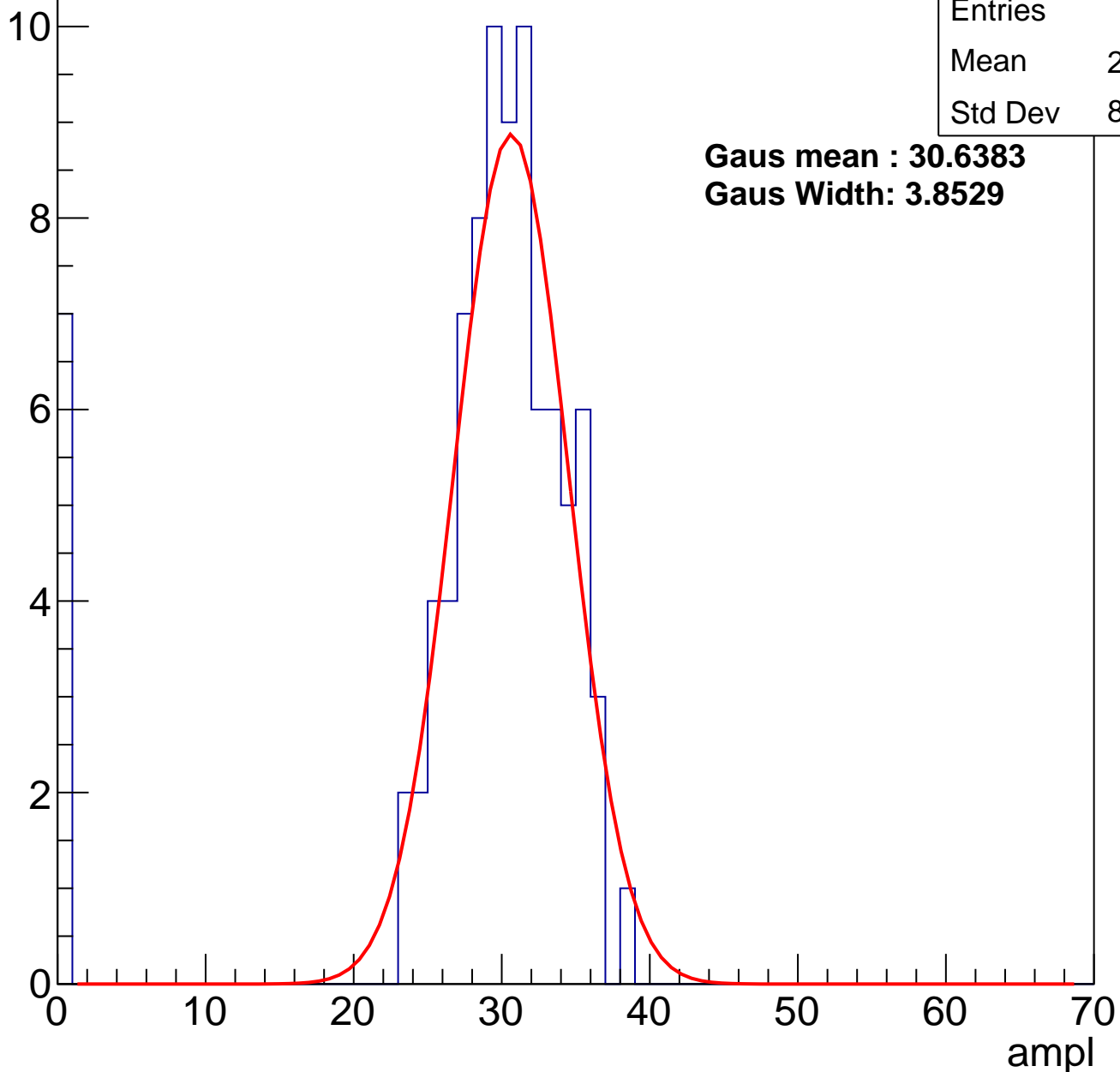
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	27.74
Std Dev	8.674

Gaus mean : 30.6383

Gaus Width: 3.8529

Entry



B1L103S, U10-ch46, adc1

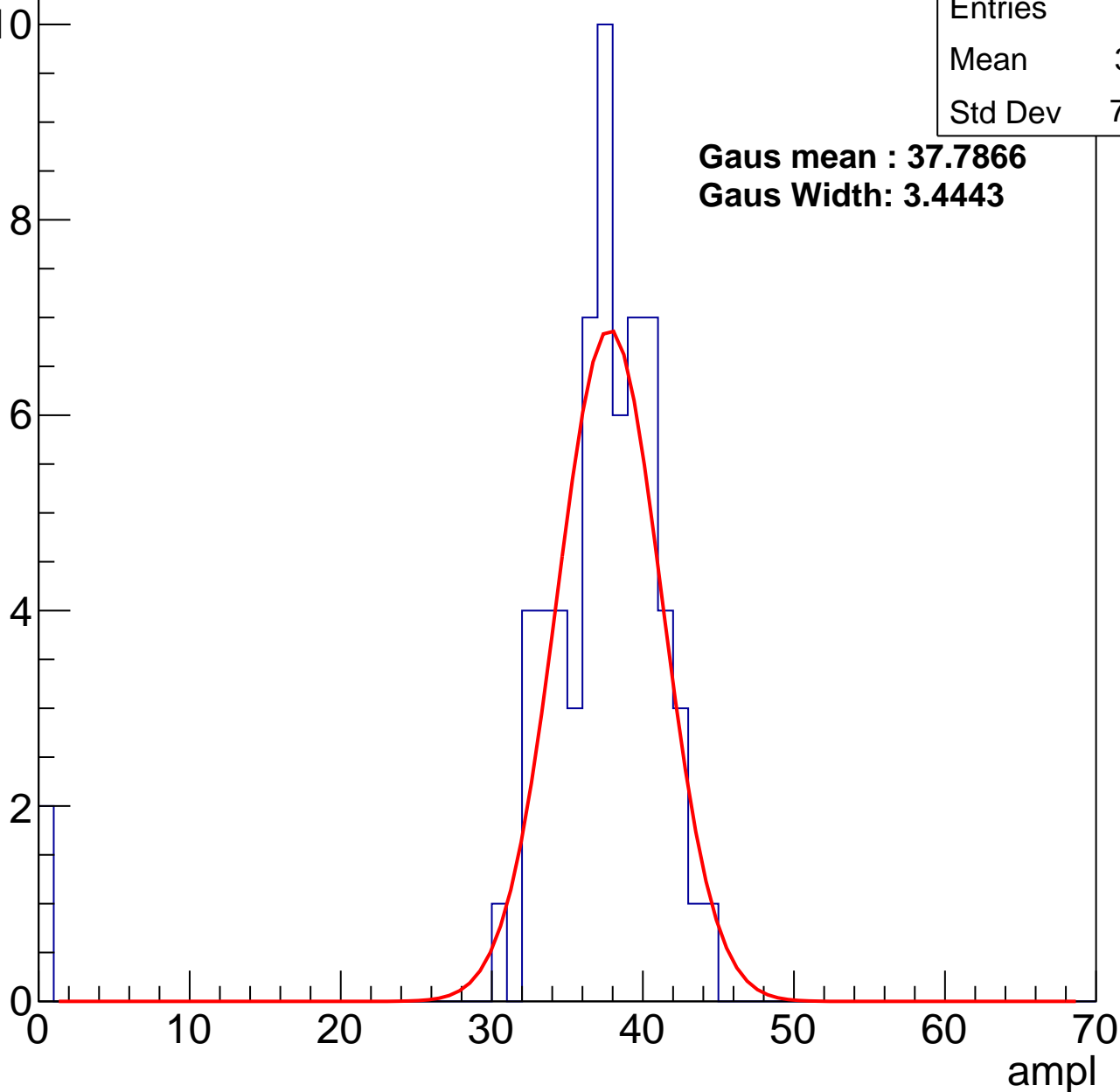
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.11
Std Dev	7.155

Gaus mean : 37.7866

Gaus Width: 3.4443



B1L103S, U10-ch46, adc2

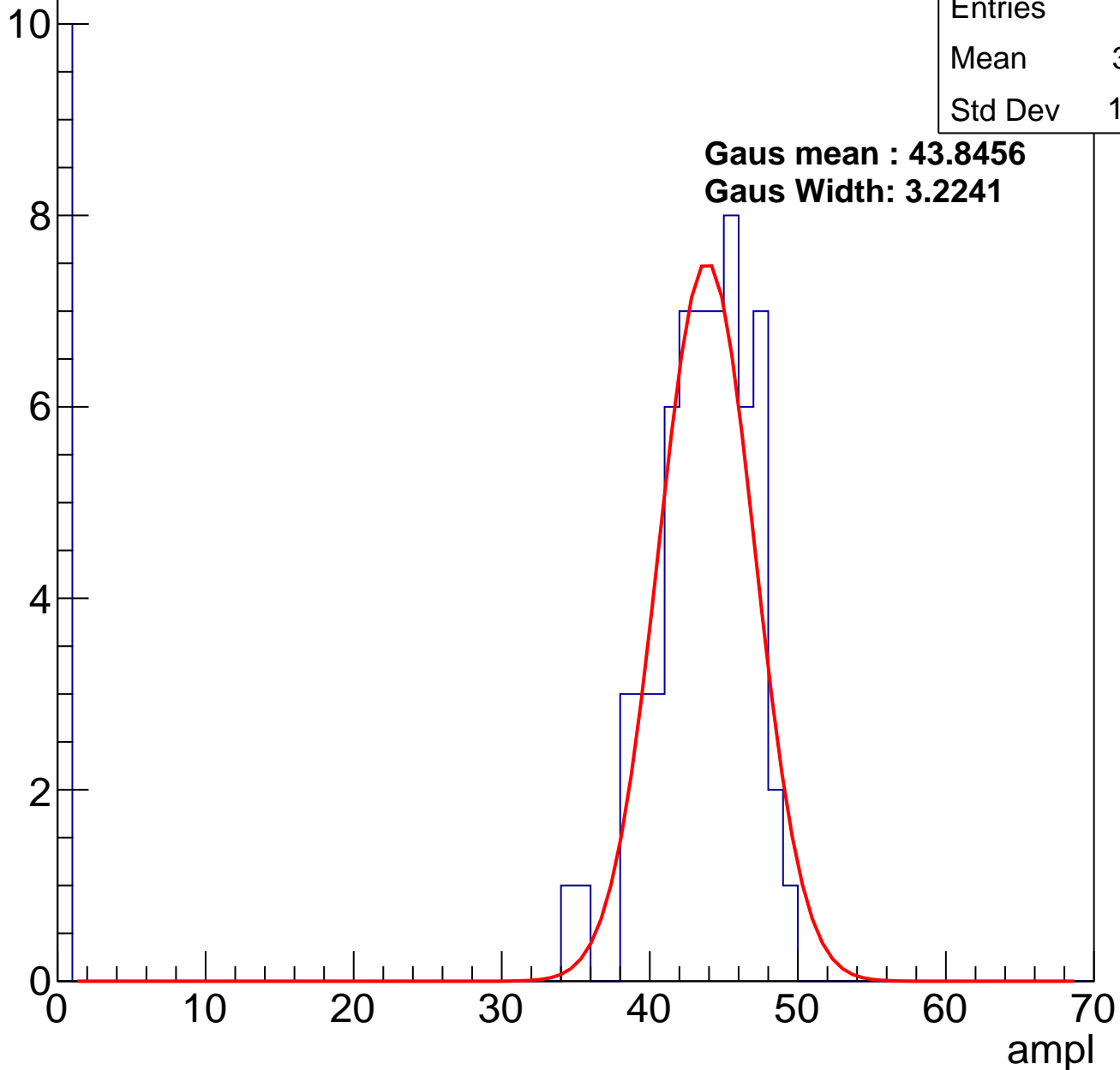
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	37.21
Std Dev	15.23

Gaus mean : 43.8456

Gaus Width: 3.2241

Entry



B1L103S, U10-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

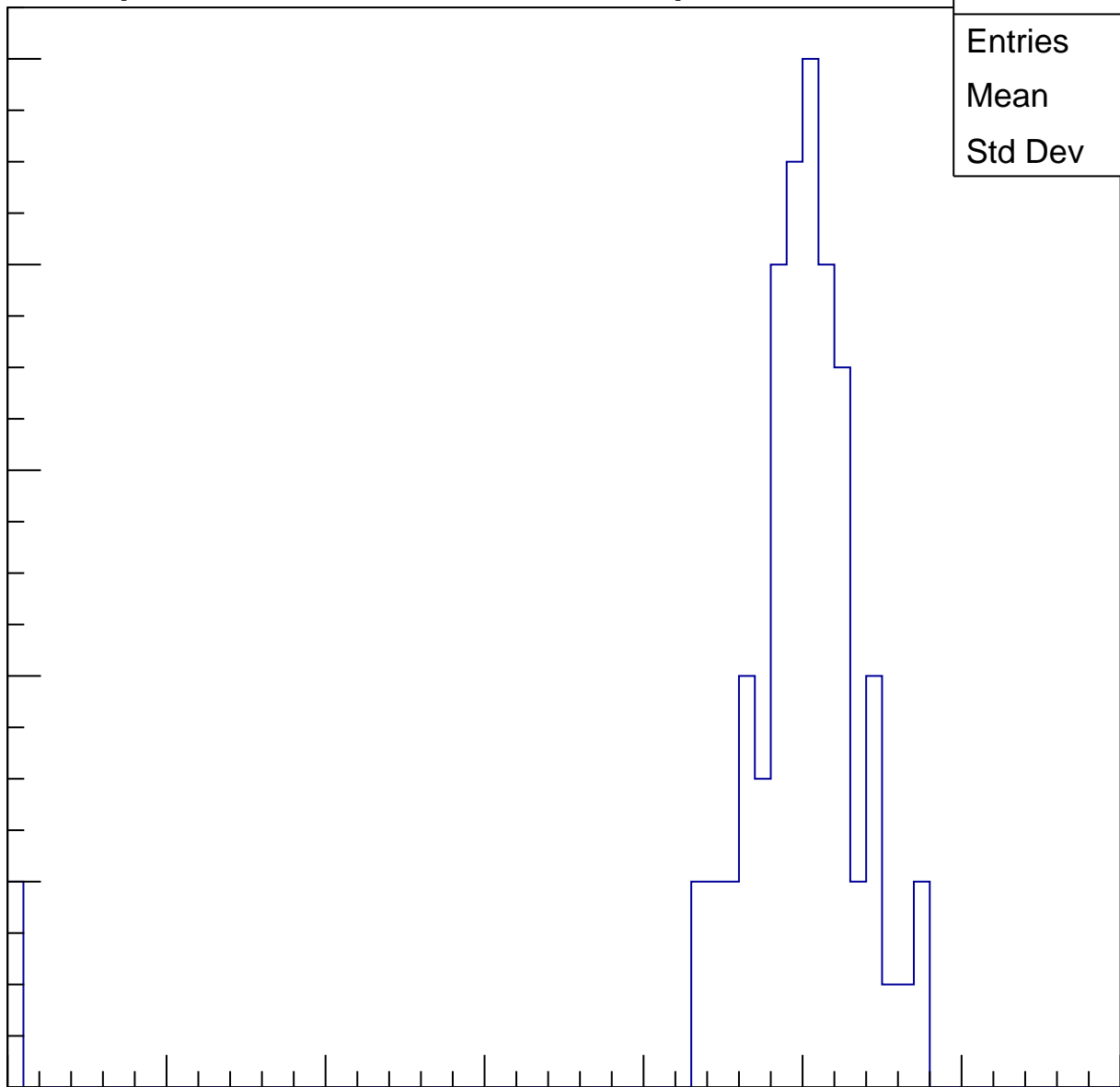
Entries	67
Mean	48.25
Std Dev	8.998

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

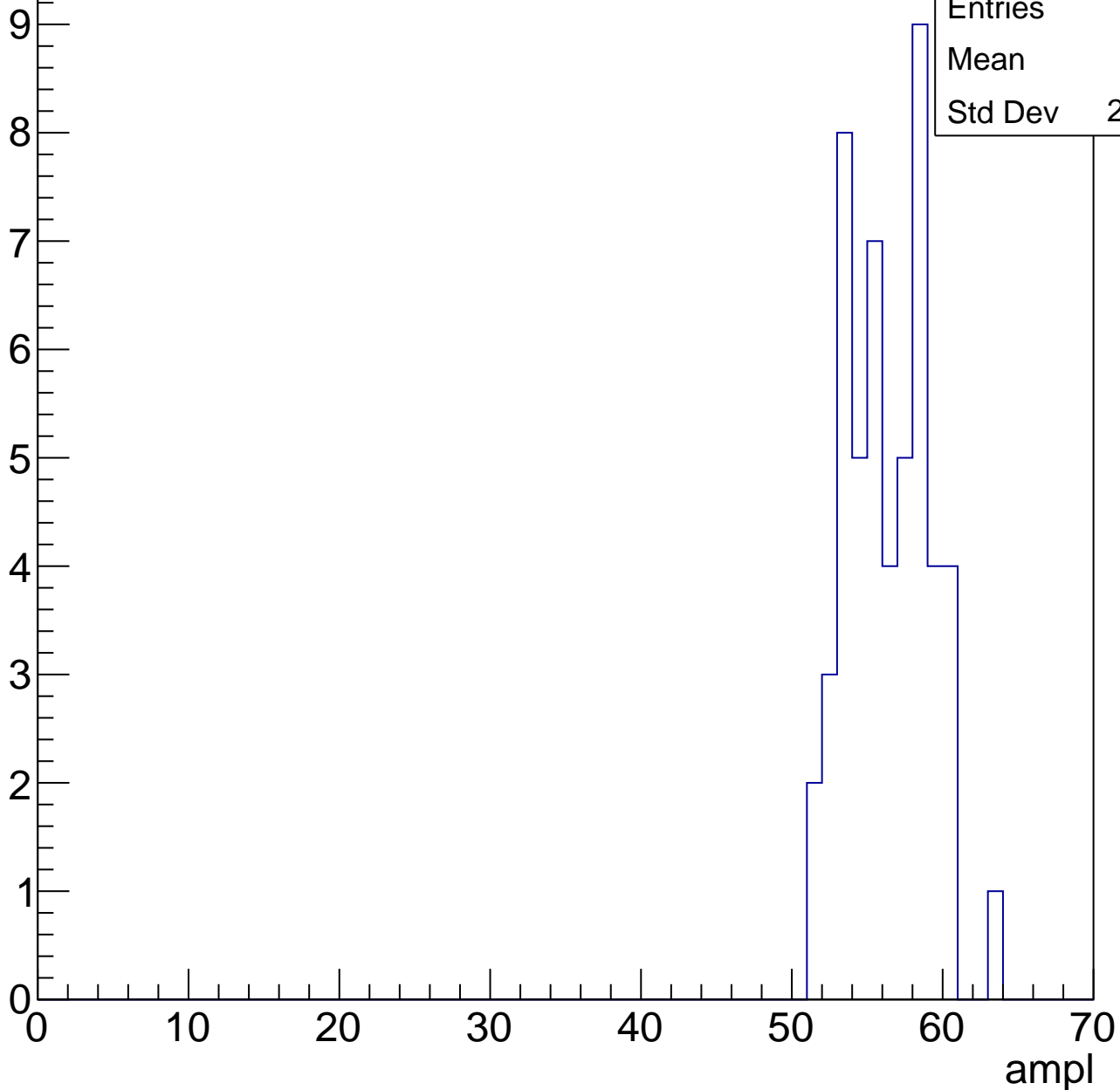


B1L103S, U10-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	55.9
Std Dev	2.726

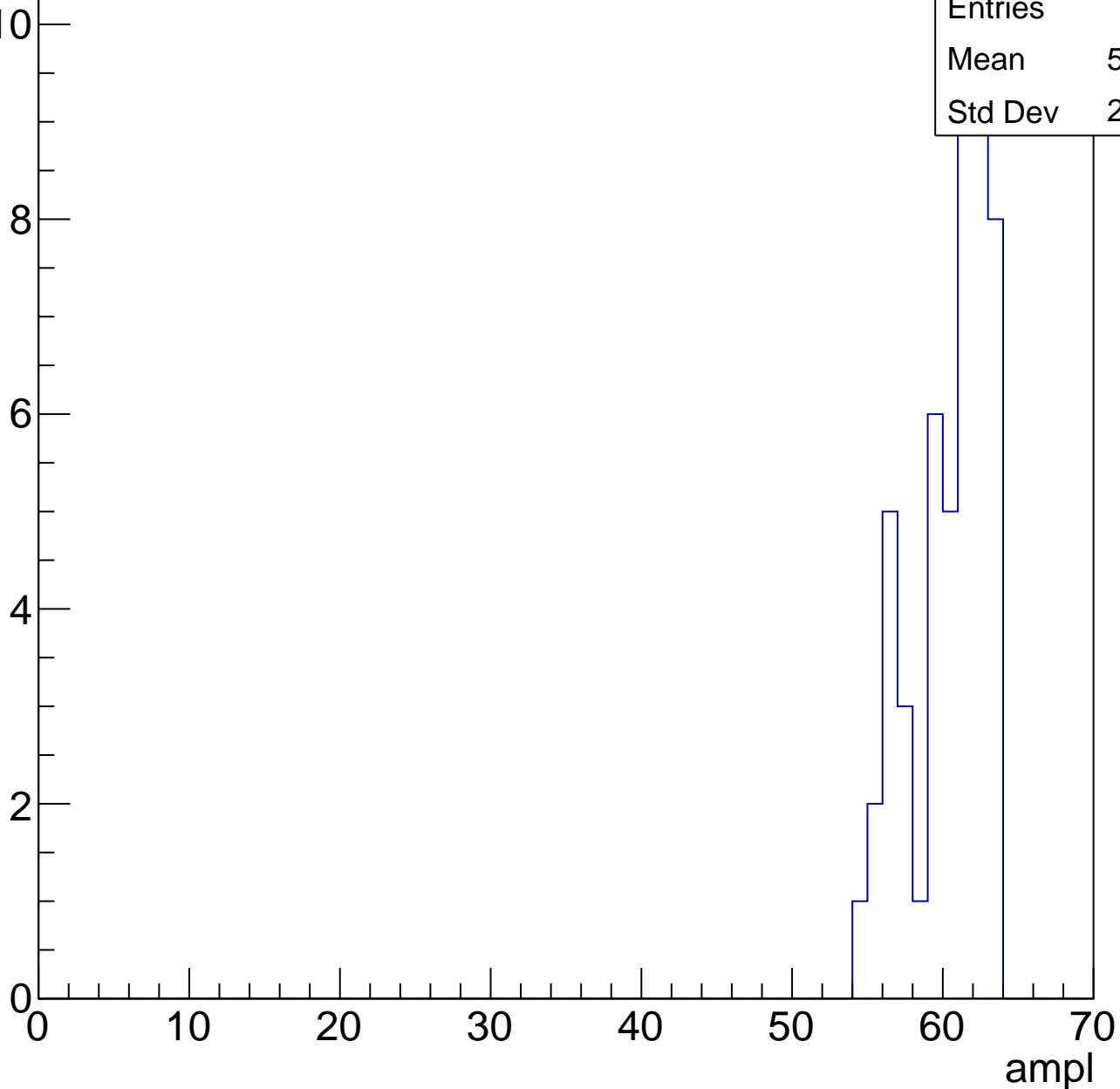


B1L103S, U10-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

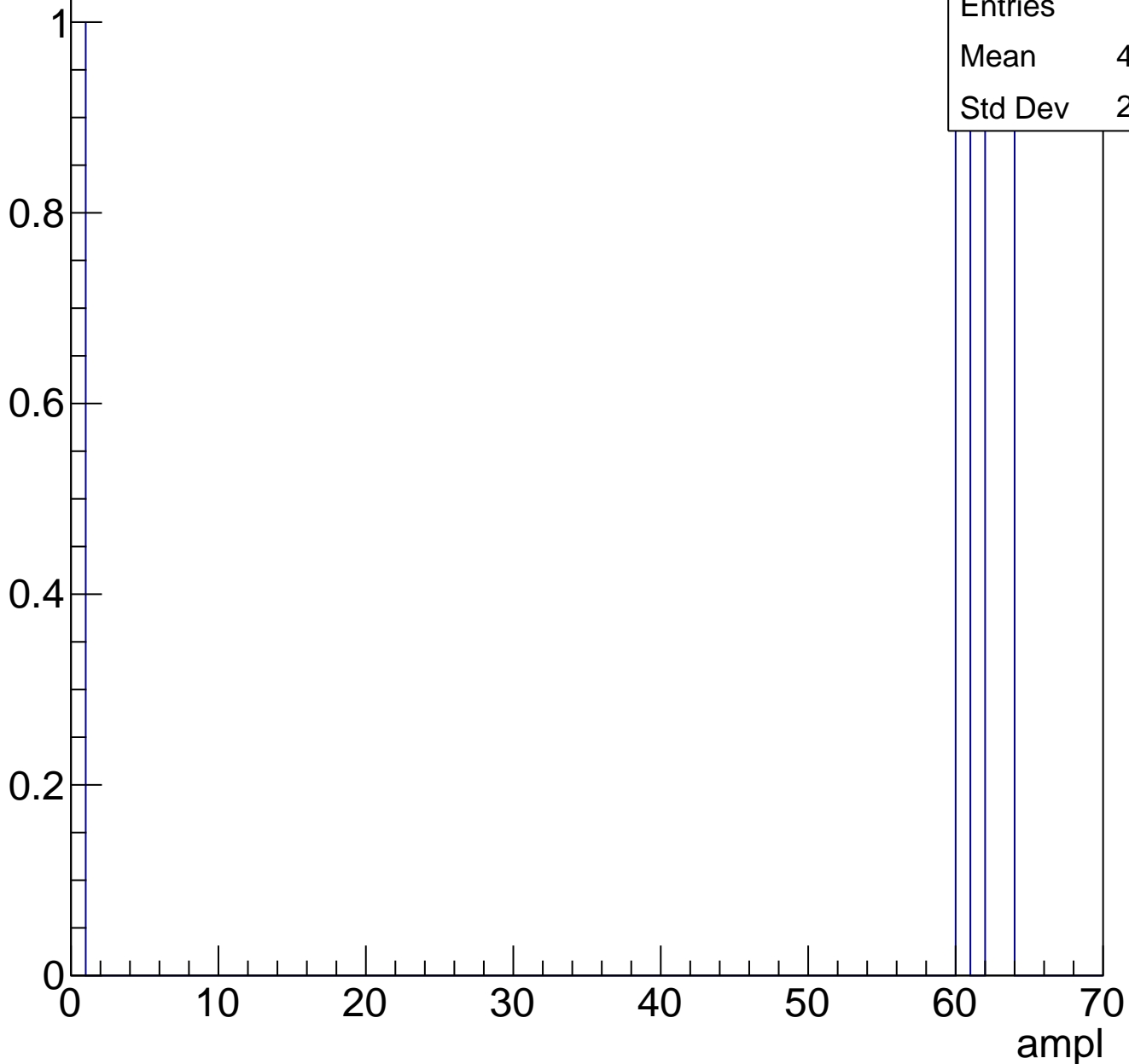
Entries	50
Mean	59.98
Std Dev	2.534



B1L103S, U10-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

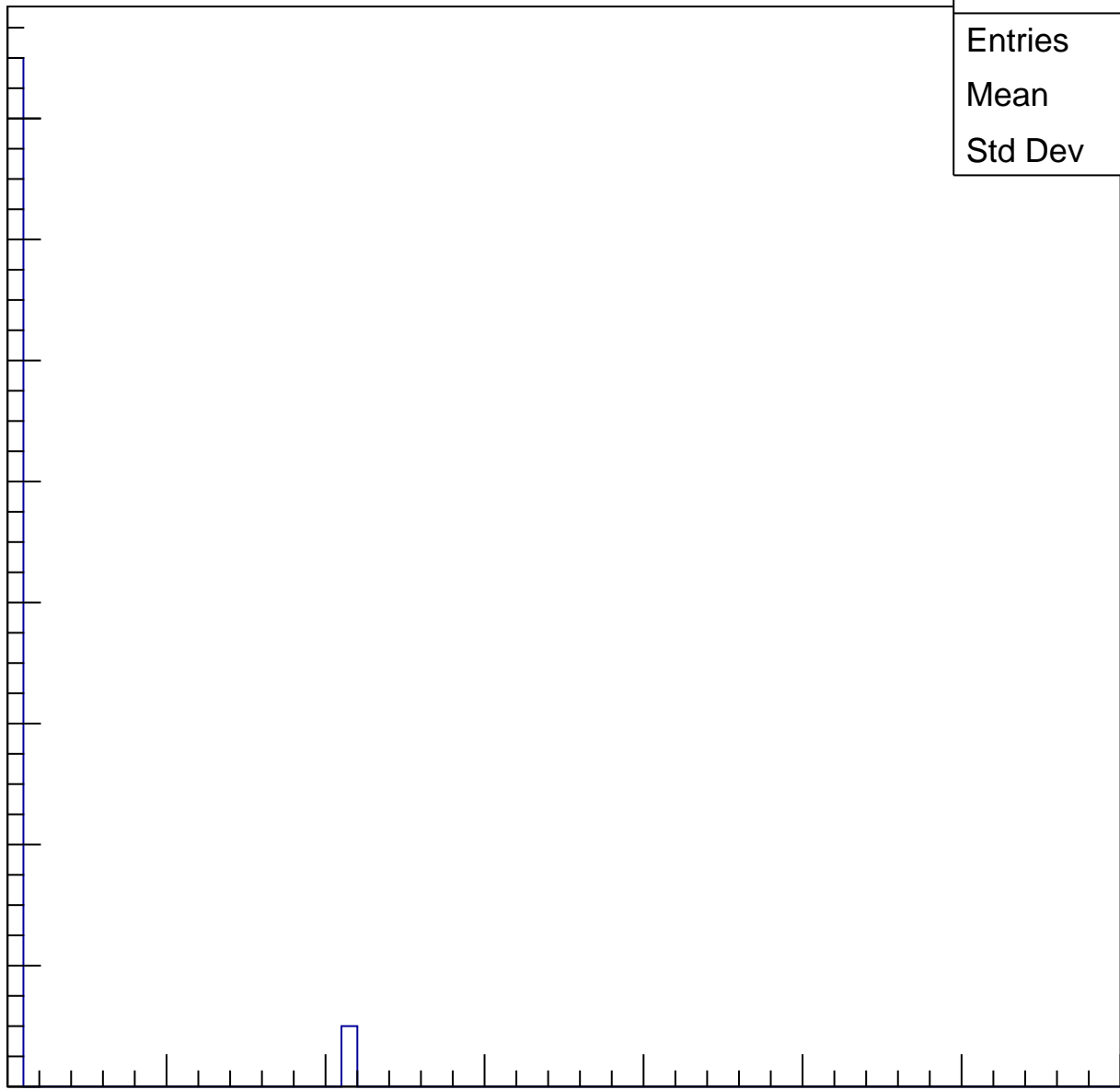
Entries	18
Mean	1.167
Std Dev	4.81

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch47, adc0

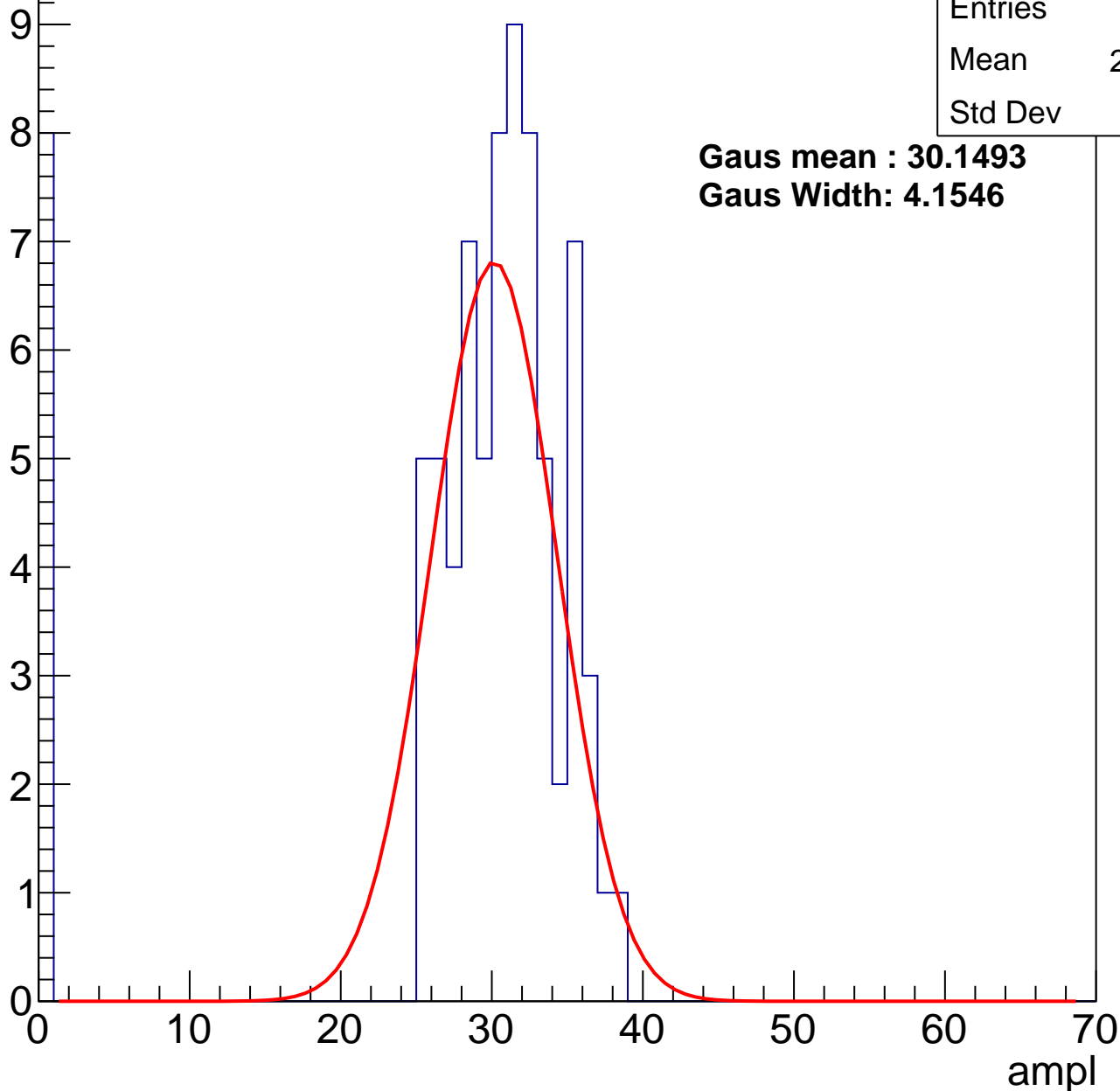
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	27.44
Std Dev	9.79

Gaus mean : 30.1493

Gaus Width: 4.1546



B1L103S, U10-ch47, adc1

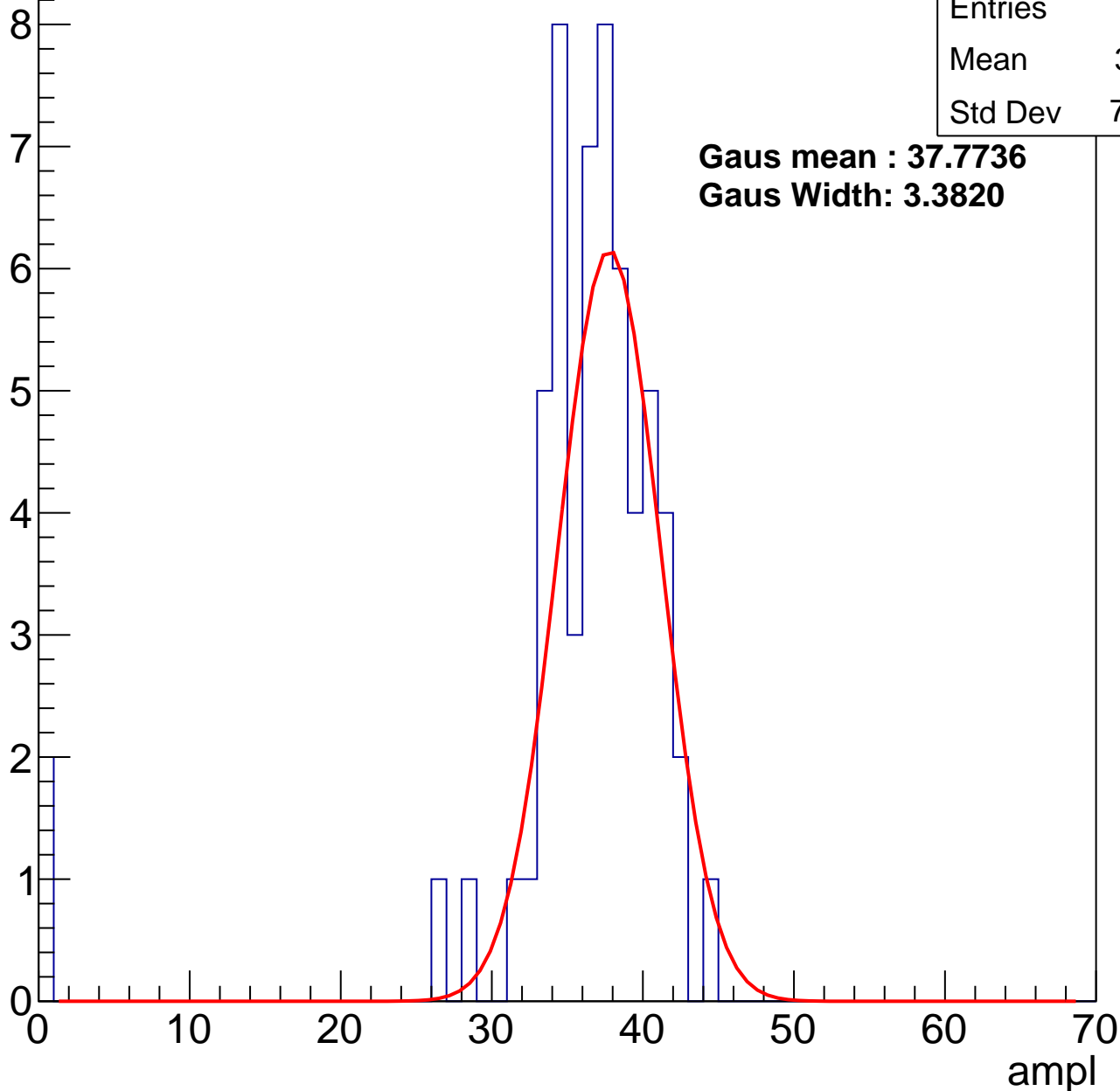
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.31
Std Dev	7.402

Gaus mean : 37.7736

Gaus Width: 3.3820



B1L103S, U10-ch47, adc2

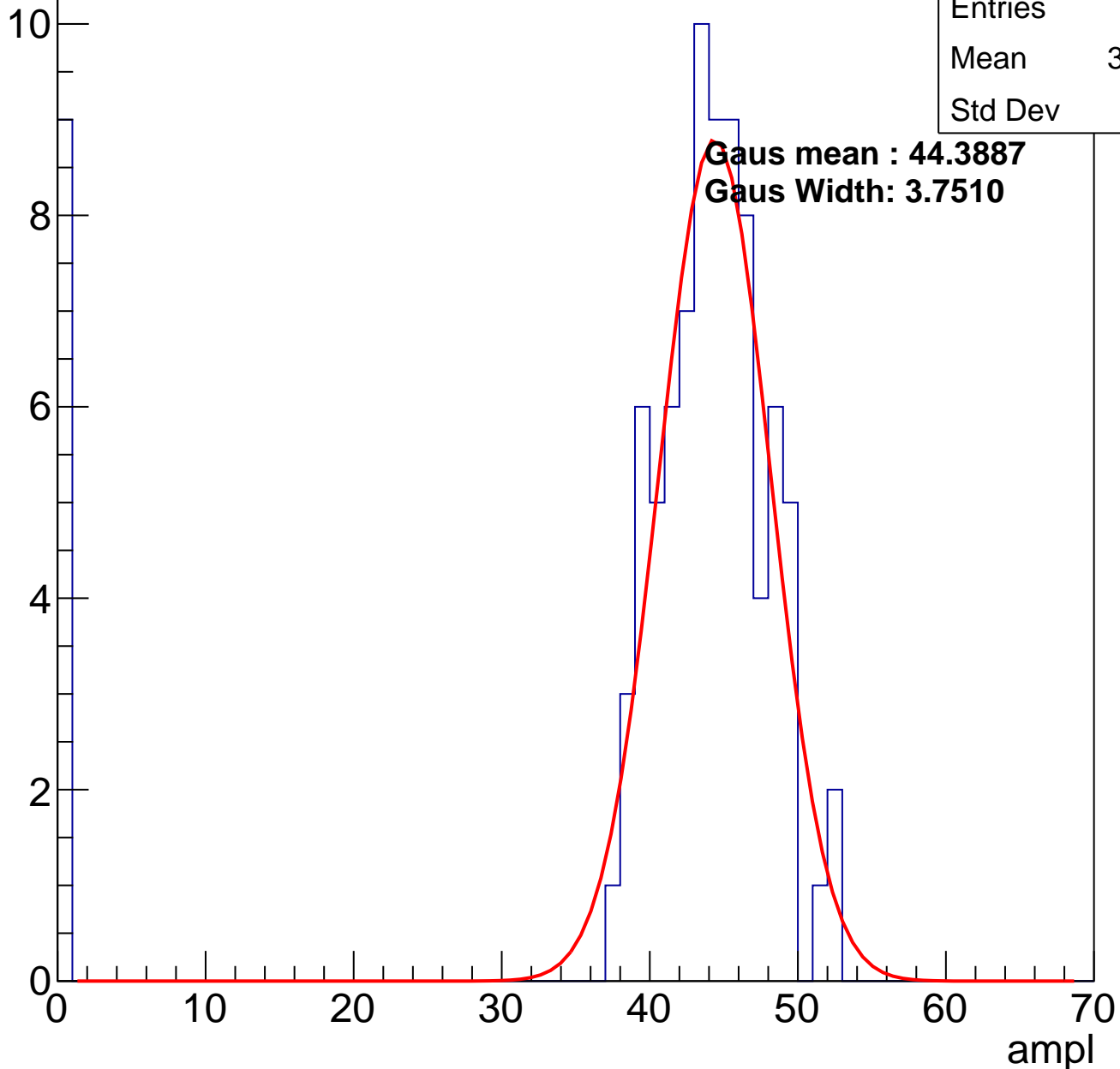
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	39.56
Std Dev	13.5

Gaus mean : 44.3887

Gaus Width: 3.7510

Entry

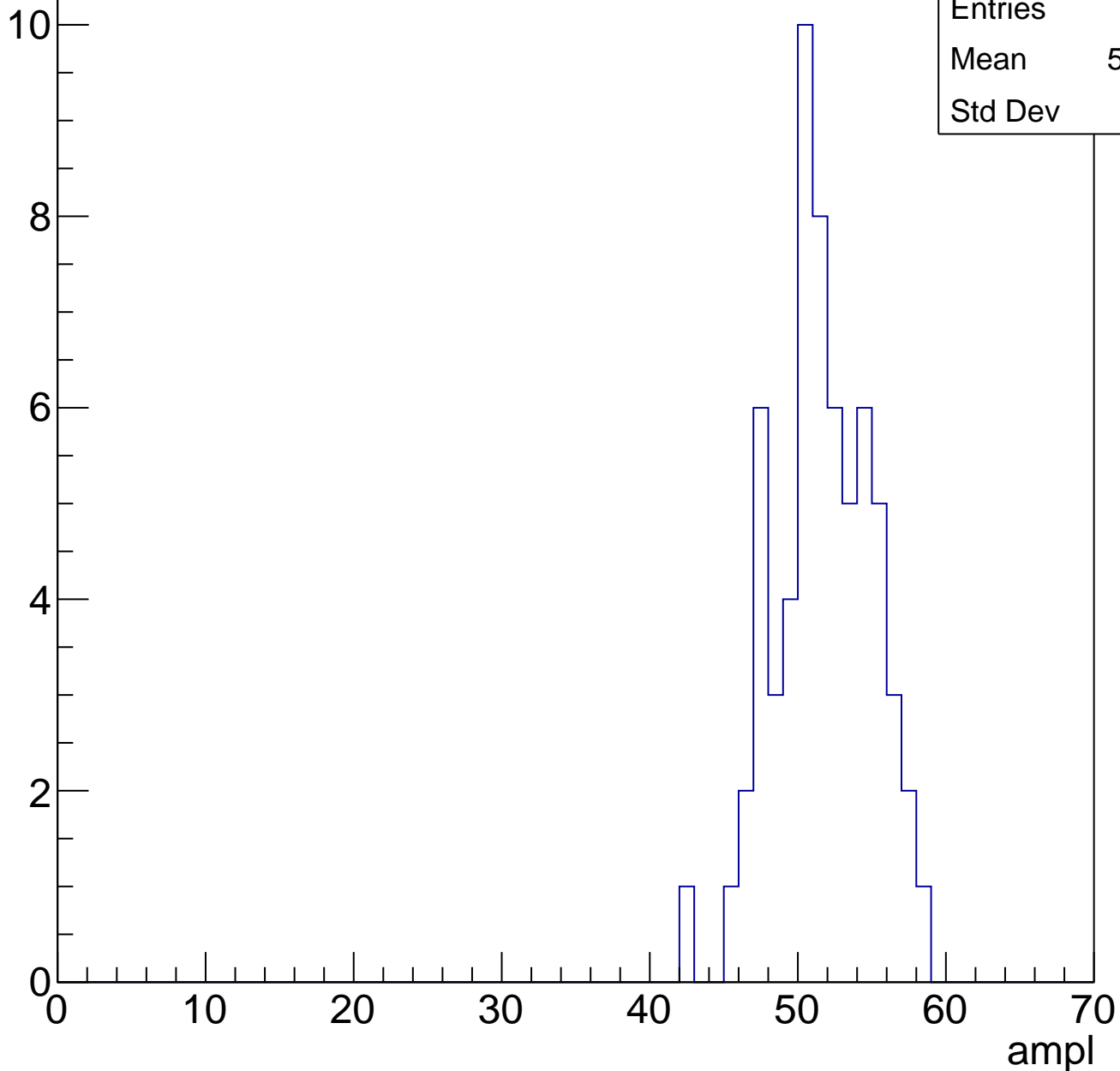


B1L103S, U10-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	51.19
Std Dev	3.26

Entry

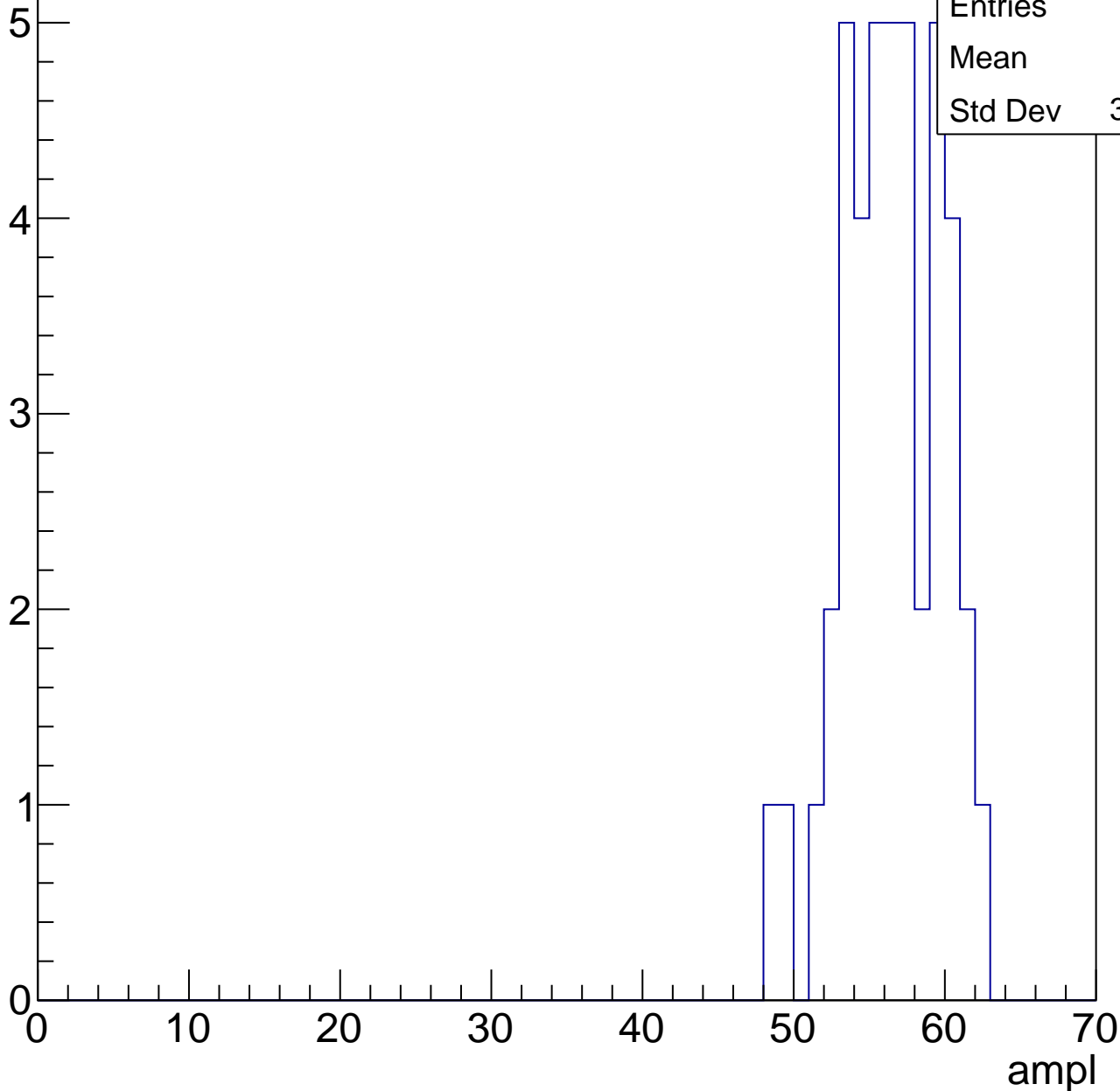


B1L103S, U10-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

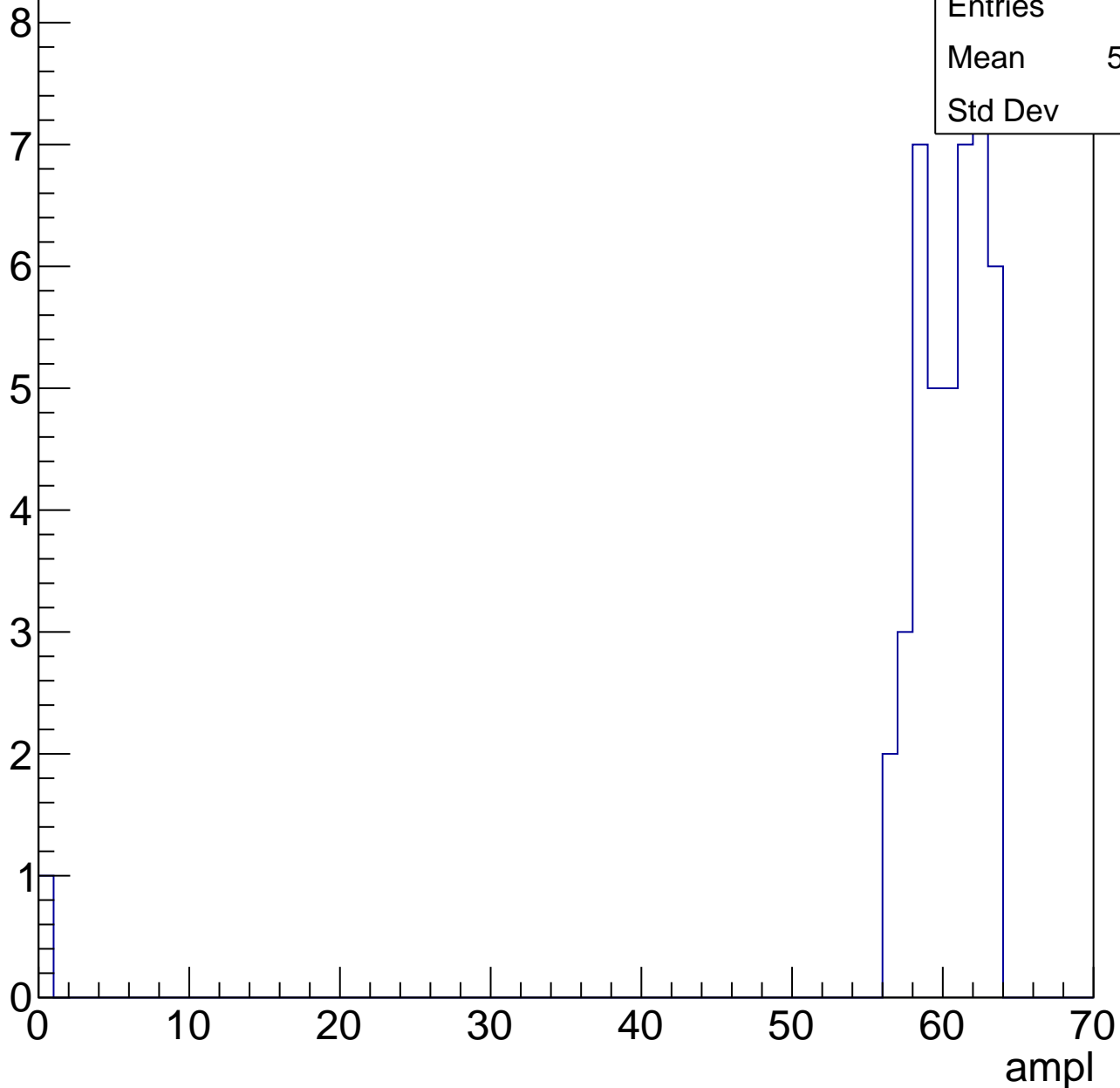
Entries	43
Mean	56
Std Dev	3.213



B1L103S, U10-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

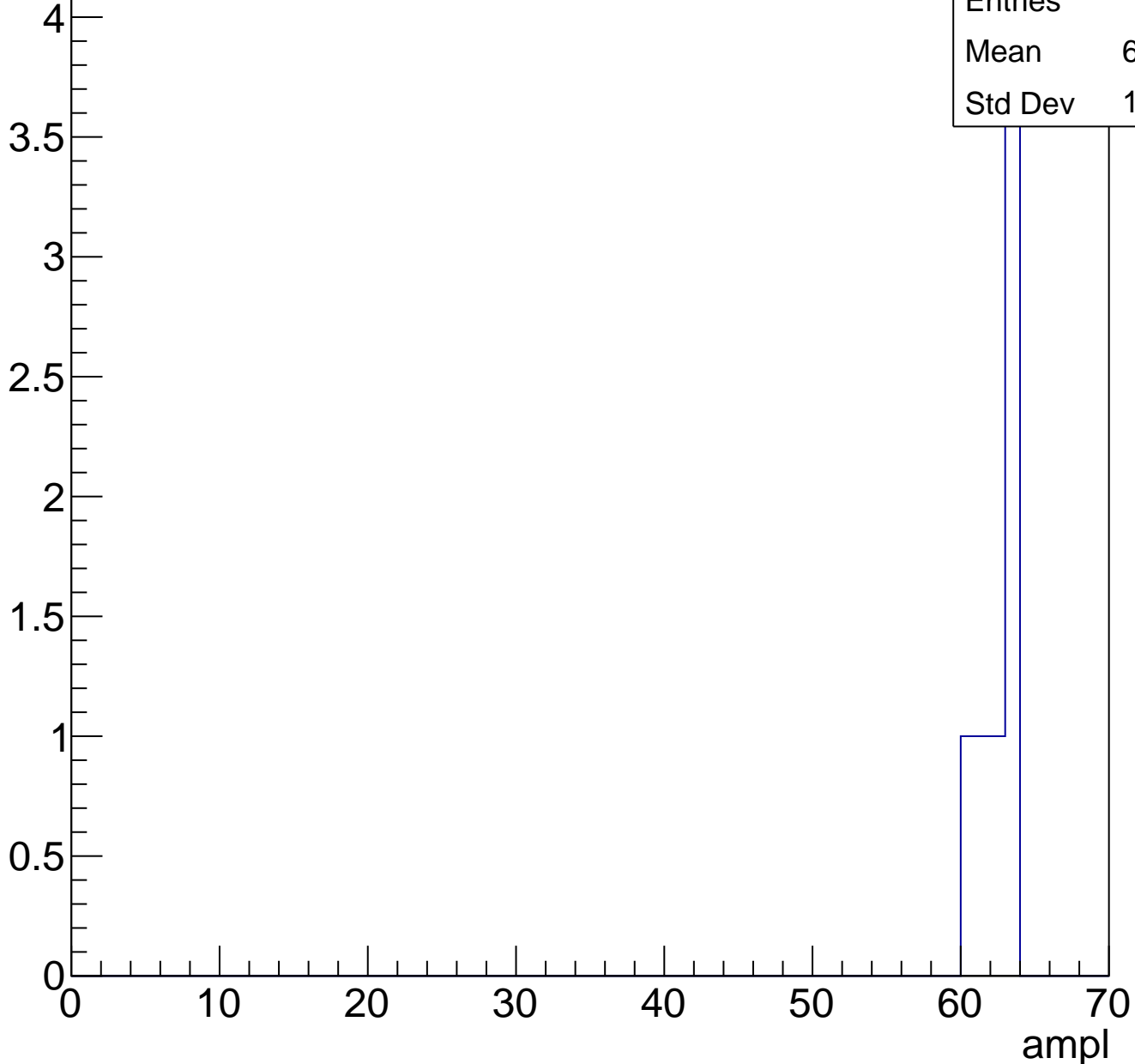
Entry



B1L103S, U10-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	7
Mean	62.14
Std Dev	1.125

B1L103S, U10-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch48, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	25.49
Std Dev	12.17

Gaus mean : 31.8425

Gaus Width: 3.7936

Entry

12

10

8

6

4

2

0

0

10

20

30

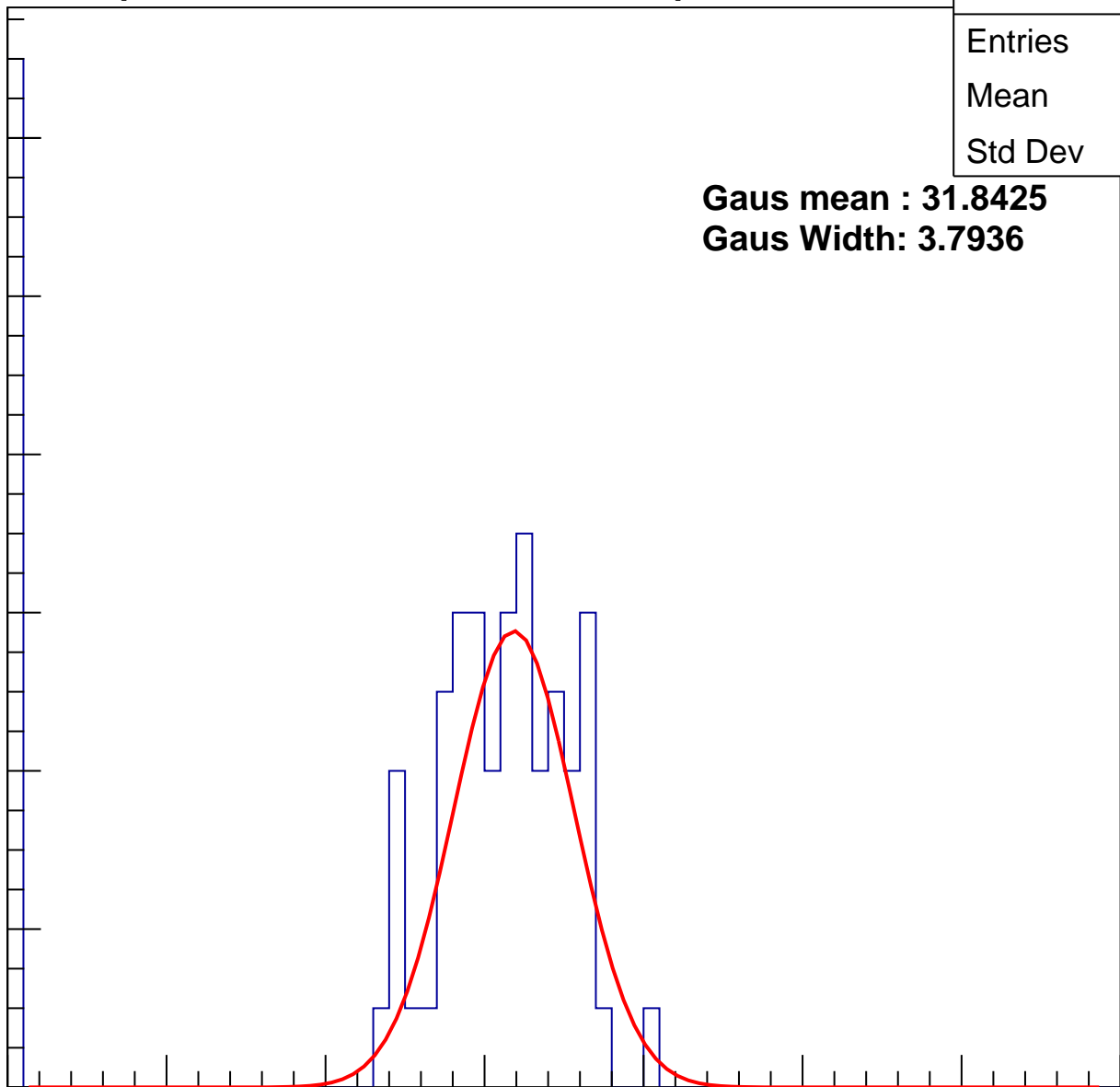
40

50

60

70

ampl



B1L103S, U10-ch48, adc1

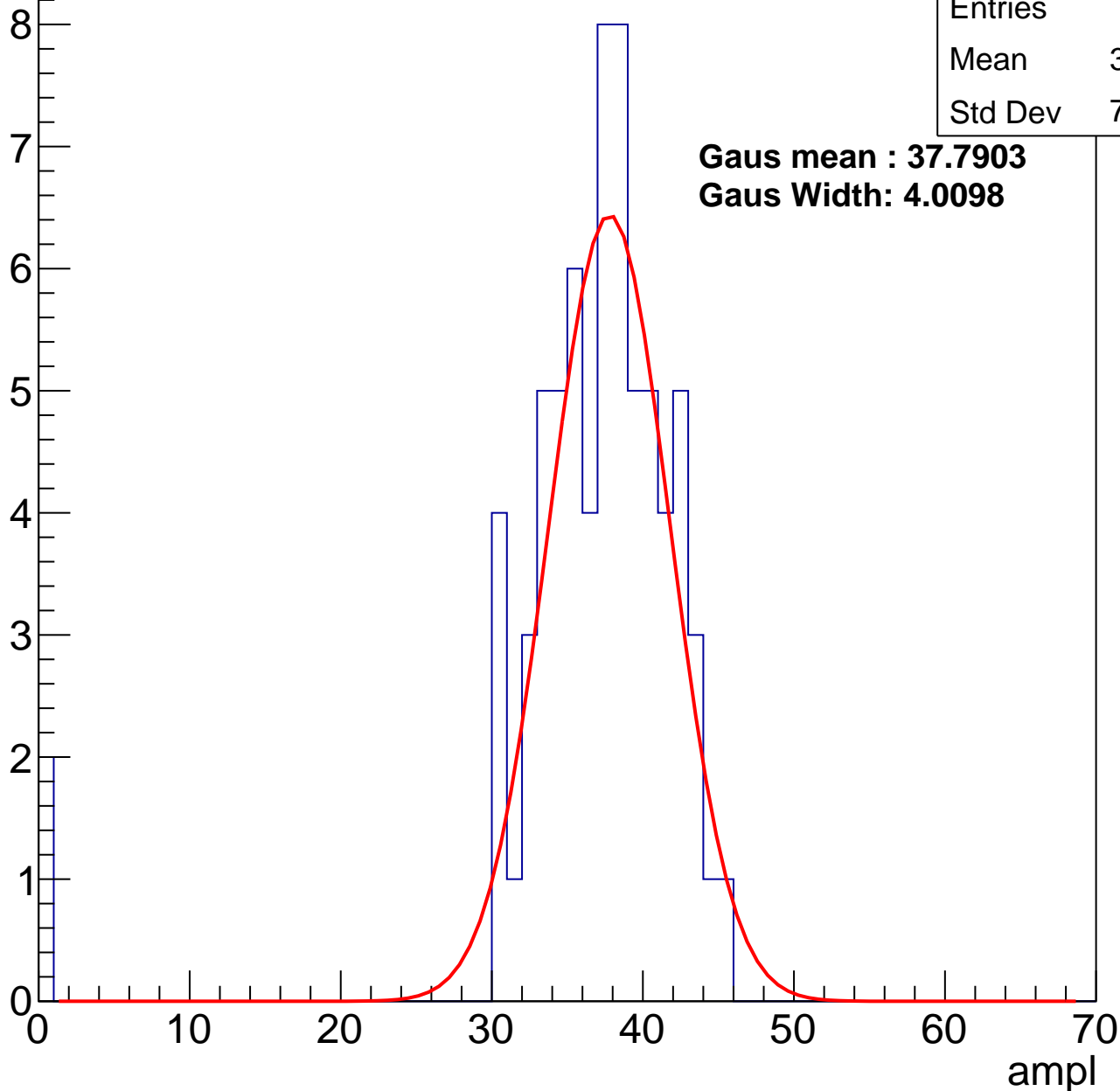
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.04
Std Dev	7.188

Gaus mean : 37.7903

Gaus Width: 4.0098



B1L103S, U10-ch48, adc2

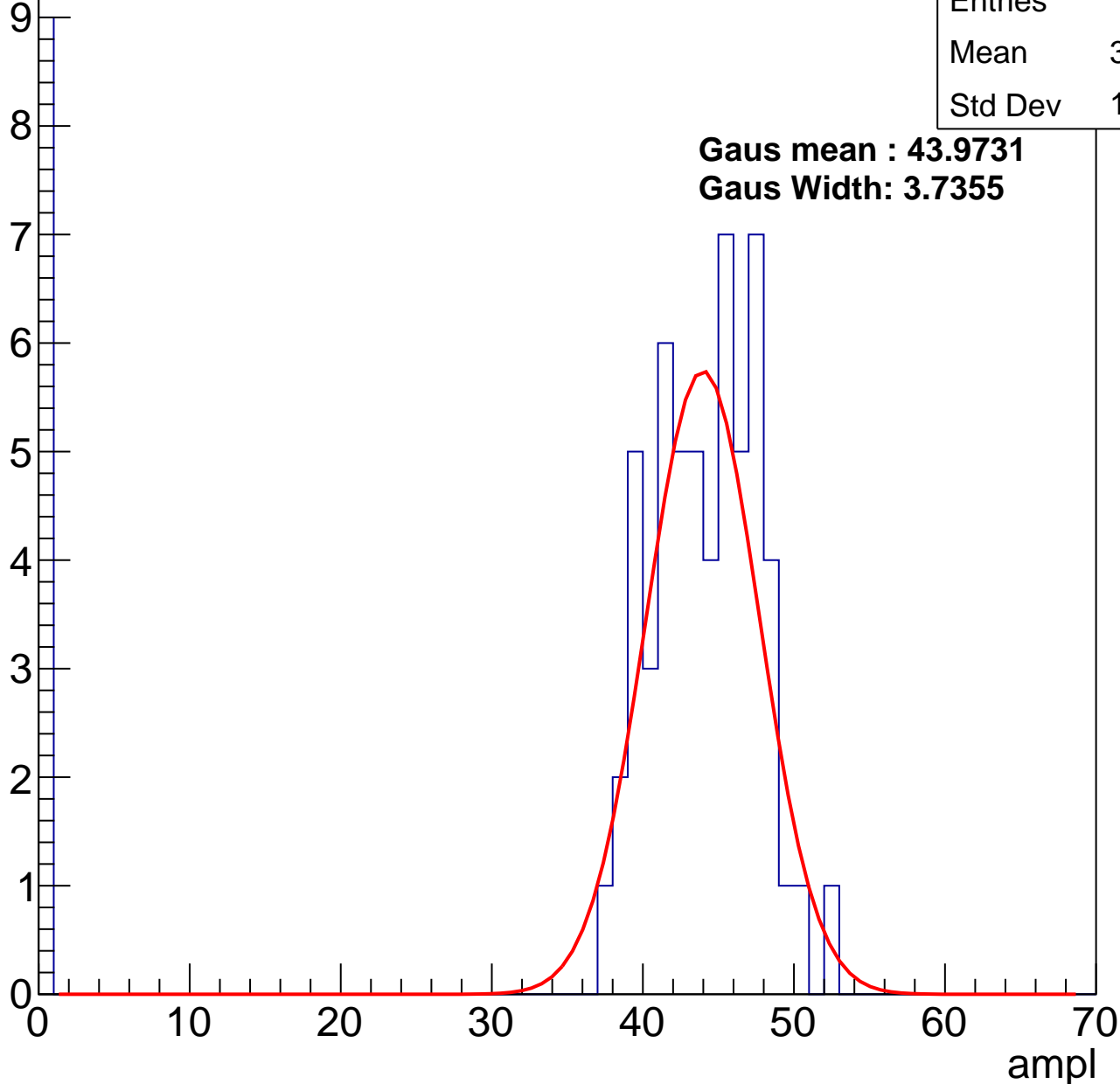
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37.76
Std Dev	15.33

Gaus mean : 43.9731

Gaus Width: 3.7355

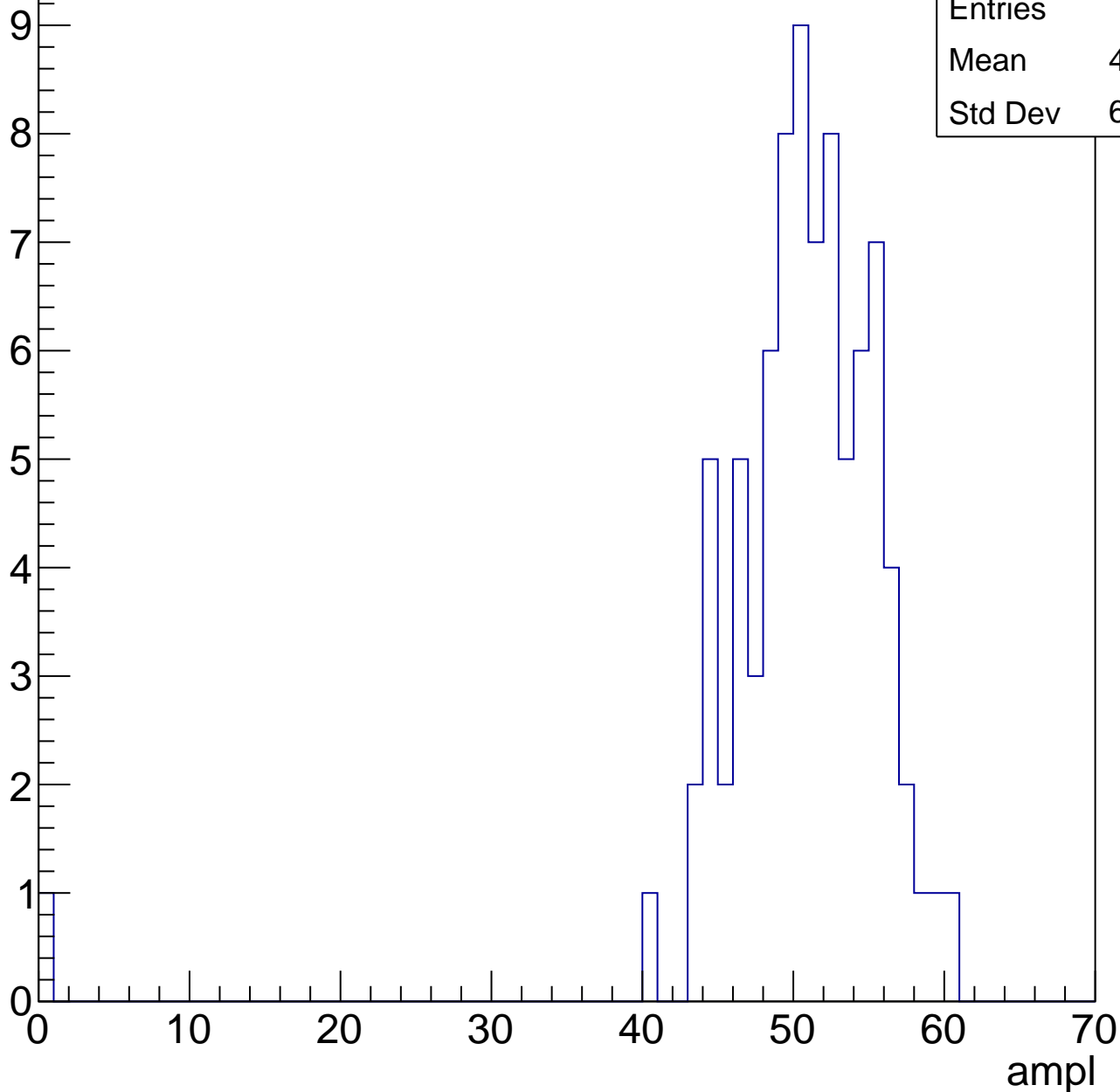


B1L103S, U10-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	49.99
Std Dev	6.822

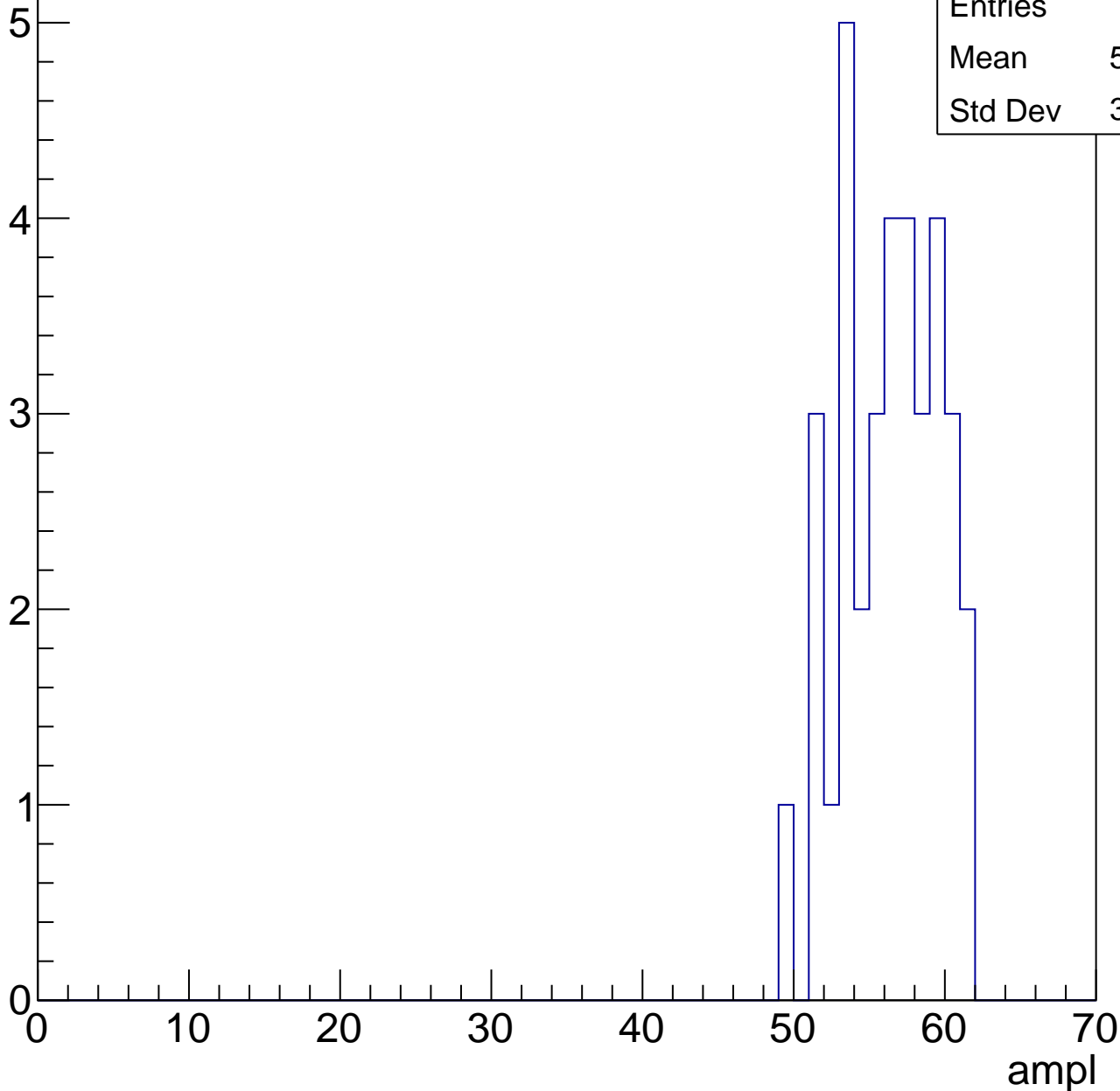


B1L103S, U10-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	55.89
Std Dev	3.142

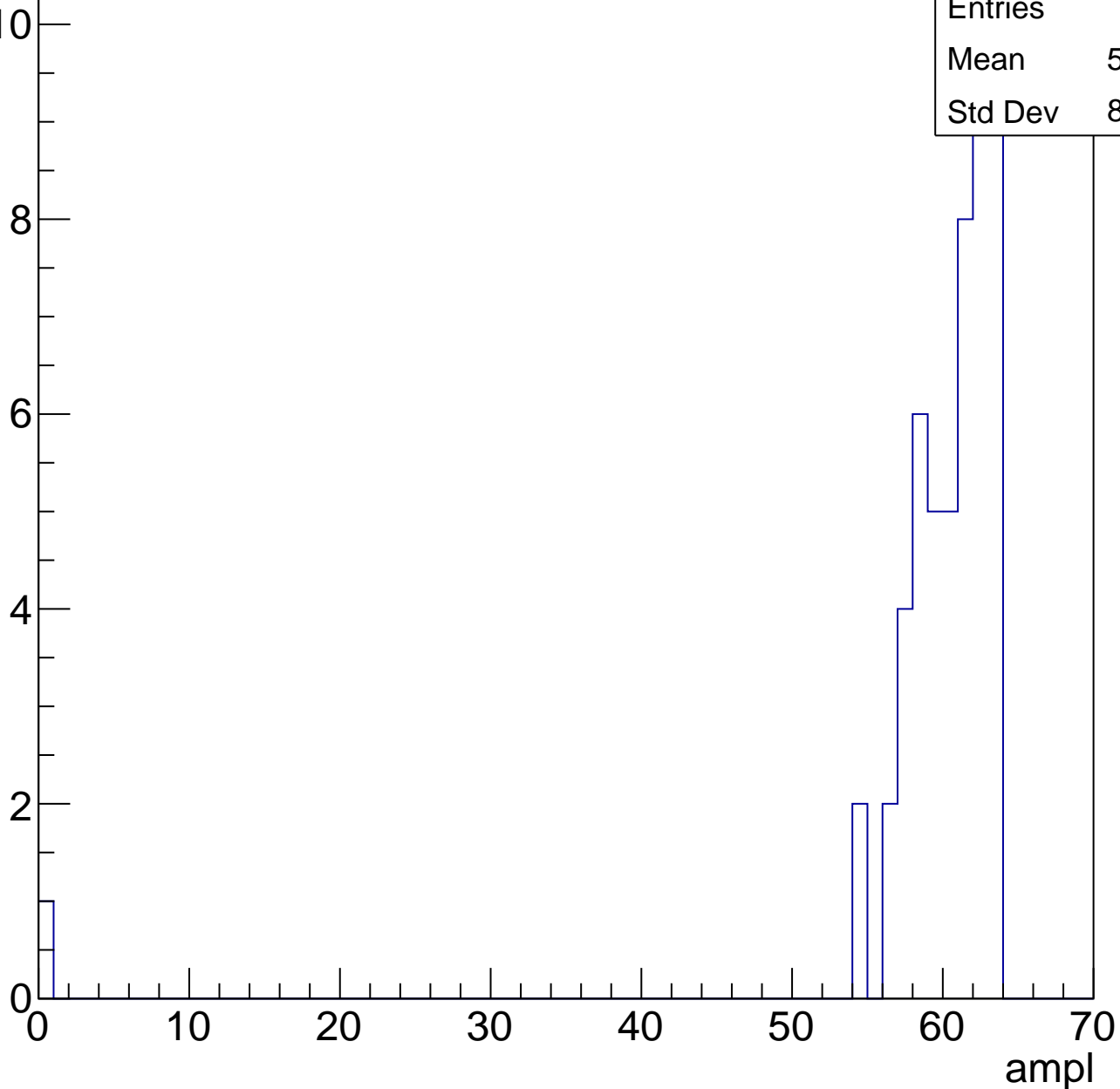


B1L103S, U10-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.96
Std Dev	8.596



B1L103S, U10-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch49, adc0

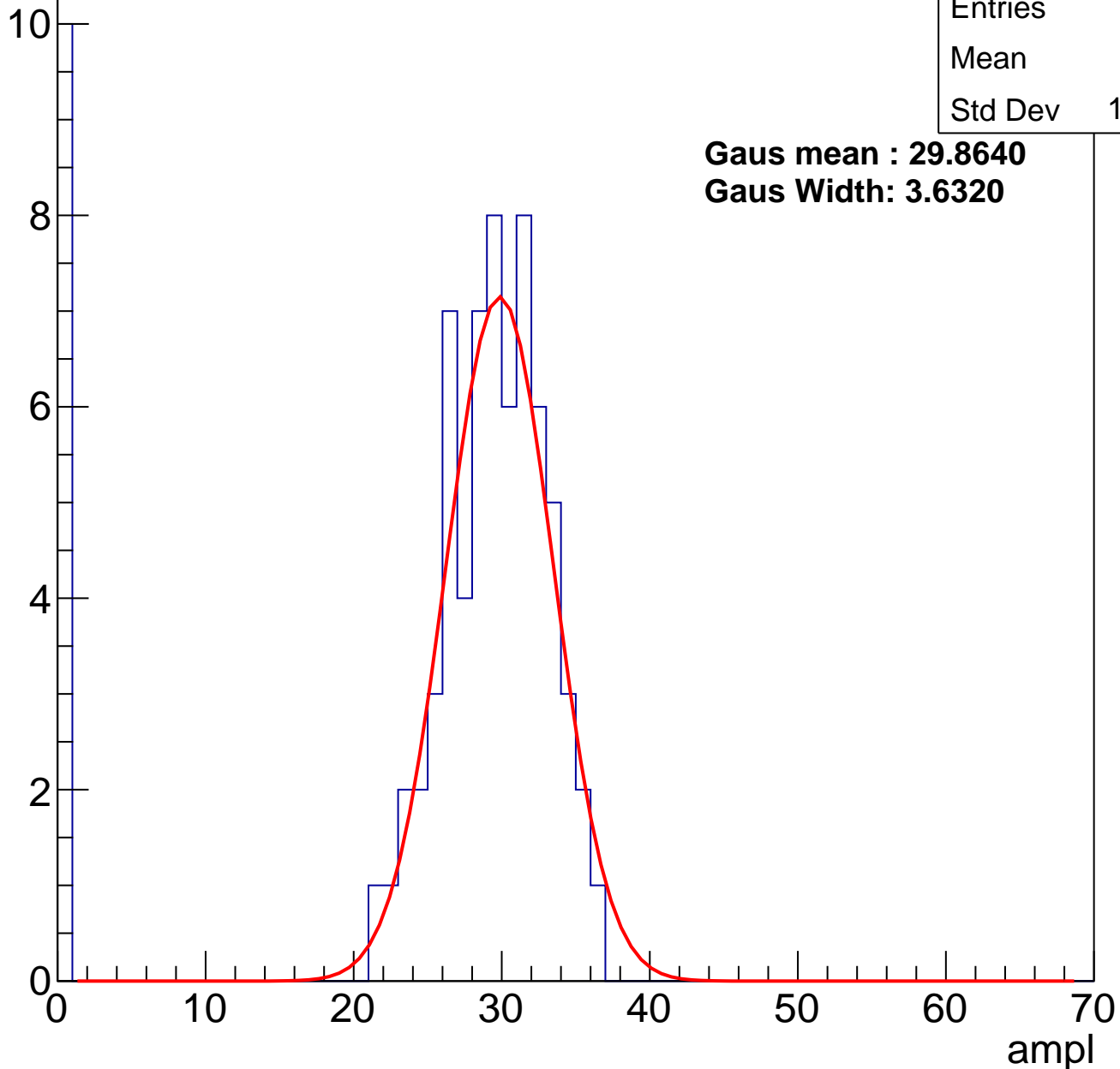
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	25.3
Std Dev	10.33

Gaus mean : 29.8640

Gaus Width: 3.6320

Entry



B1L103S, U10-ch49, adc1

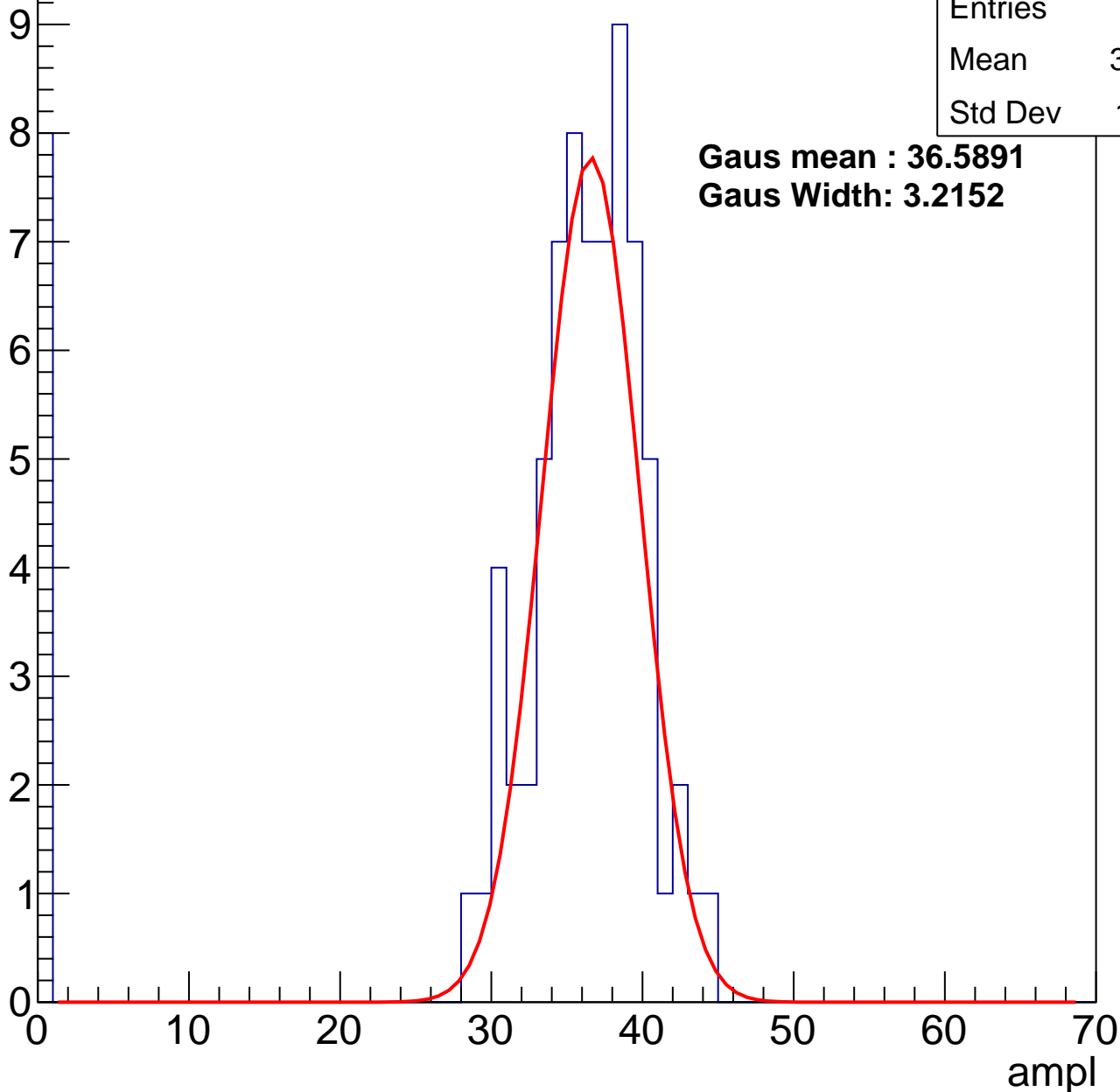
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.36
Std Dev	11.41

Gaus mean : 36.5891

Gaus Width: 3.2152



B1L103S, U10-ch49, adc2

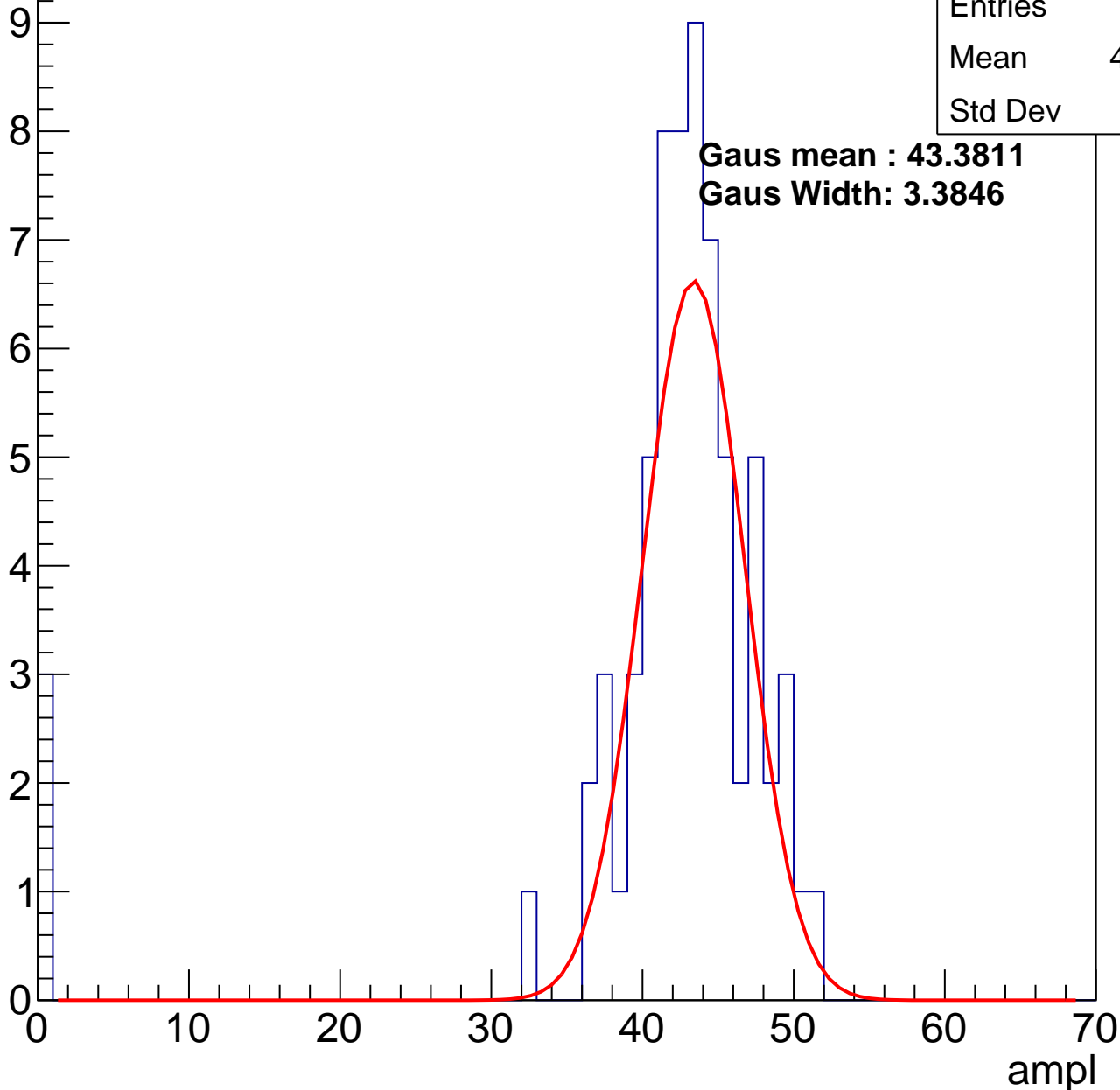
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.94
Std Dev	9.43

Gaus mean : 43.3811

Gaus Width: 3.3846

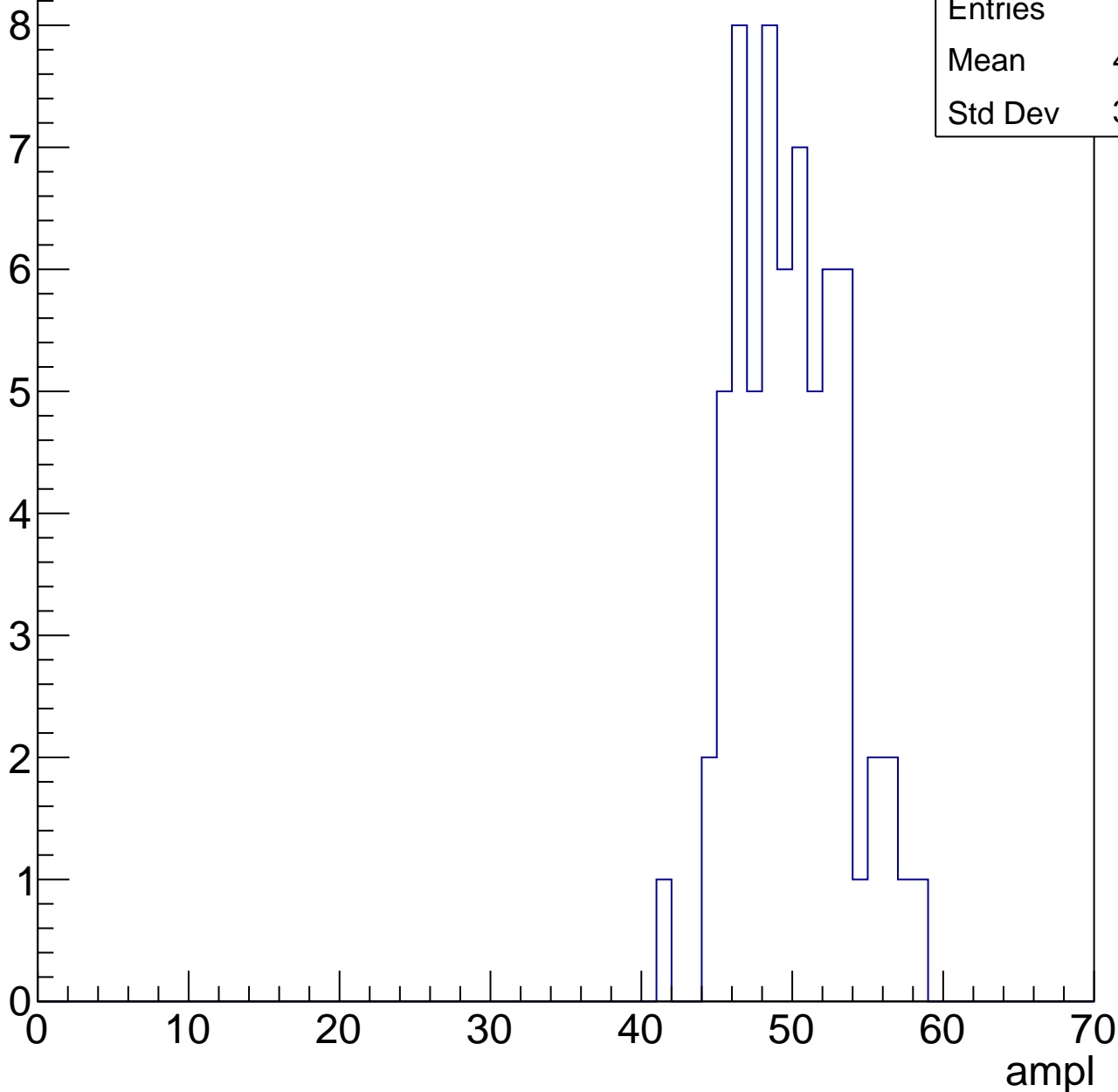


B1L103S, U10-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	49.41
Std Dev	3.481

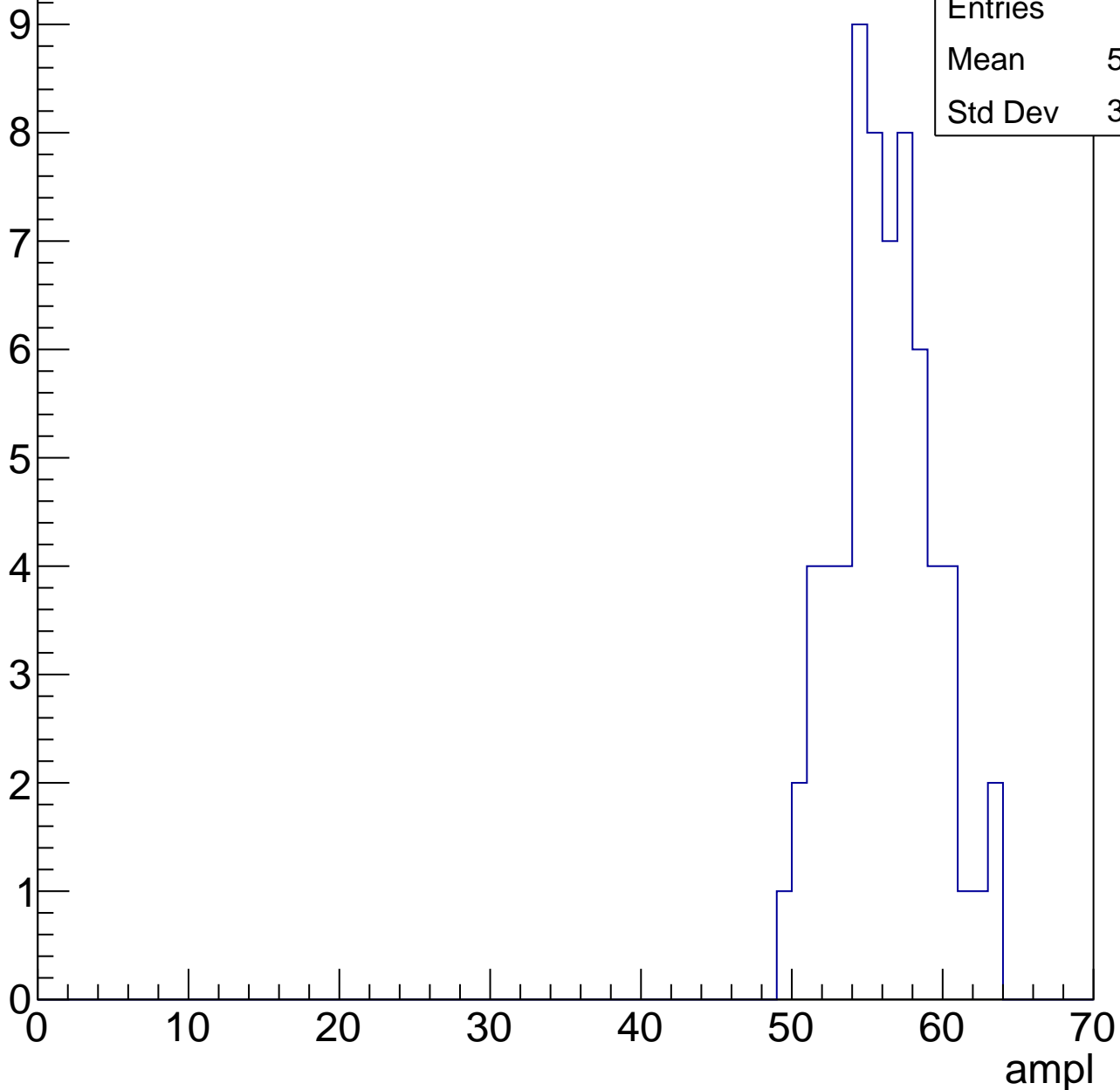


B1L103S, U10-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.69
Std Dev	3.162

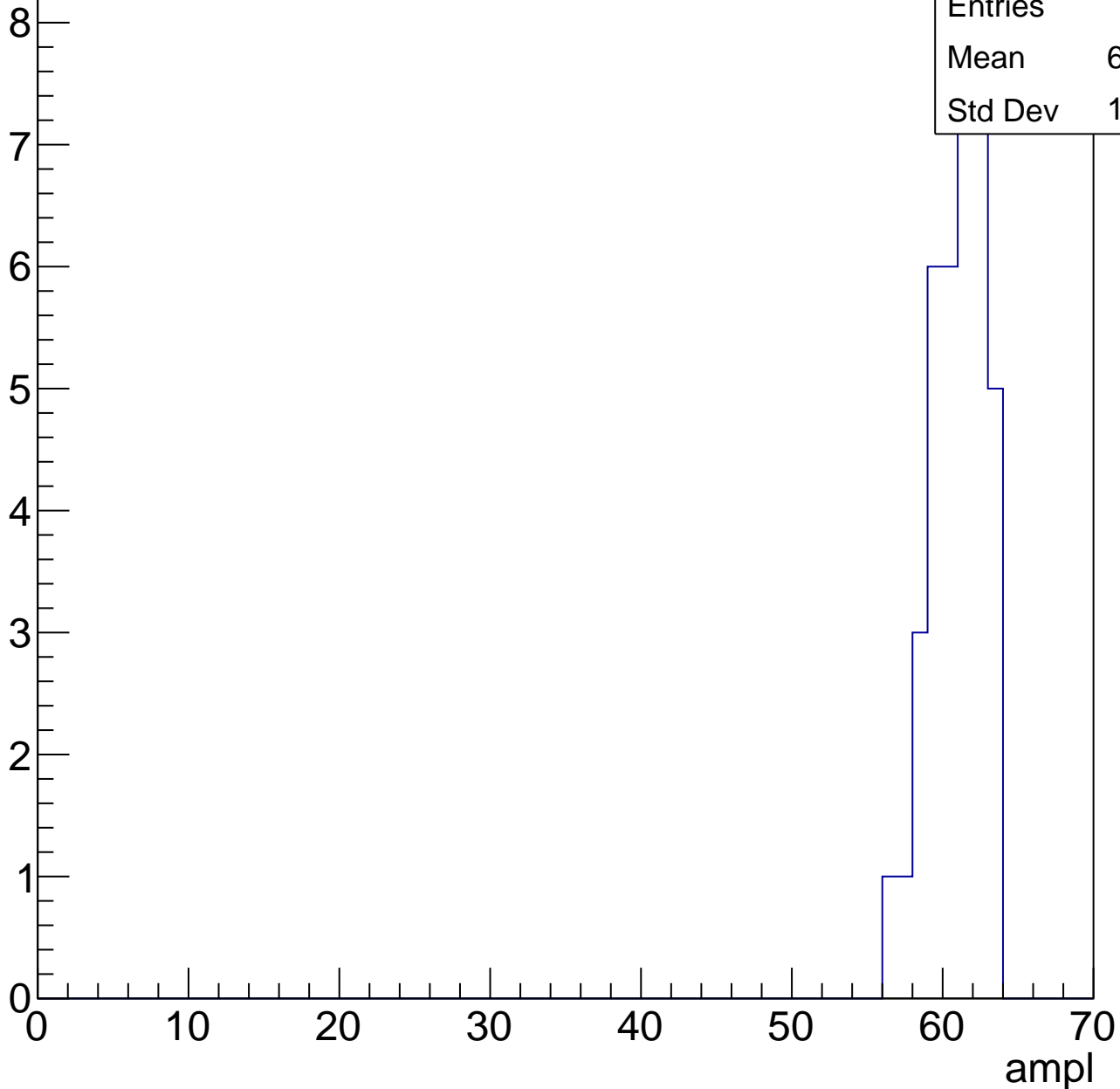


B1L103S, U10-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	60.53
Std Dev	1.758



B1L103S, U10-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch50, adc0

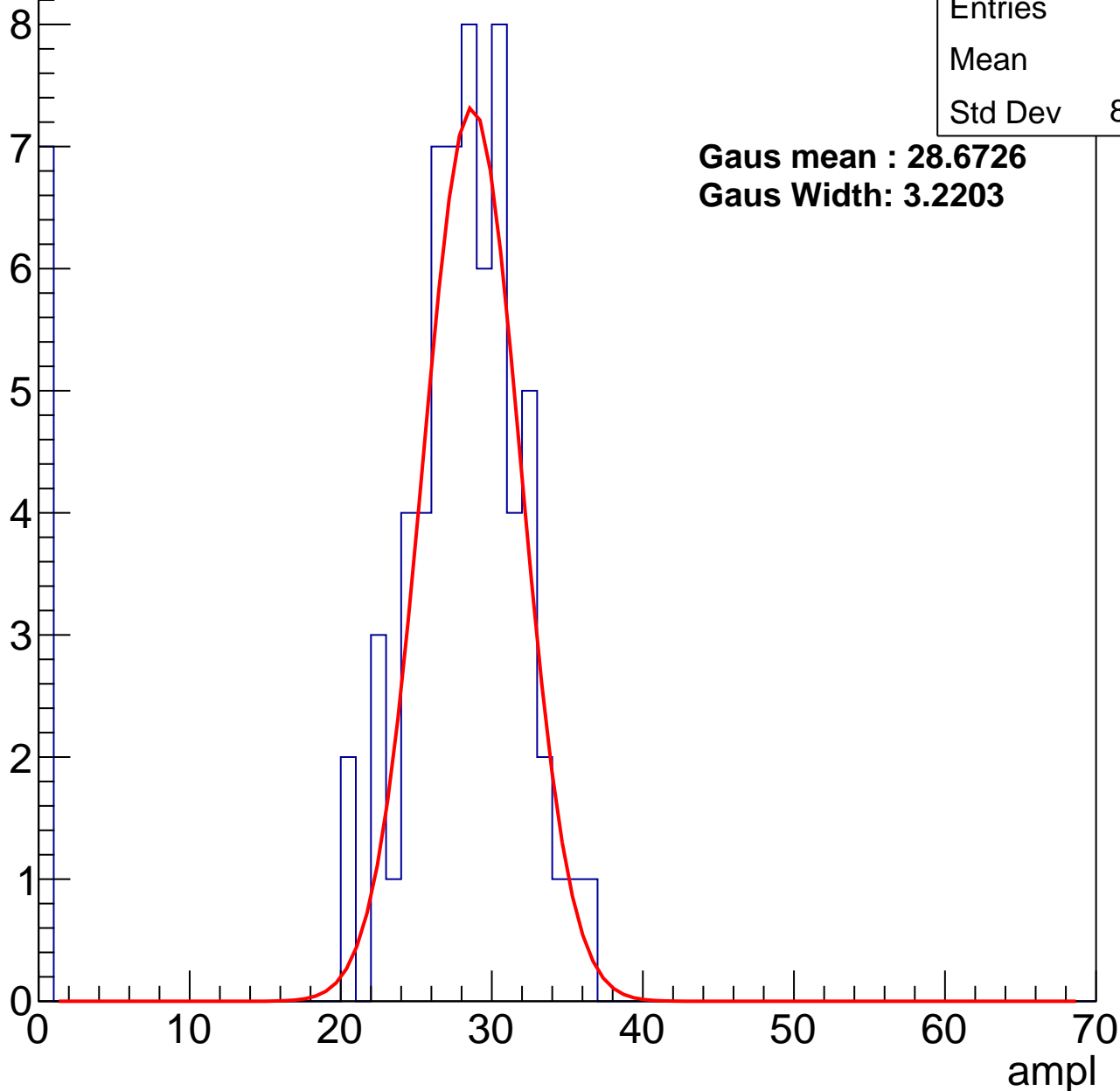
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	25.2
Std Dev	8.944

Gaus mean : 28.6726

Gaus Width: 3.2203



B1L103S, U10-ch50, adc1

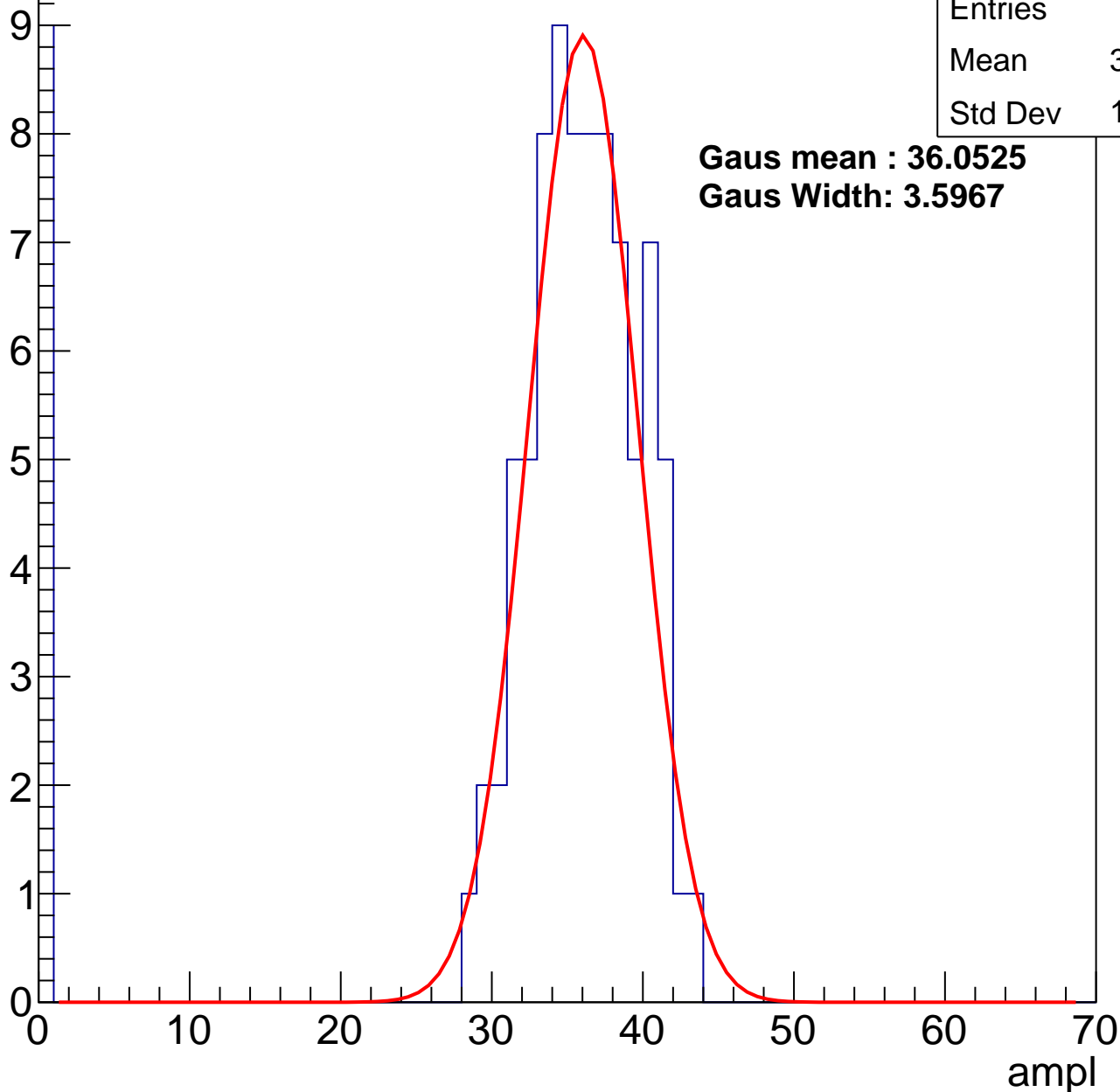
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	91
Mean	32.15
Std Dev	11.13

Gaus mean : 36.0525

Gaus Width: 3.5967



B1L103S, U10-ch50, adc2

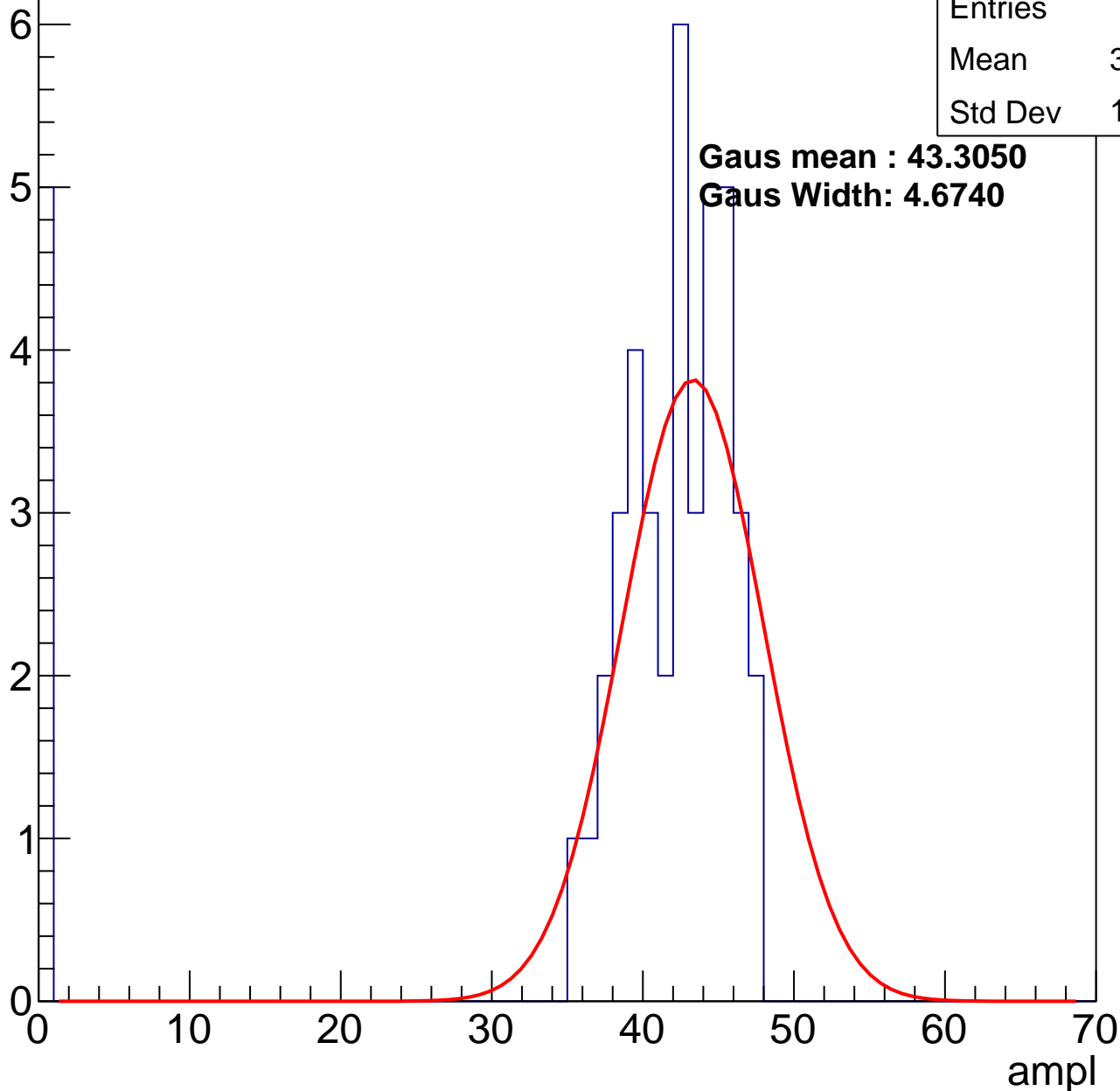
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	37.22
Std Dev	13.49

Gaus mean : 43.3050

Gaus Width: 4.6740

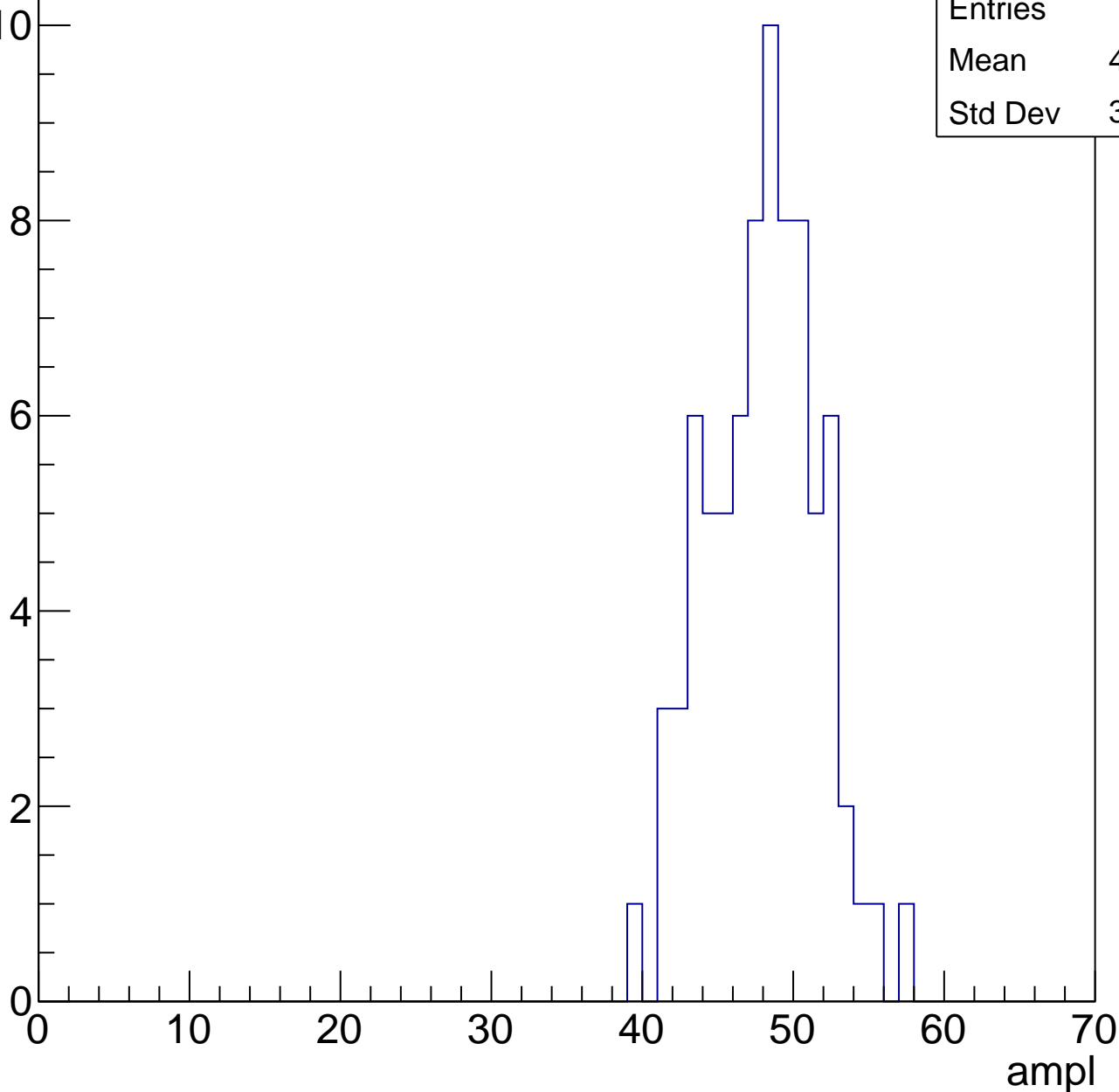


B1L103S, U10-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	47.52
Std Dev	3.596

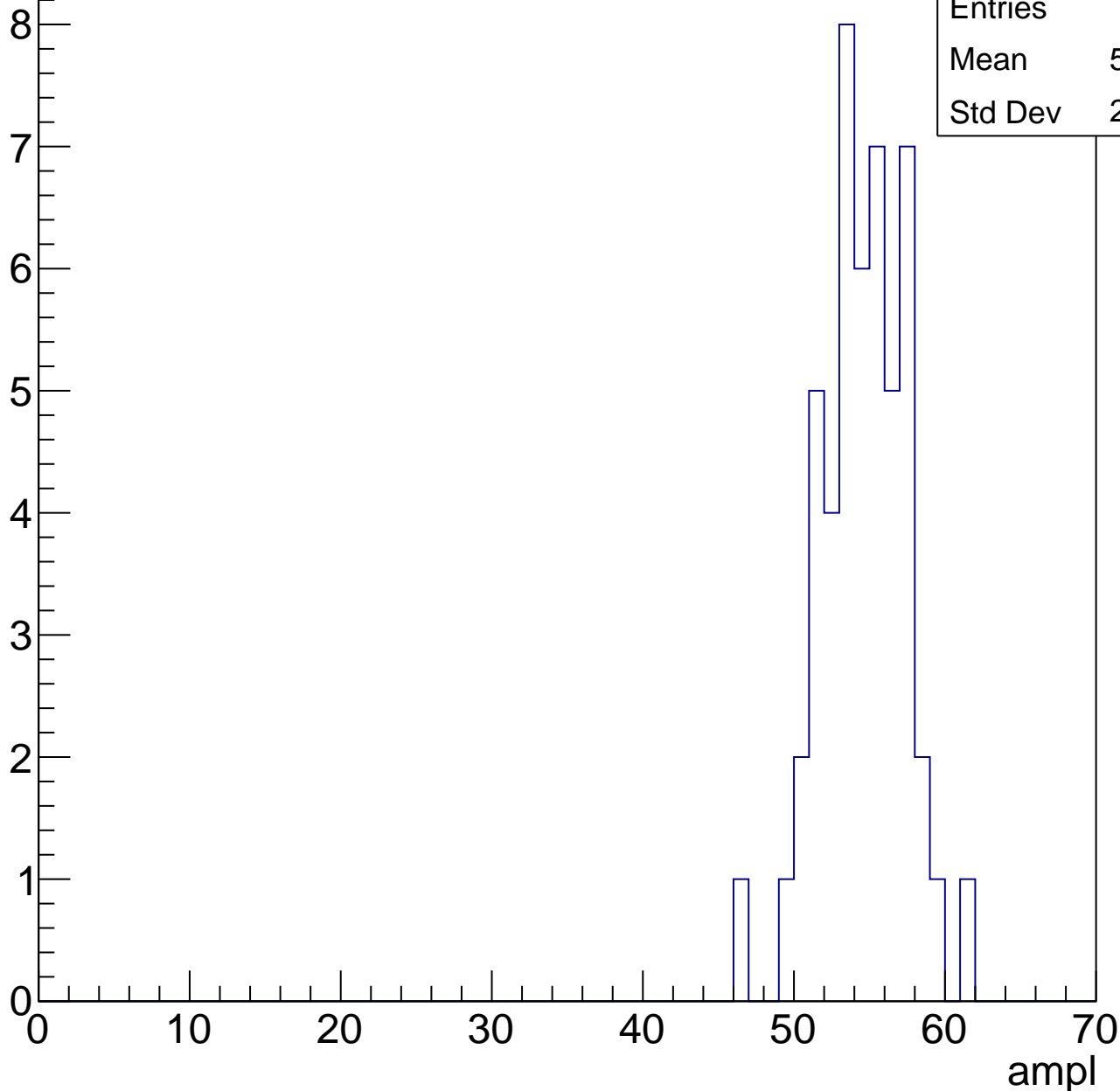


B1L103S, U10-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	54.12
Std Dev	2.776

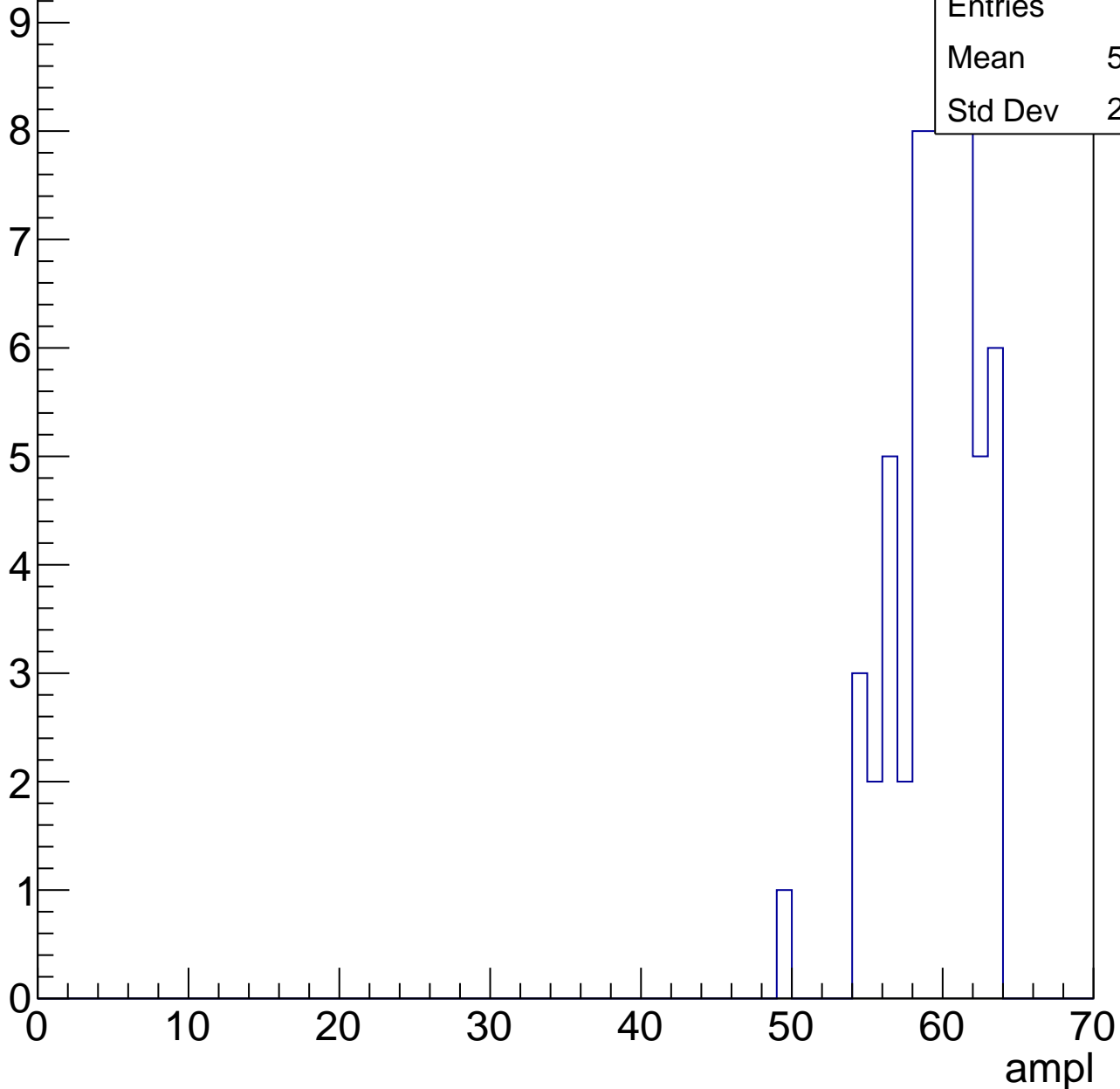


B1L103S, U10-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	59.07
Std Dev	2.809

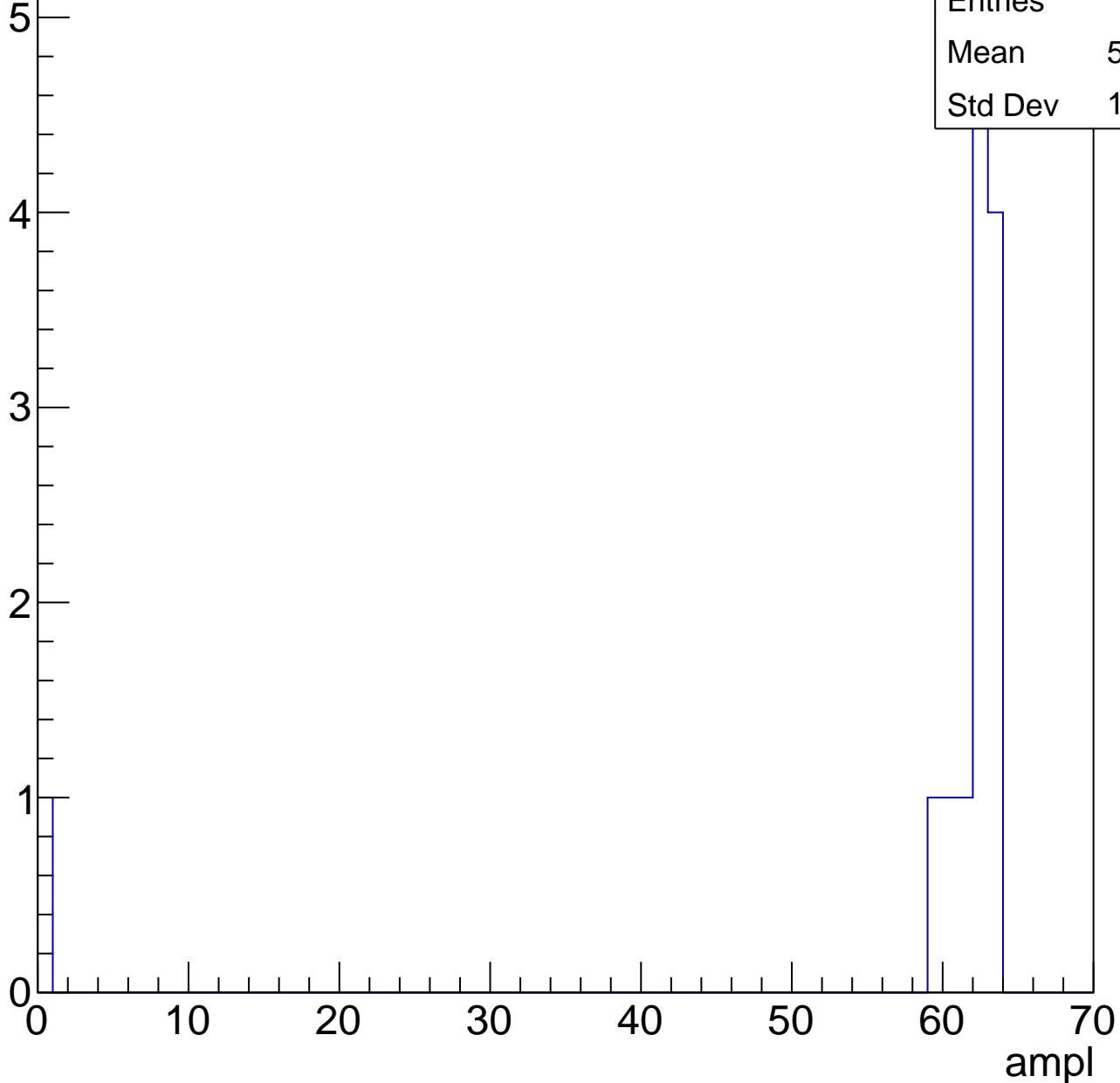


B1L103S, U10-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.52



B1L103S, U10-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch51, adc0

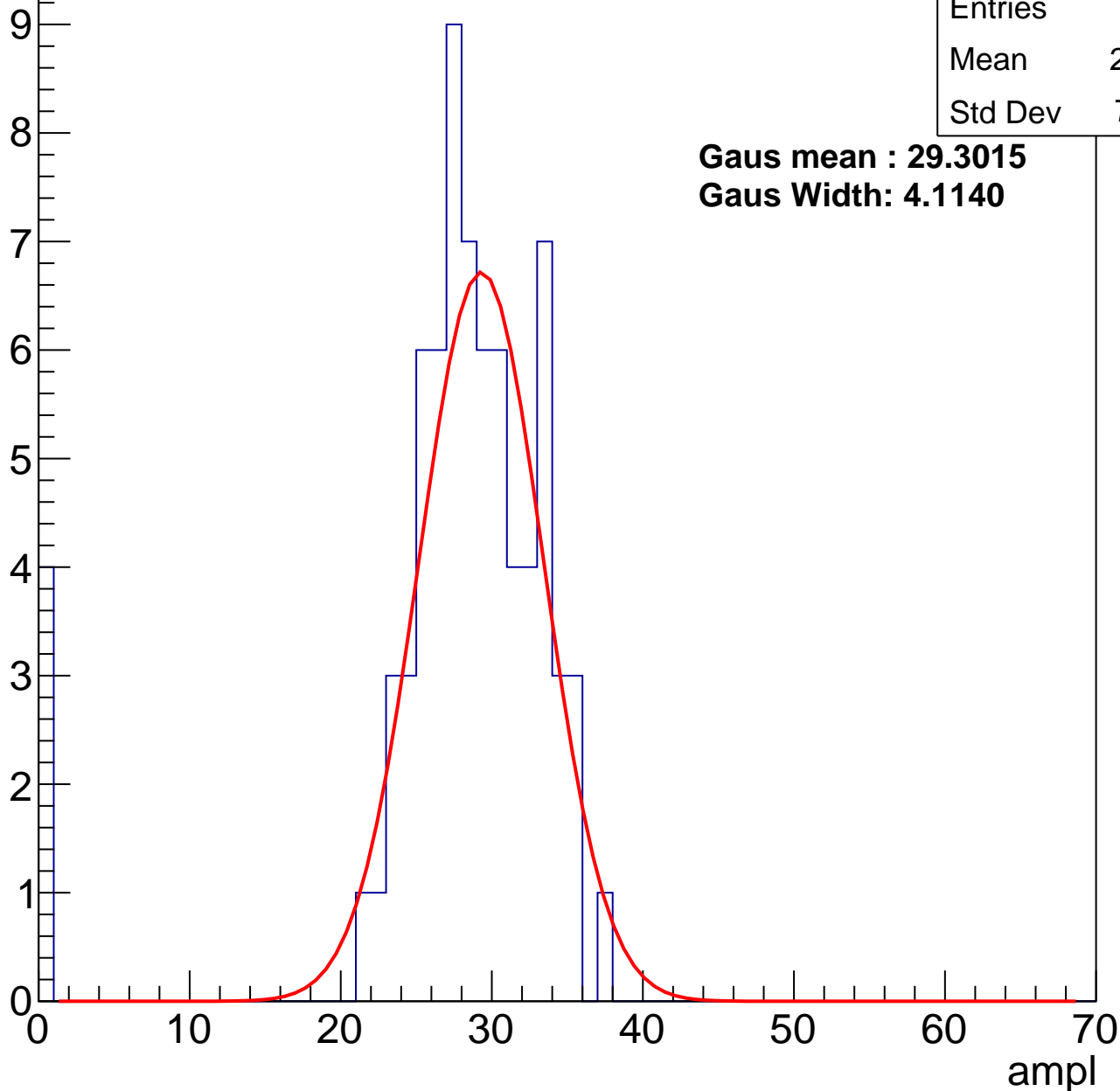
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	27.16
Std Dev	7.371

Gaus mean : 29.3015

Gaus Width: 4.1140



B1L103S, U10-ch51, adc1

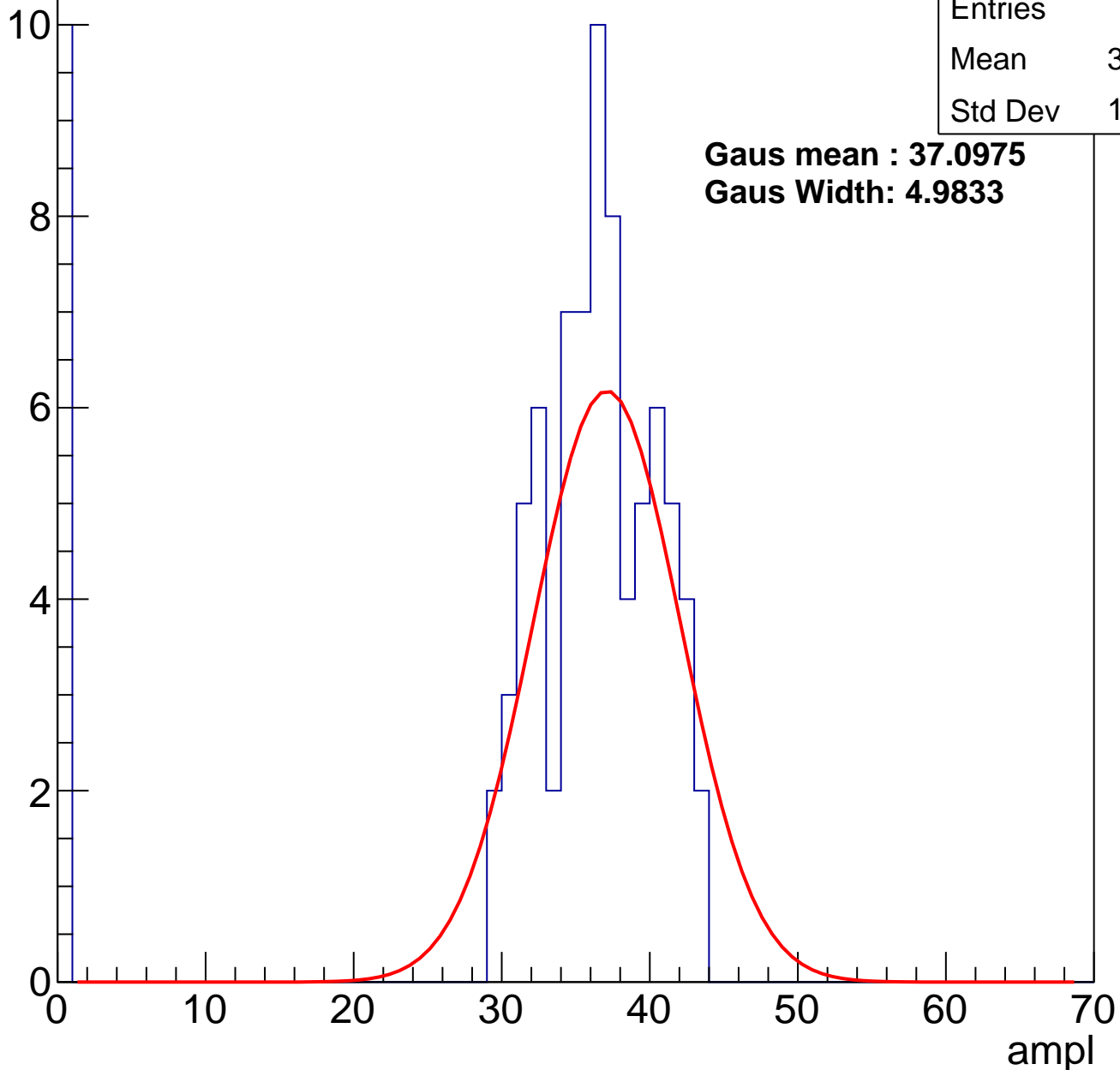
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	31.93
Std Dev	12.08

Gaus mean : 37.0975

Gaus Width: 4.9833

Entry



B1L103S, U10-ch51, adc2

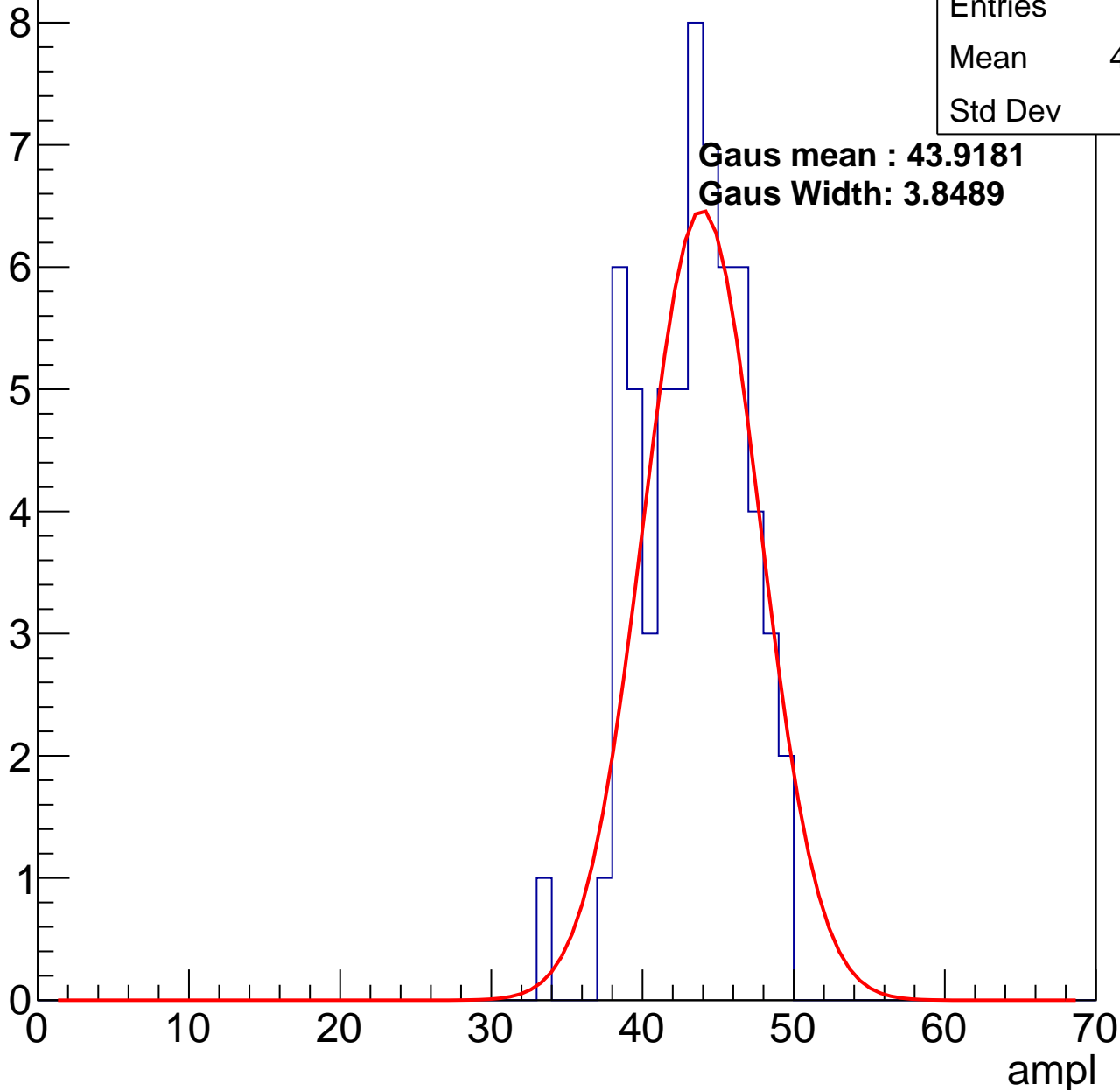
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	42.84
Std Dev	3.39

Gaus mean : 43.9181

Gaus Width: 3.8489

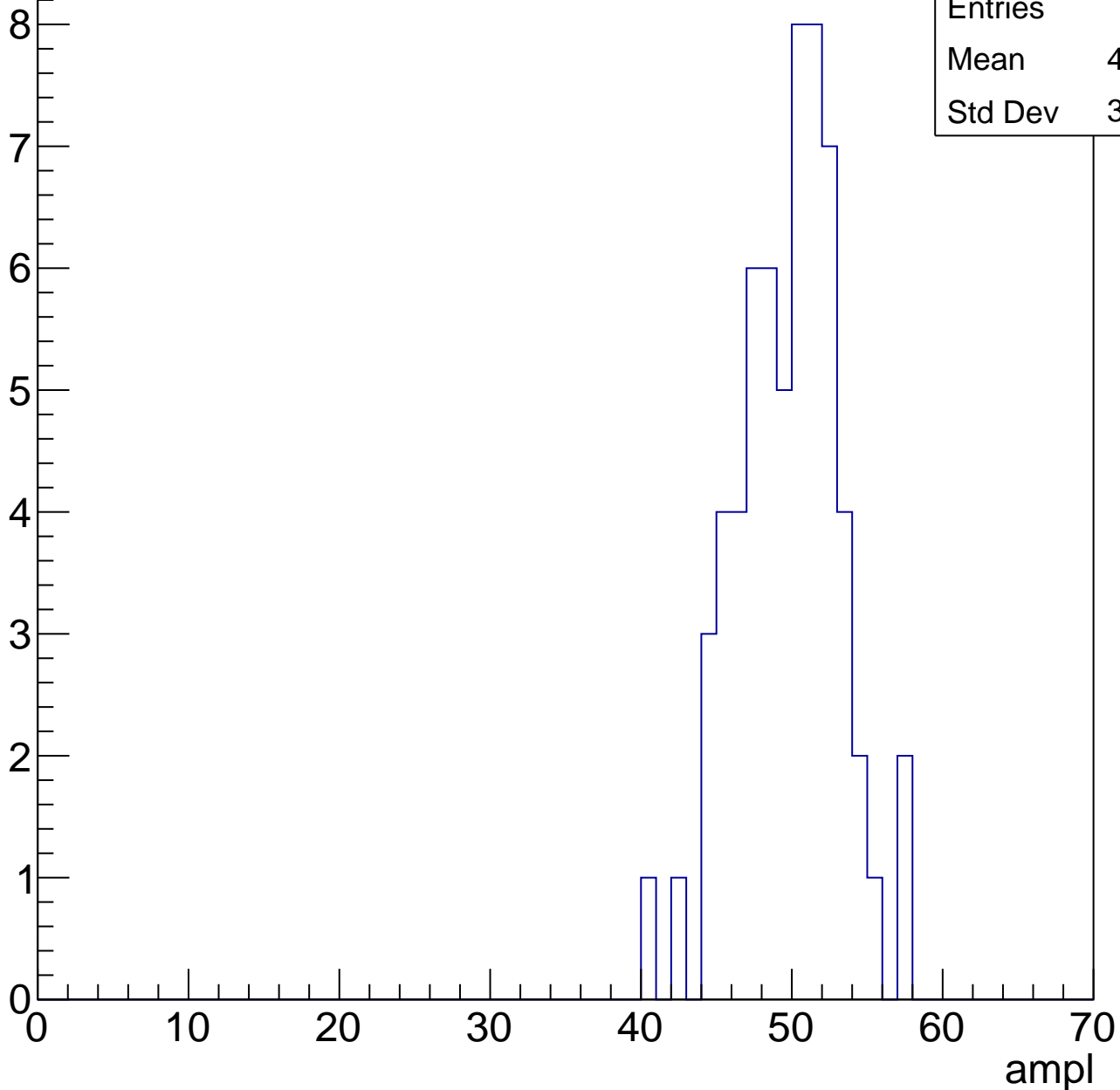


B1L103S, U10-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

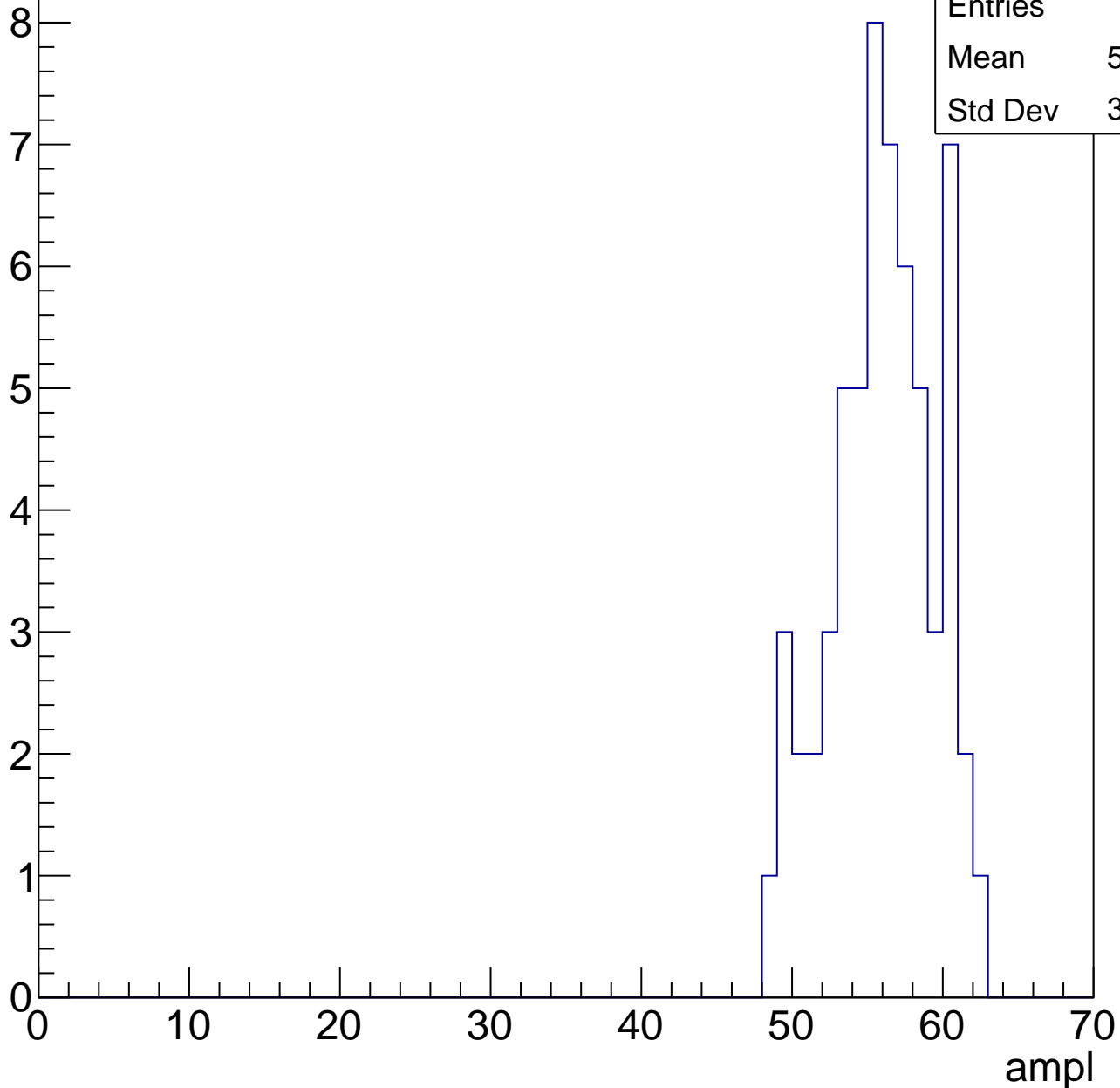
Entries	62
Mean	49.26
Std Dev	3.388



B1L103S, U10-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

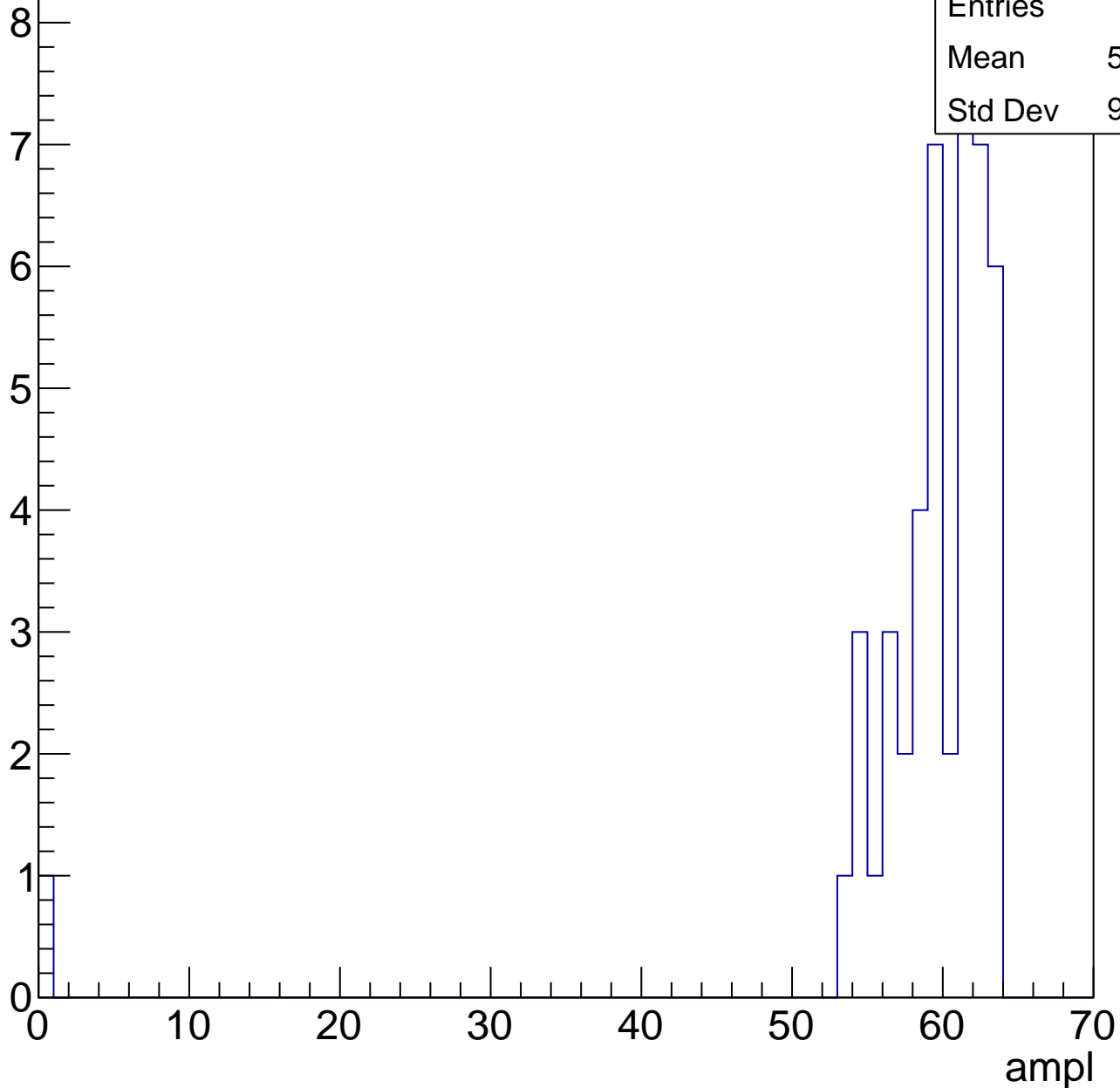


B1L103S, U10-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

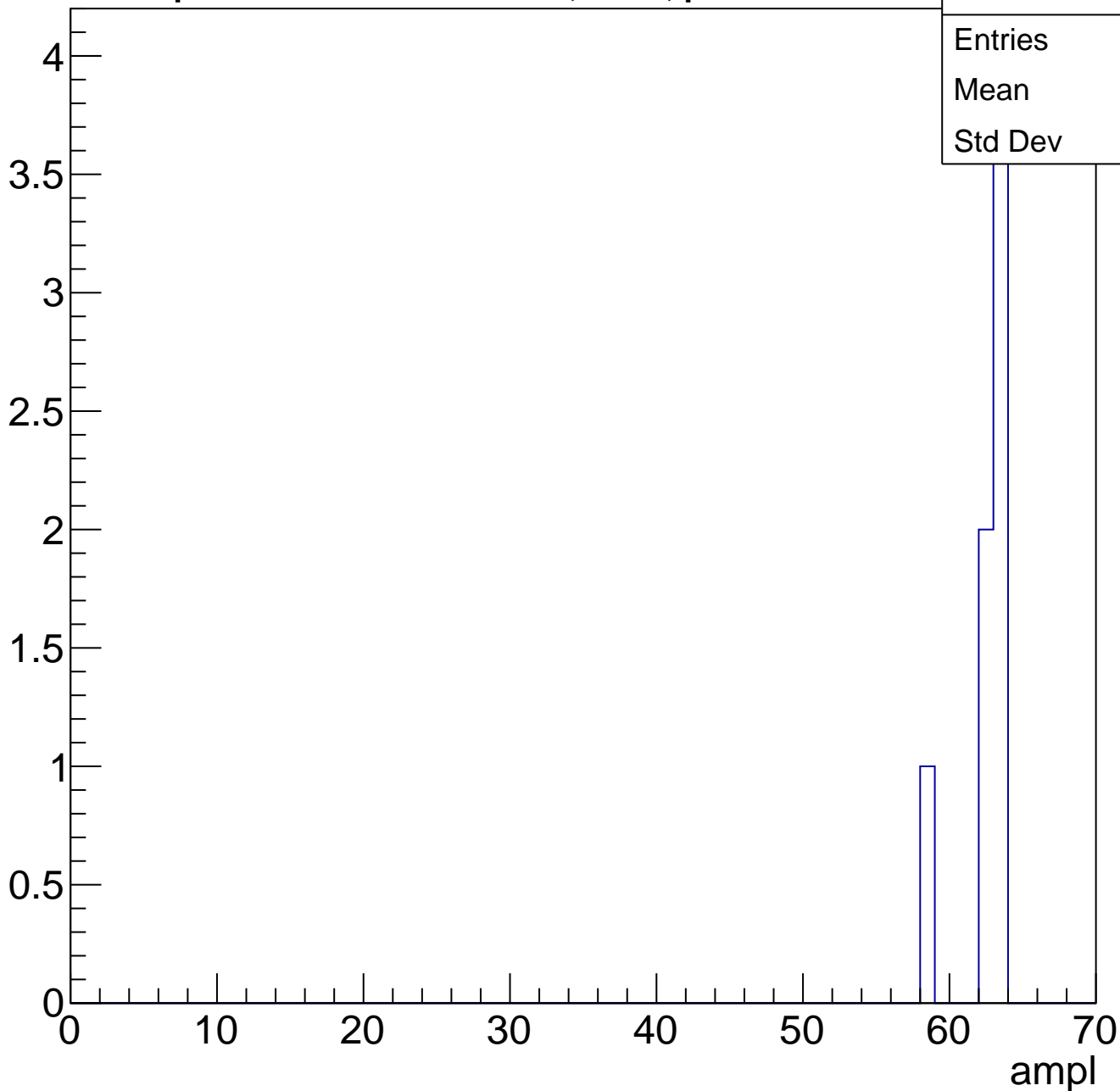
Entries	45
Mean	58.16
Std Dev	9.199



B1L103S, U10-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

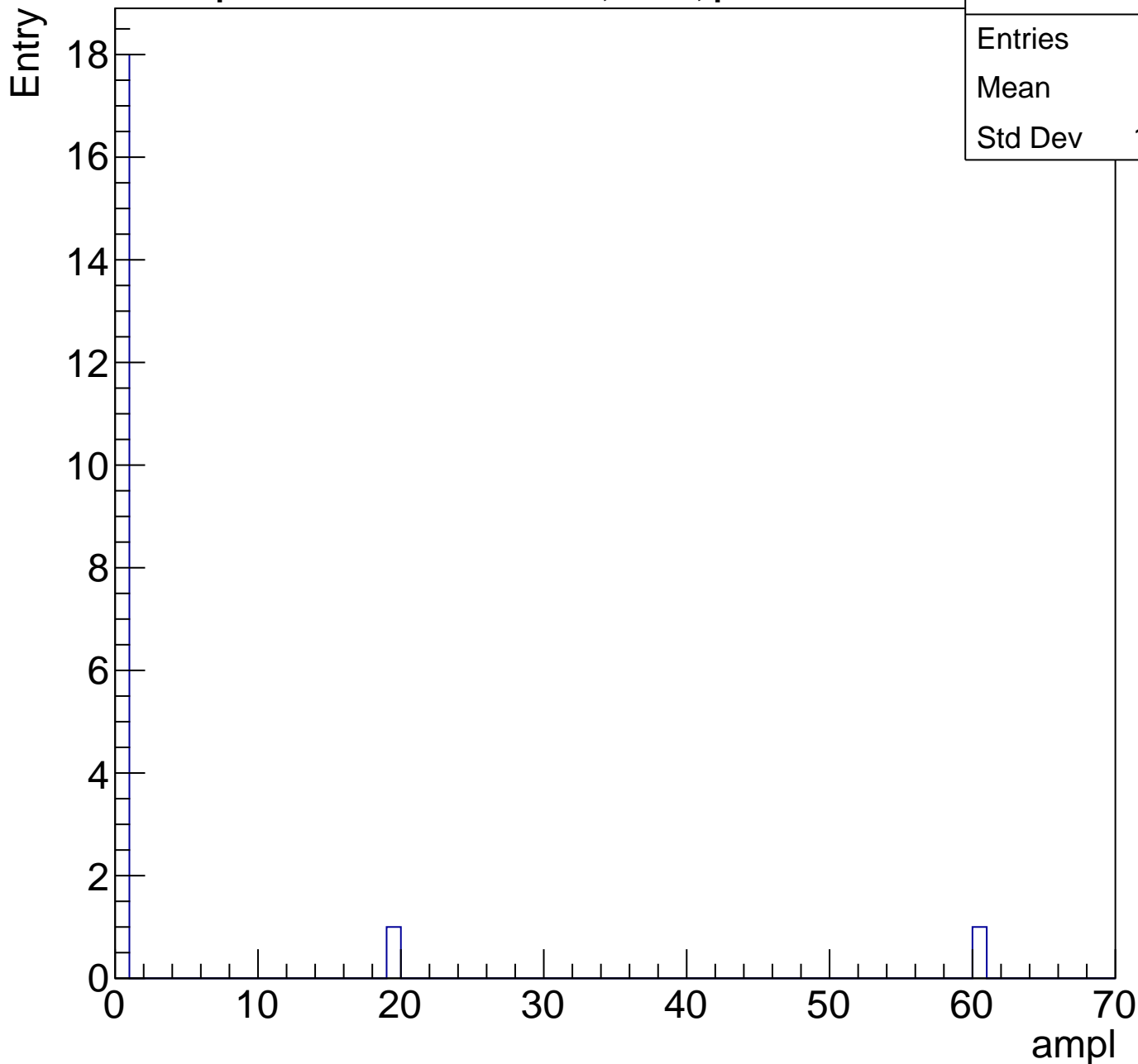
Entry



B1L103S, U10-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.95
Std Dev	13.51



B1L103S, U10-ch52, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	26.06
Std Dev	6.709

Gaus mean : 27.5427

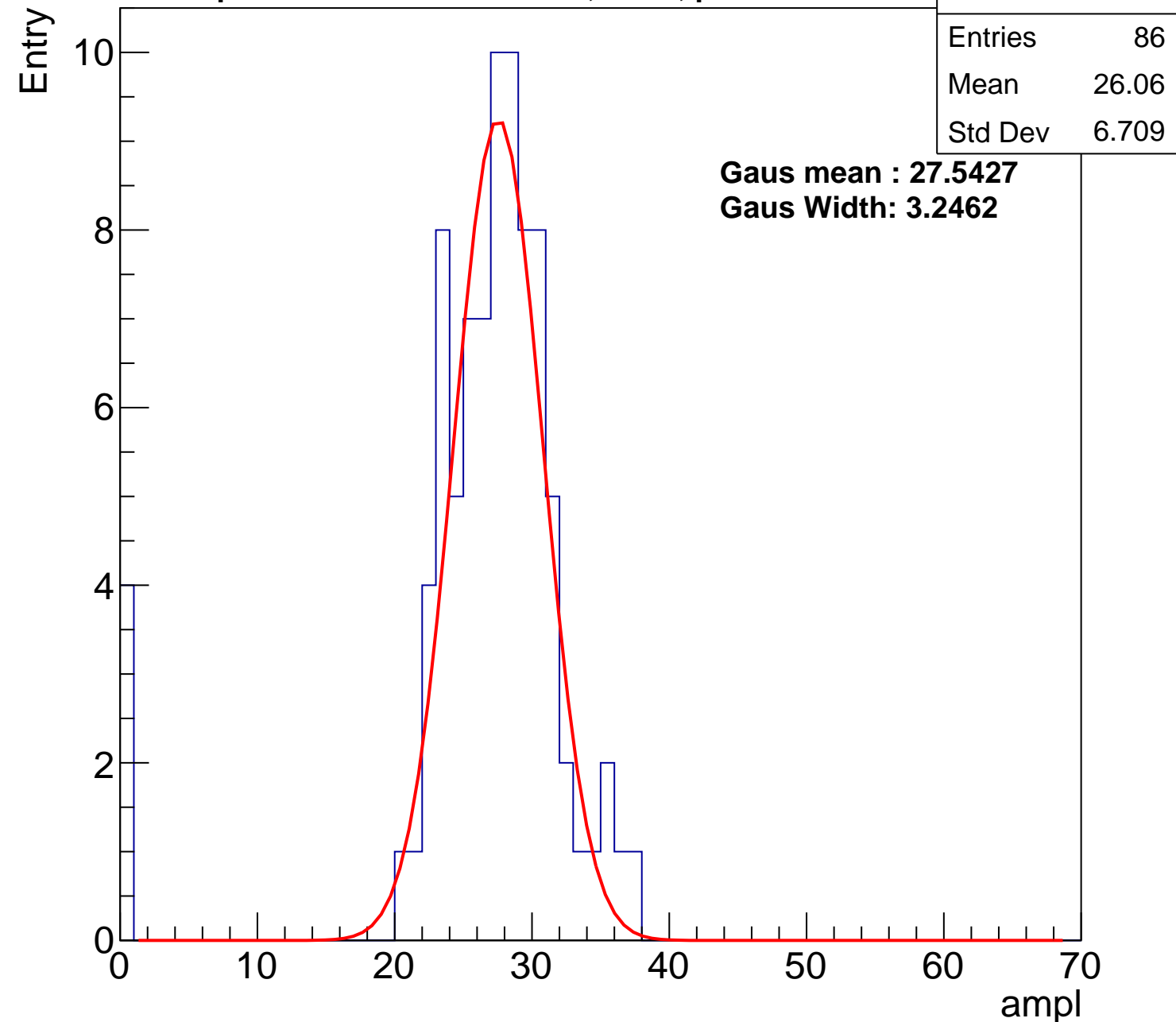
Gaus Width: 3.2462

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch52, adc1

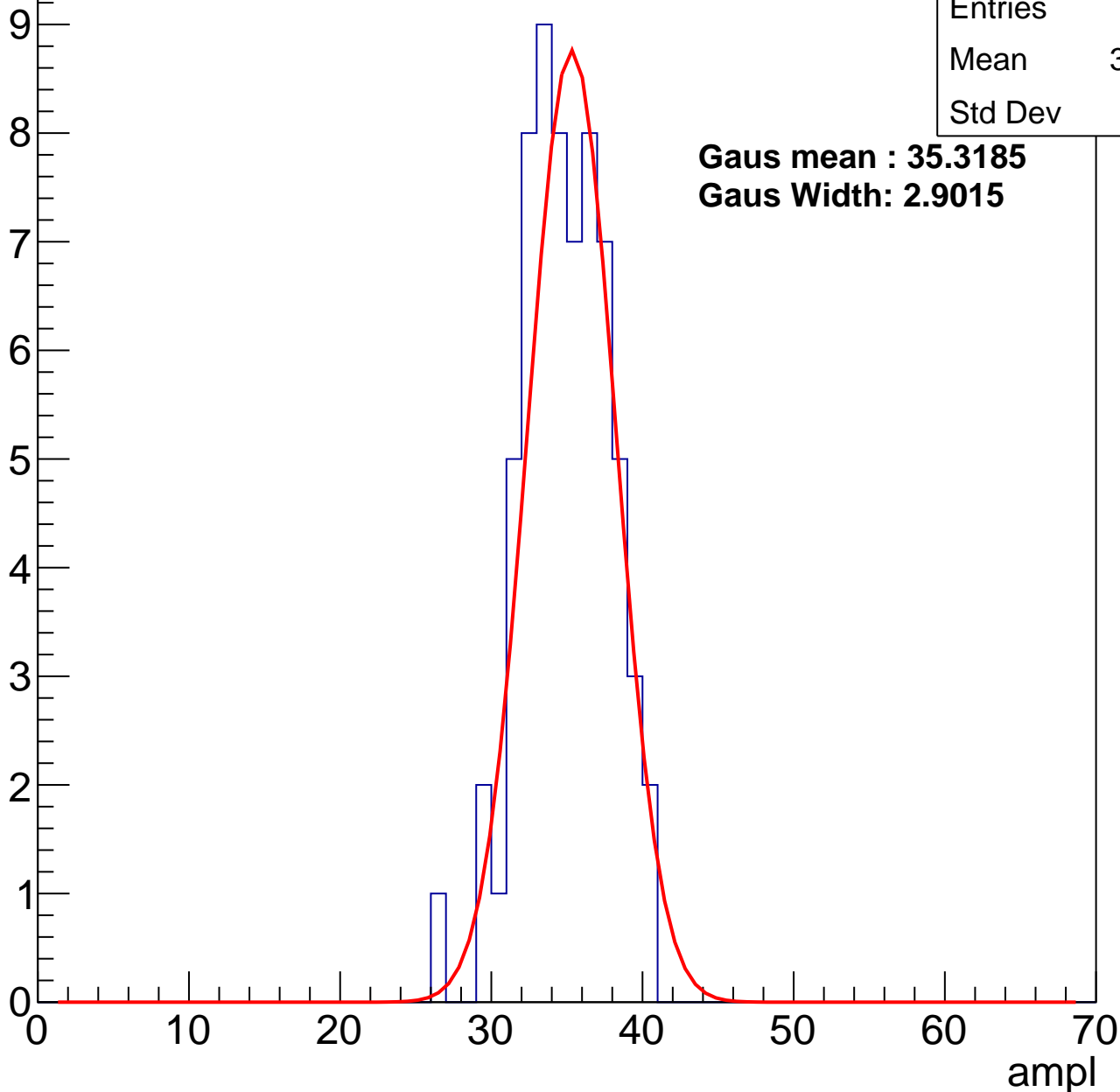
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.44
Std Dev	2.84

Gaus mean : 35.3185

Gaus Width: 2.9015



B1L103S, U10-ch52, adc2

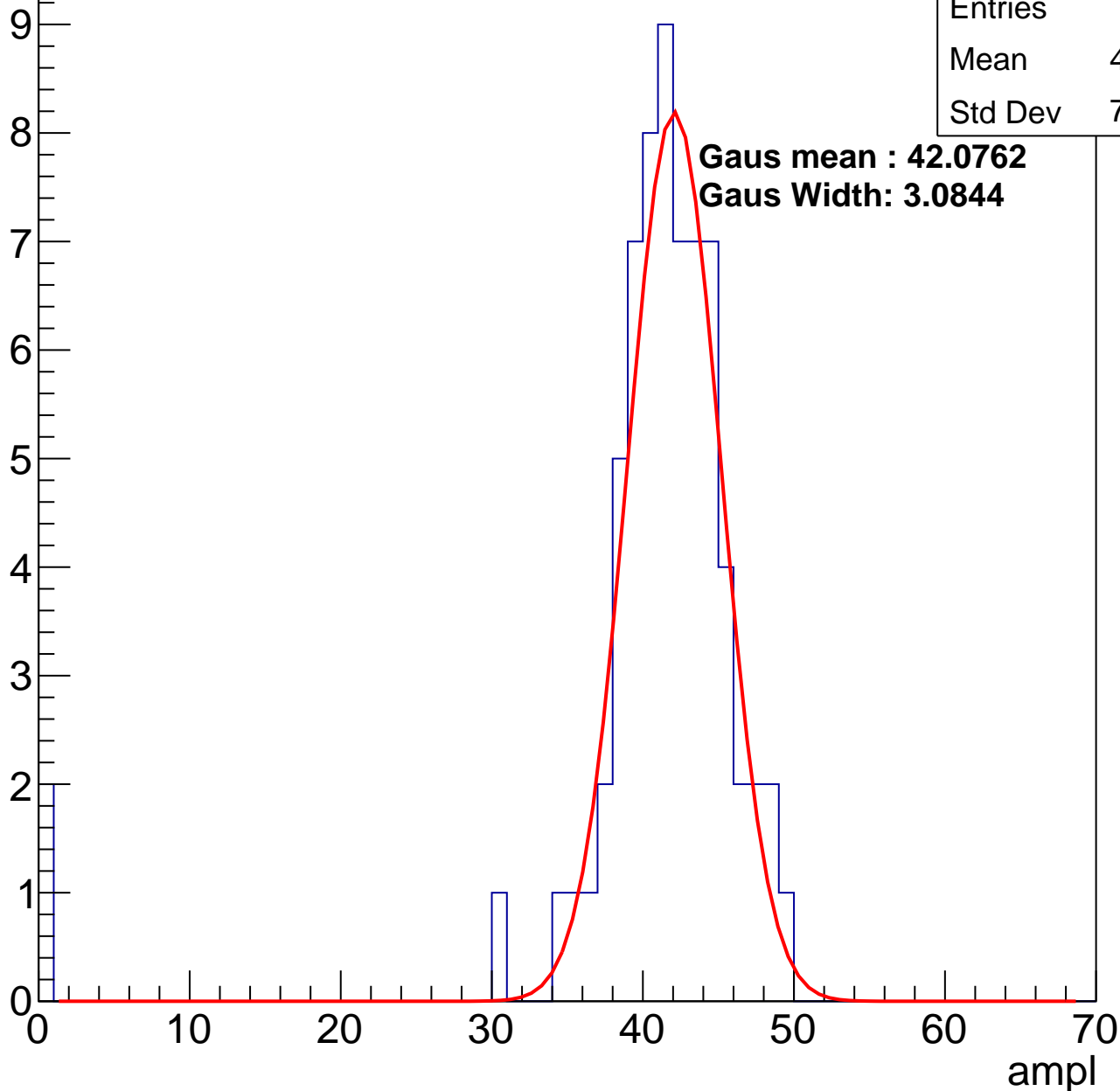
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.22
Std Dev	7.718

Gaus mean : 42.0762

Gaus Width: 3.0844

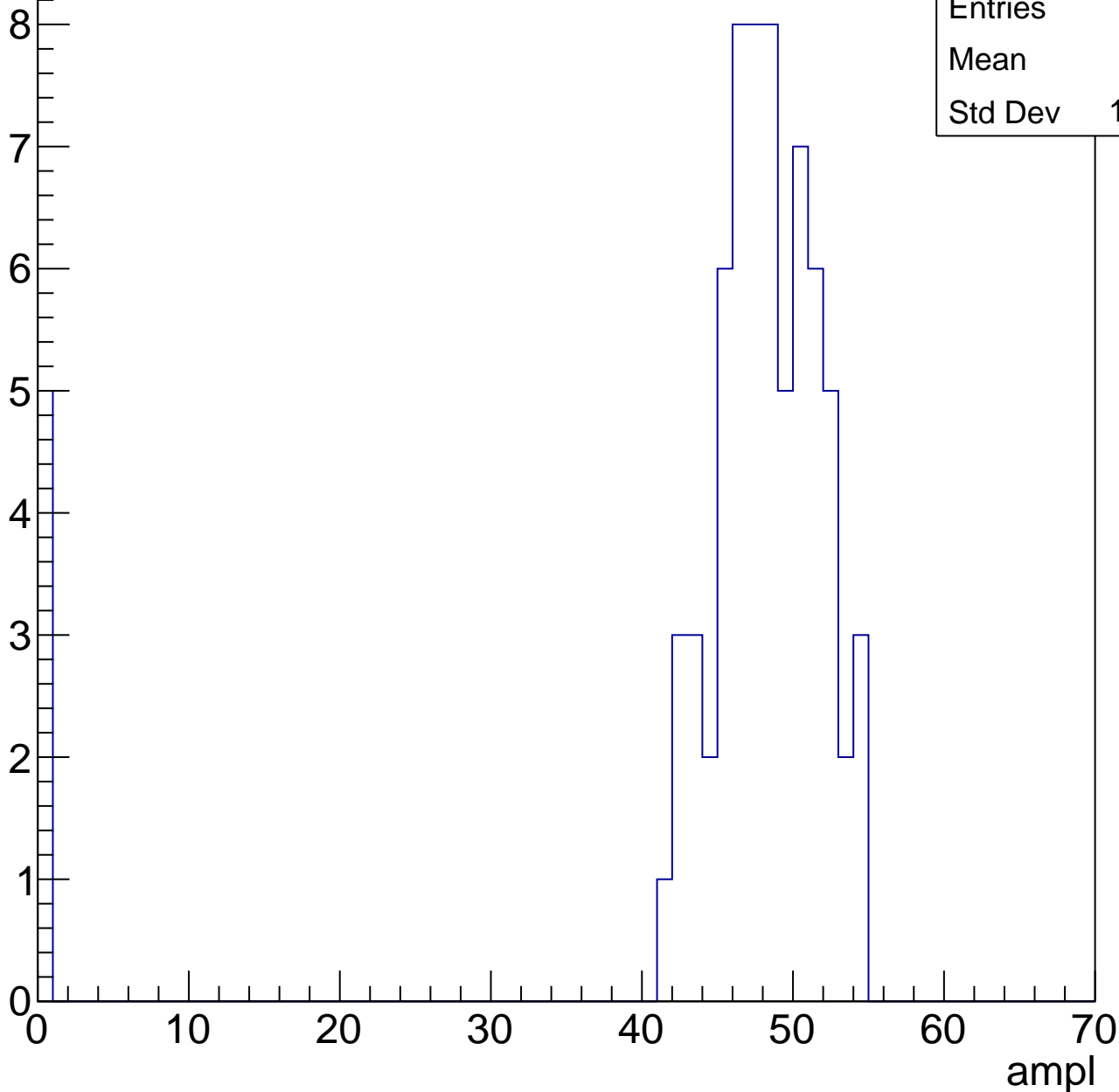


B1L103S, U10-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	44.6
Std Dev	12.57

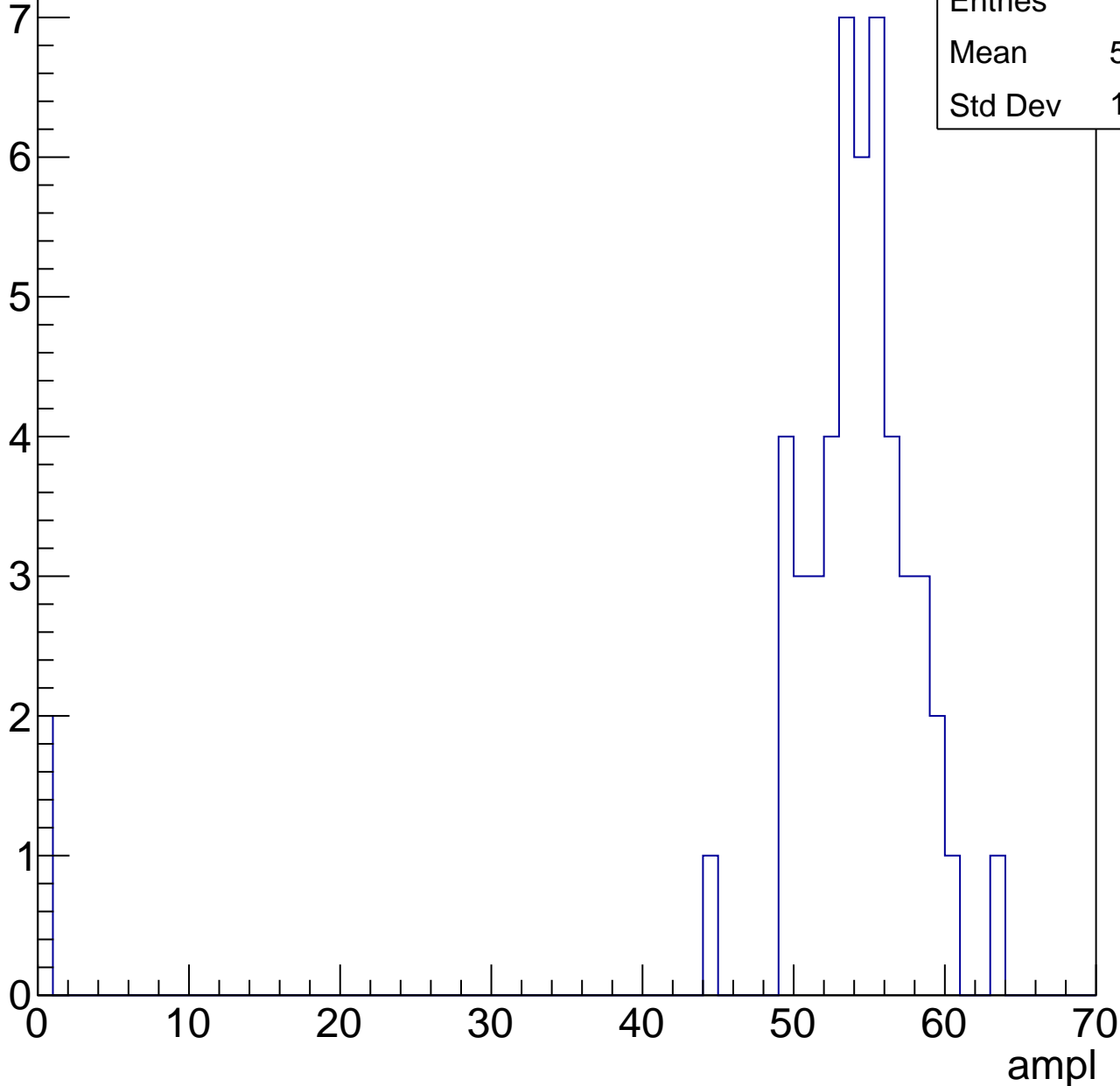


B1L103S, U10-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.78
Std Dev	10.98

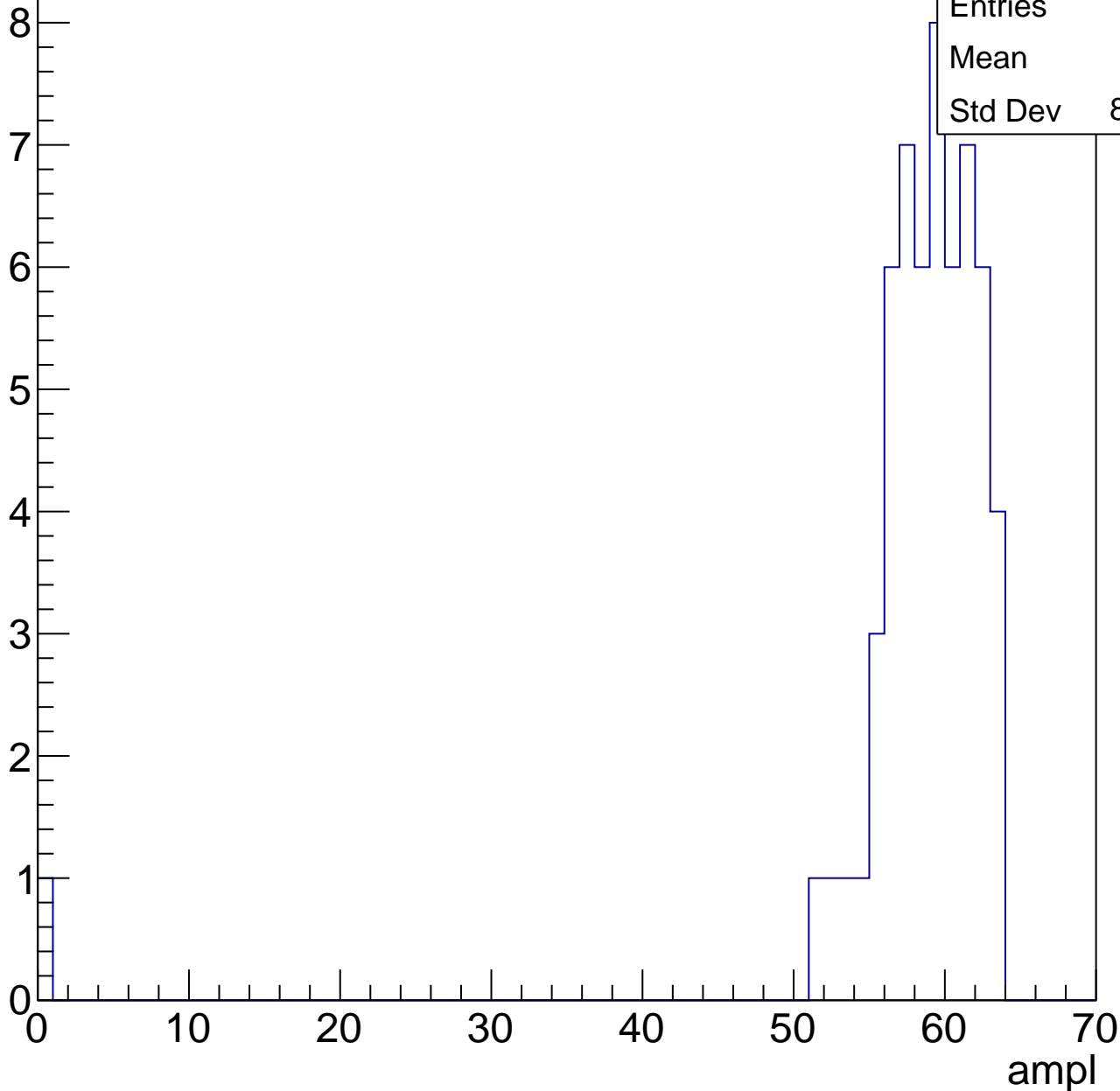


B1L103S, U10-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.6
Std Dev	8.126

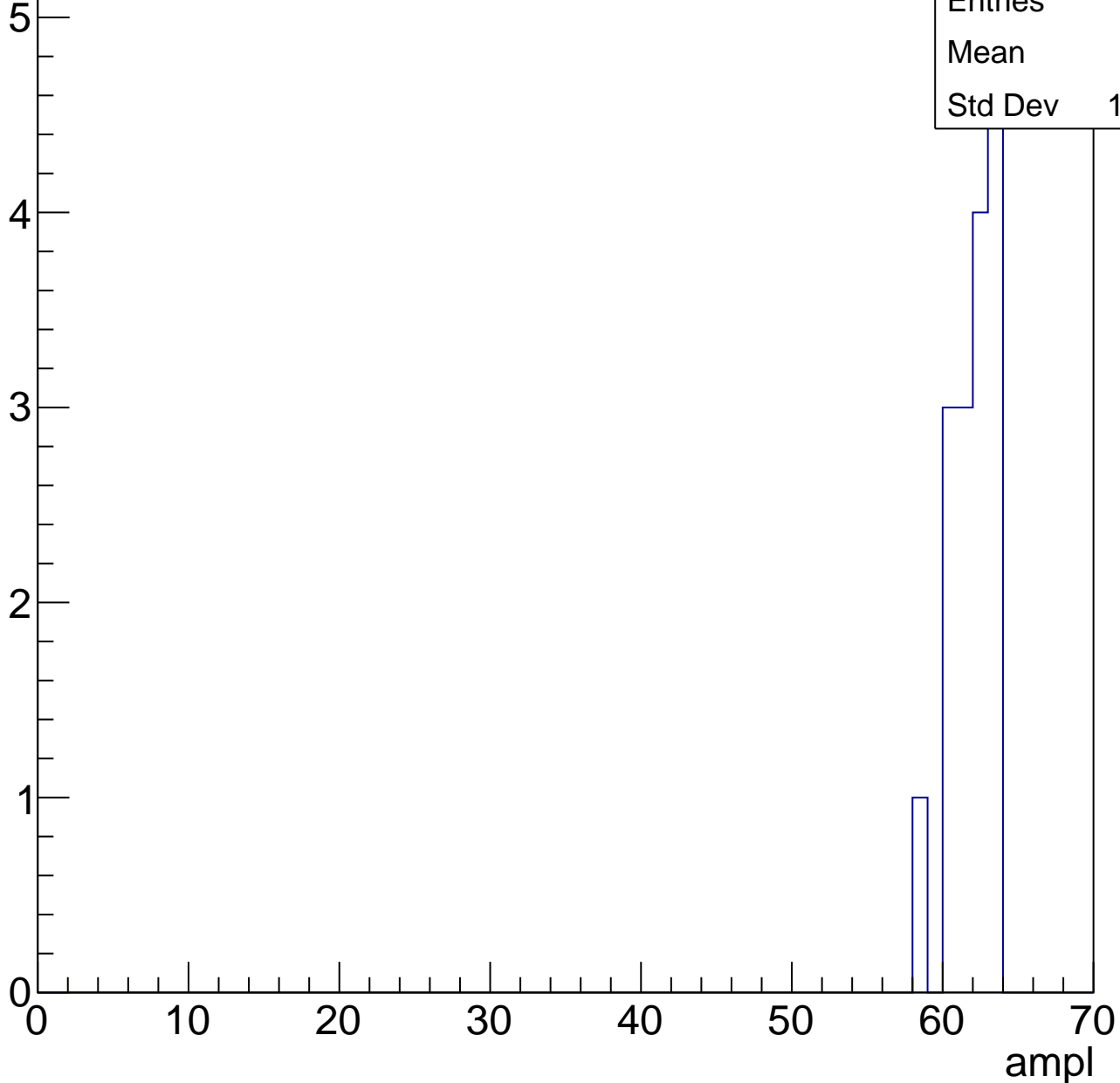


B1L103S, U10-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.5
Std Dev	1.414

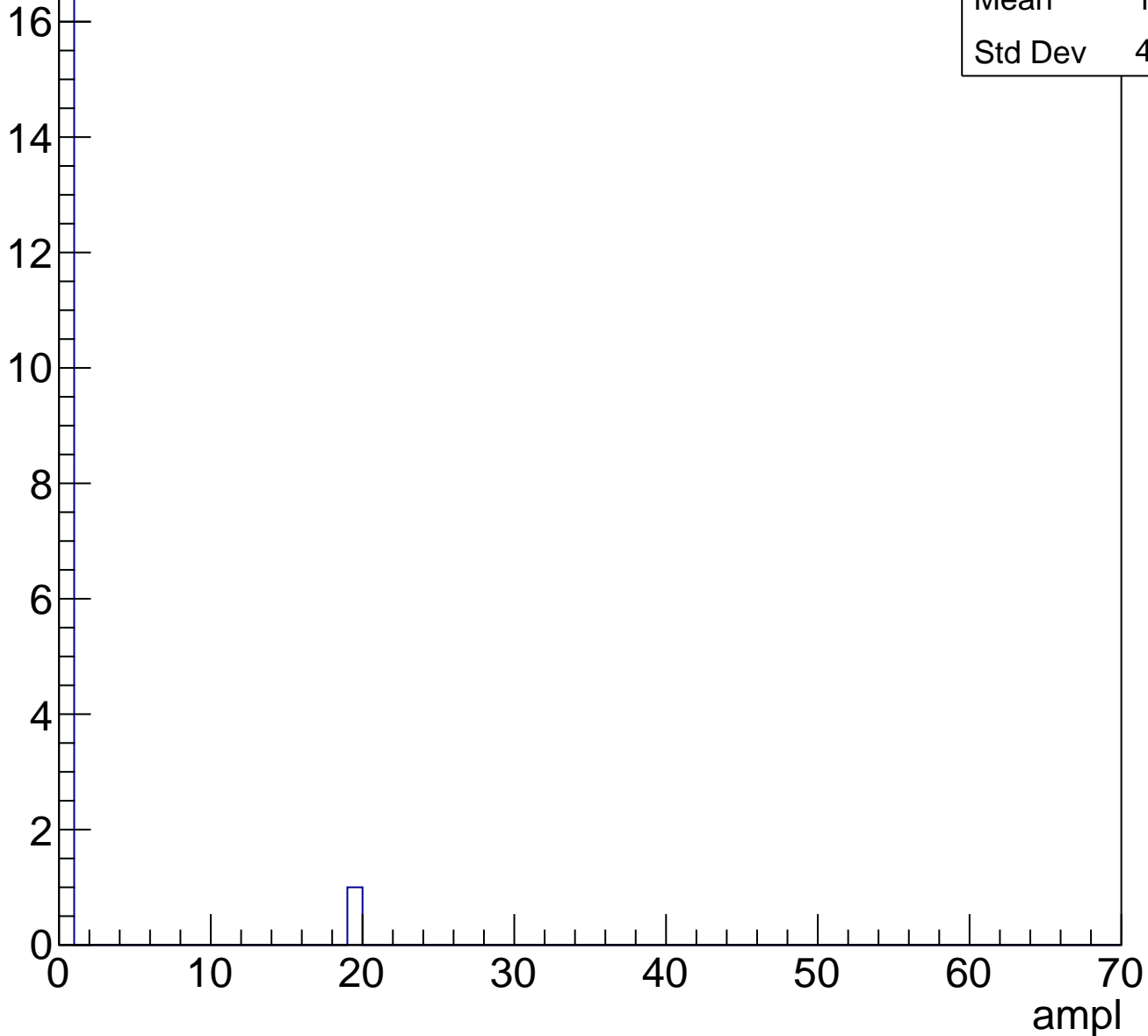


B1L103S, U10-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.056
Std Dev	4.352

Entry



B1L103S, U10-ch53, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	22.14
Std Dev	11.92

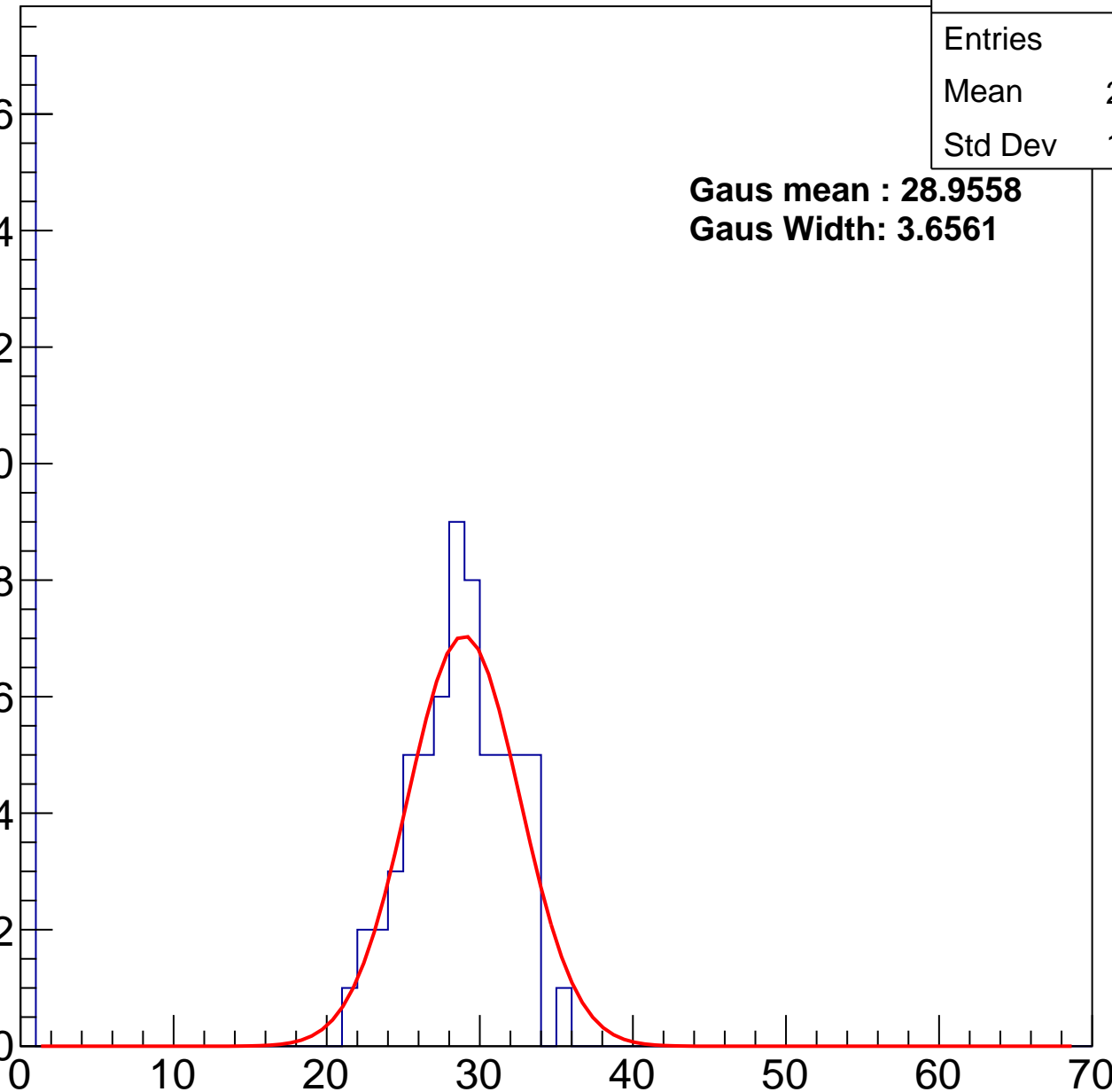
Gaus mean : 28.9558

Gaus Width: 3.6561

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U10-ch53, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	30.86
Std Dev	12.82

Gaus mean : 36.0231

Gaus Width: 3.1151

Entry

12

10

8

6

4

2

0

0

10

20

30

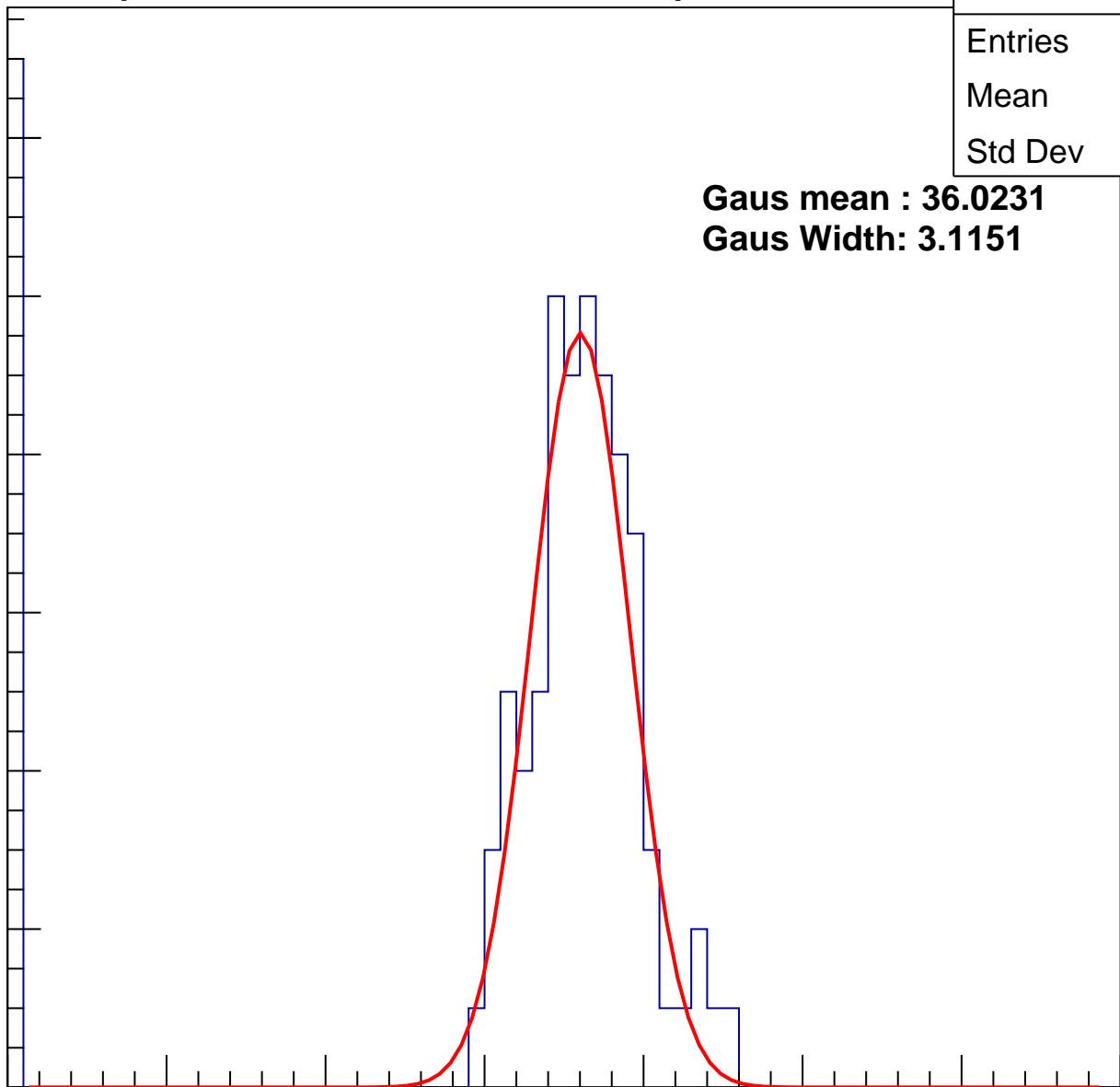
40

50

60

70

ampl



B1L103S, U10-ch53, adc2

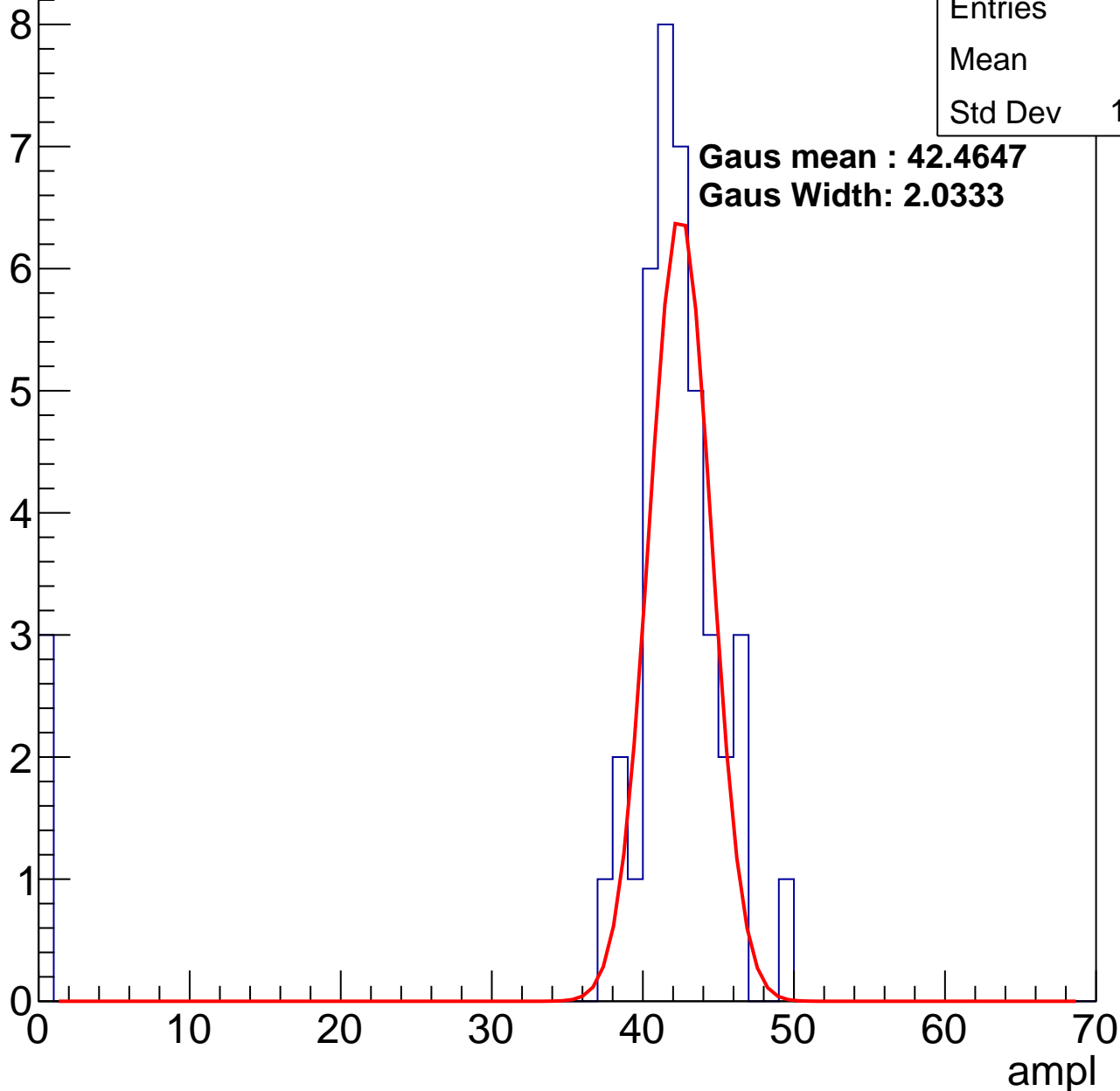
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	39
Std Dev	11.07

Gaus mean : 42.4647

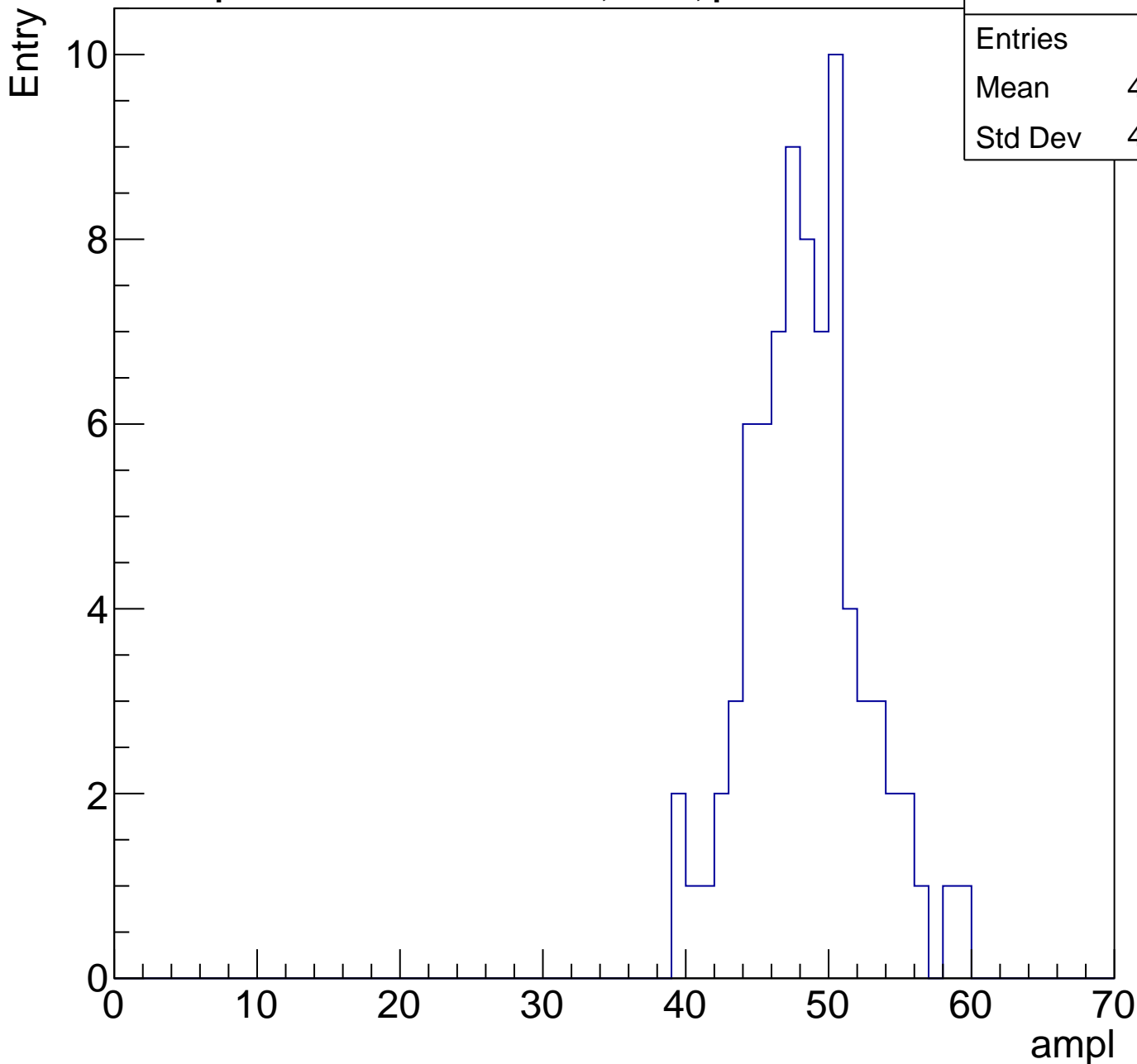
Gaus Width: 2.0333



B1L103S, U10-ch53, adc3

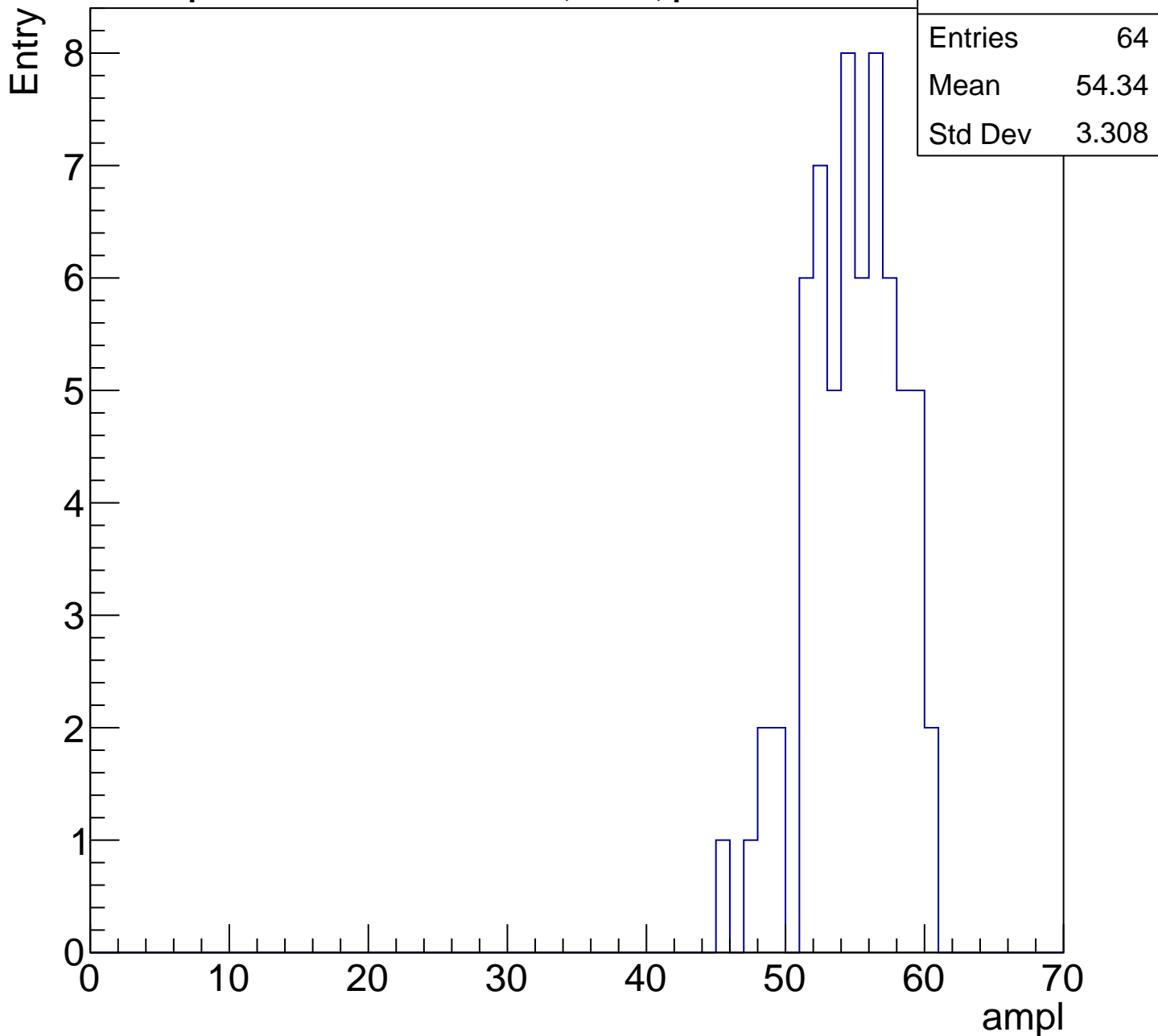
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	47.95
Std Dev	4.009



B1L103S, U10-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

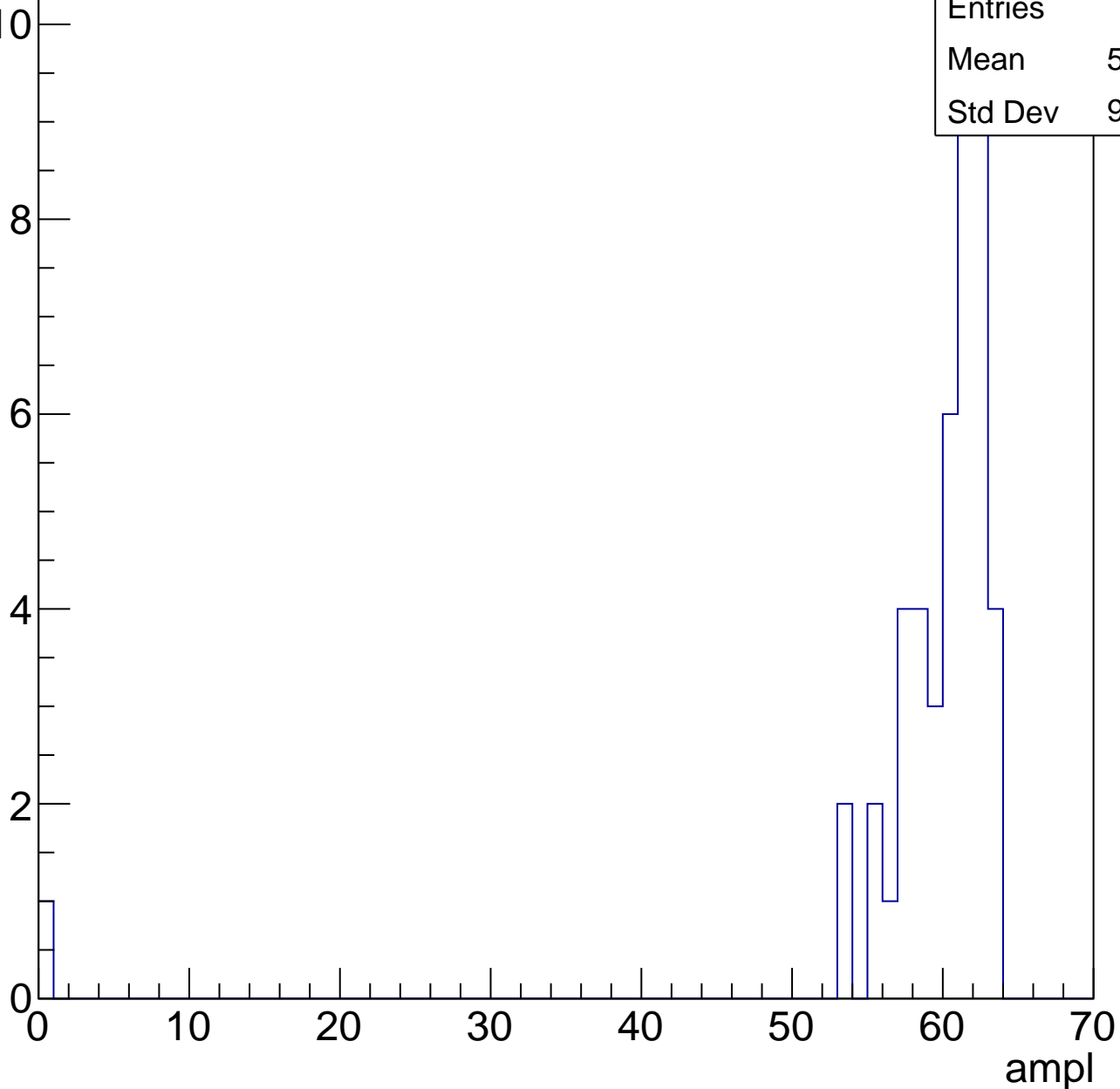


B1L103S, U10-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.46
Std Dev	9.079

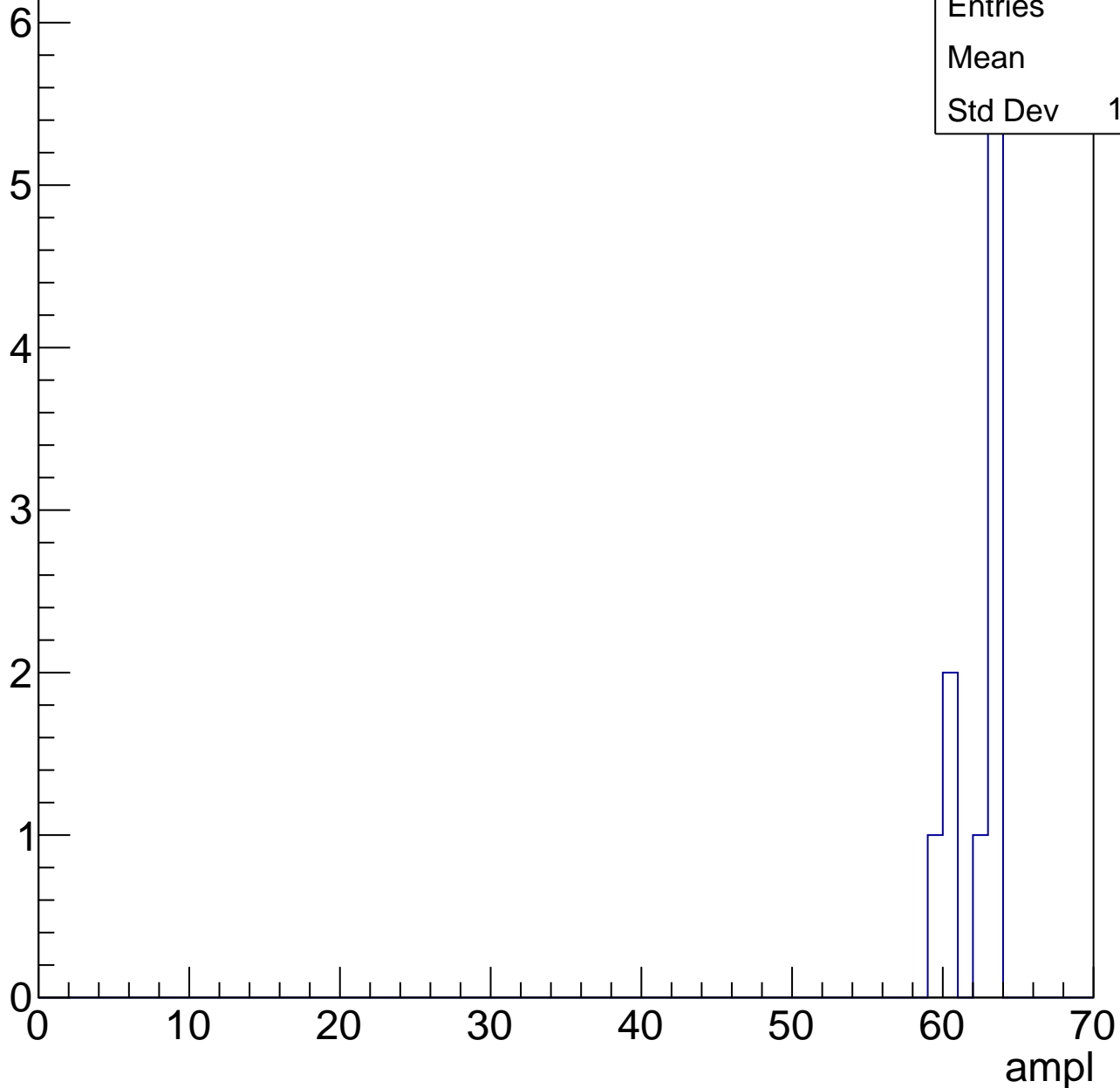


B1L103S, U10-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	61.9
Std Dev	1.513



B1L103S, U10-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

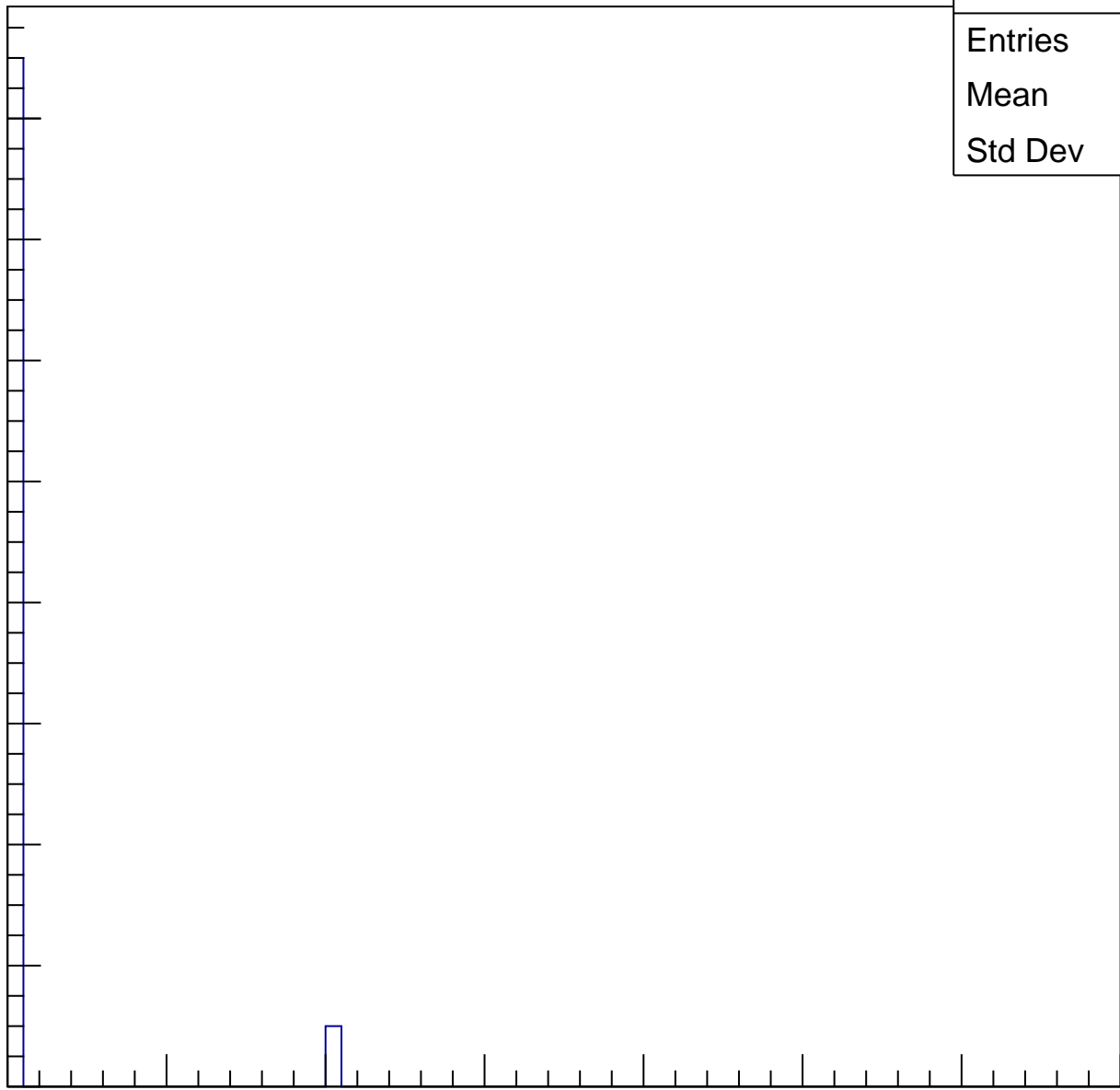
Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch54, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	24.52
Std Dev	11.89

Gaus mean : 29.9646

Gaus Width: 2.9367

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U10-ch54, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	32.11
Std Dev	12.97

Gaus mean : 37.7556

Gaus Width: 3.5073

Entry

10

8

6

4

2

0

0

10

20

30

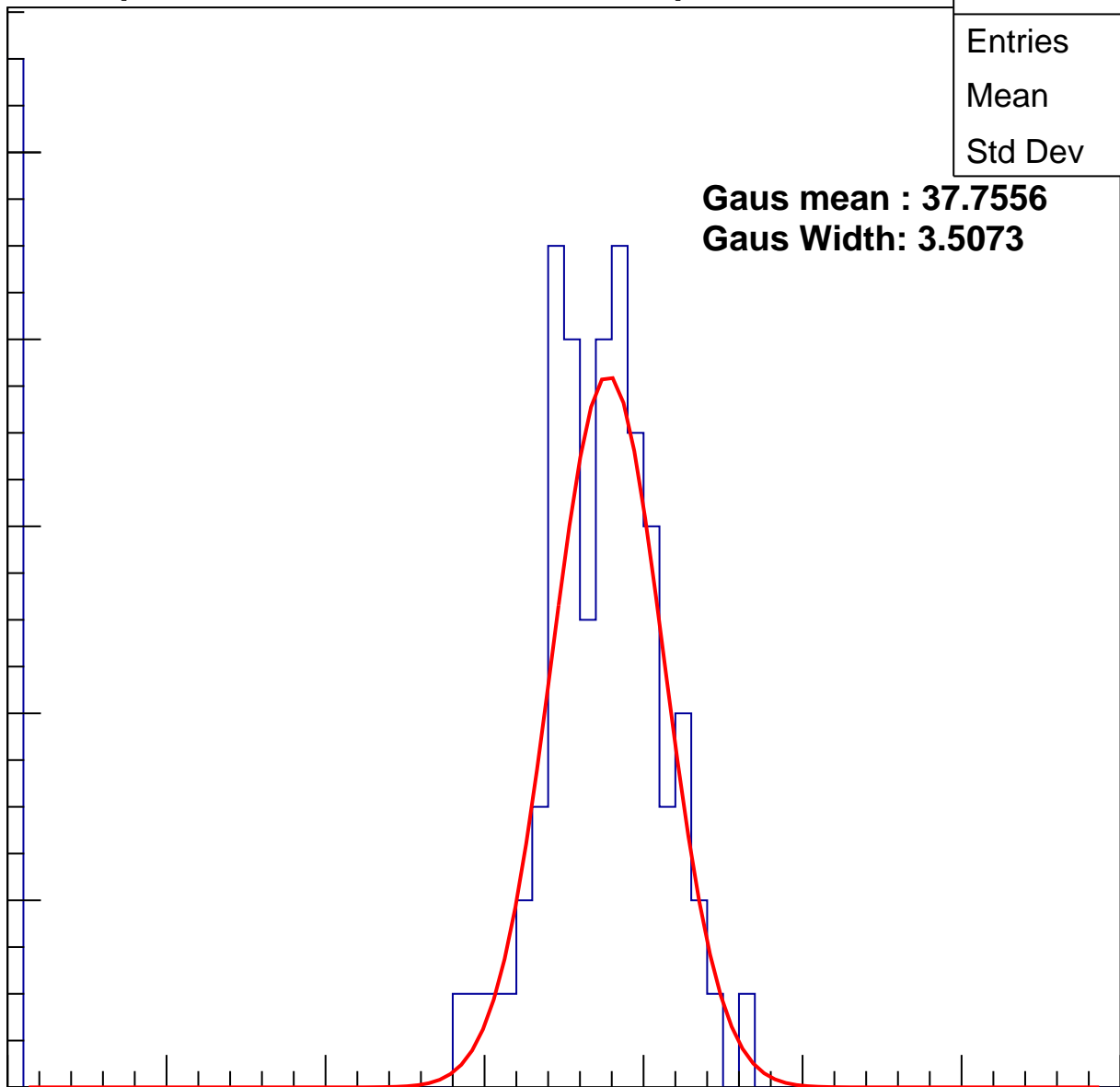
40

50

60

70

ampl



B1L103S, U10-ch54, adc2

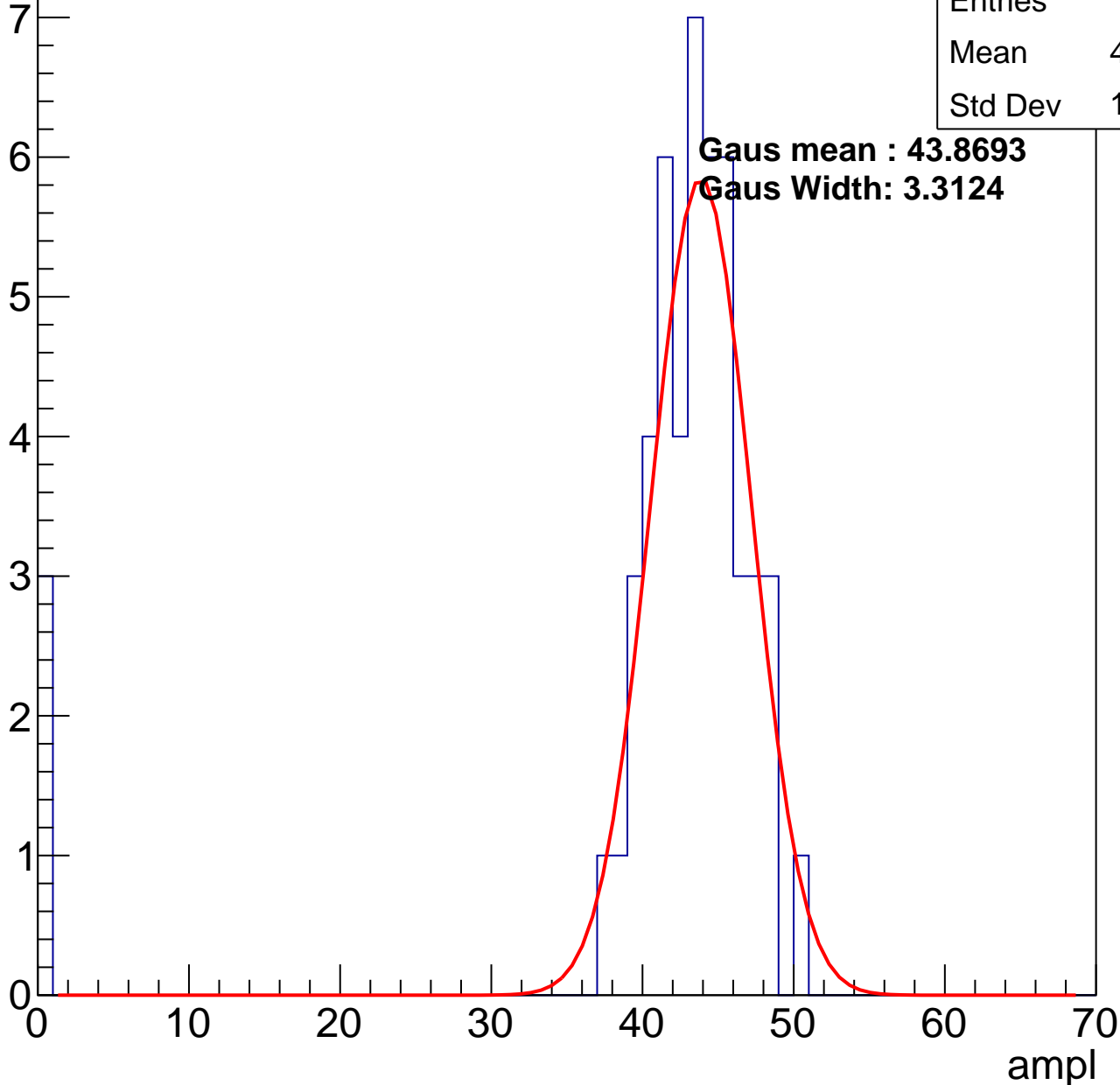
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	40.67
Std Dev	10.55

Gaus mean : 43.8693

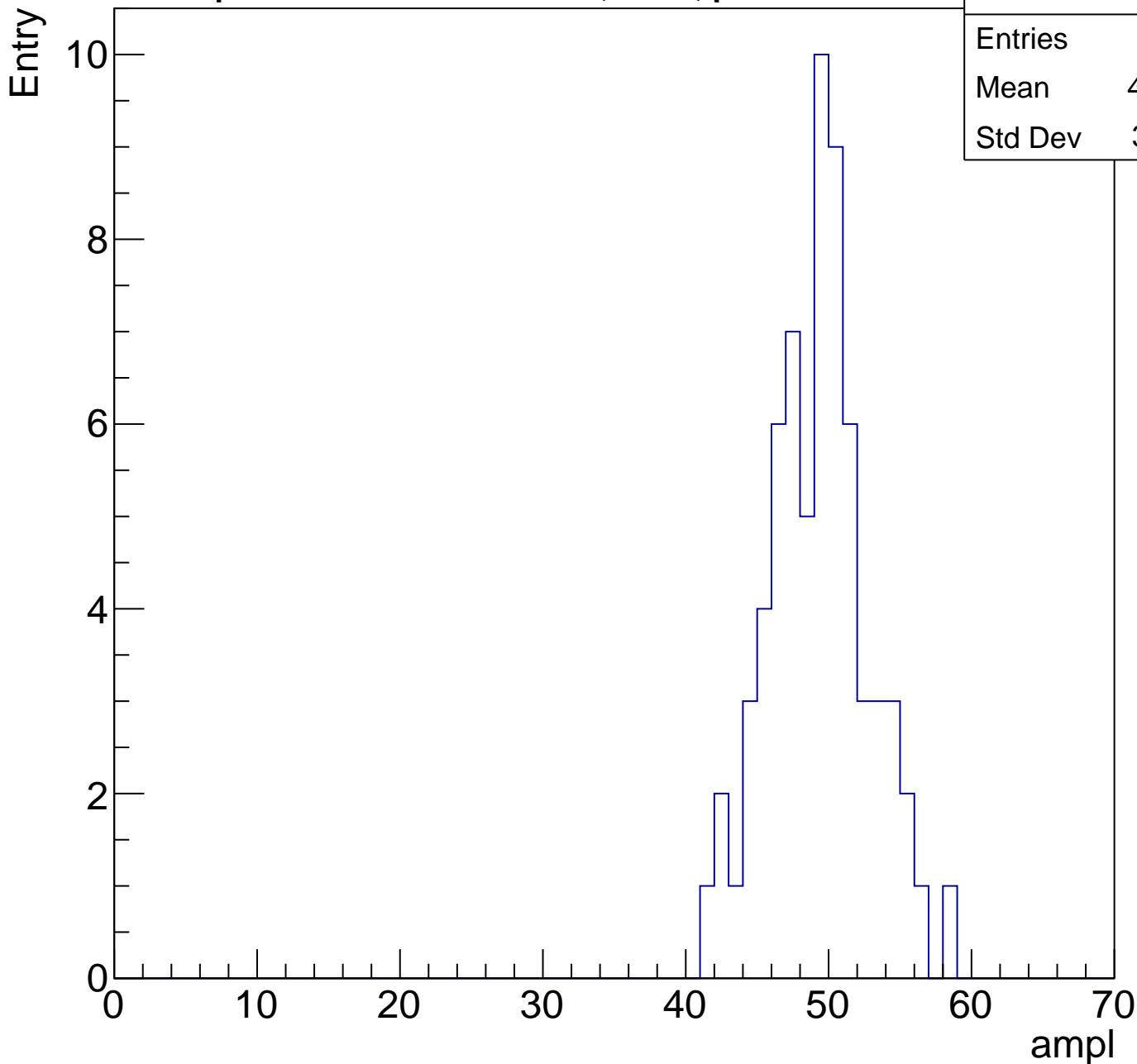
Gaus Width: 3.3124



B1L103S, U10-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	48.84
Std Dev	3.471

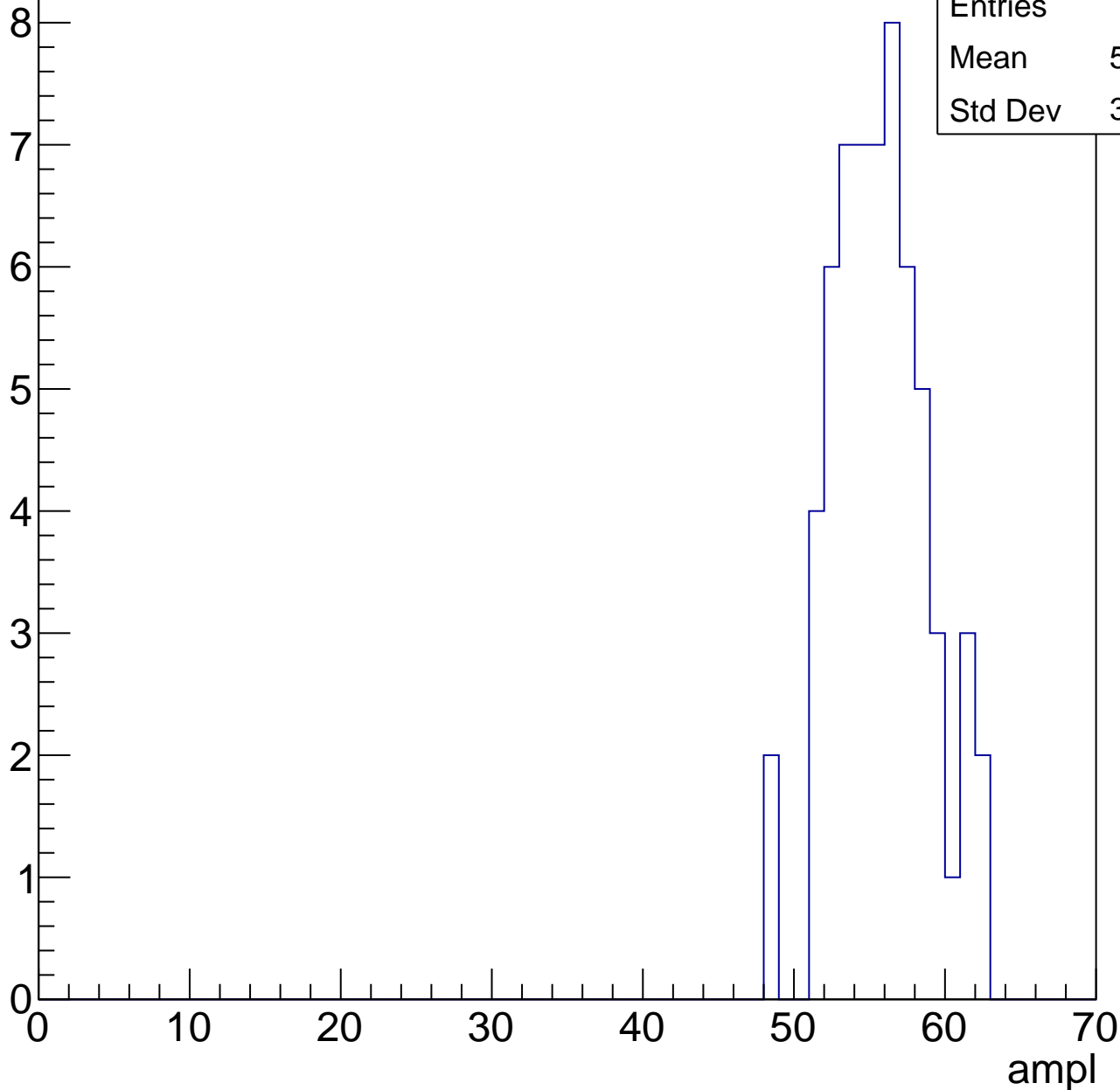


B1L103S, U10-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.25
Std Dev	3.145

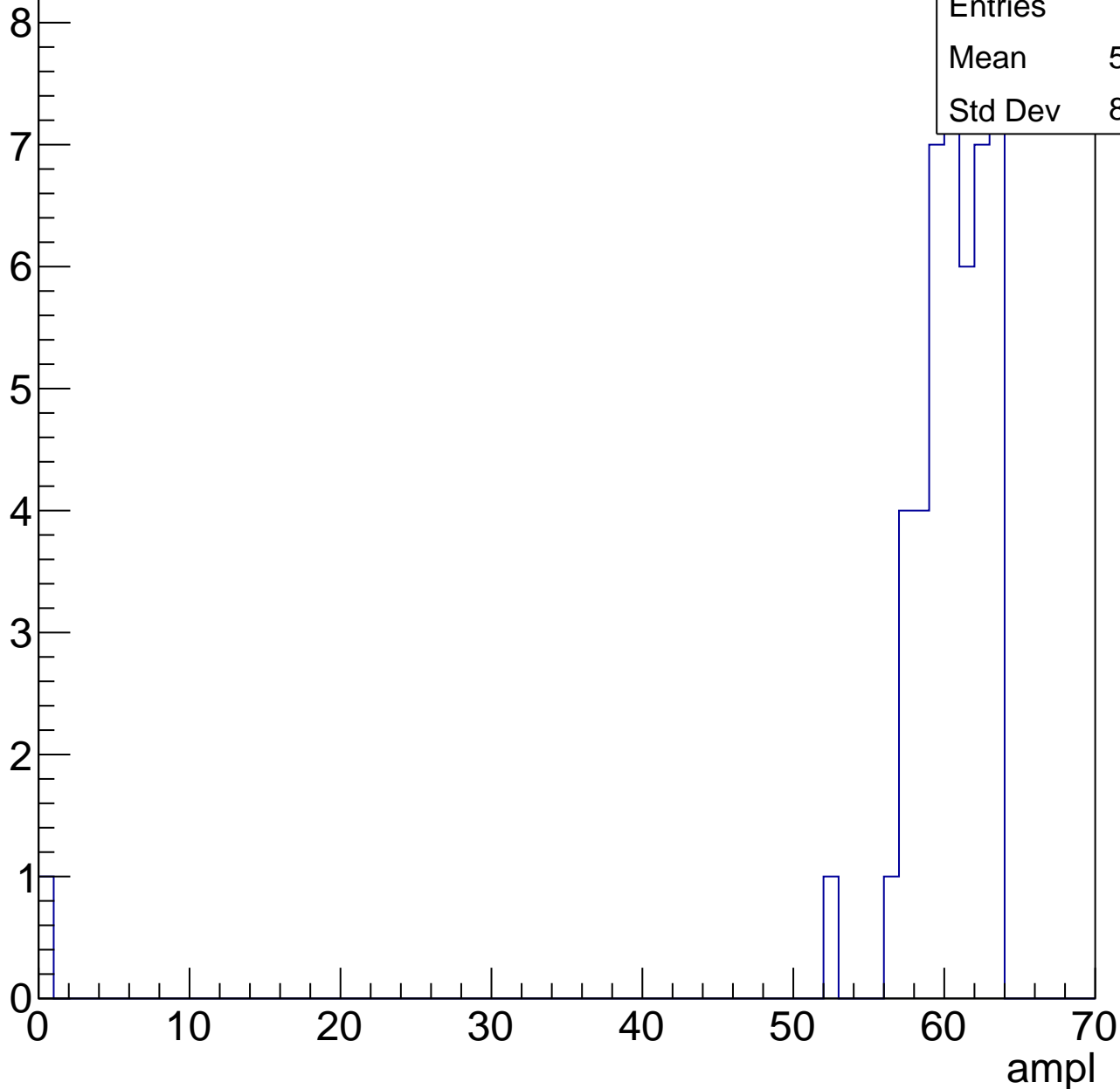


B1L103S, U10-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.83
Std Dev	8.969



B1L103S, U10-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

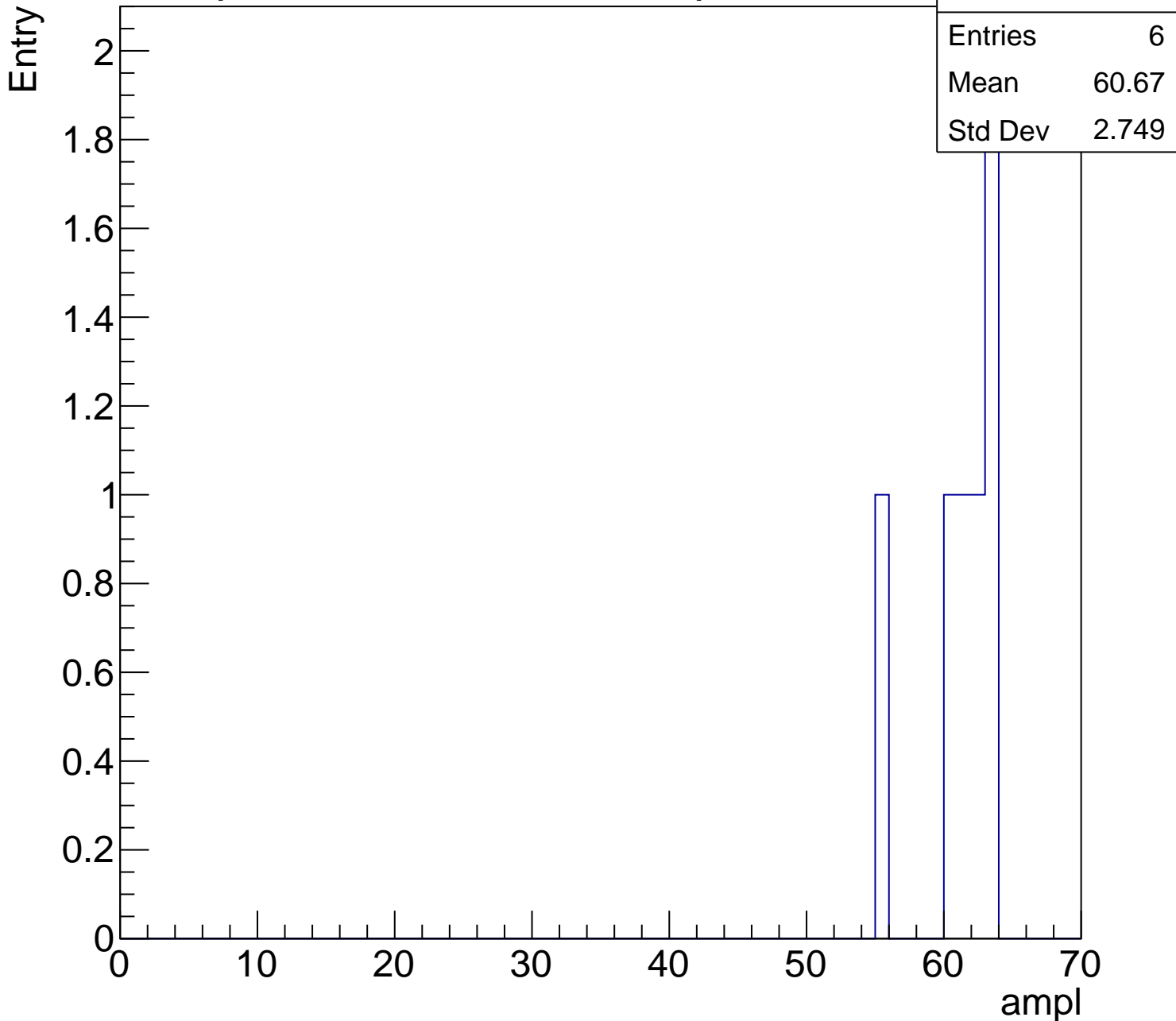
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	2.749

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch55, adc0

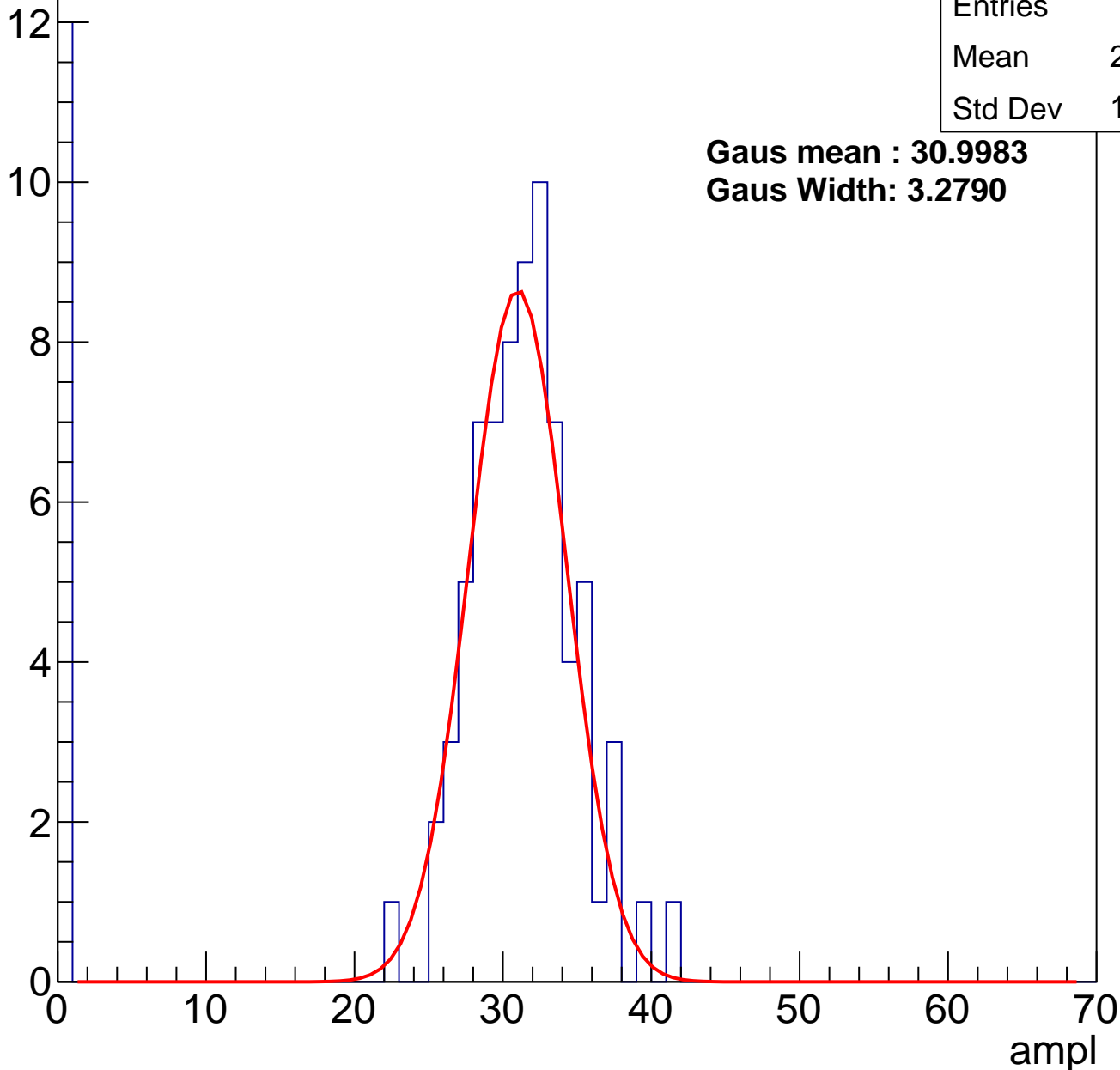
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	26.65
Std Dev	11.19

Gaus mean : 30.9983

Gaus Width: 3.2790

Entry



B1L103S, U10-ch55, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	35.46
Std Dev	9.412

Gaus mean : 37.7300

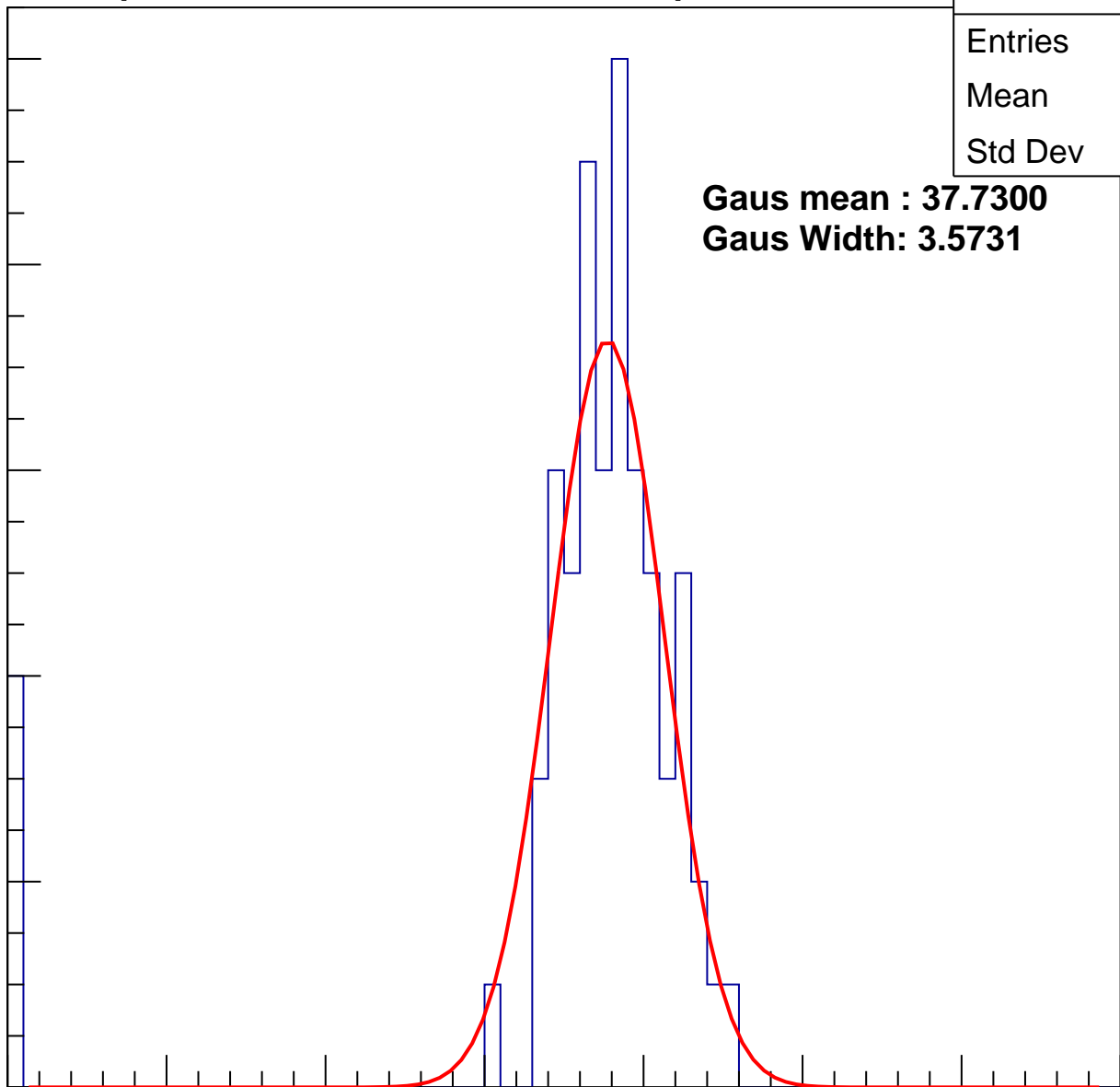
Gaus Width: 3.5731

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch55, adc2

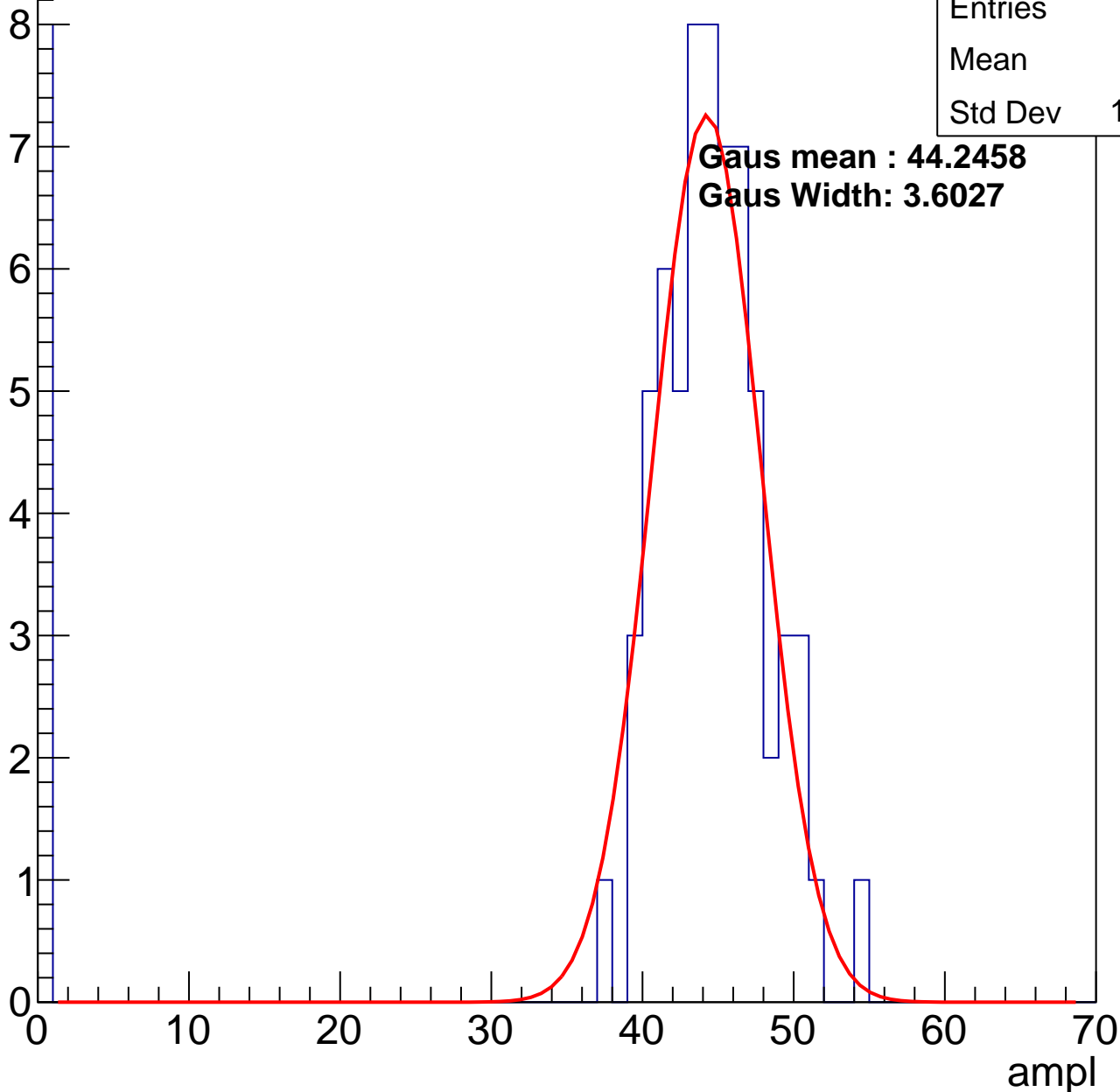
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.4
Std Dev	14.18

Gaus mean : 44.2458

Gaus Width: 3.6027

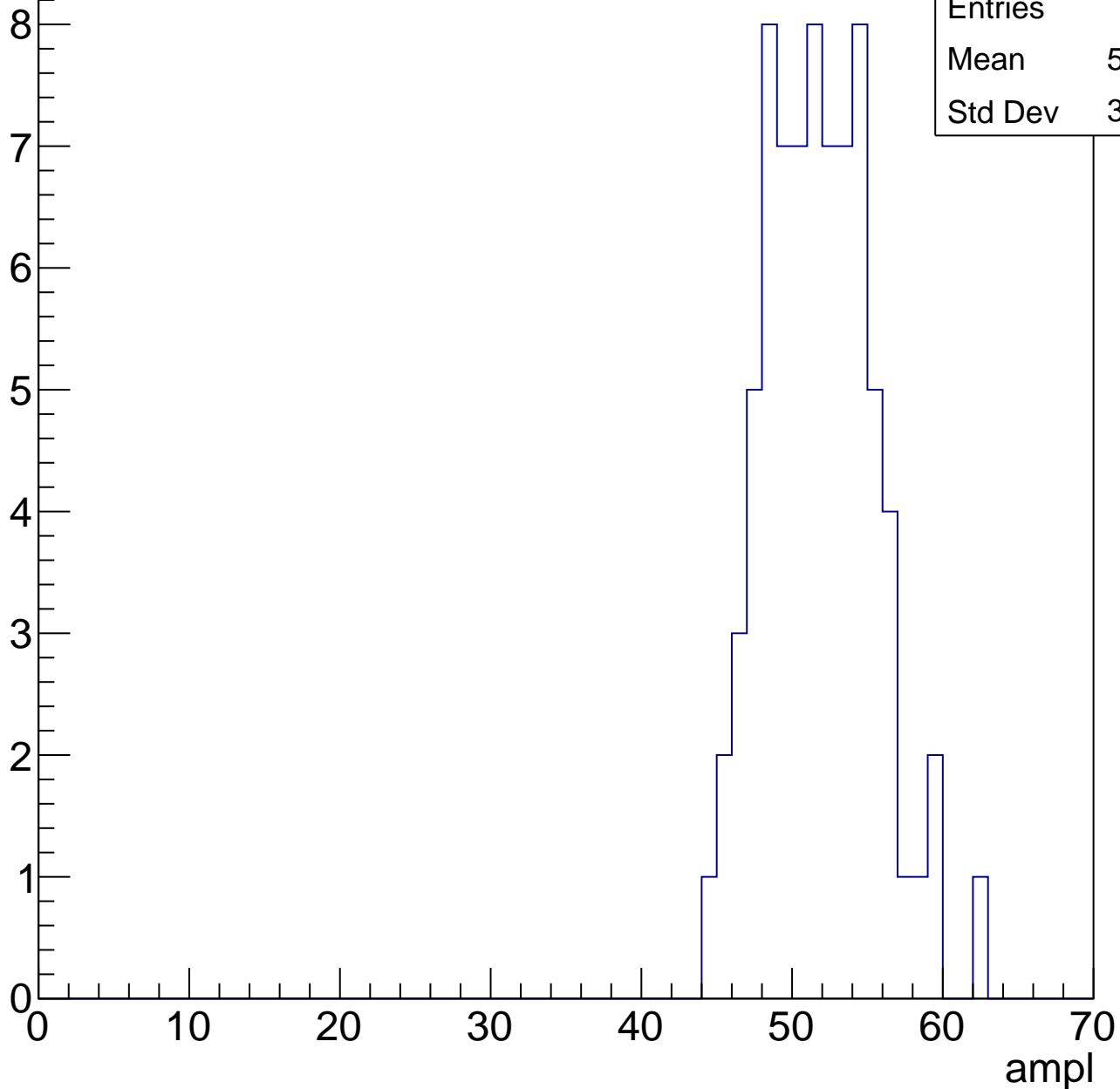


B1L103S, U10-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	51.34
Std Dev	3.595

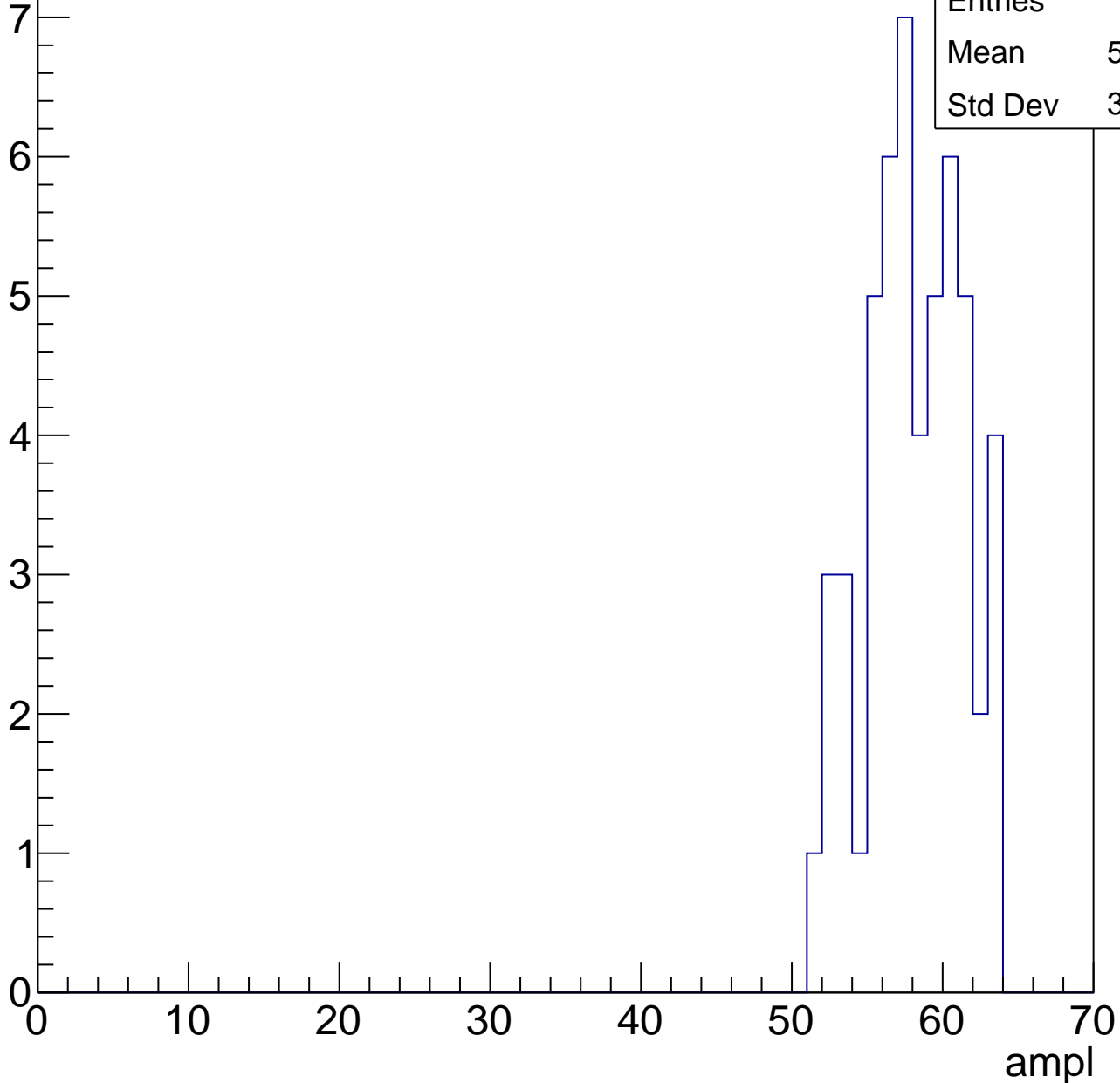


B1L103S, U10-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	57.65
Std Dev	3.174

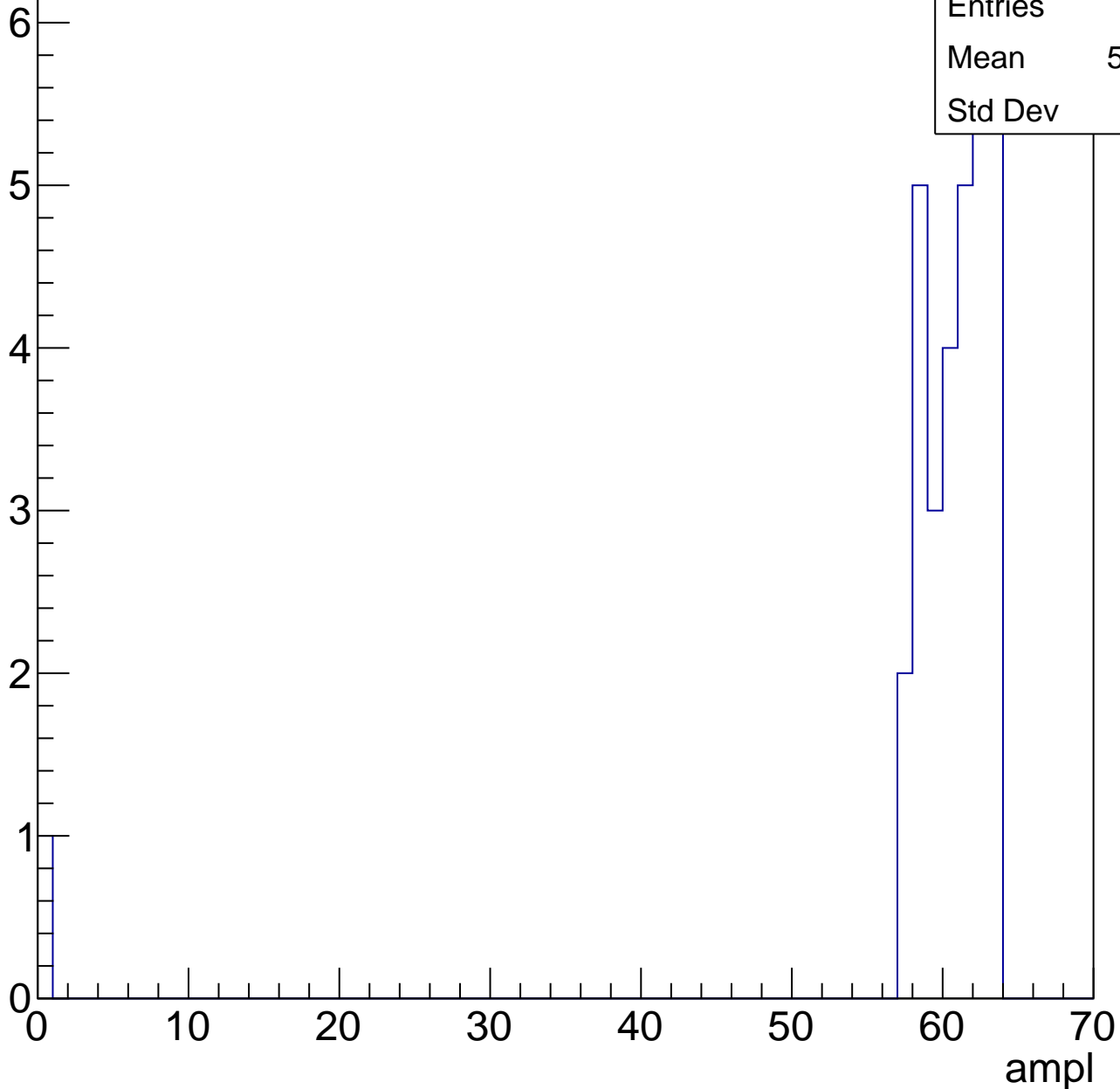


B1L103S, U10-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.62
Std Dev	10.7



B1L103S, U10-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch56, adc0

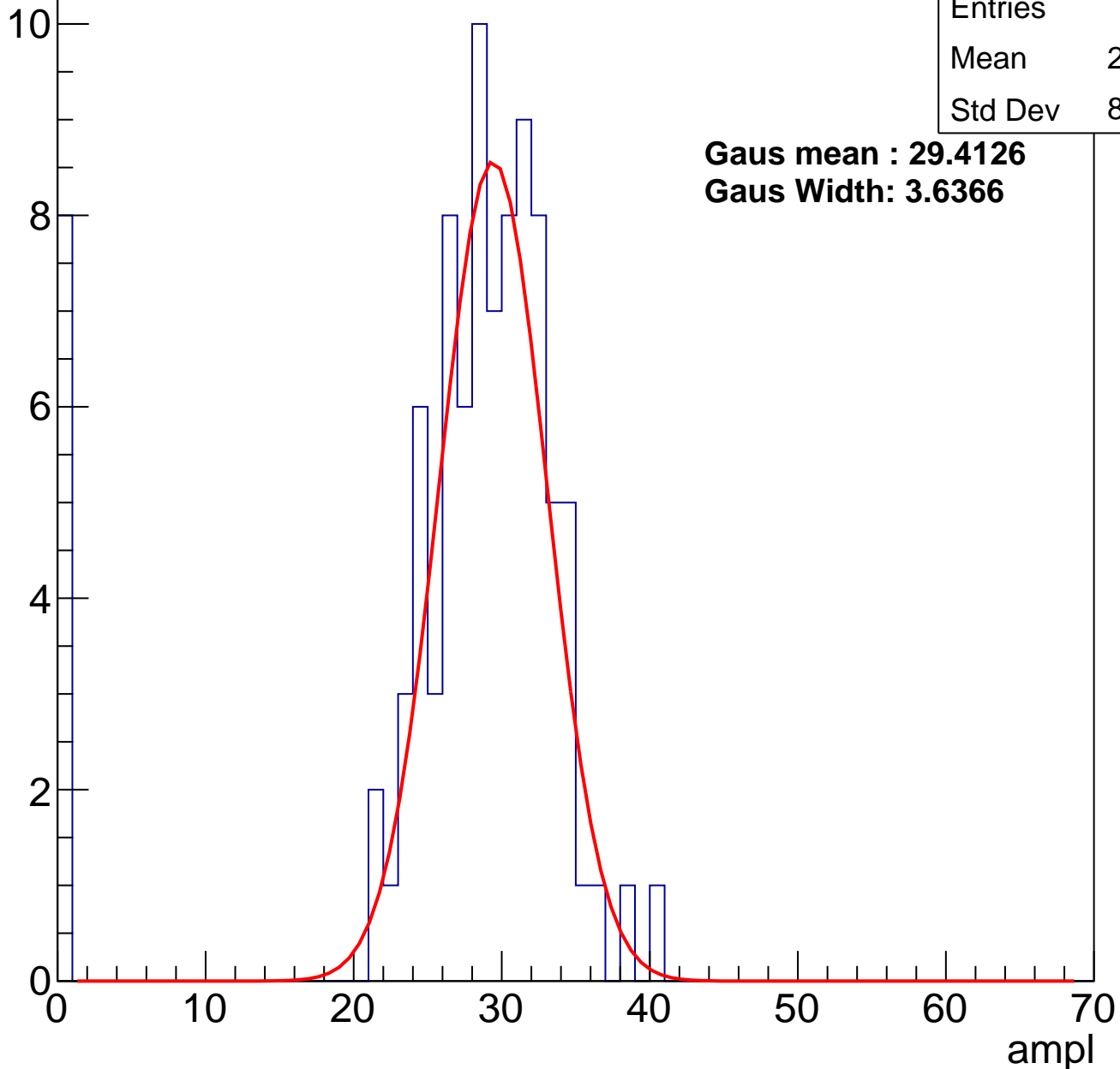
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	26.49
Std Dev	8.885

Gaus mean : 29.4126

Gaus Width: 3.6366

Entry



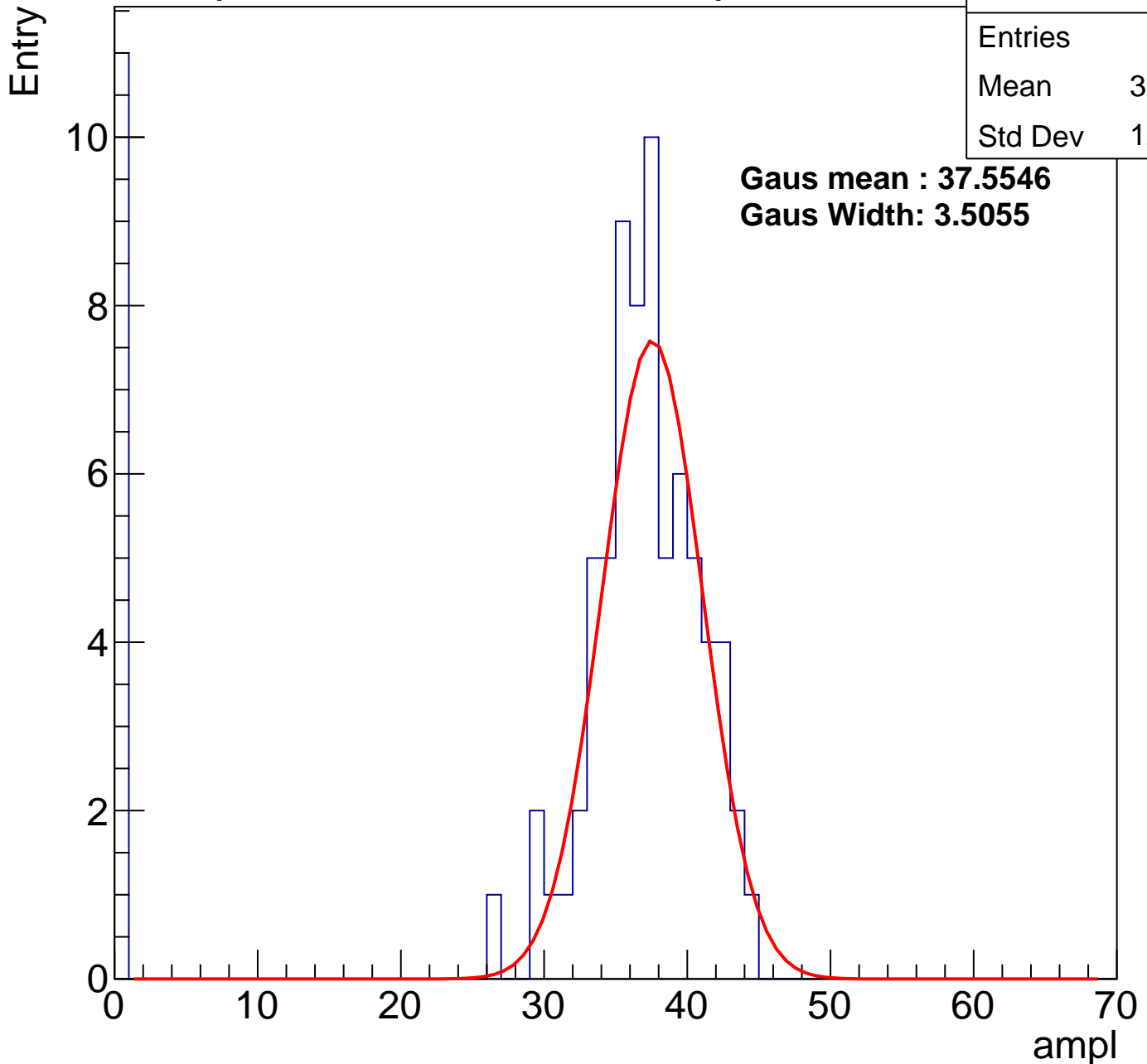
B1L103S, U10-ch56, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	31.74
Std Dev	12.93

Gaus mean : 37.5546

Gaus Width: 3.5055



B1L103S, U10-ch56, adc2

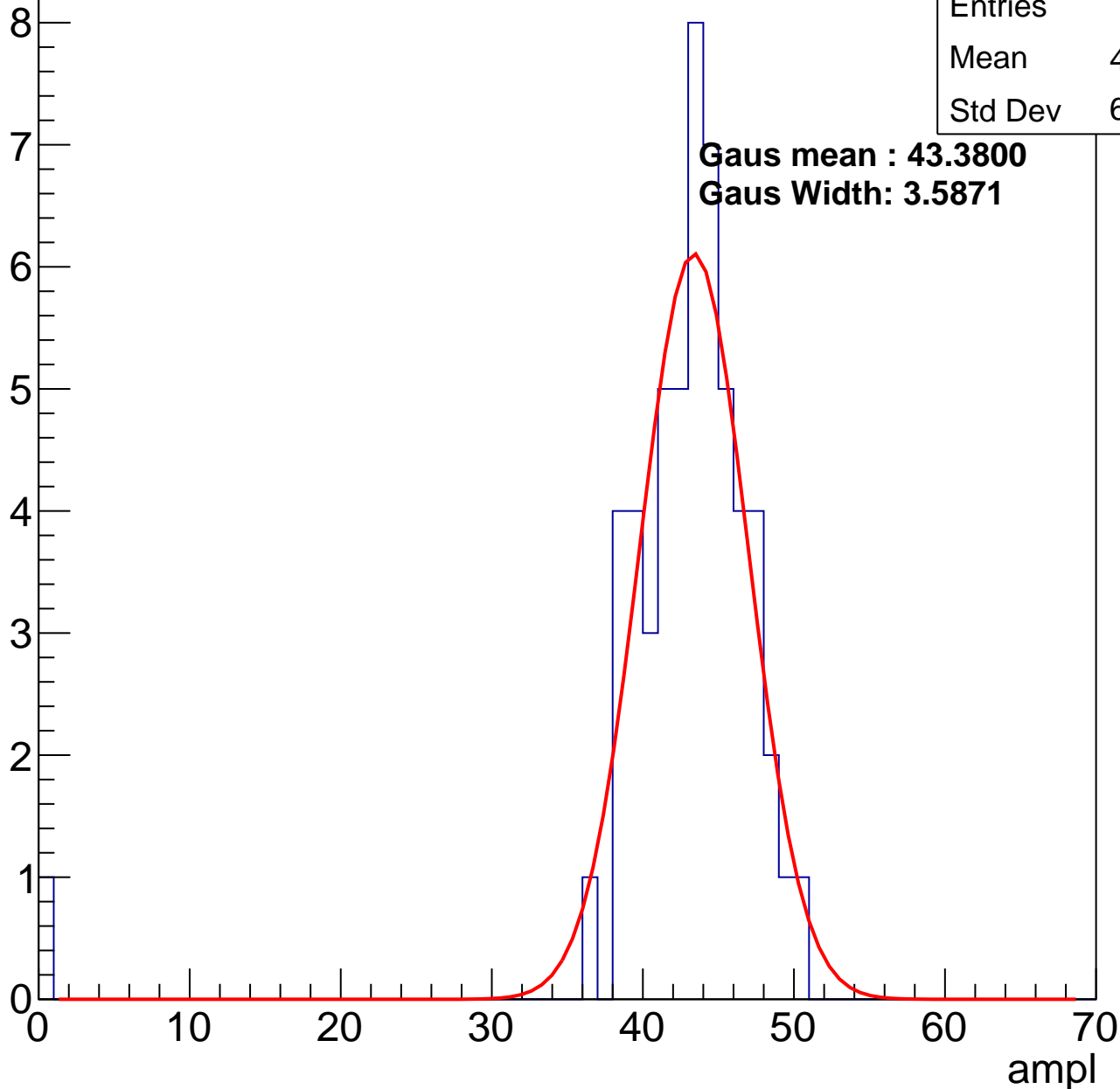
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	42.24
Std Dev	6.528

Gaus mean : 43.3800

Gaus Width: 3.5871

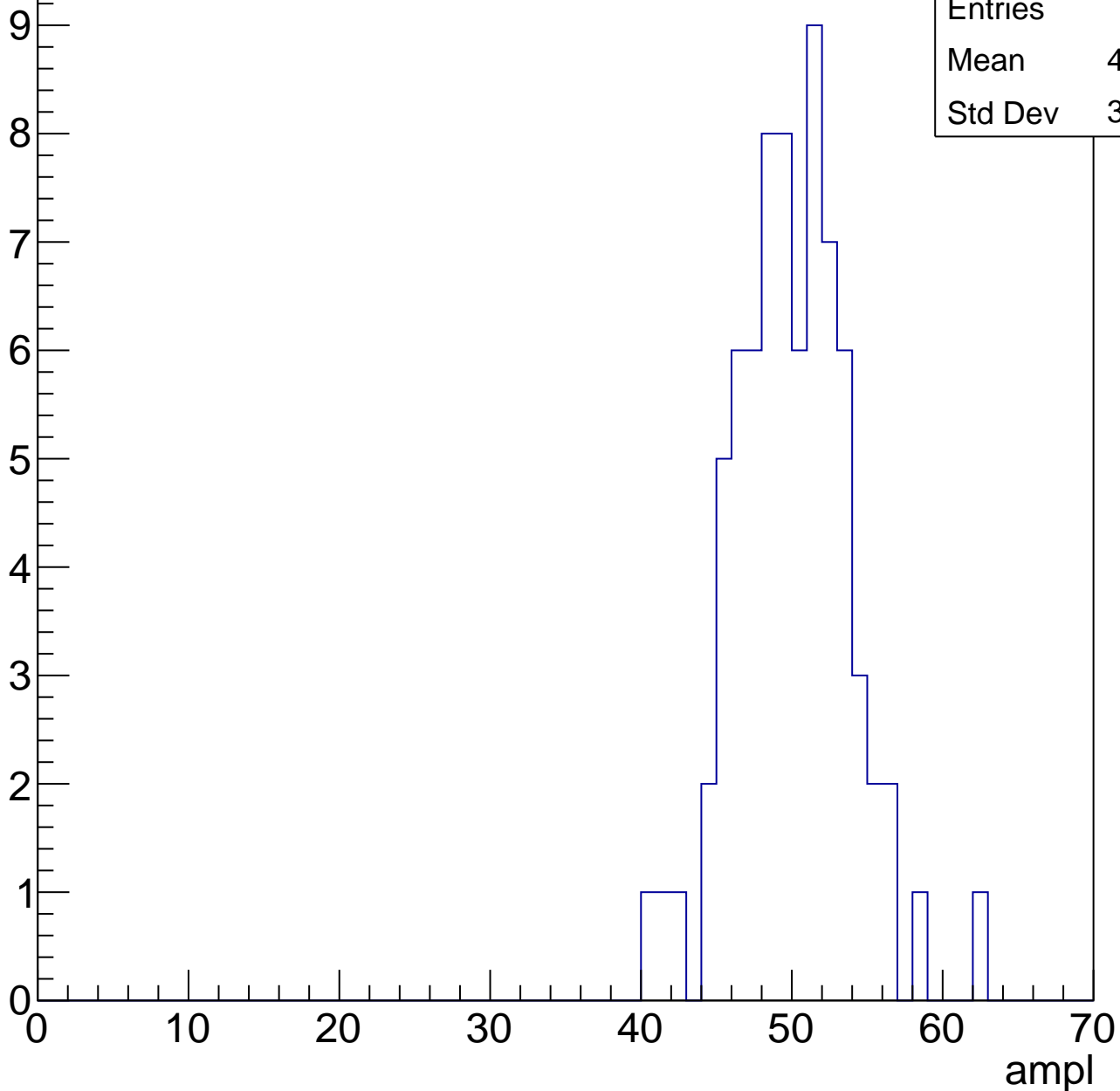


B1L103S, U10-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	49.53
Std Dev	3.803

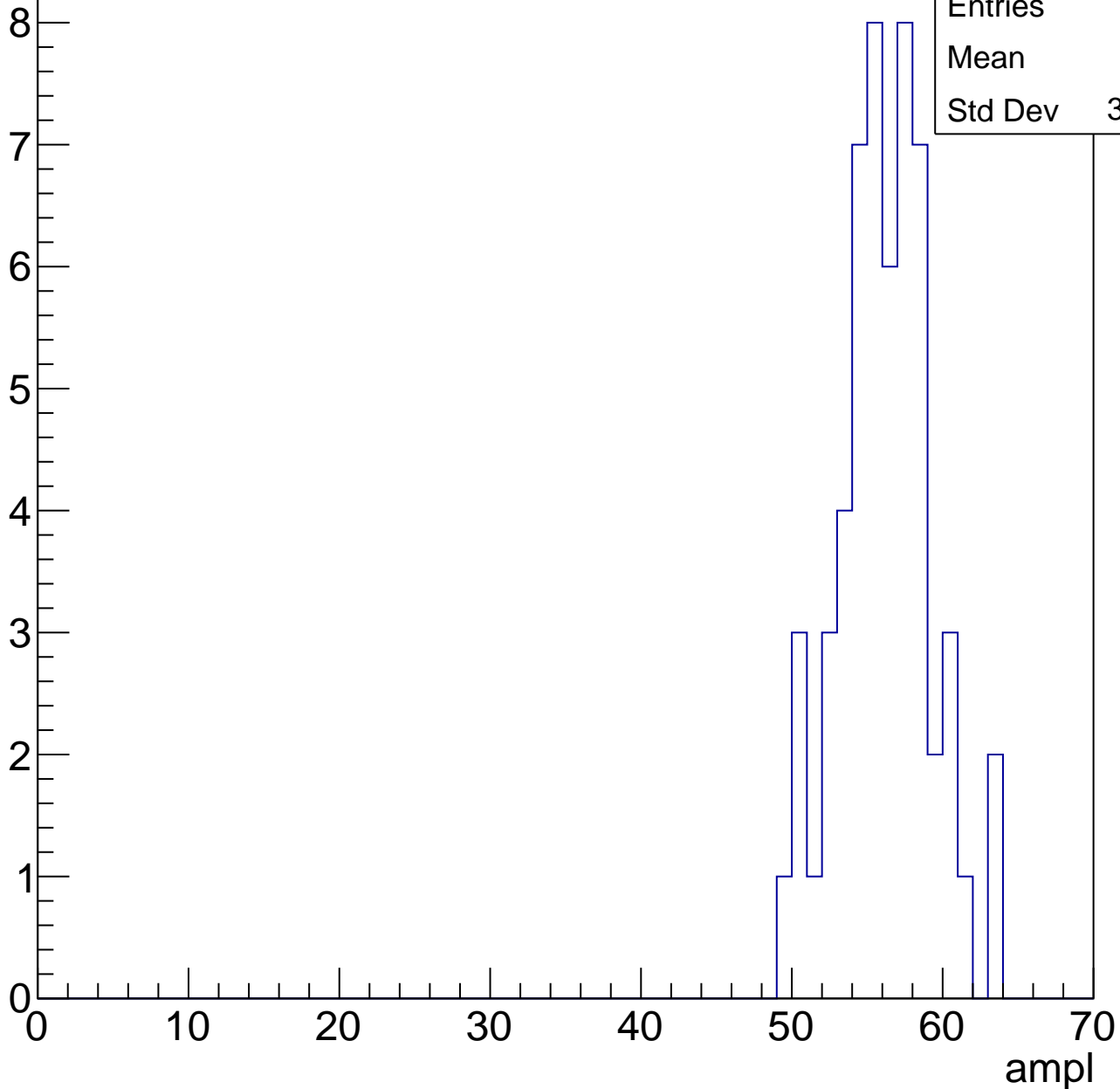


B1L103S, U10-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

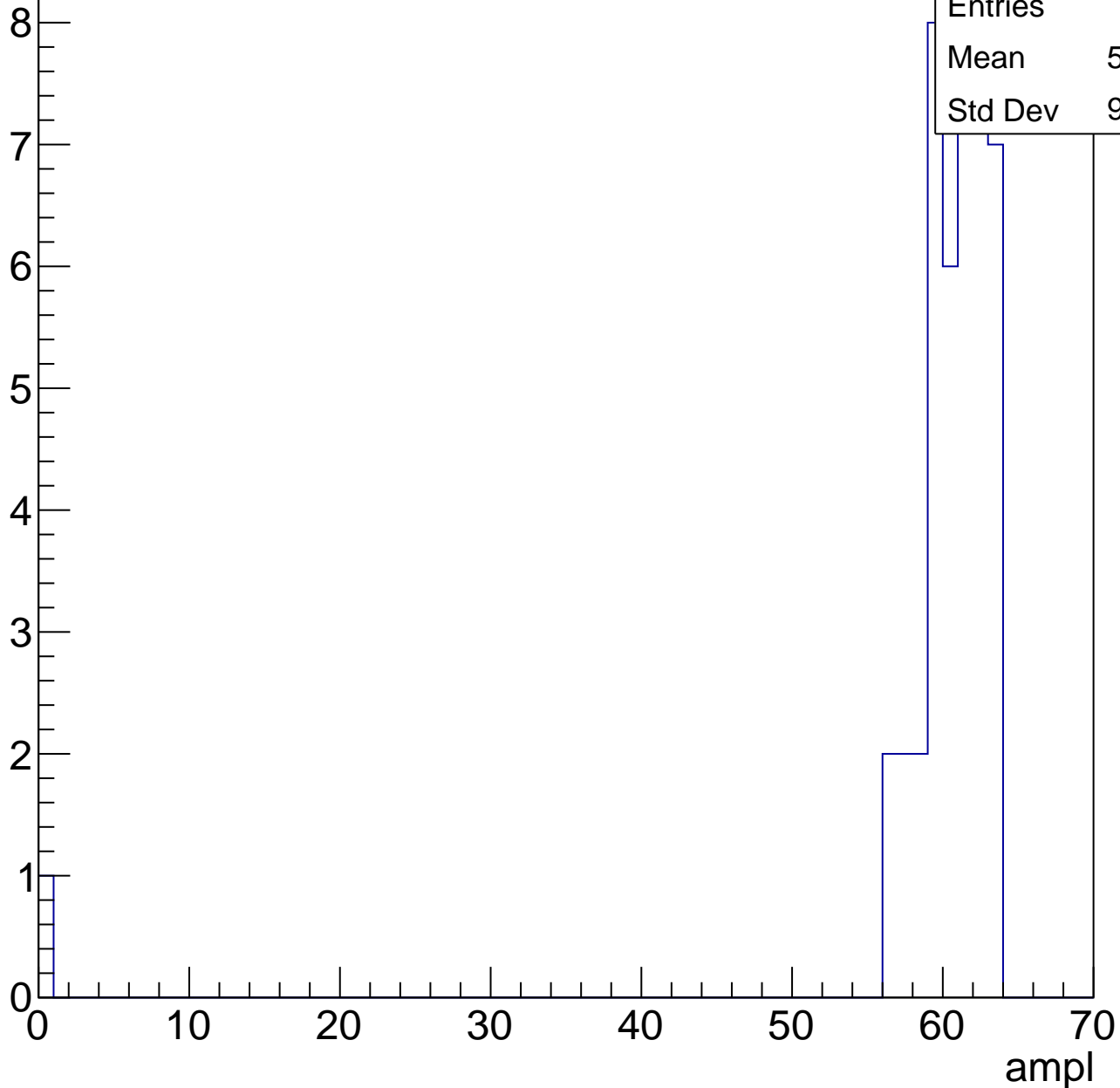
Entries	56
Mean	55.7
Std Dev	3.053



B1L103S, U10-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch57, adc0

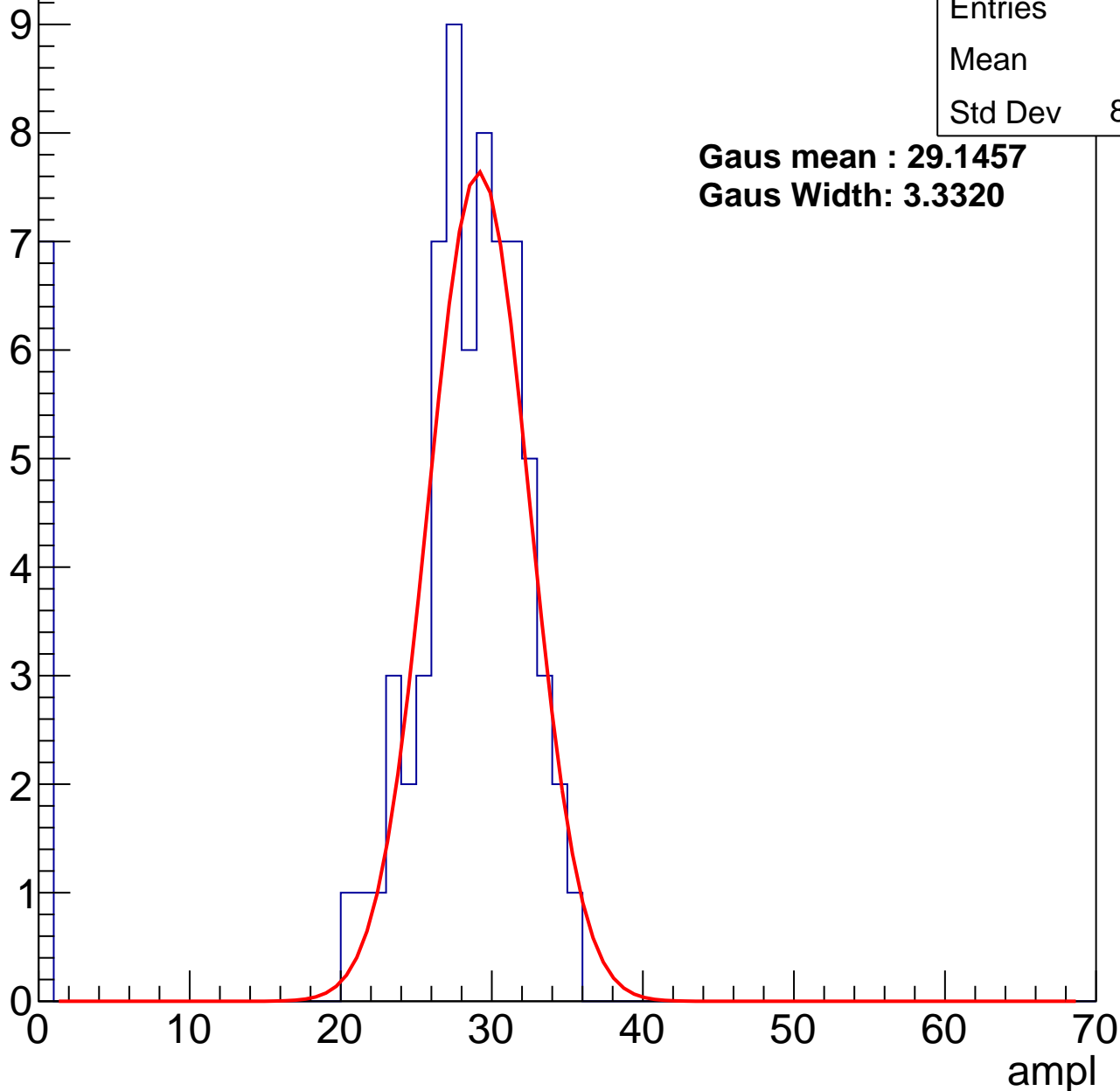
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	25.6
Std Dev	8.885

Gaus mean : 29.1457

Gaus Width: 3.3320



B1L103S, U10-ch57, adc1

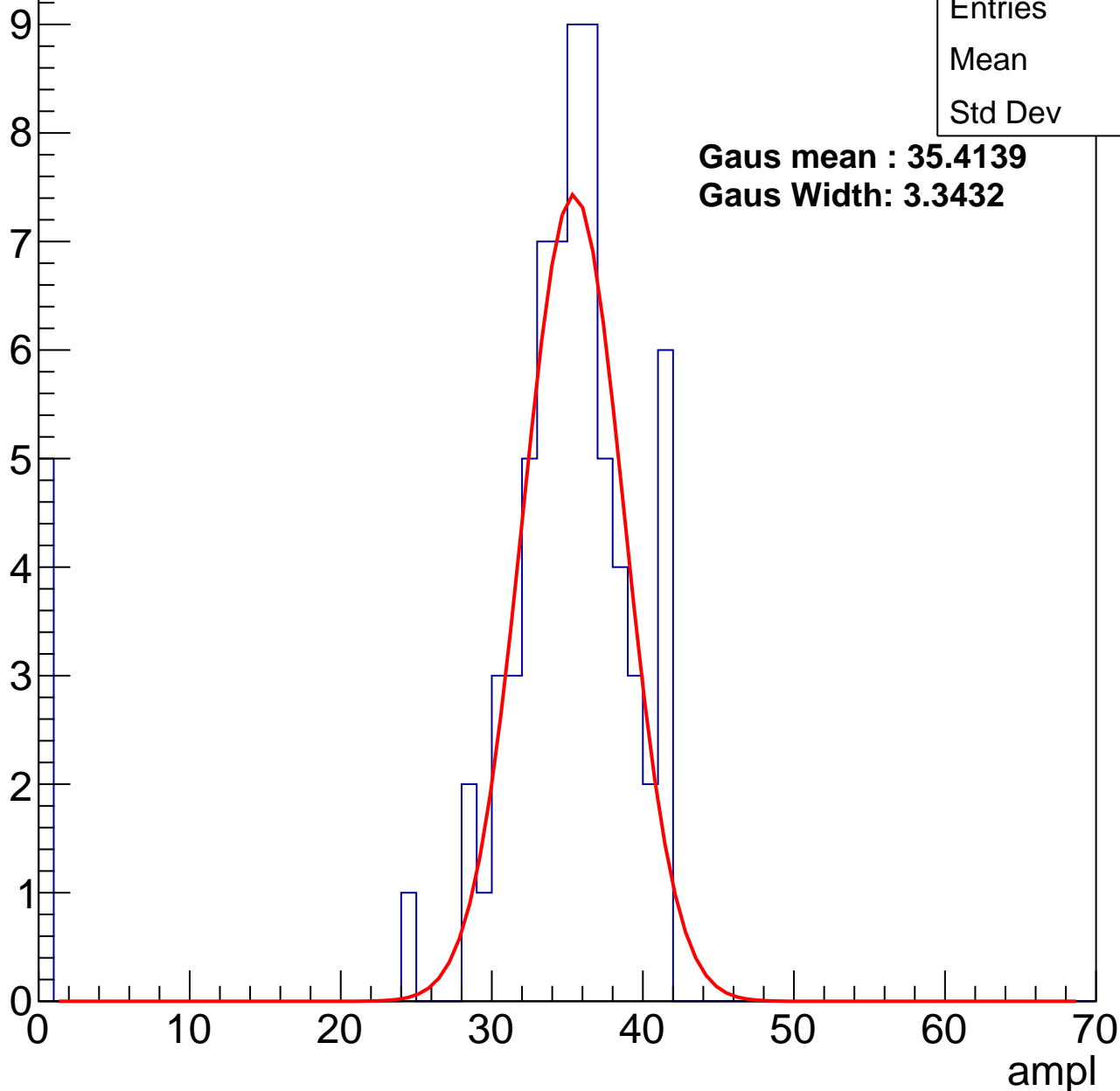
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	32.5
Std Dev	9.51

Gaus mean : 35.4139

Gaus Width: 3.3432



B1L103S, U10-ch57, adc2

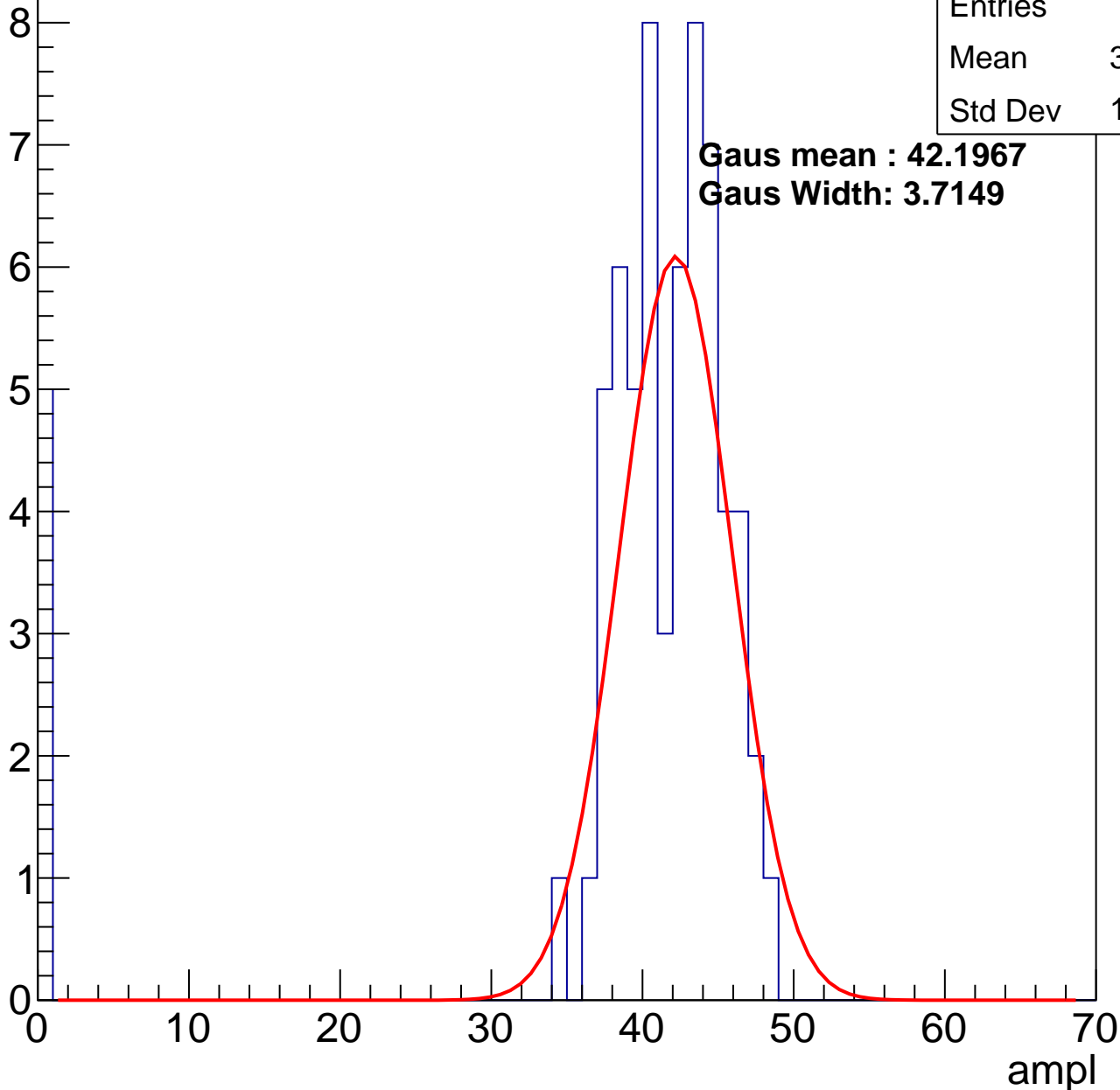
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	38.35
Std Dev	11.39

Gaus mean : 42.1967

Gaus Width: 3.7149

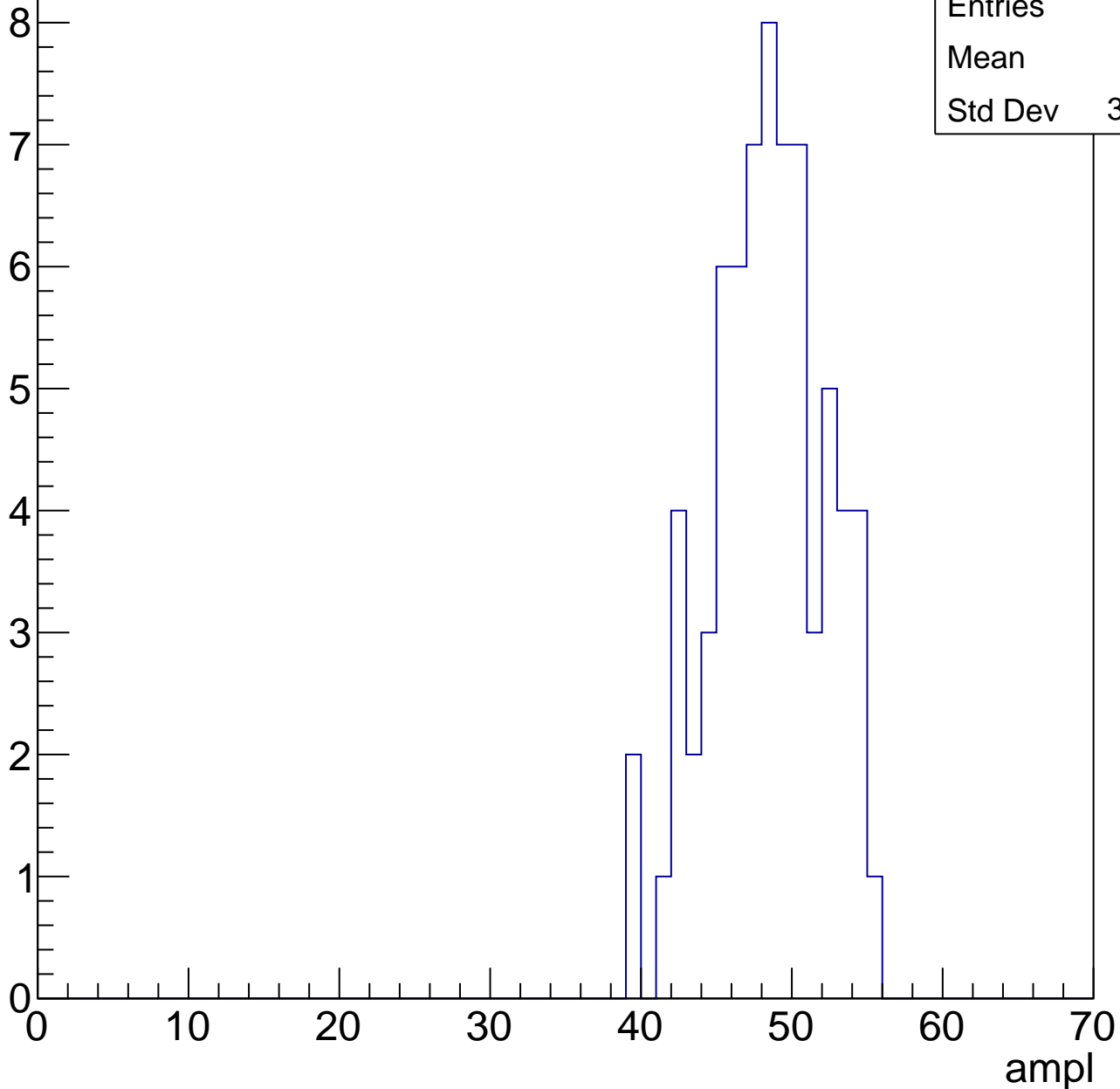


B1L103S, U10-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

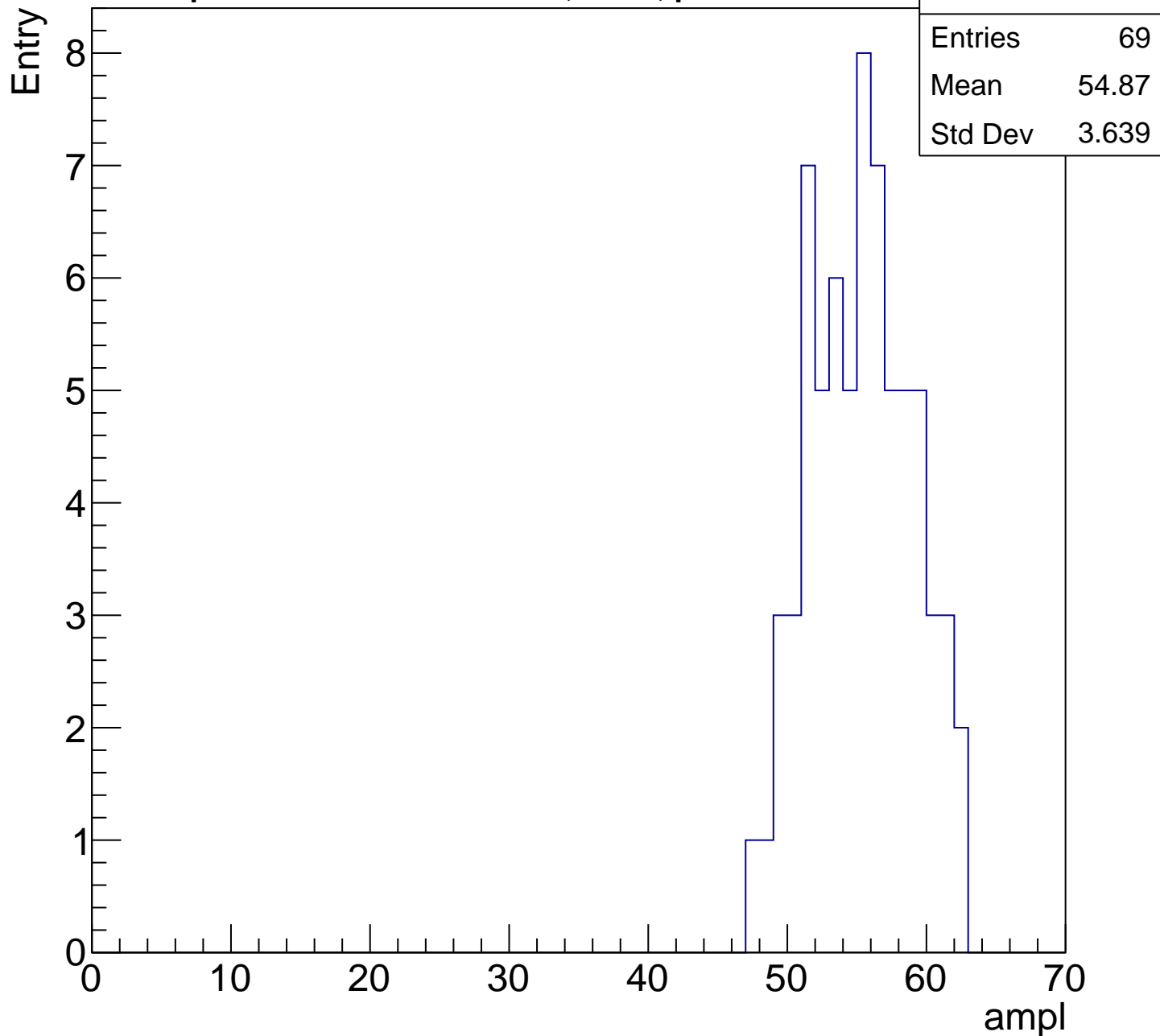
Entry

Entries	70
Mean	47.9
Std Dev	3.727



B1L103S, U10-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

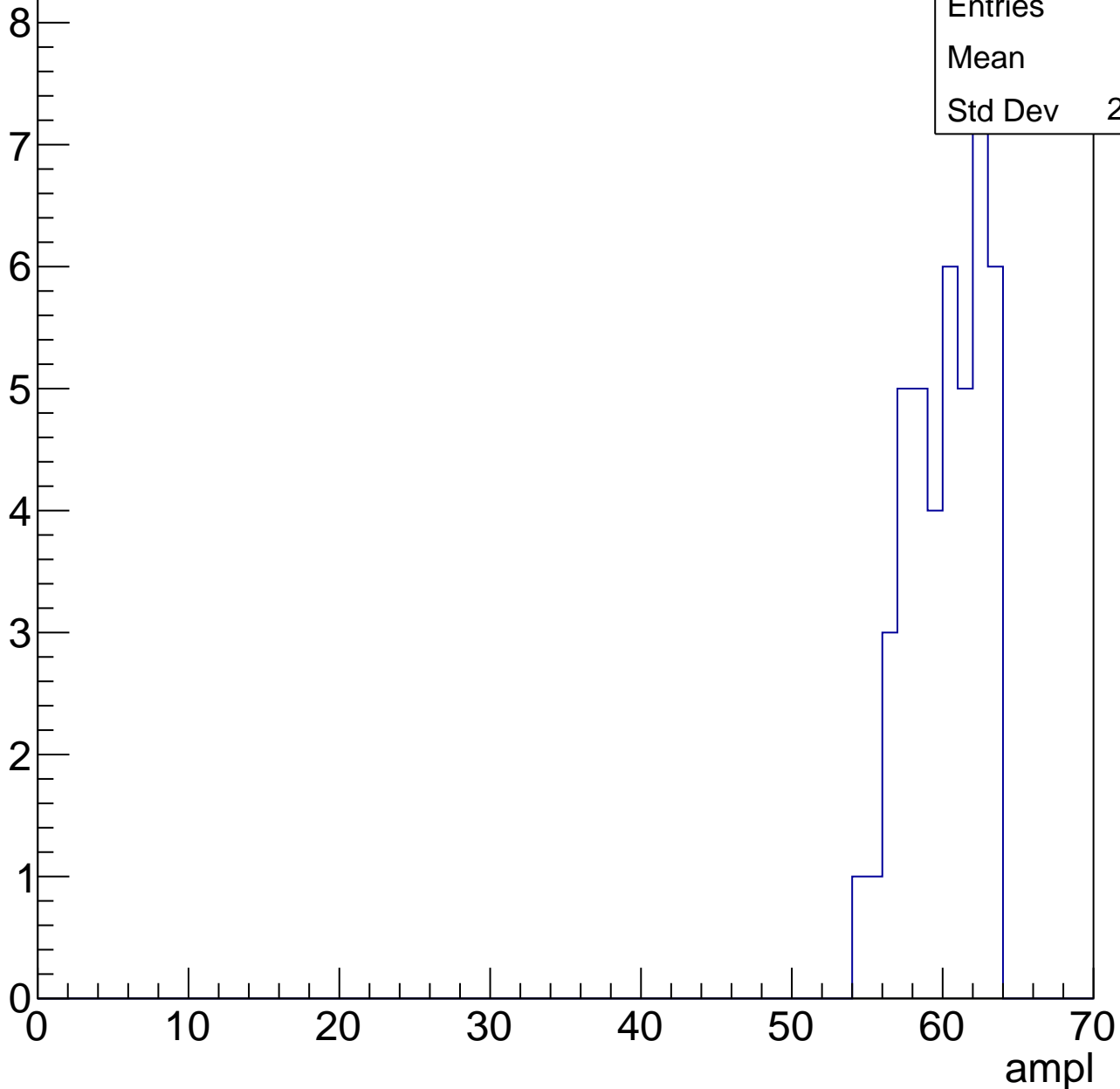


B1L103S, U10-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

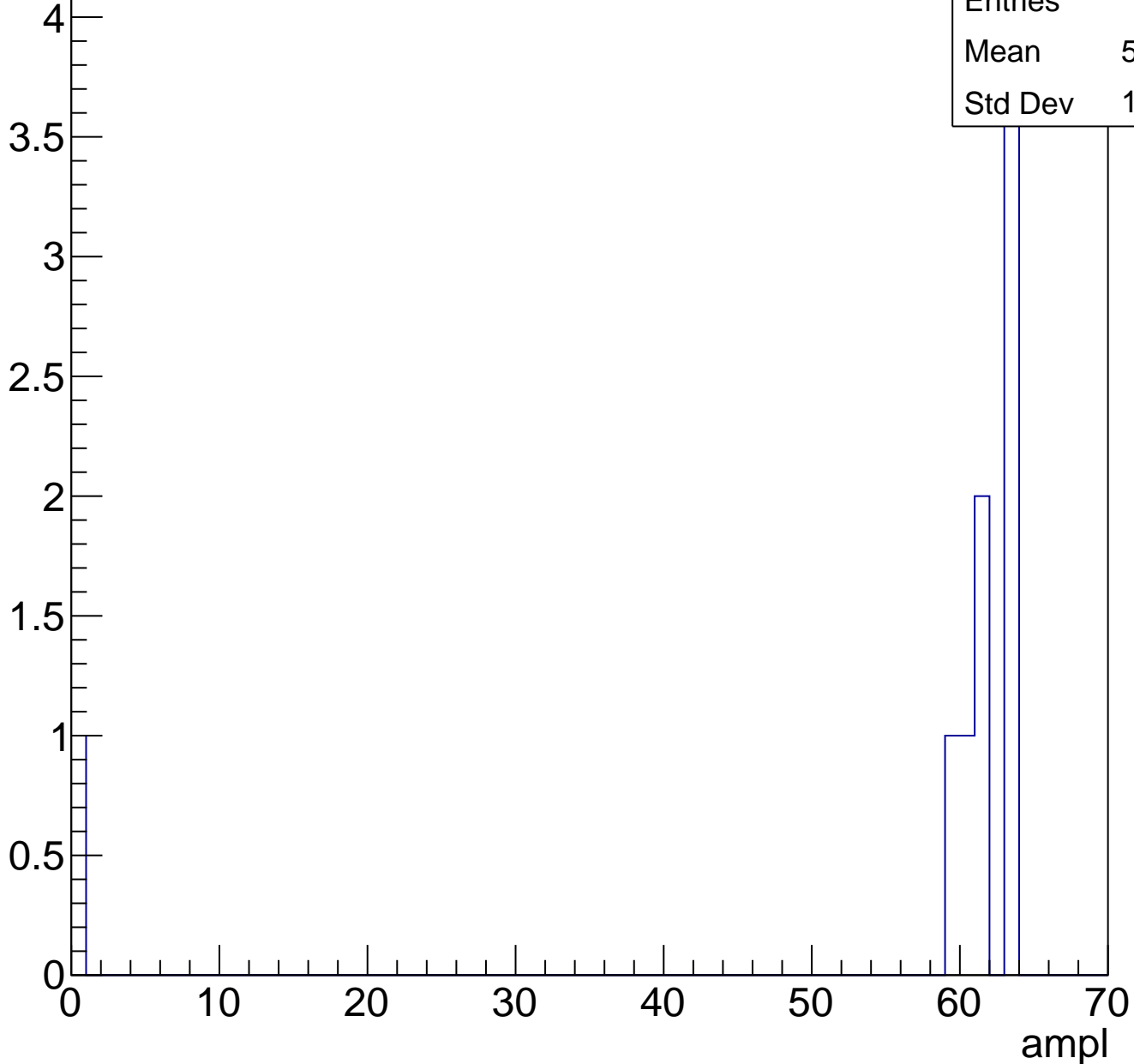
Entries	44
Mean	59.7
Std Dev	2.455



B1L103S, U10-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch58, adc0

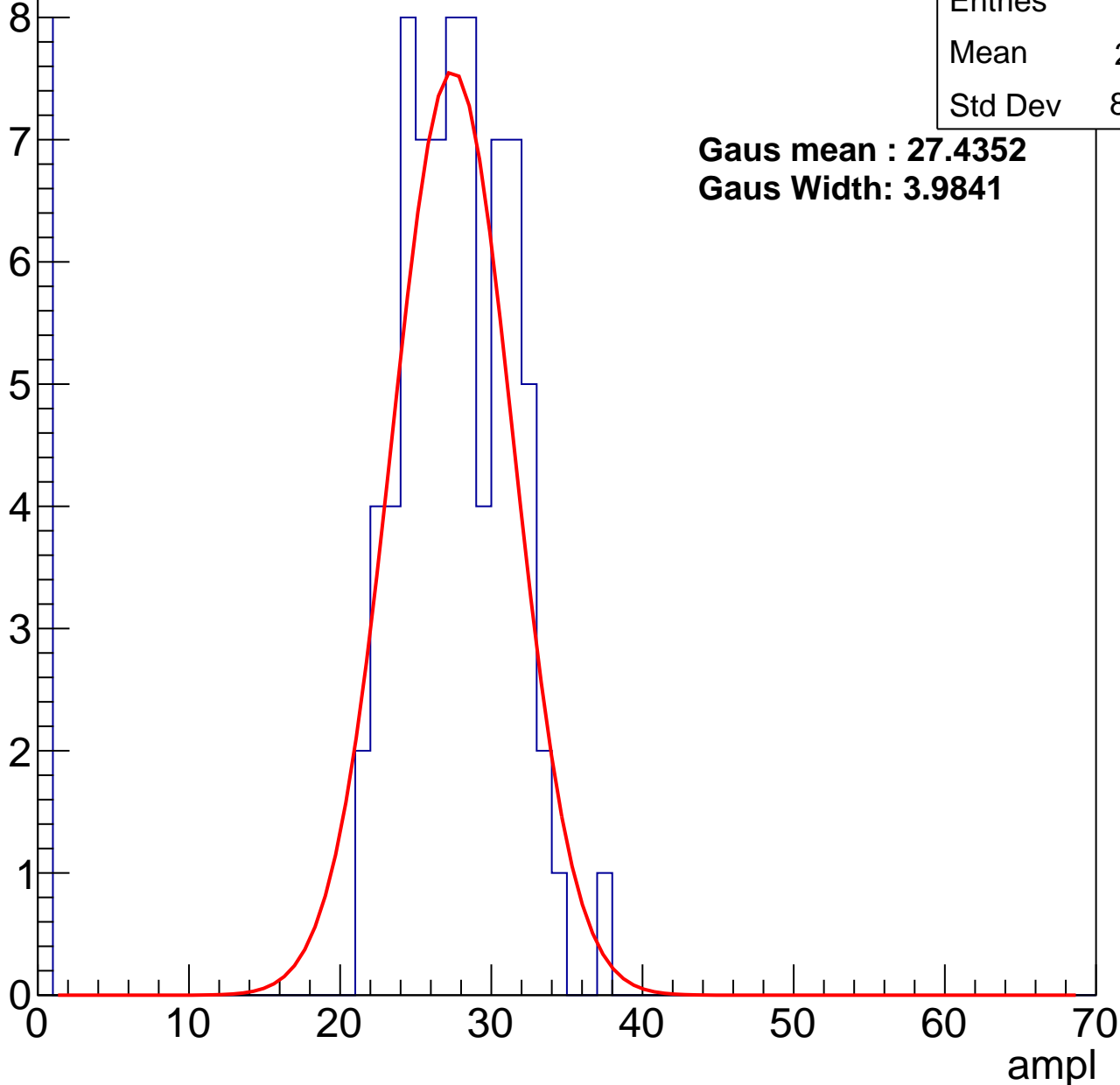
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	24.71
Std Dev	8.702

Gaus mean : 27.4352

Gaus Width: 3.9841



B1L103S, U10-ch58, adc1

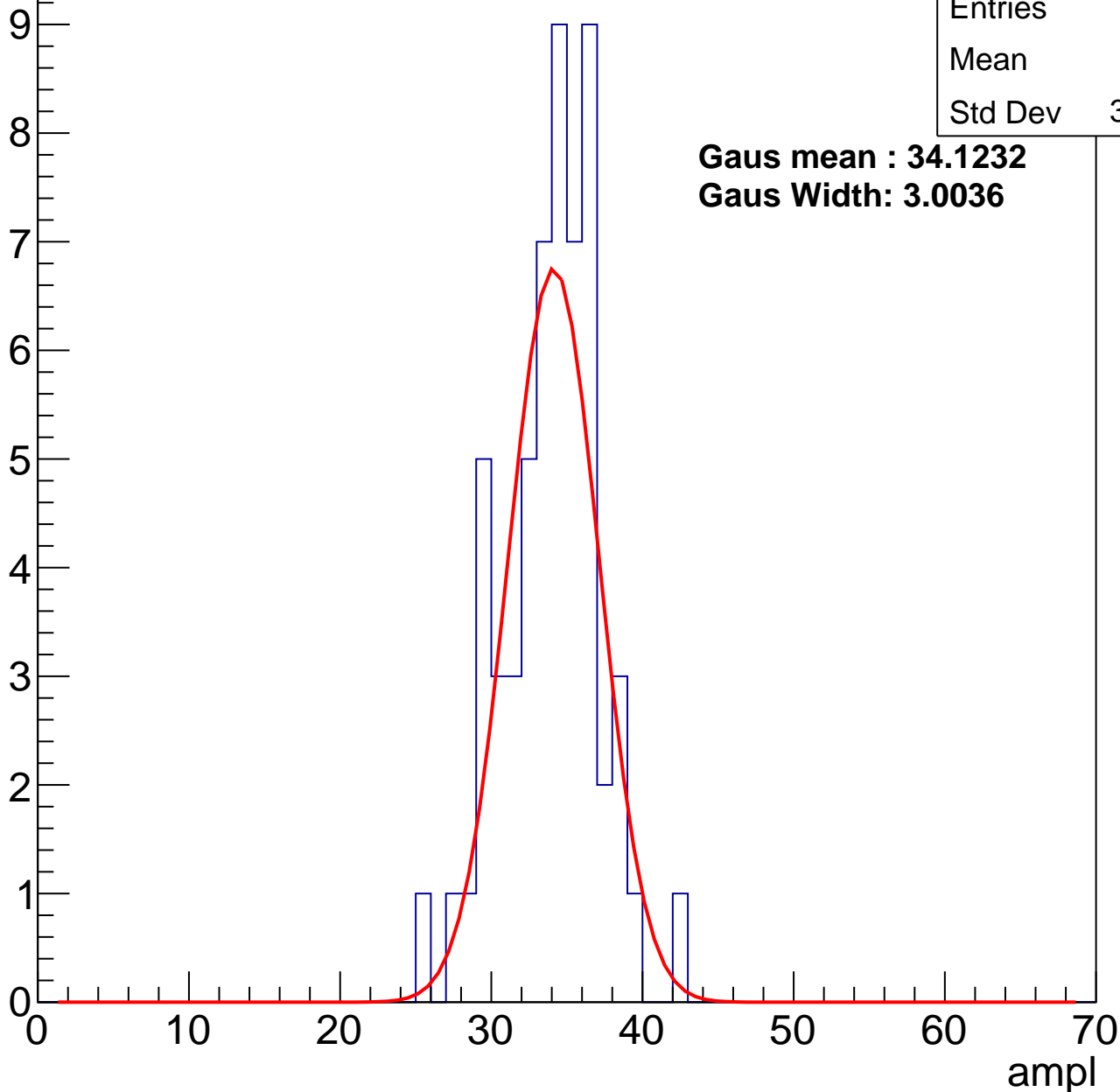
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.5
Std Dev	3.153

Gaus mean : 34.1232

Gaus Width: 3.0036



B1L103S, U10-ch58, adc2

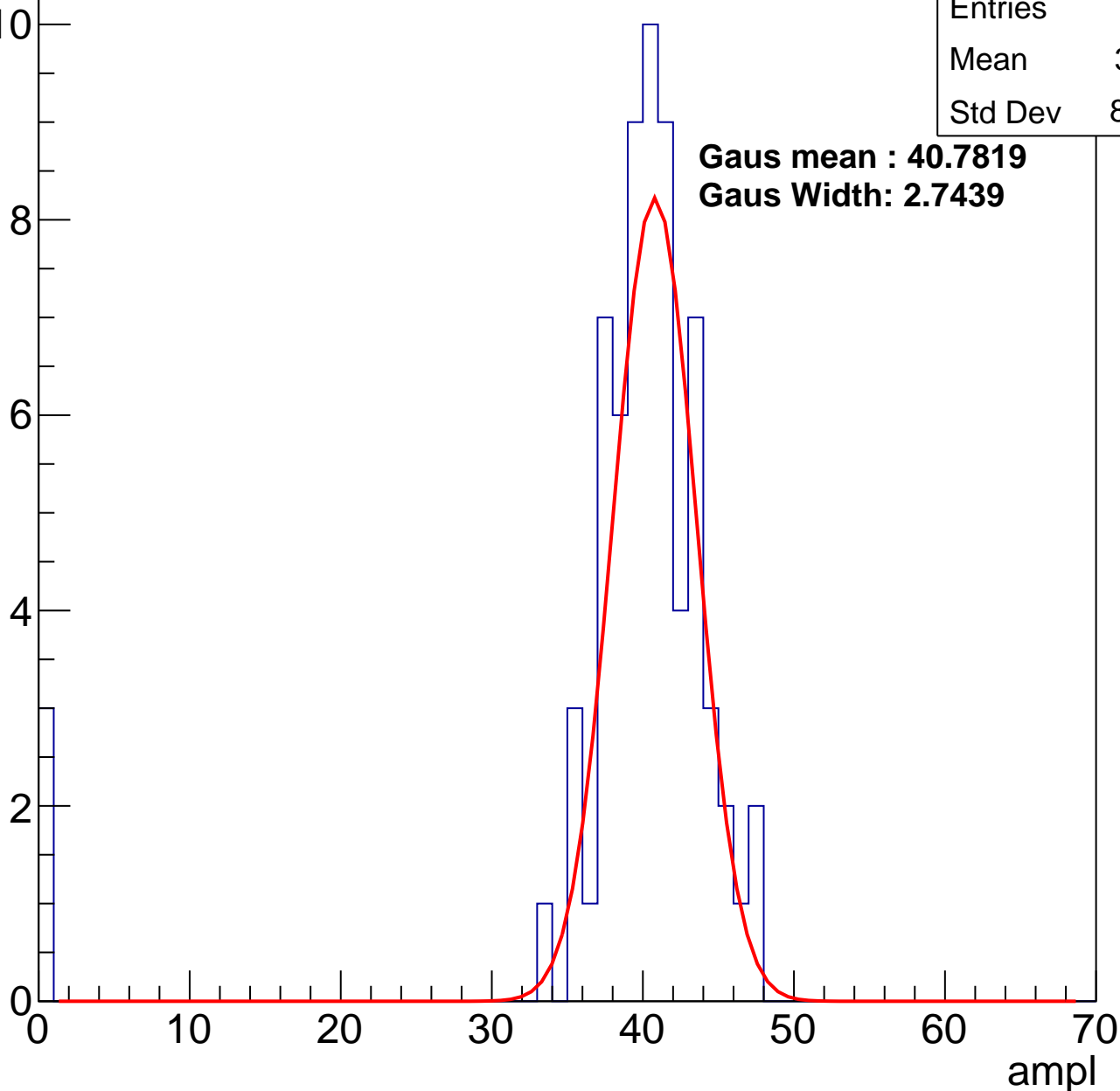
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.41
Std Dev	8.732

Gaus mean : 40.7819

Gaus Width: 2.7439



B1L103S, U10-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

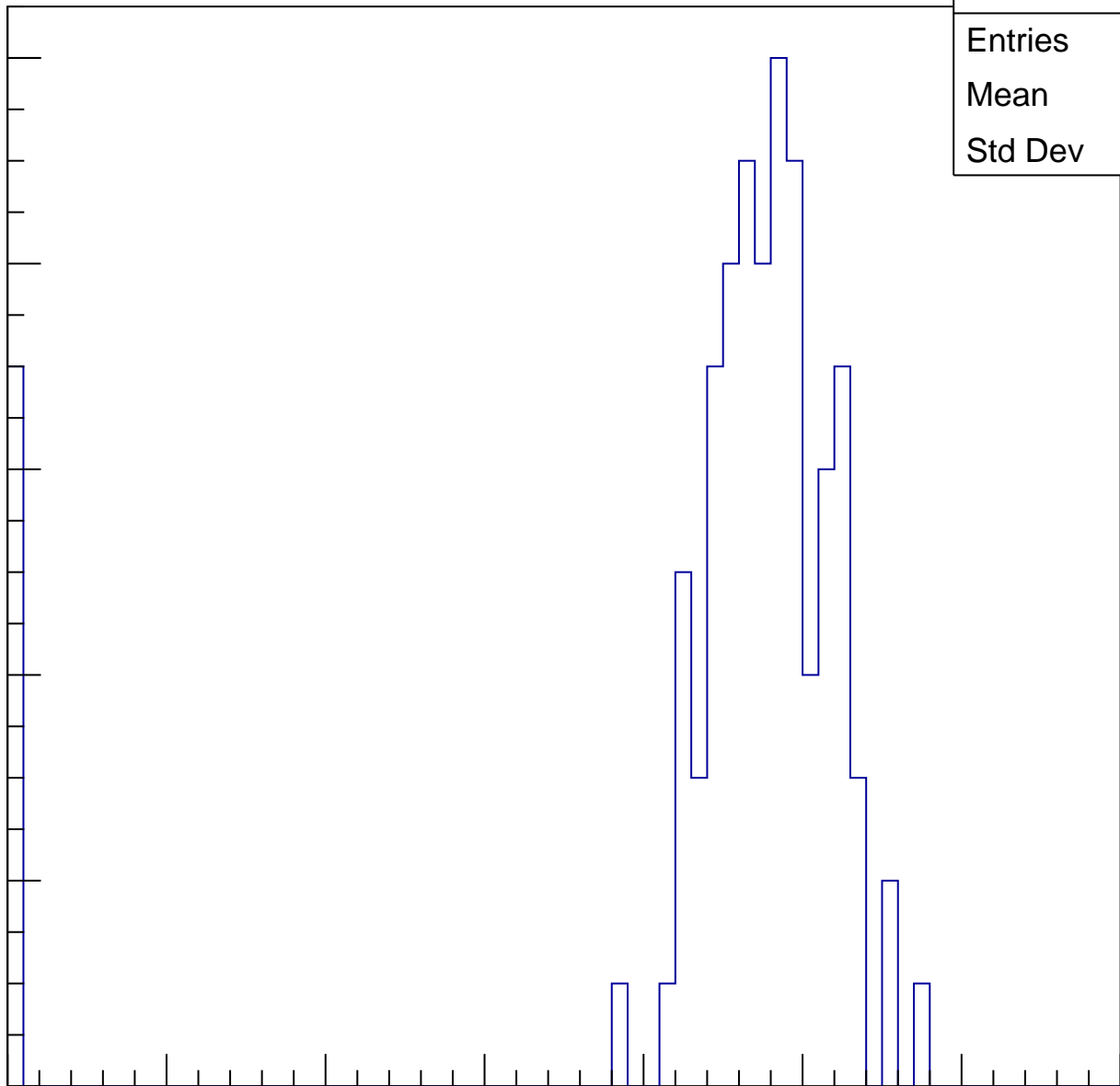
Entries	91
Mean	43.88
Std Dev	13.12

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

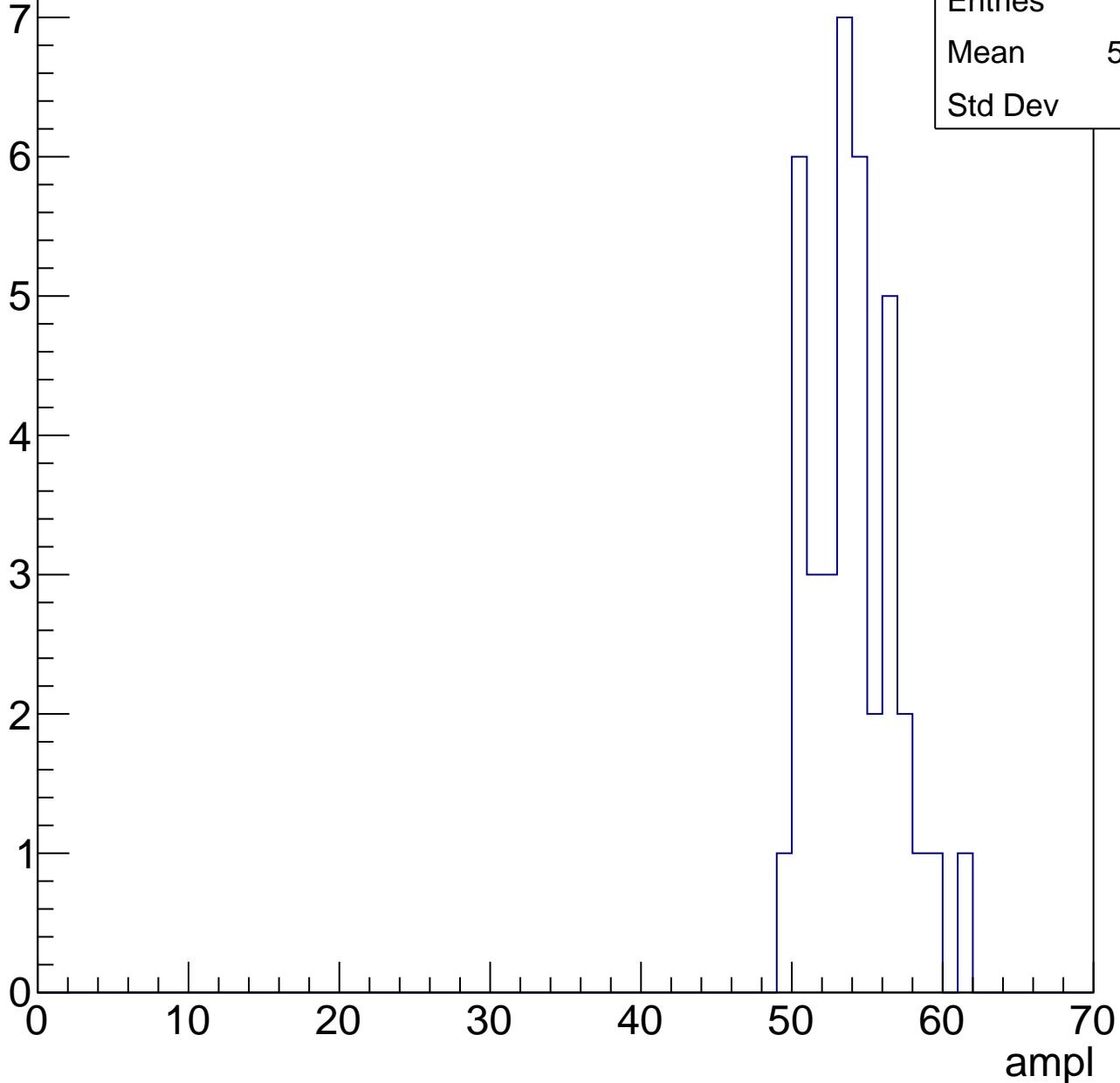


B1L103S, U10-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	53.55
Std Dev	2.76

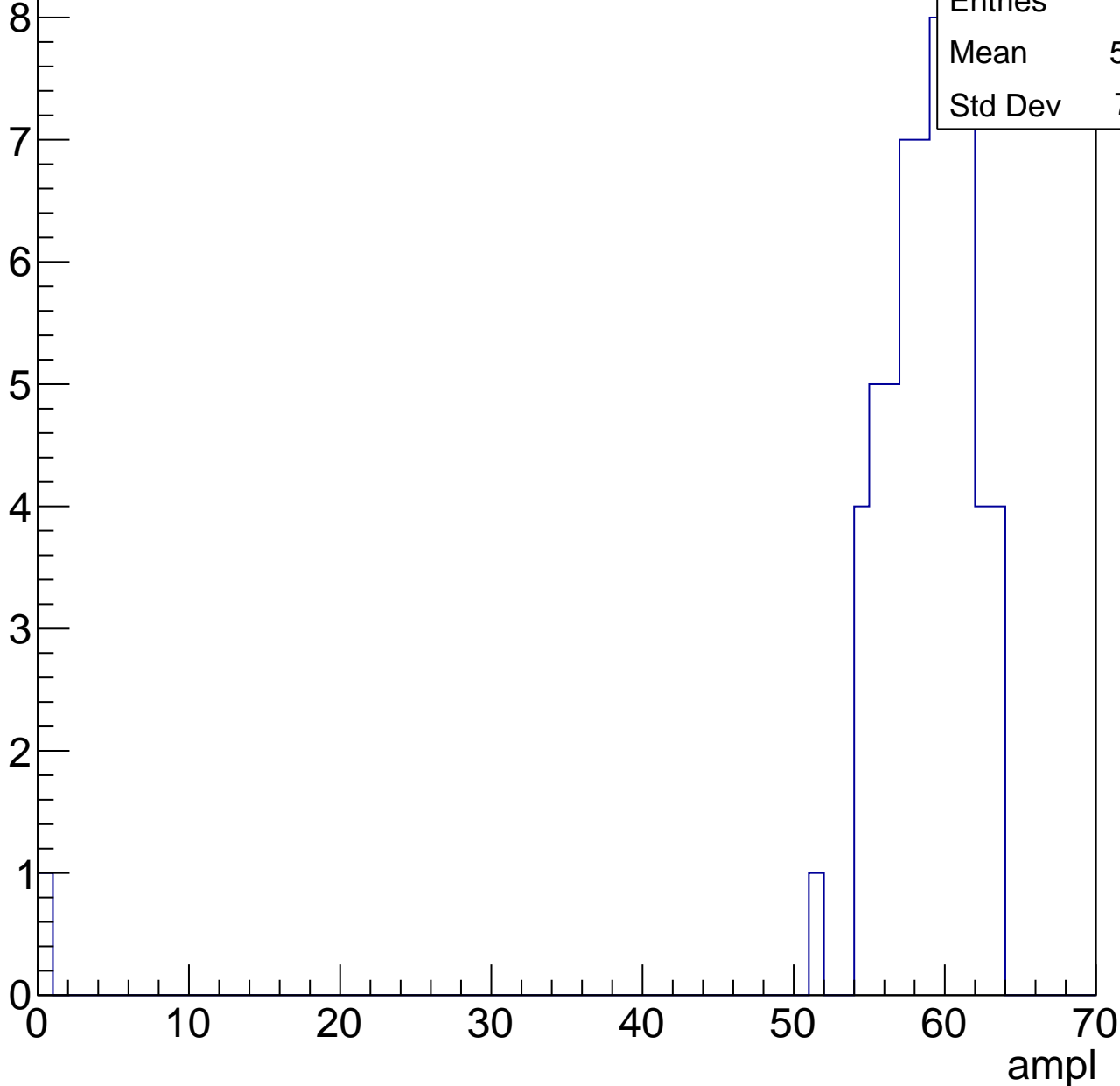


B1L103S, U10-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	57.53
Std Dev	7.841

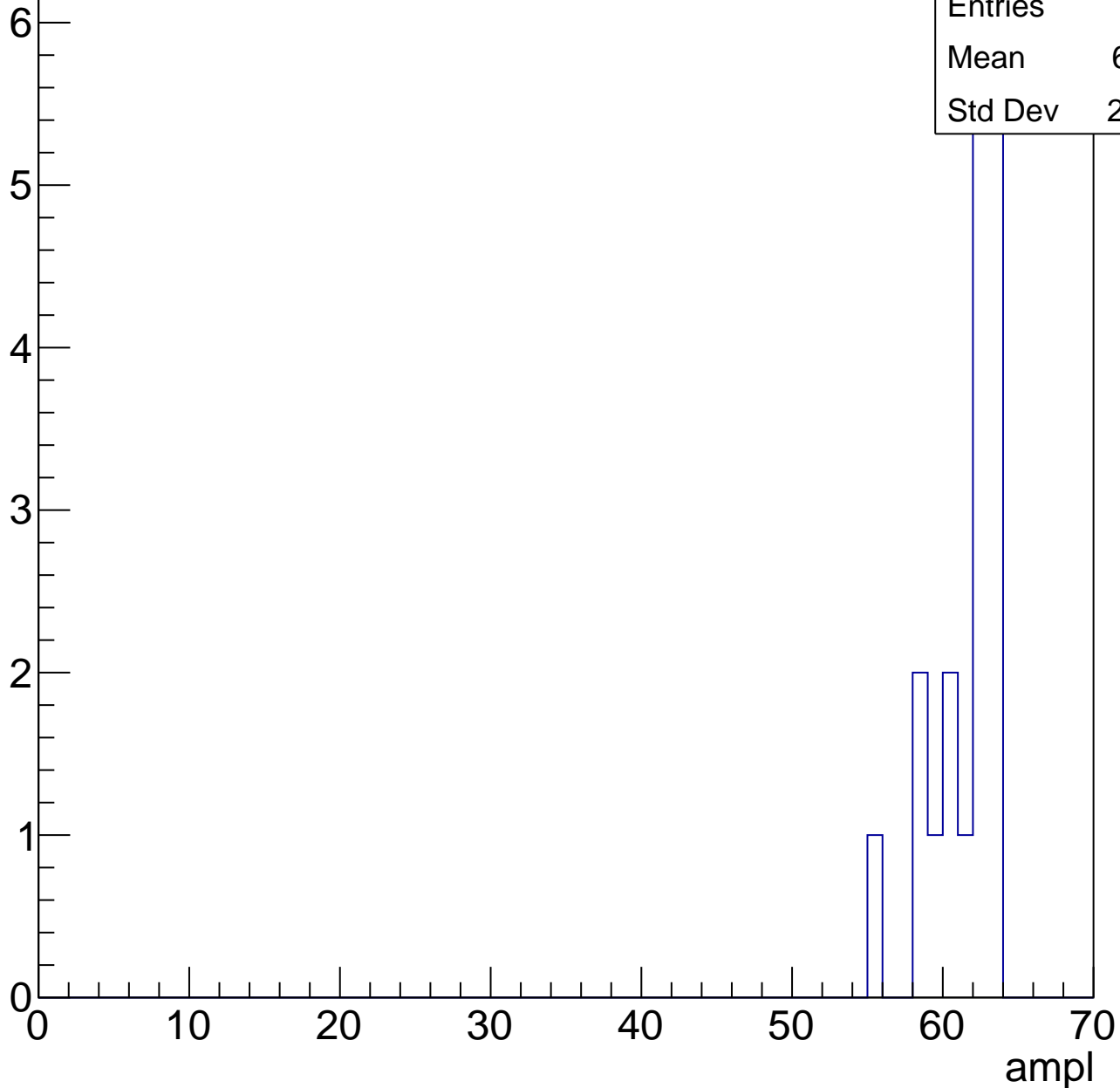


B1L103S, U10-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.11
Std Dev	2.174

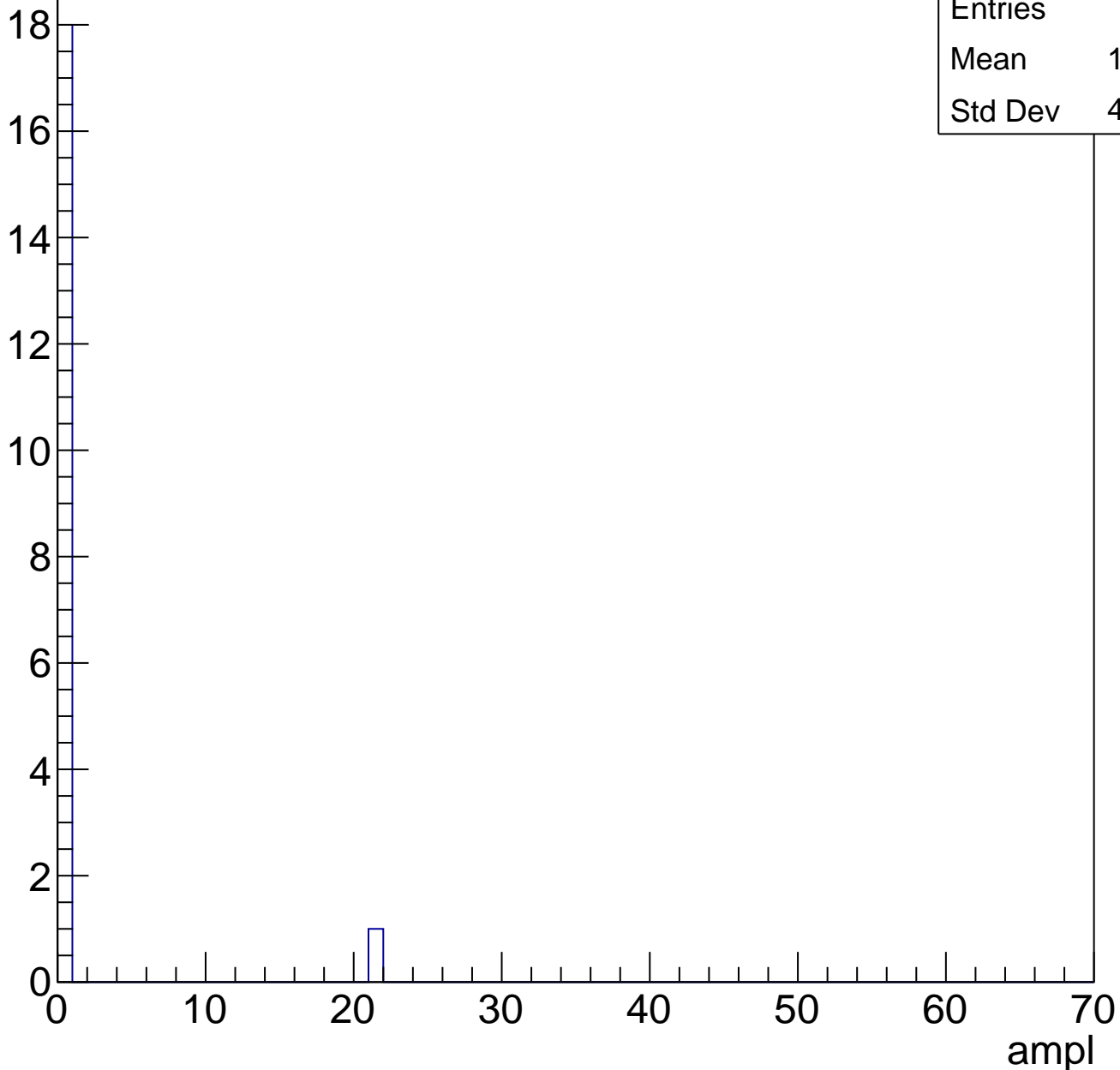


B1L103S, U10-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U10-ch59, adc0

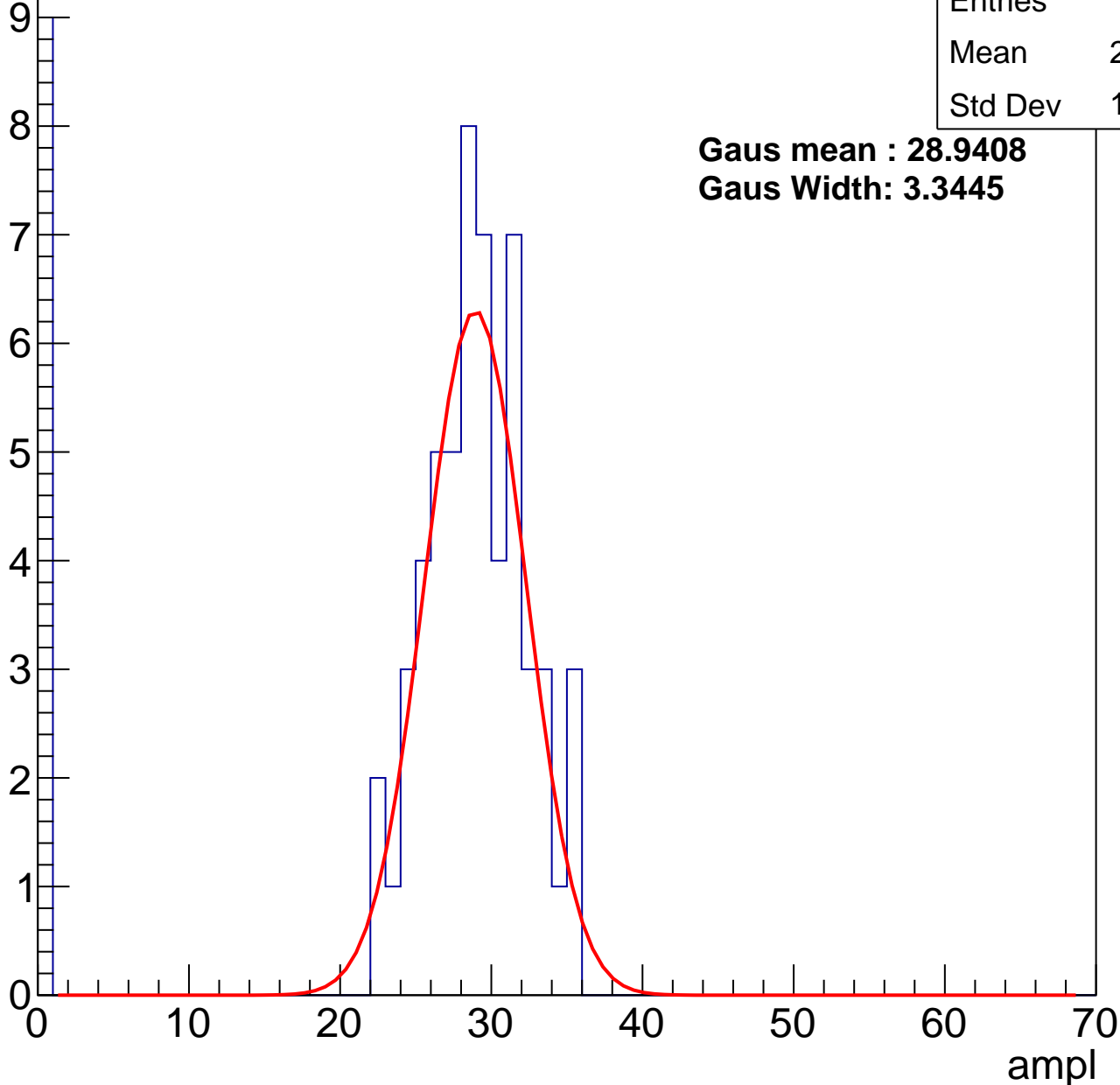
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	24.65
Std Dev	10.32

Gaus mean : 28.9408

Gaus Width: 3.3445



B1L103S, U10-ch59, adc1

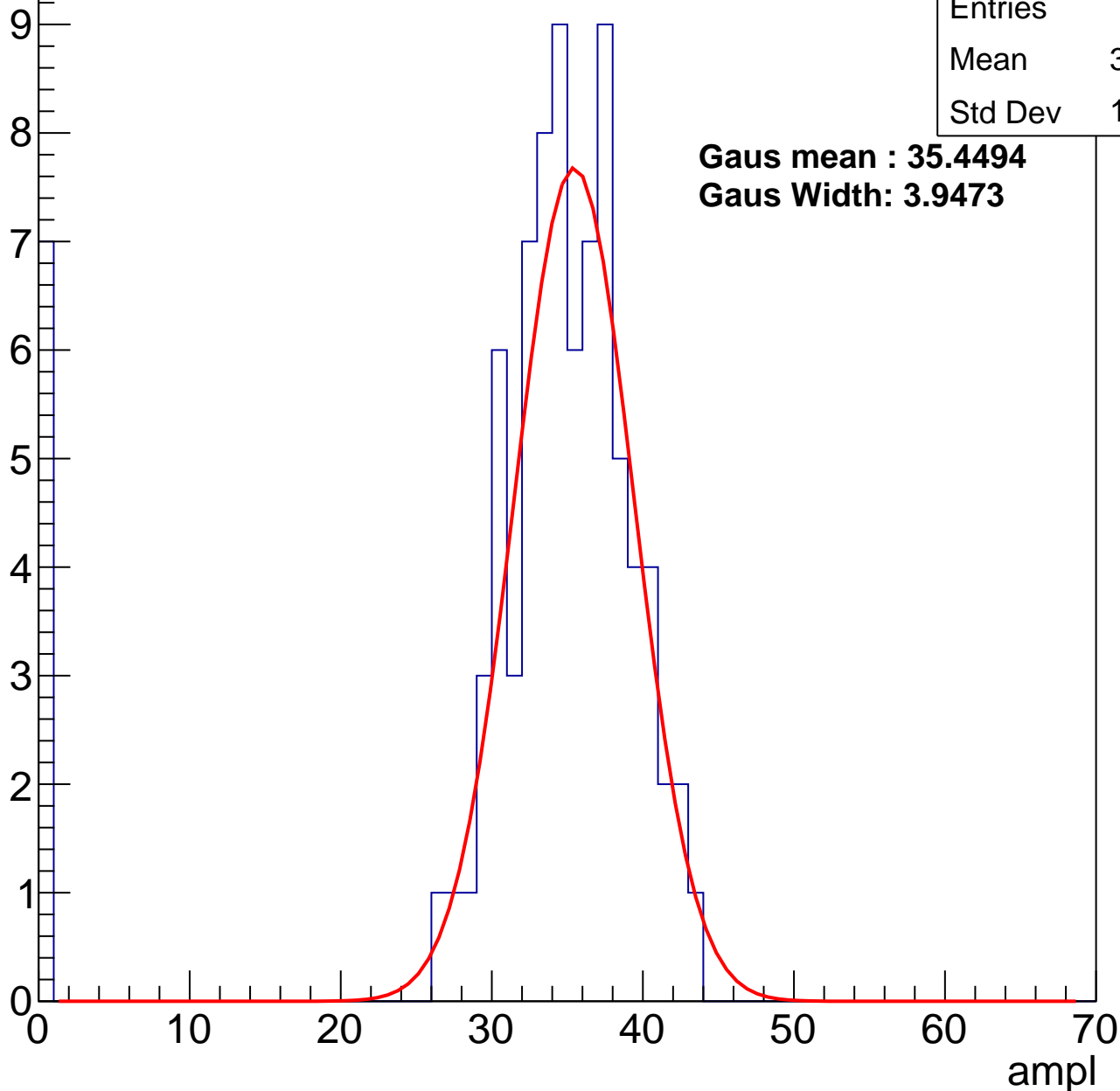
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	31.92
Std Dev	10.14

Gaus mean : 35.4494

Gaus Width: 3.9473



B1L103S, U10-ch59, adc2

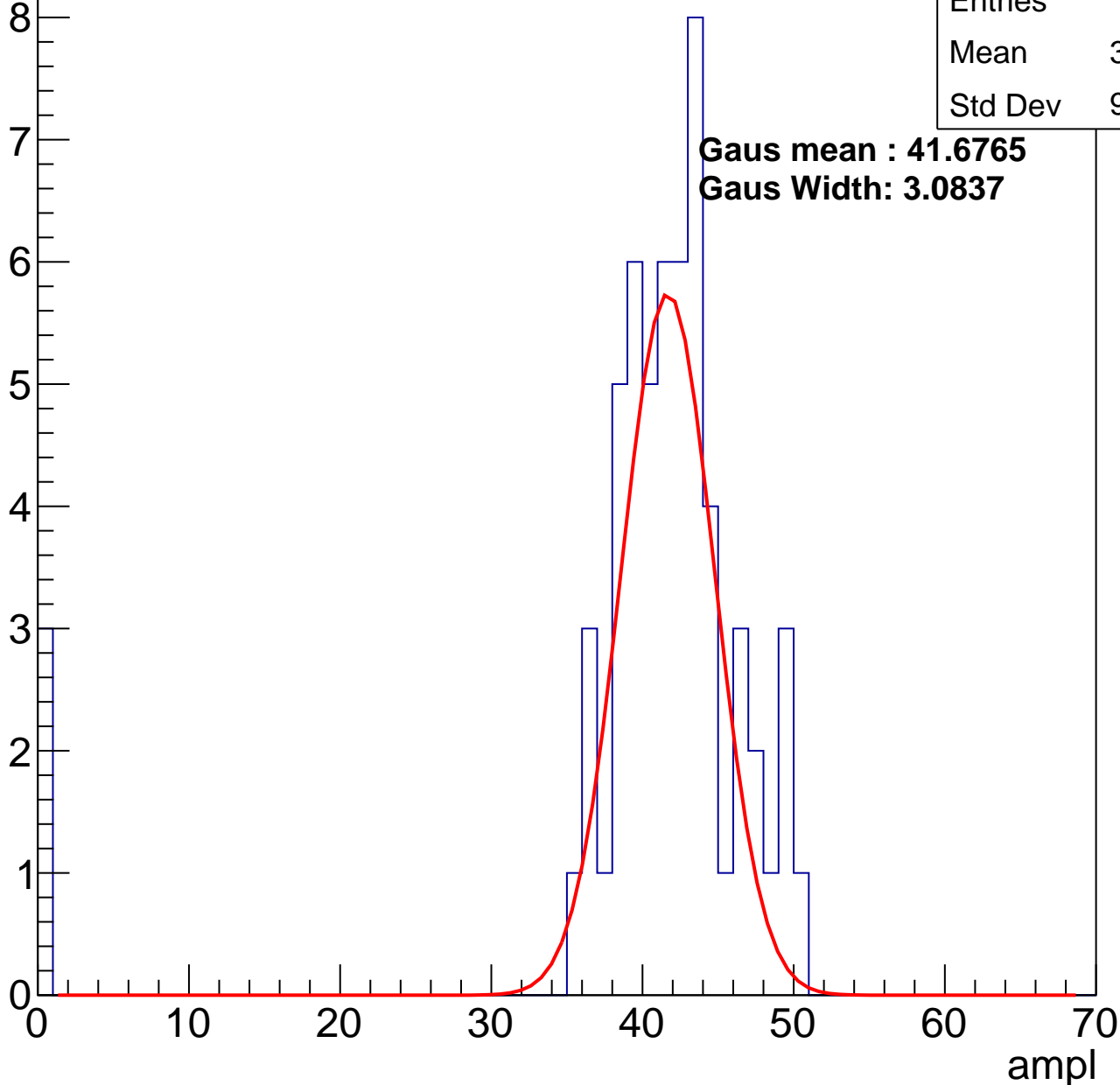
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	39.73
Std Dev	9.836

Gaus mean : 41.6765

Gaus Width: 3.0837

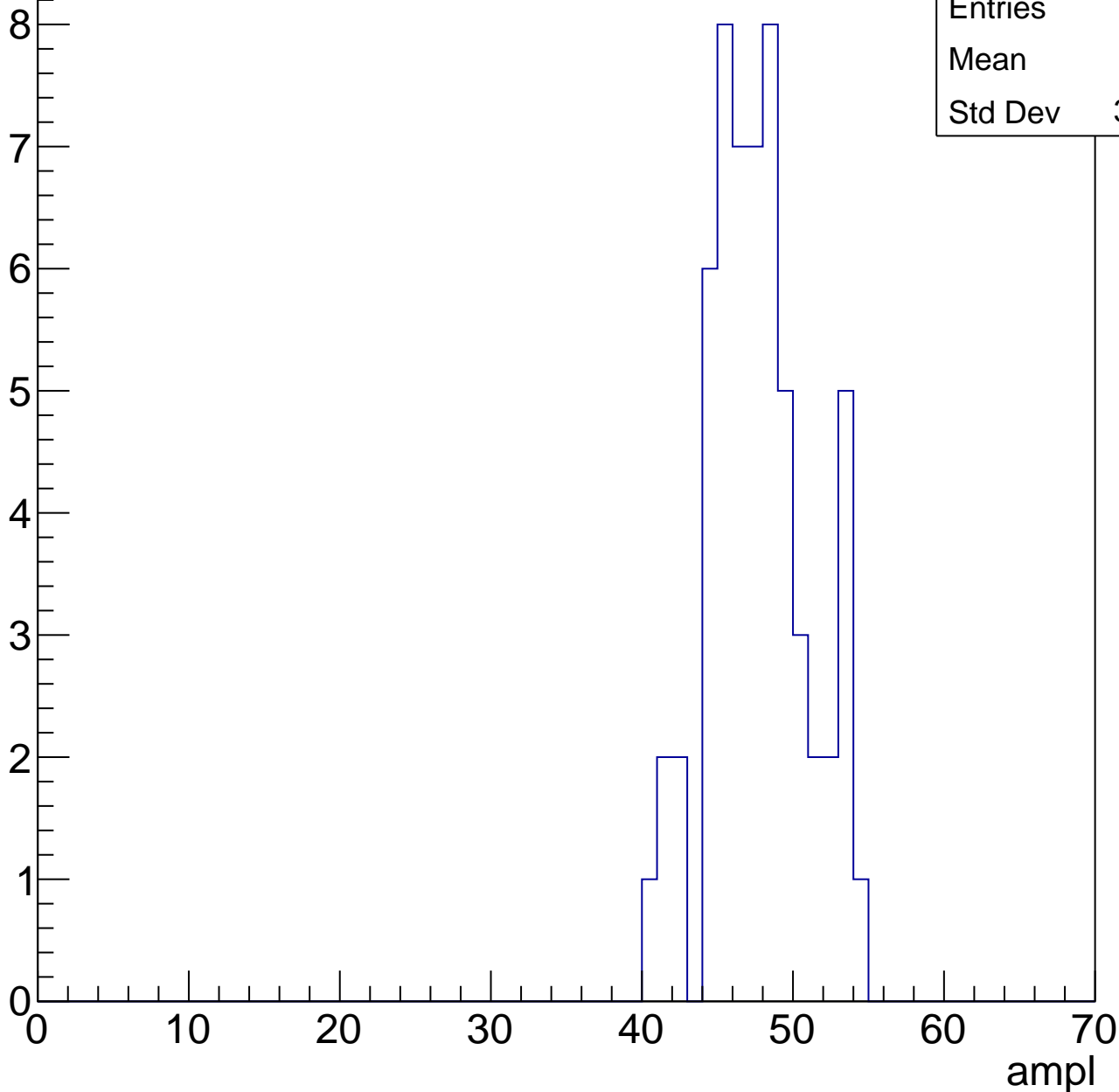


B1L103S, U10-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.2
Std Dev	3.261

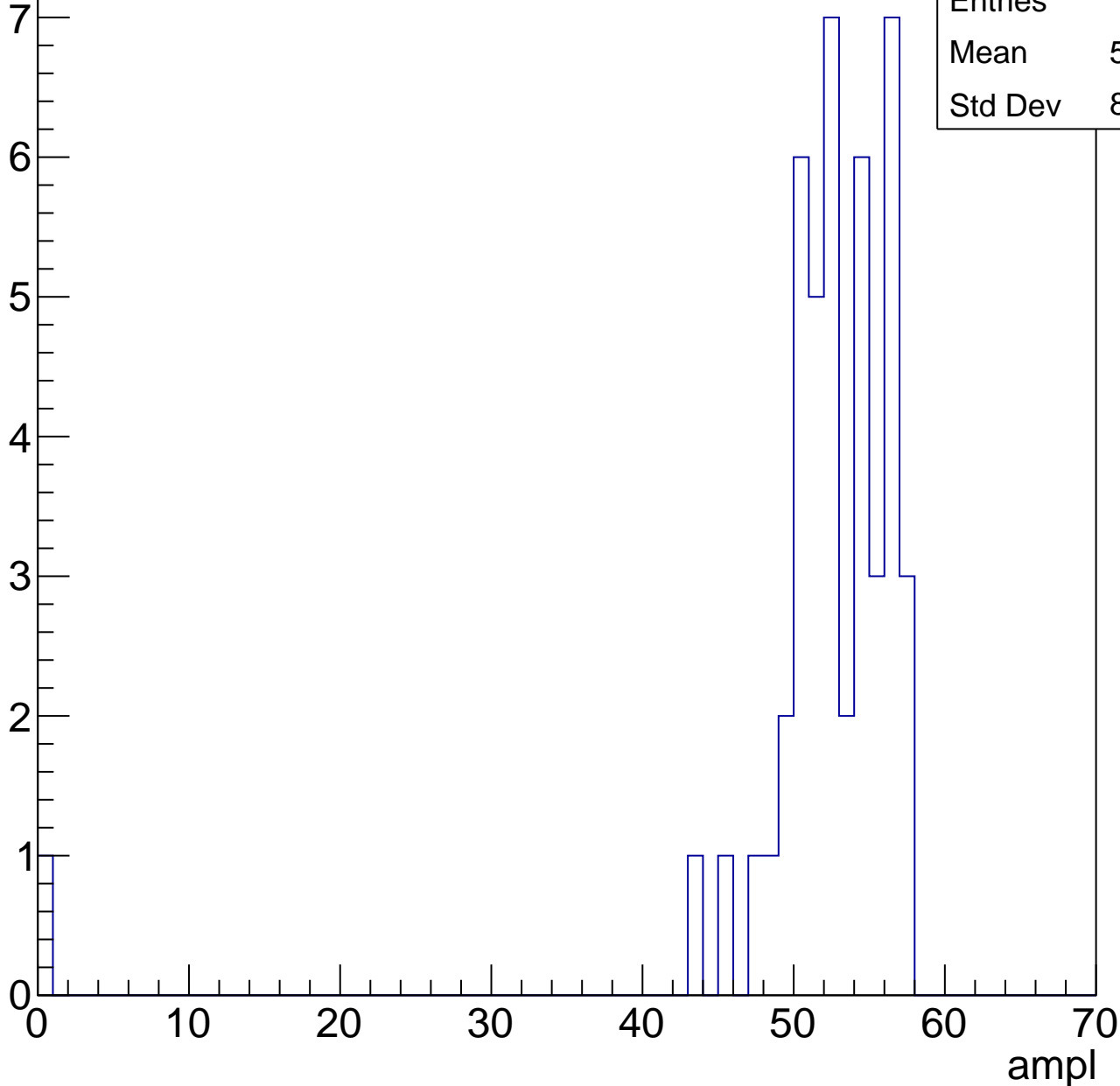


B1L103S, U10-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

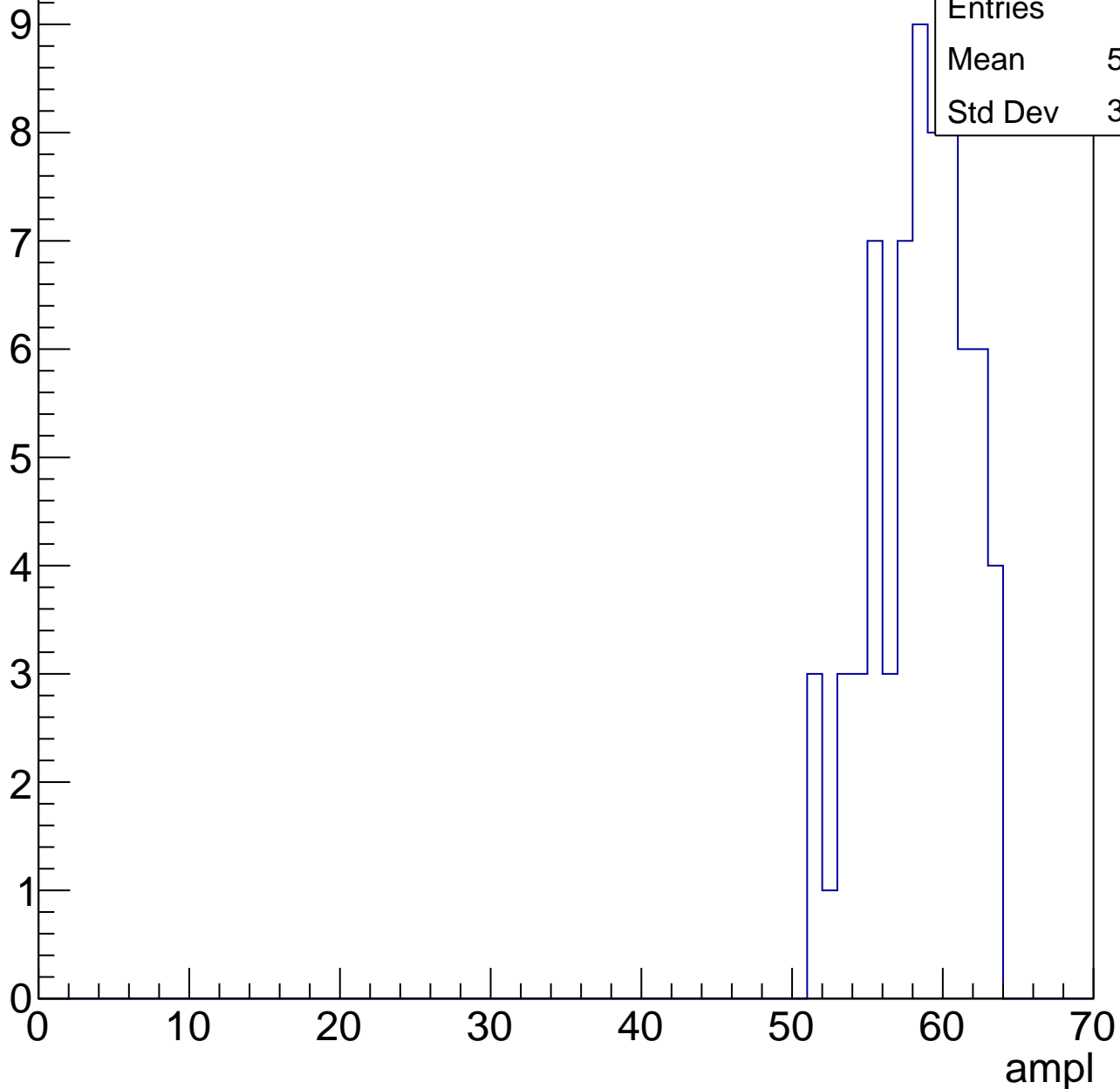
Entries	46
Mean	51.26
Std Dev	8.258



B1L103S, U10-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

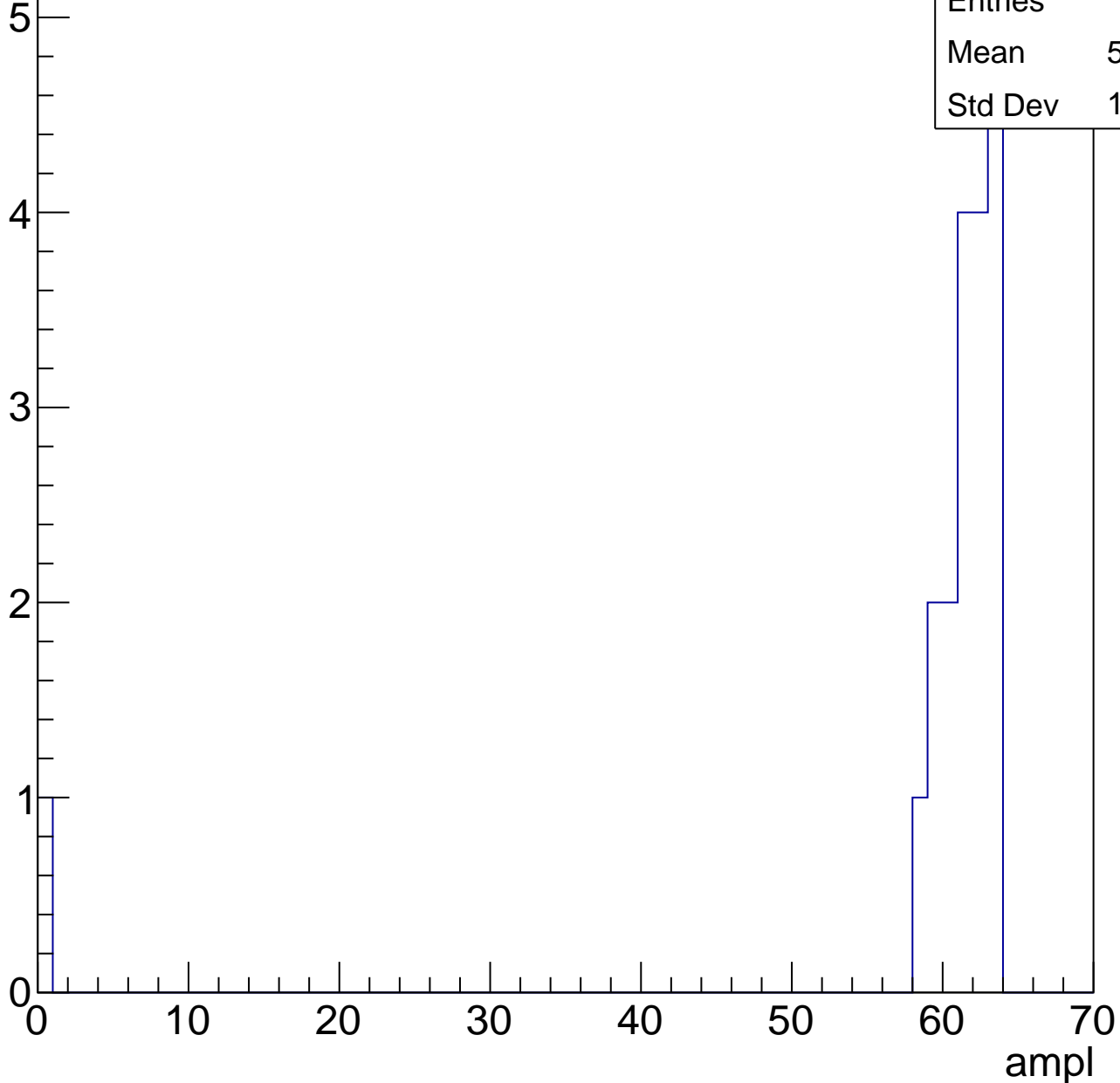


B1L103S, U10-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.05
Std Dev	13.76

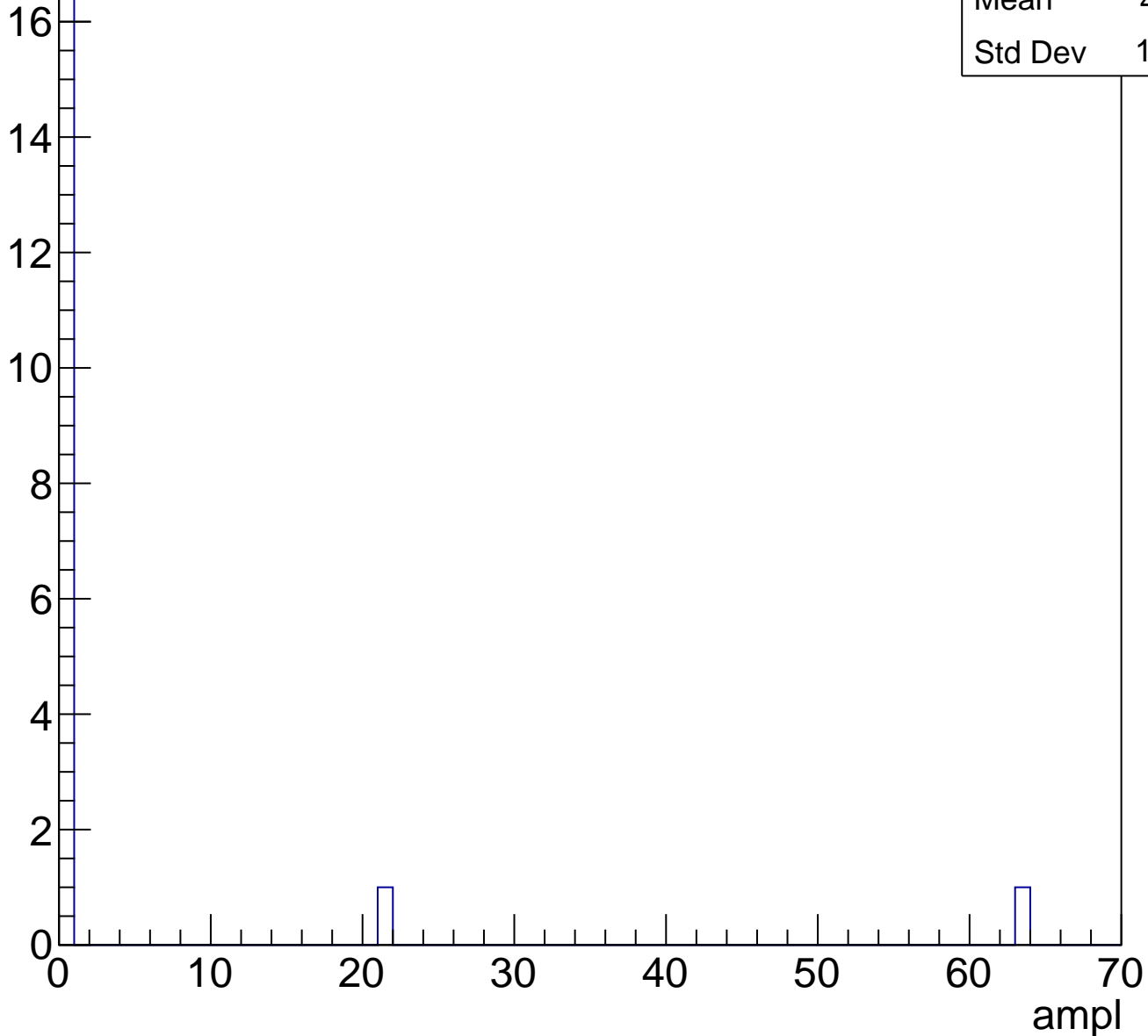


B1L103S, U10-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.421
Std Dev	14.58

Entry



B1L103S, U10-ch60, adc0

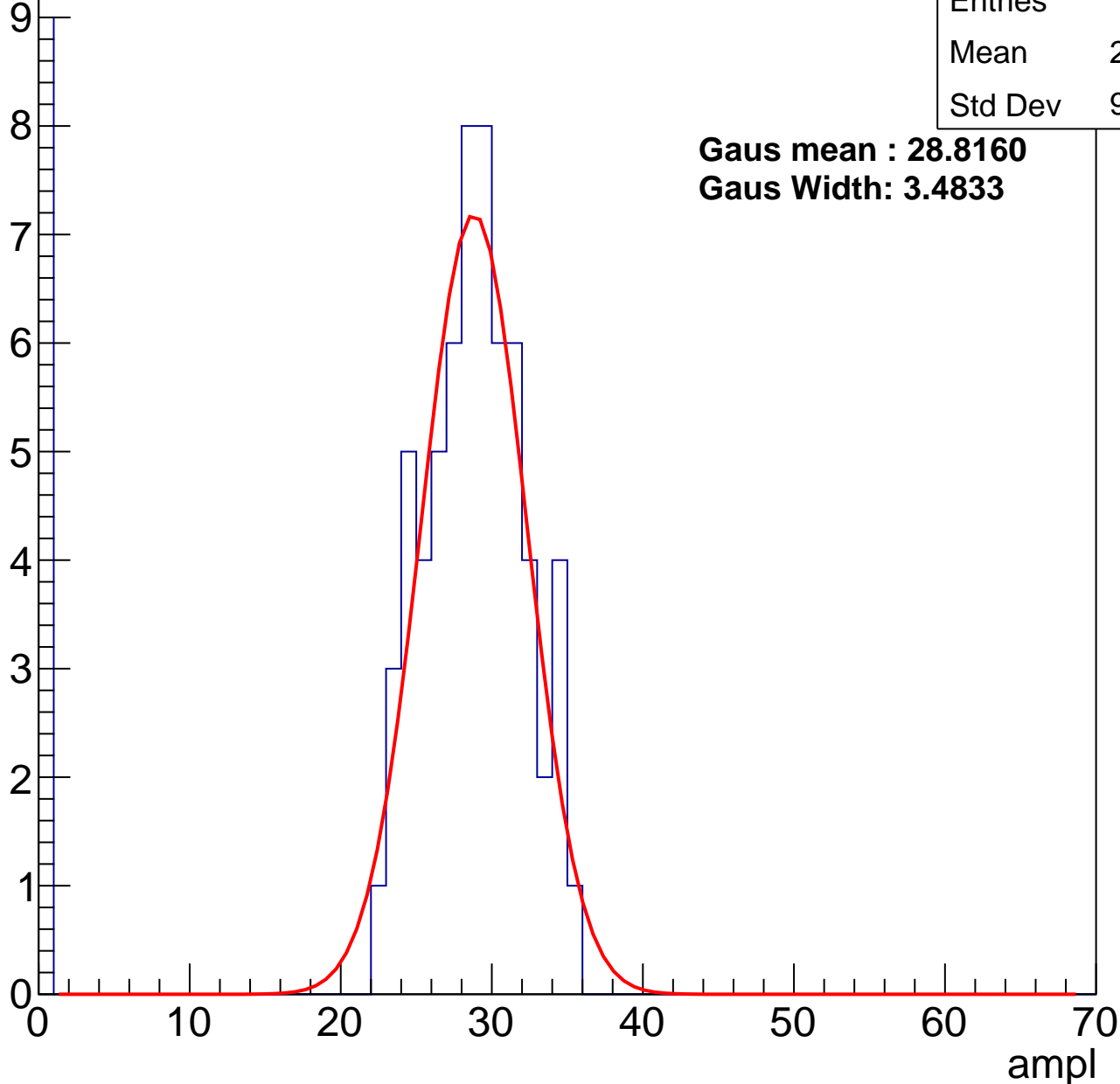
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	24.86
Std Dev	9.855

Gaus mean : 28.8160

Gaus Width: 3.4833



B1L103S, U10-ch60, adc1

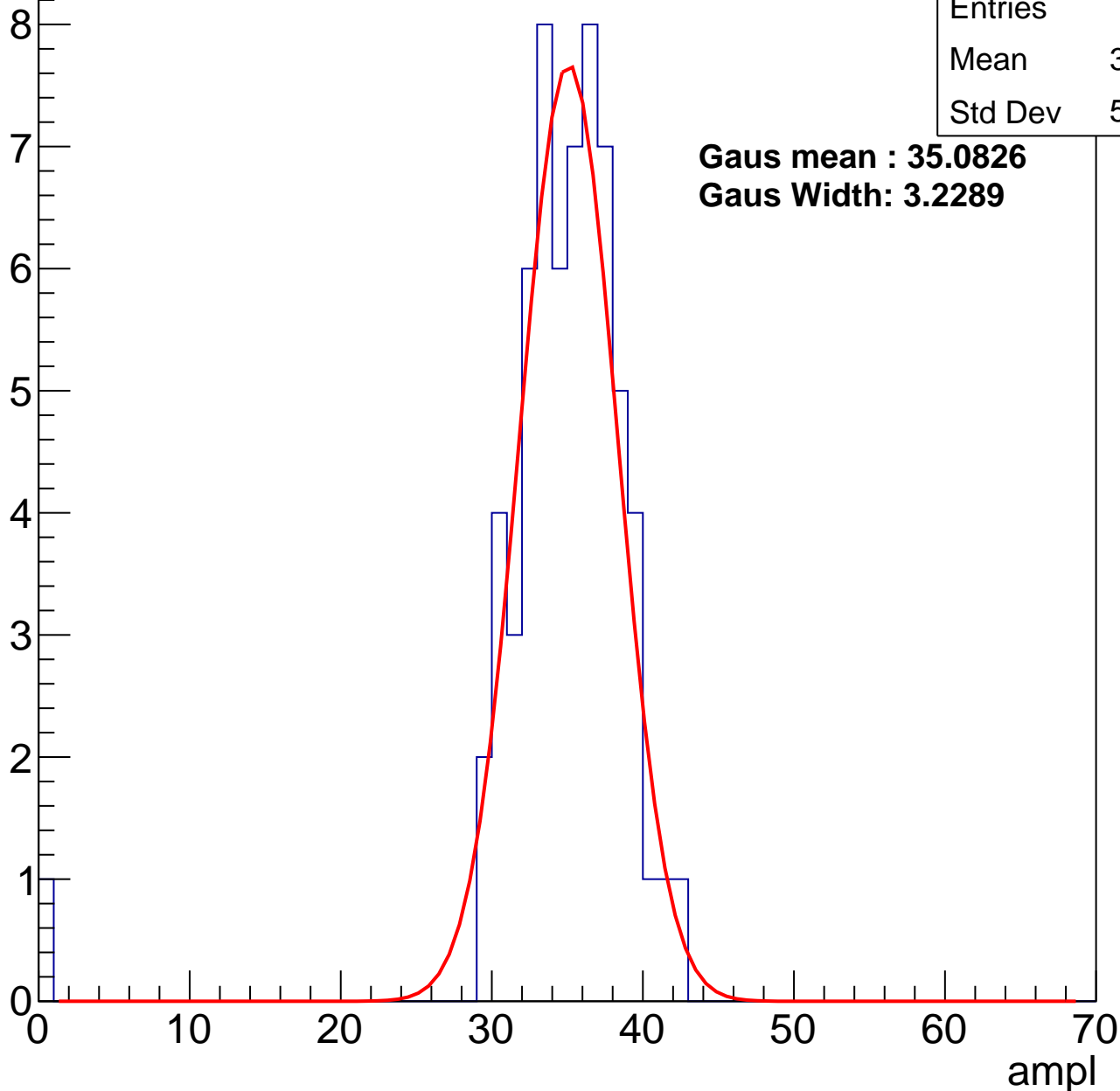
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.25
Std Dev	5.238

Gaus mean : 35.0826

Gaus Width: 3.2289



B1L103S, U10-ch60, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	36.03
Std Dev	15.31

Gaus mean : 42.2824

Gaus Width: 4.0004

Entry

12

10

8

6

4

2

0

0

10

20

30

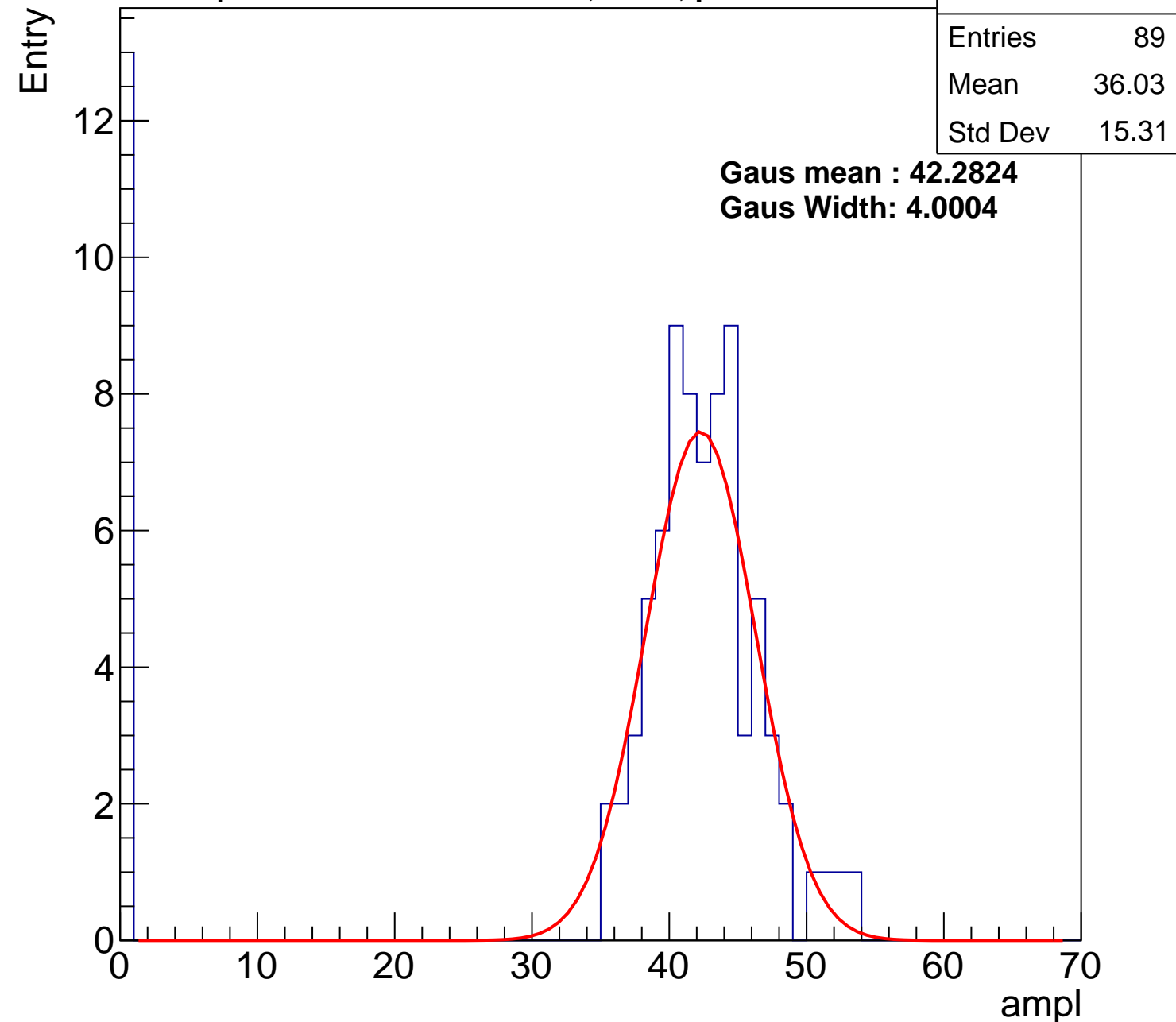
40

50

60

70

ampl

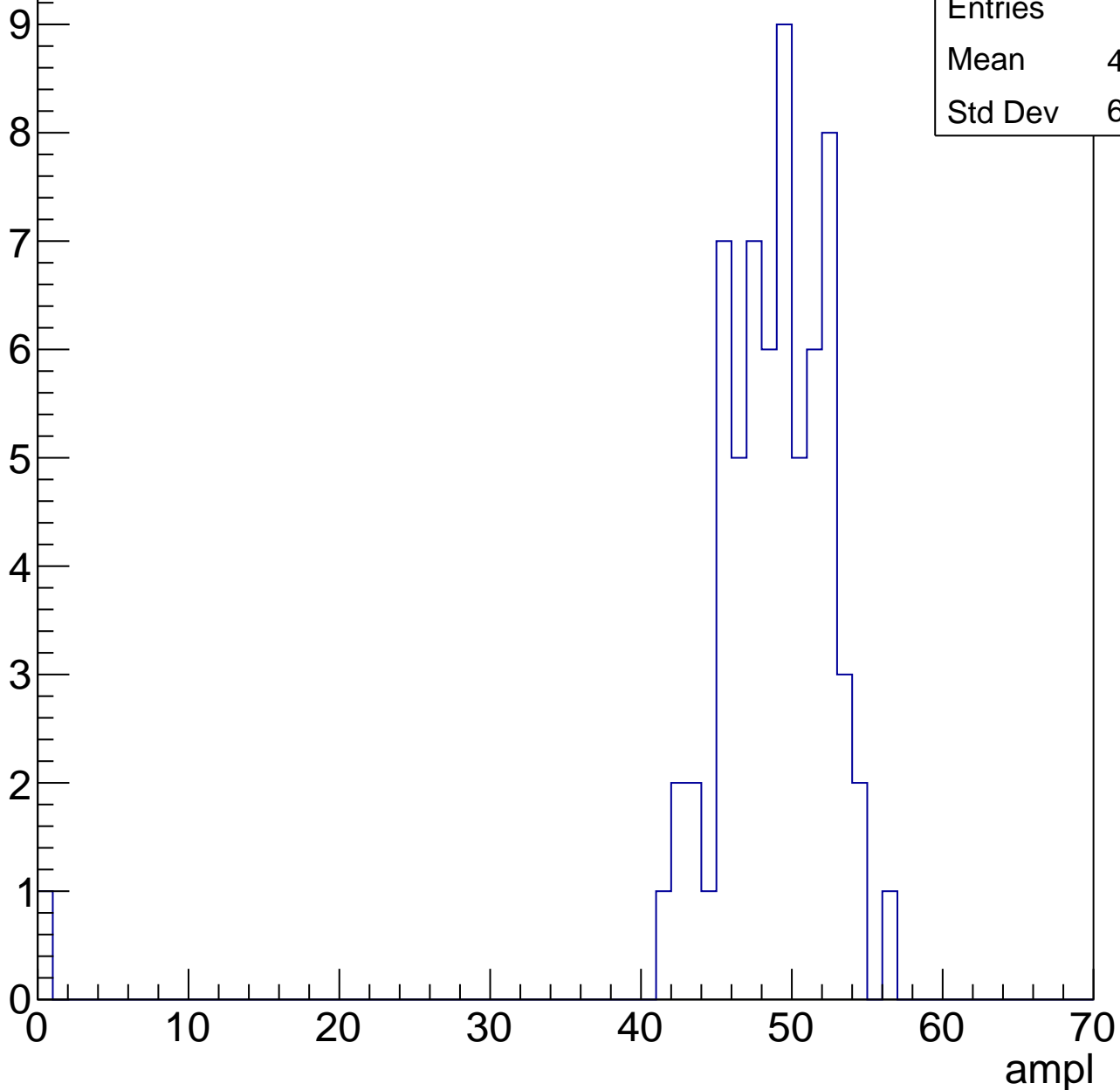


B1L103S, U10-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

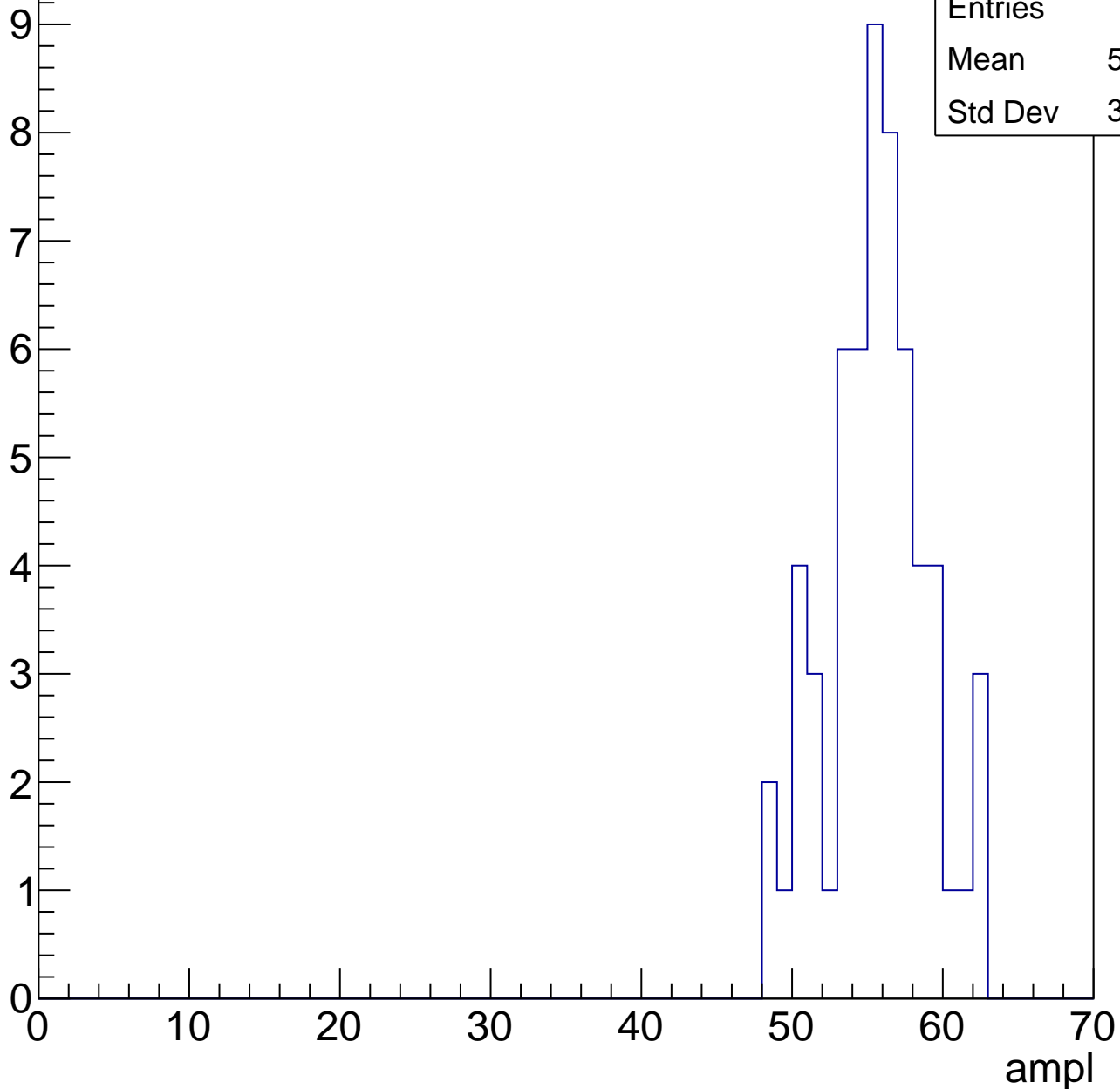
Entries	66
Mean	47.77
Std Dev	6.742



B1L103S, U10-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

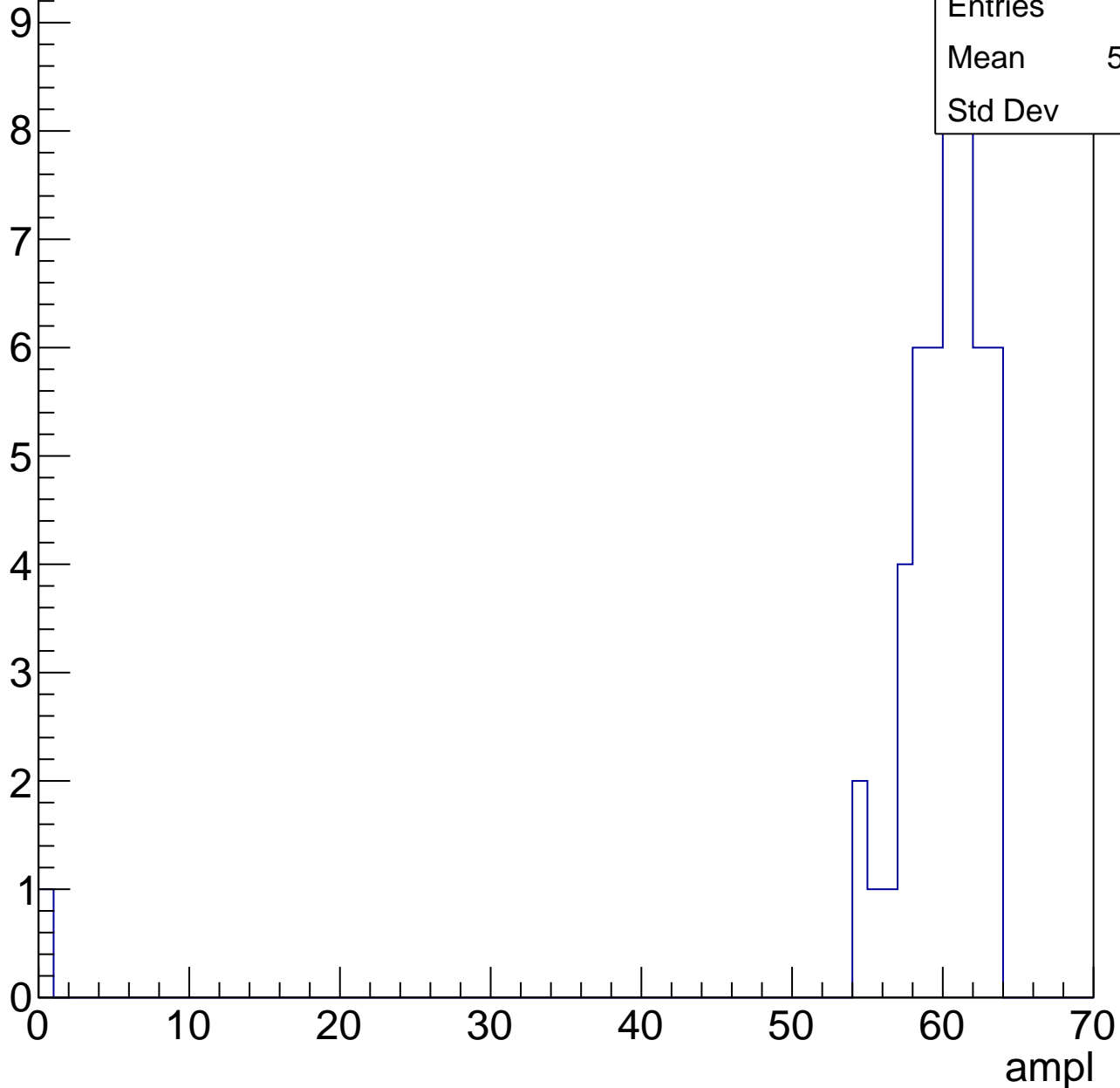


Entries	59
Mean	55.12
Std Dev	3.345

B1L103S, U10-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch61, adc0

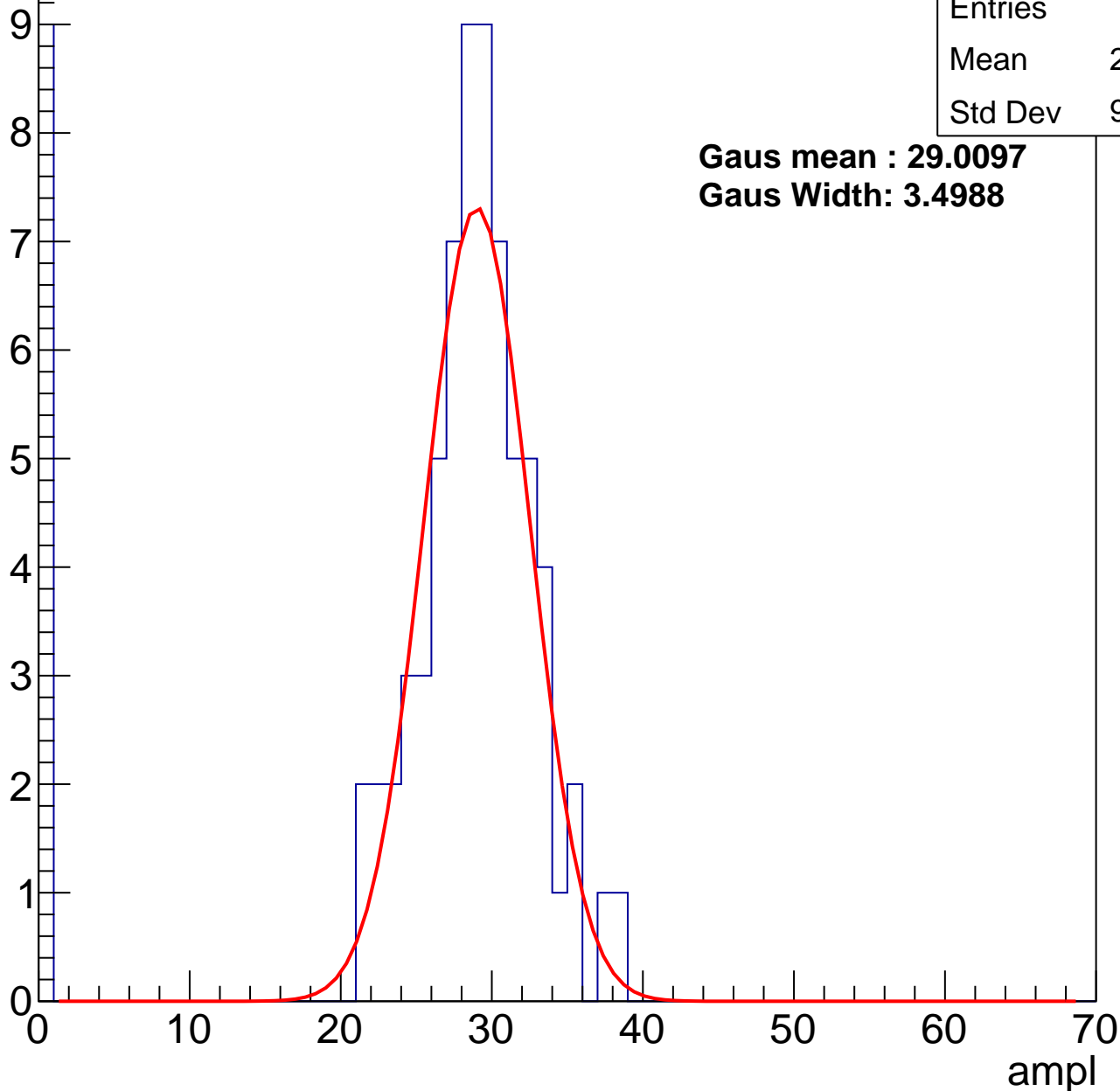
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	25.29
Std Dev	9.795

Gaus mean : 29.0097

Gaus Width: 3.4988



B1L103S, U10-ch61, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	31.54
Std Dev	11.46

Gaus mean : 35.8298

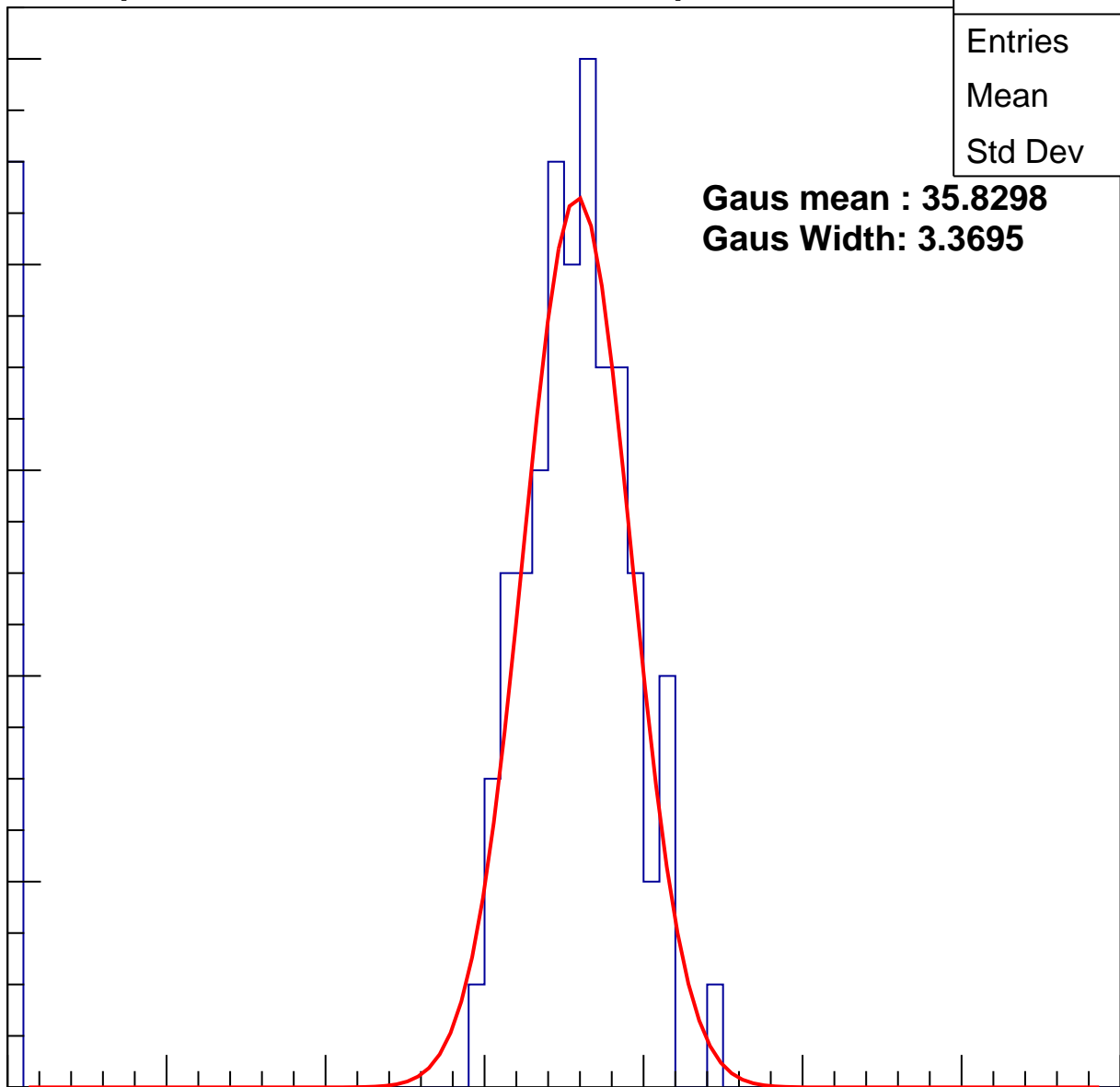
Gaus Width: 3.3695

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch61, adc2

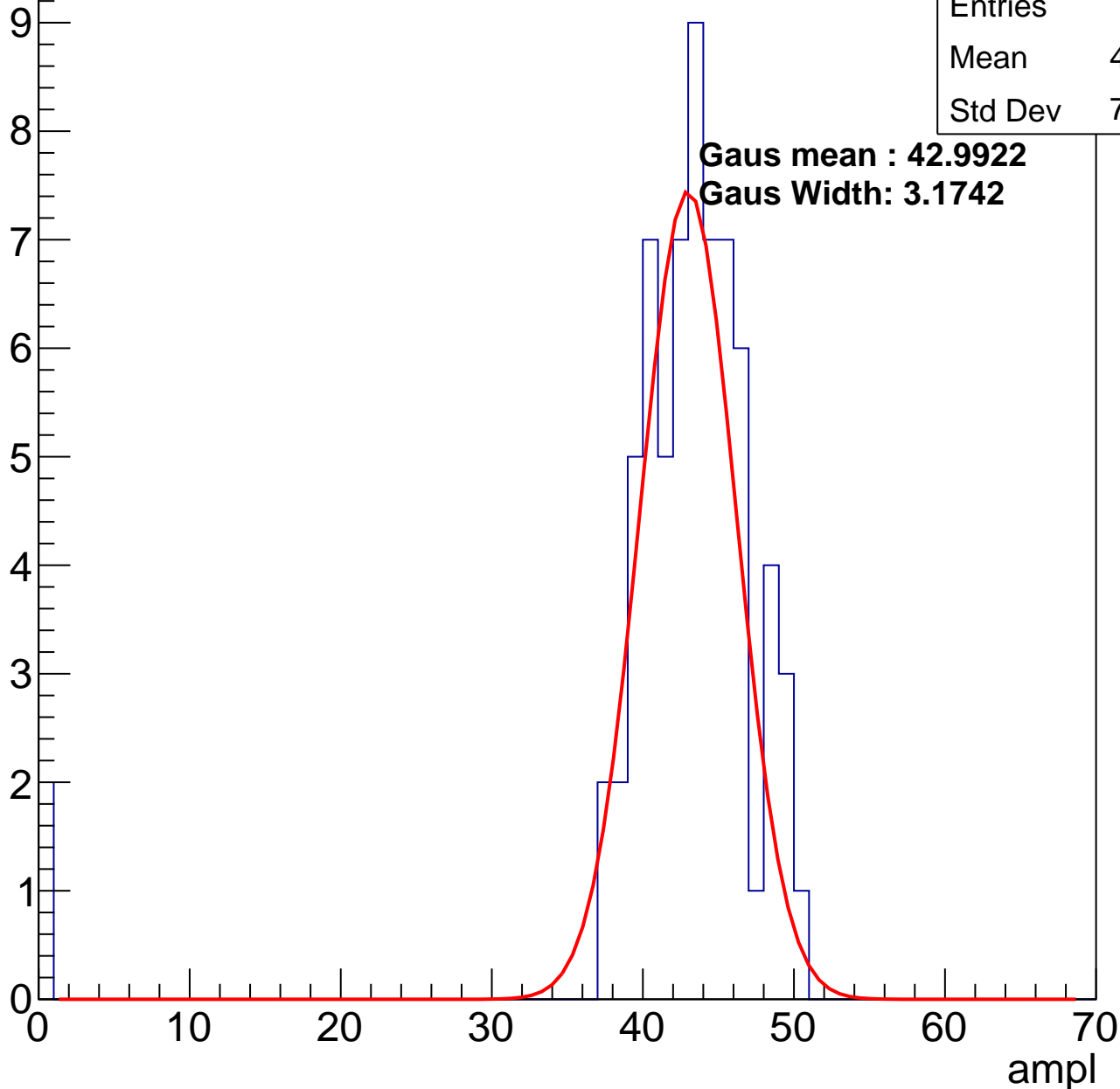
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.85
Std Dev	7.917

Gaus mean : 42.9922

Gaus Width: 3.1742

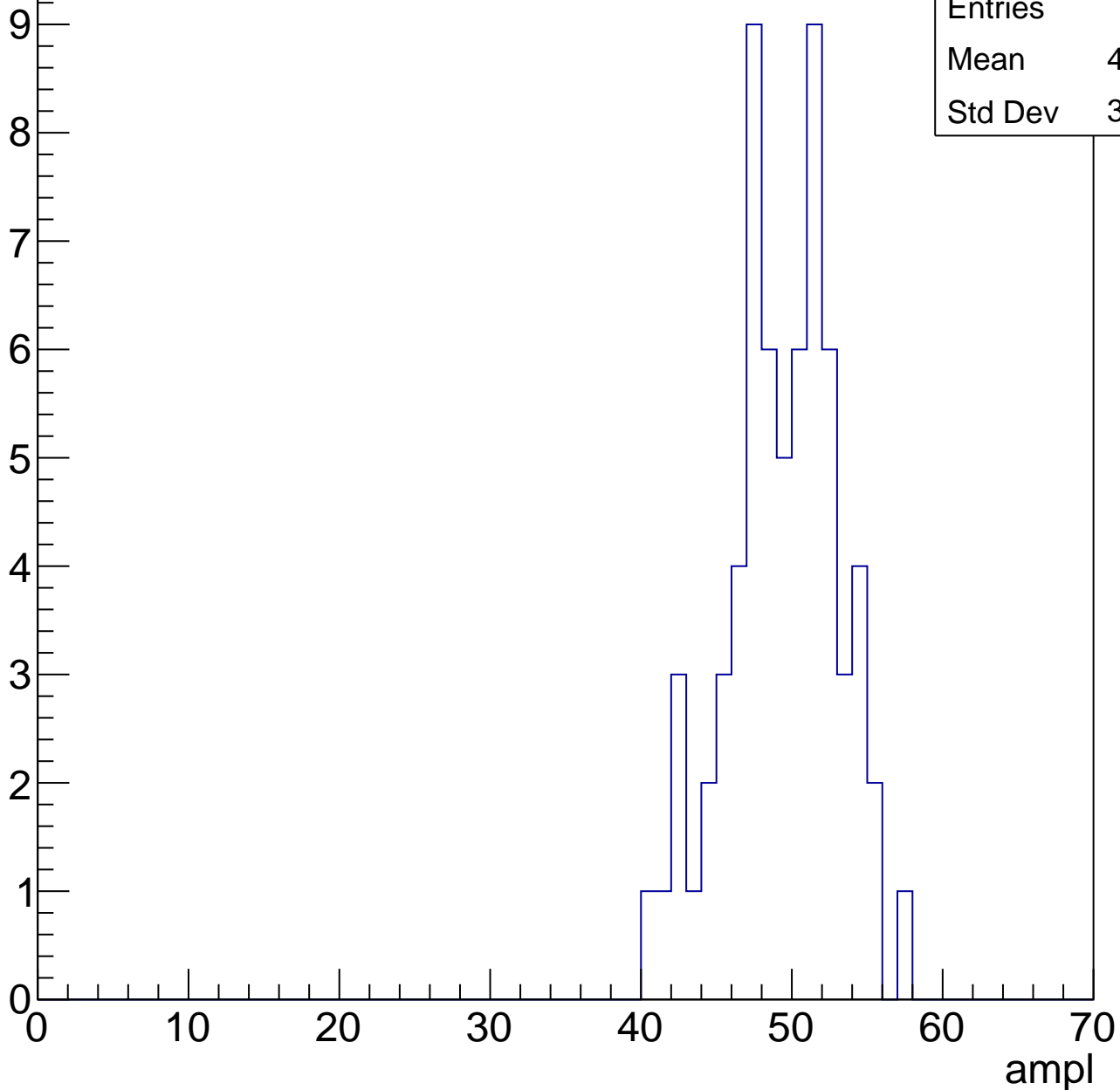


B1L103S, U10-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

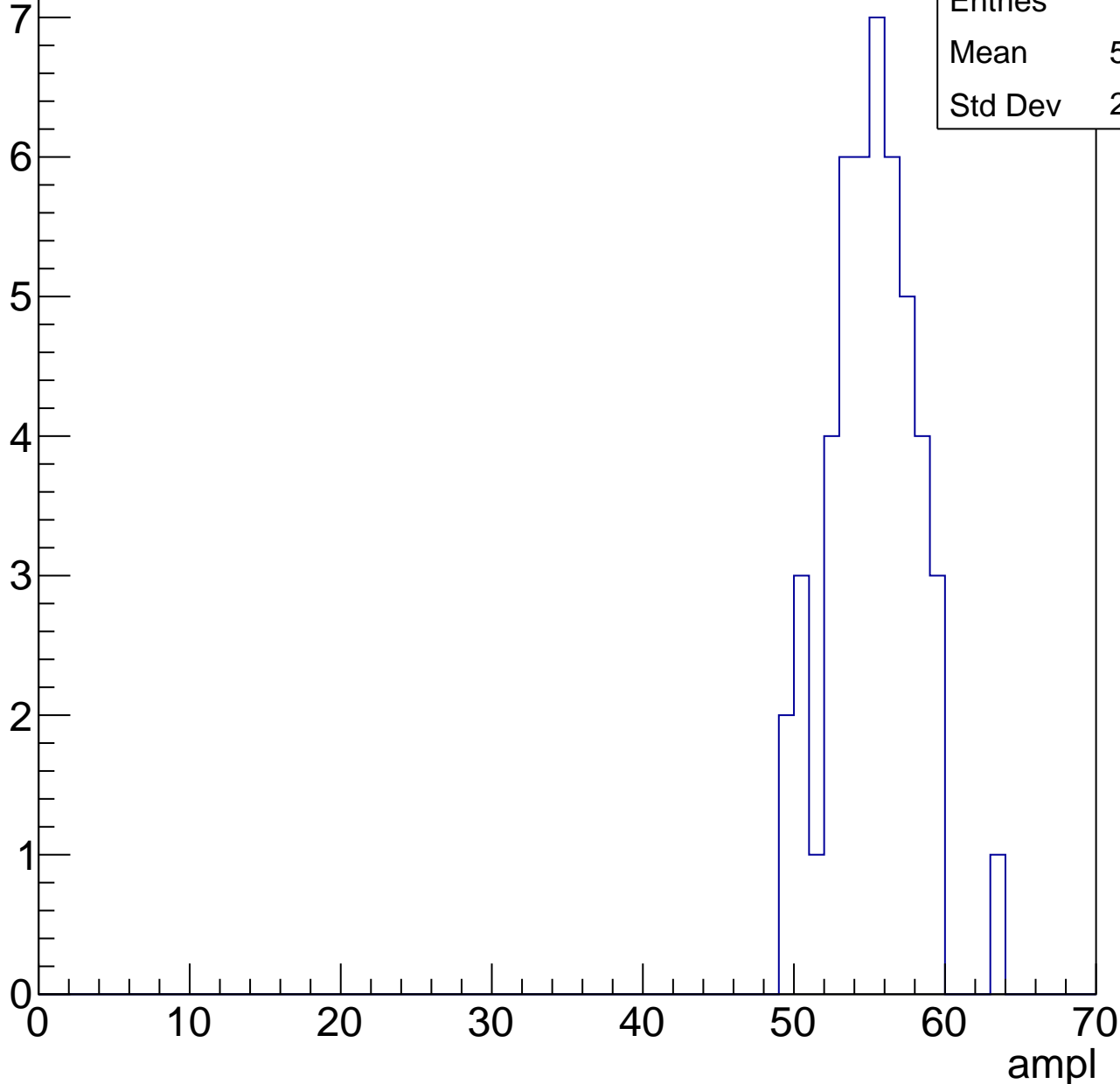
Entries	66
Mean	48.88
Std Dev	3.658



B1L103S, U10-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	48
Mean	54.73
Std Dev	2.885

B1L103S, U10-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

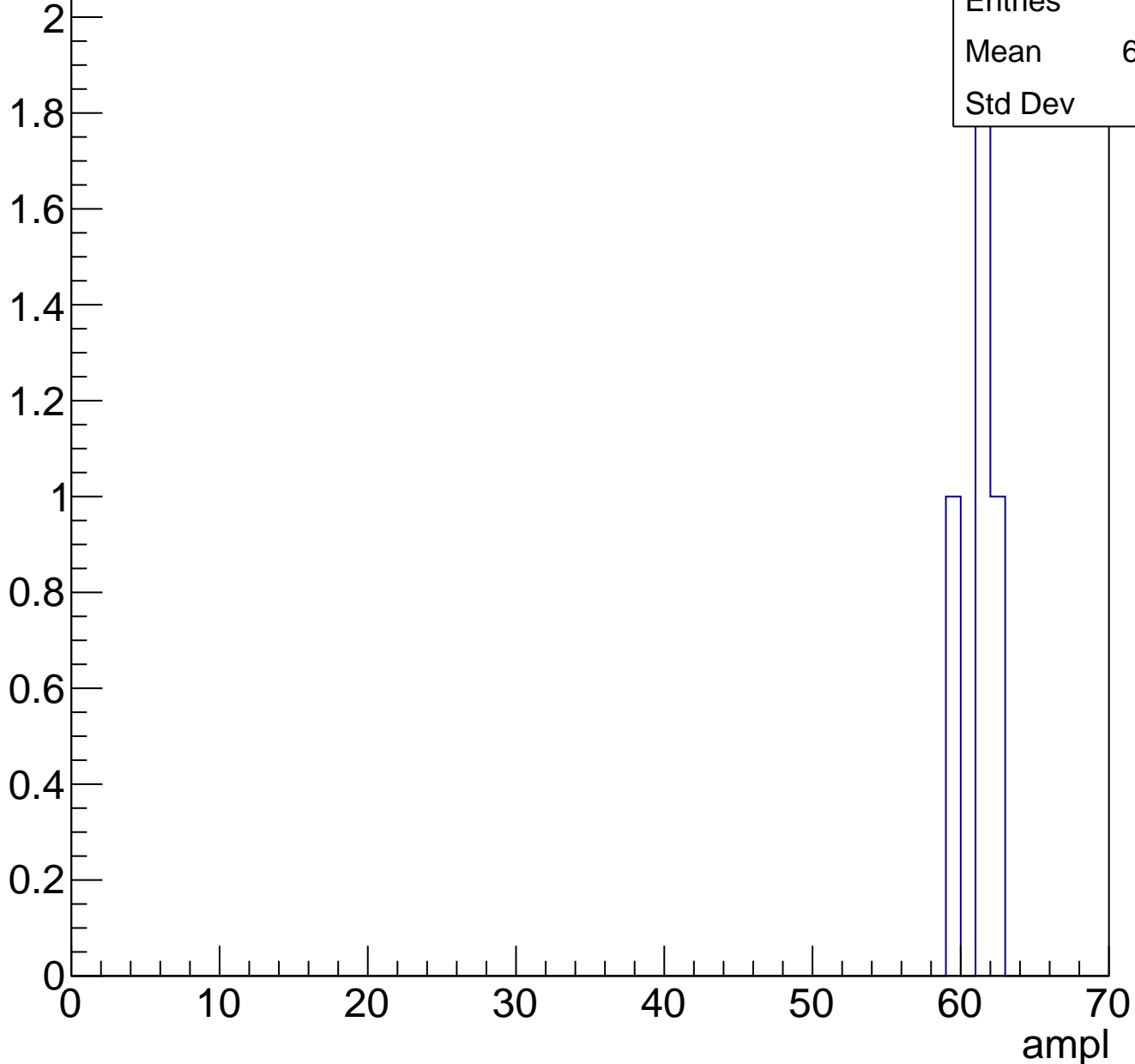
ampl

Entries	59
Mean	58.9
Std Dev	8.086

B1L103S, U10-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch62, adc0

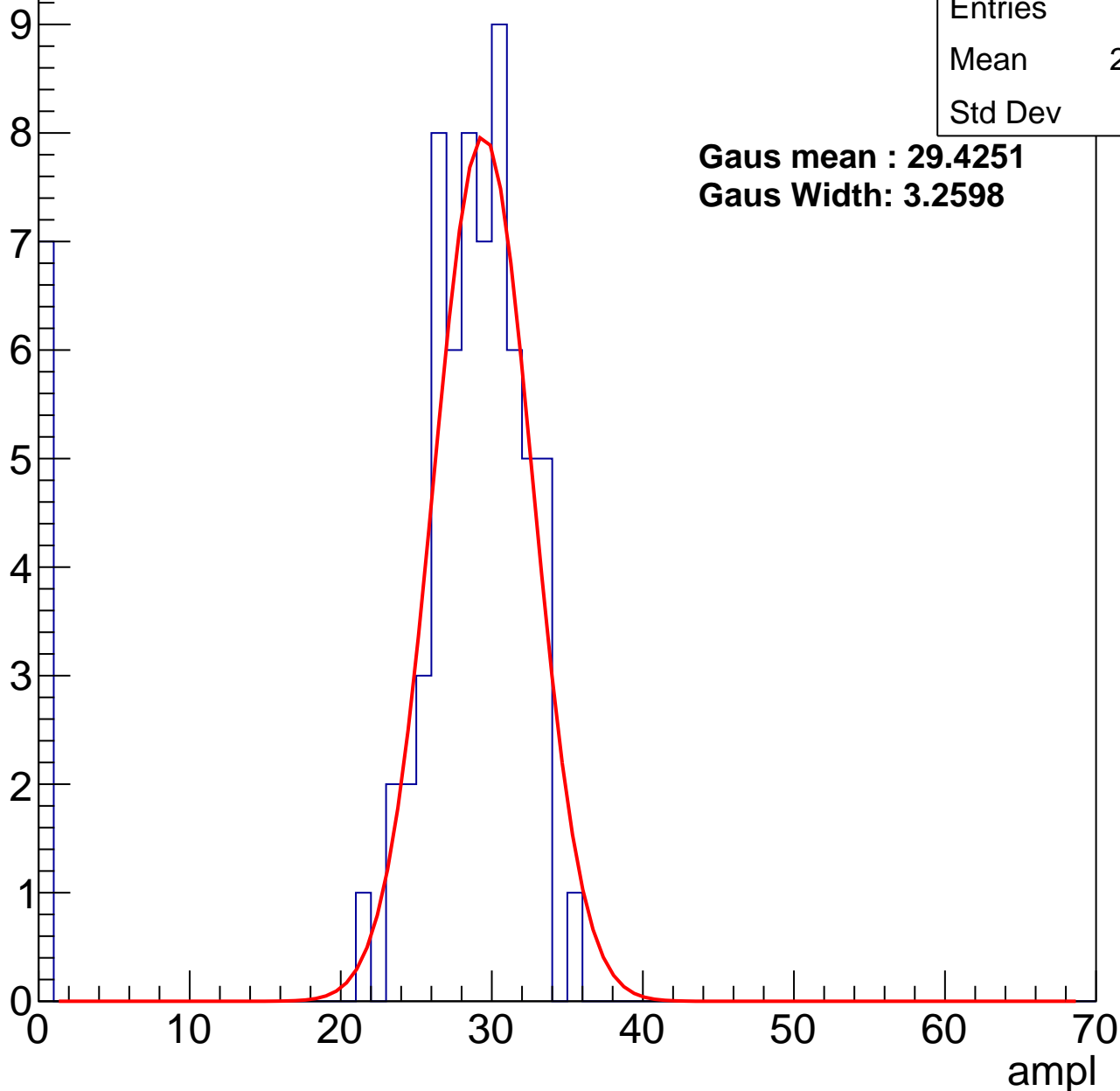
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	25.76
Std Dev	9.01

Gaus mean : 29.4251

Gaus Width: 3.2598



B1L103S, U10-ch62, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	34.55
Std Dev	5.458

Gaus mean : 35.4216

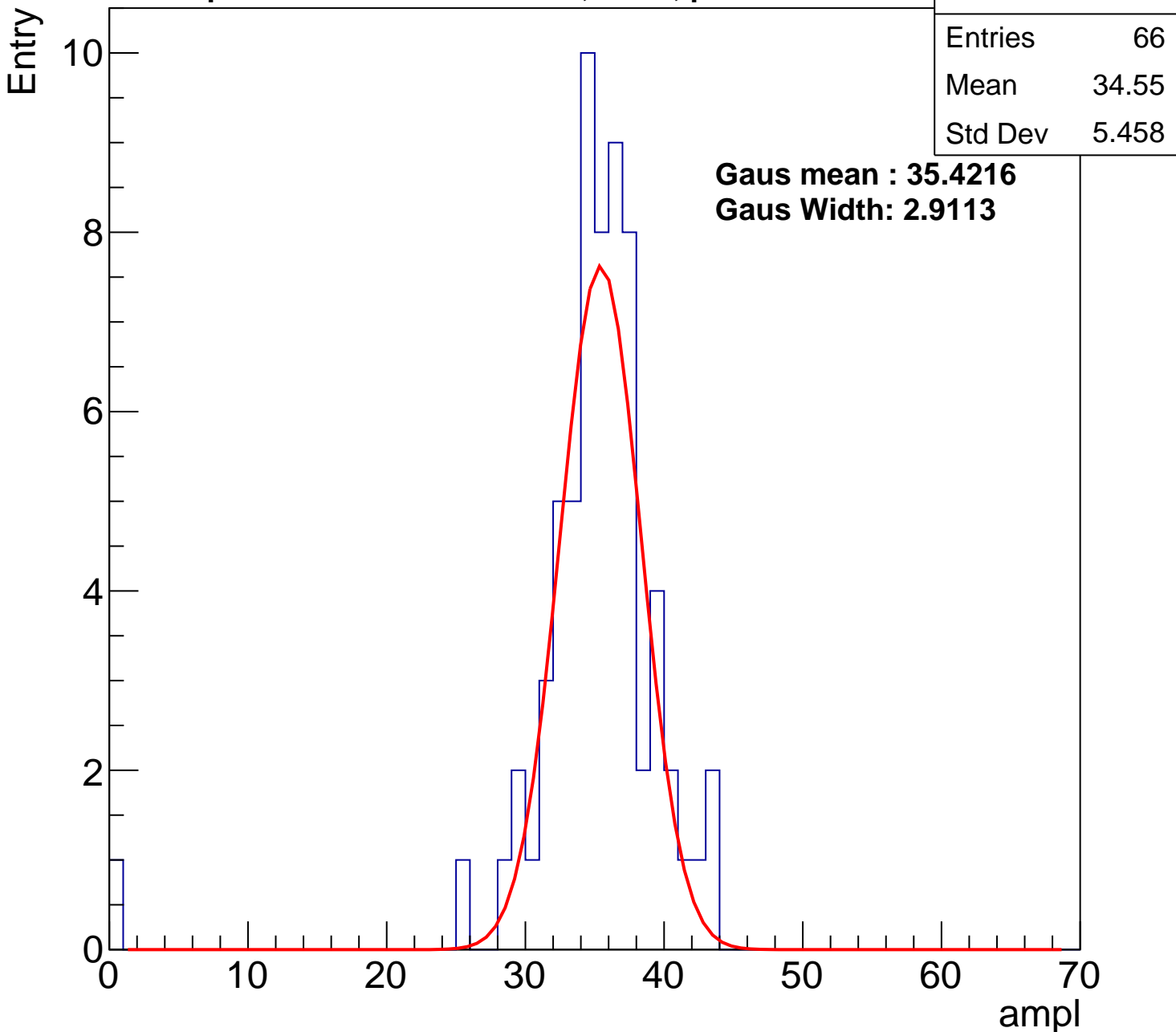
Gaus Width: 2.9113

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch62, adc2

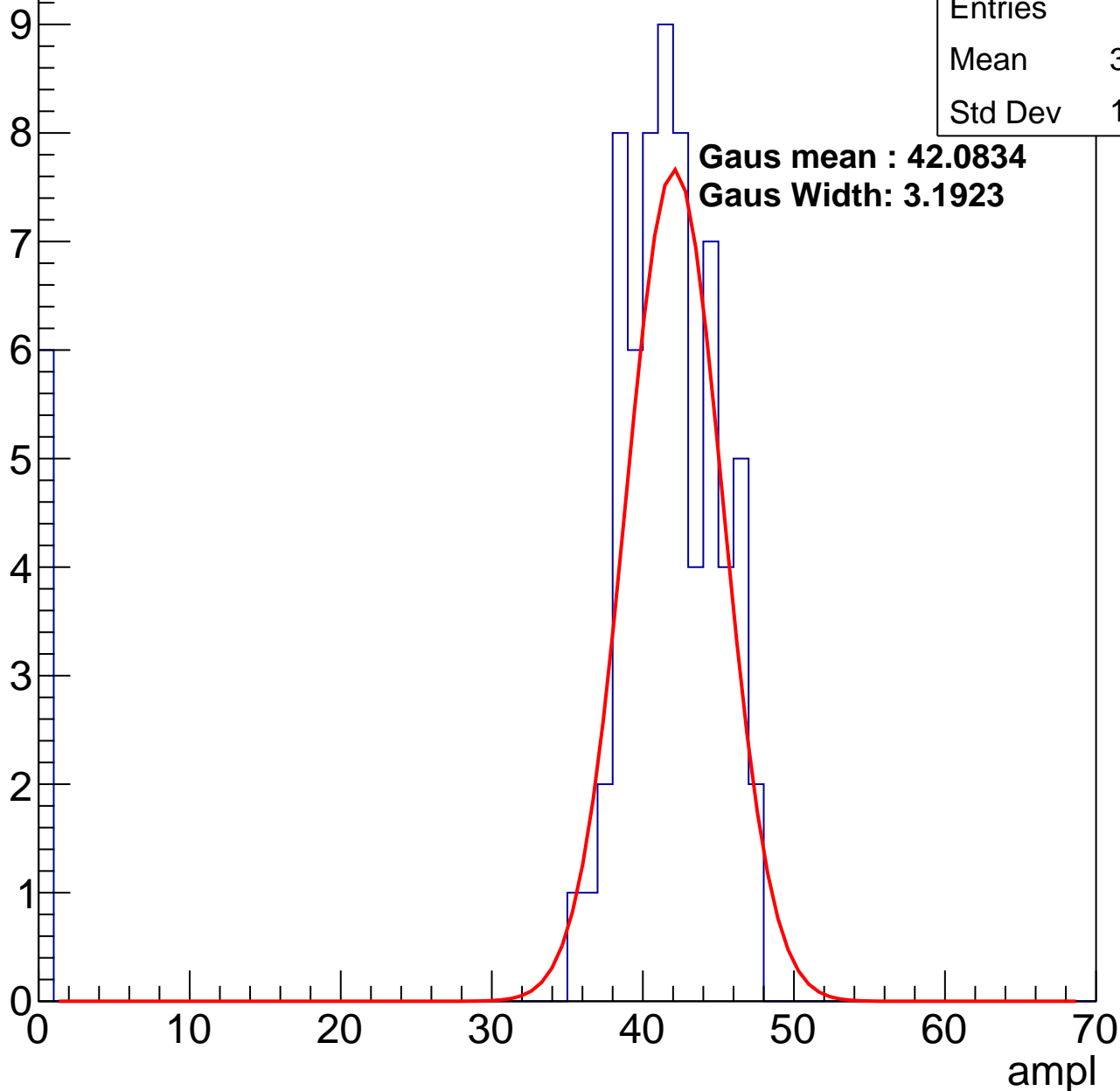
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	37.92
Std Dev	11.84

Gaus mean : 42.0834

Gaus Width: 3.1923

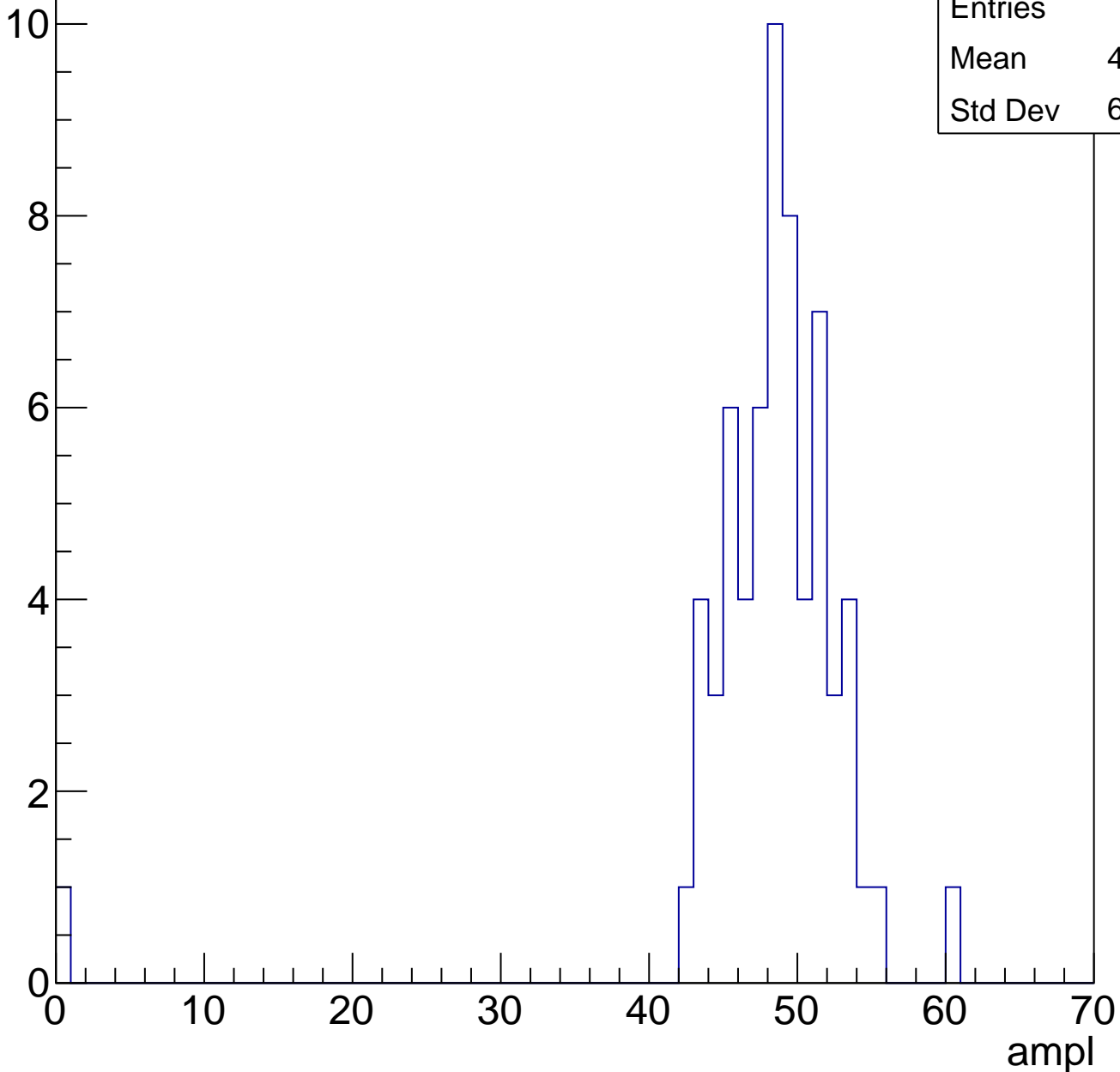


B1L103S, U10-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	47.62
Std Dev	6.868

Entry

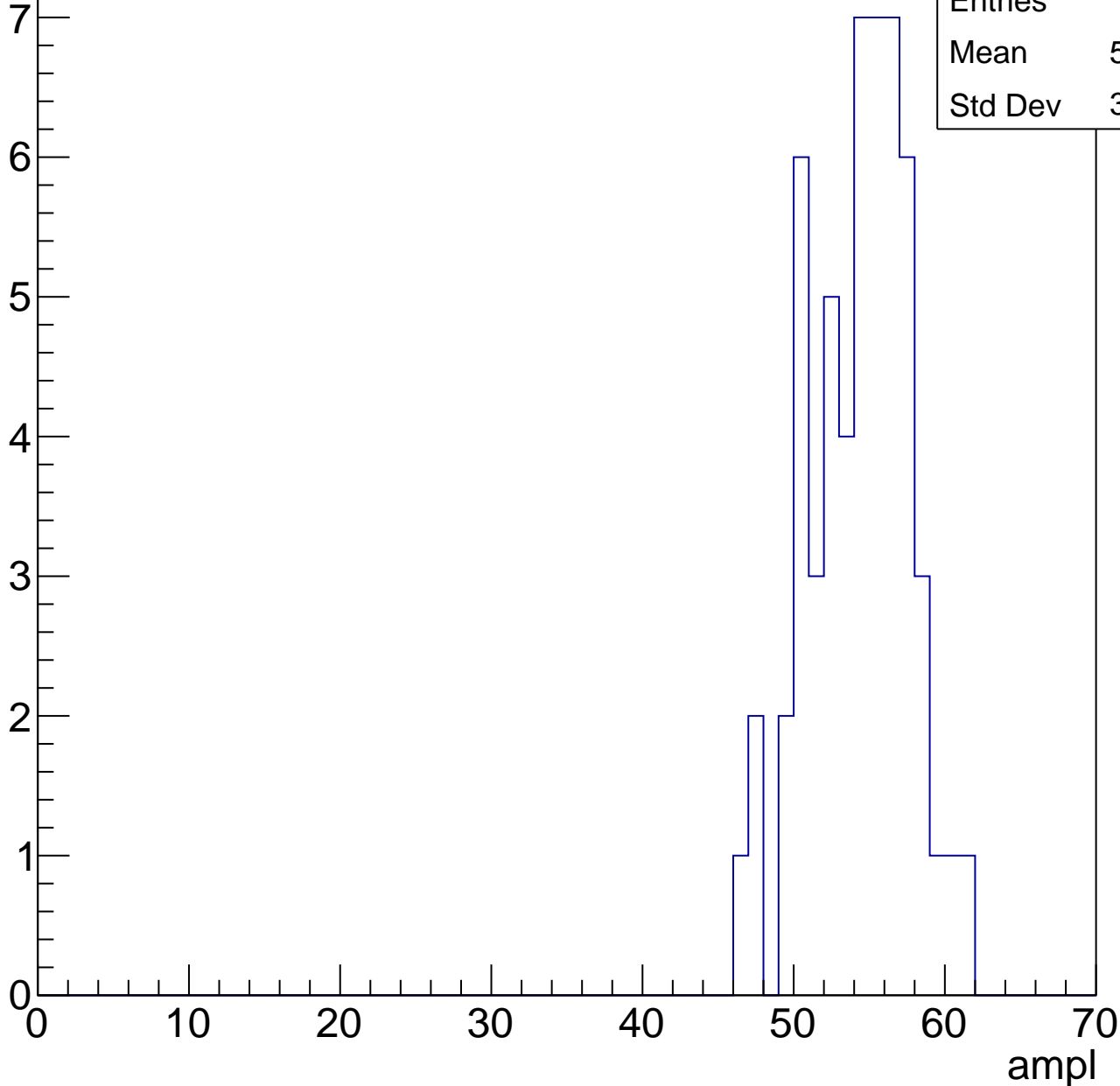


B1L103S, U10-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

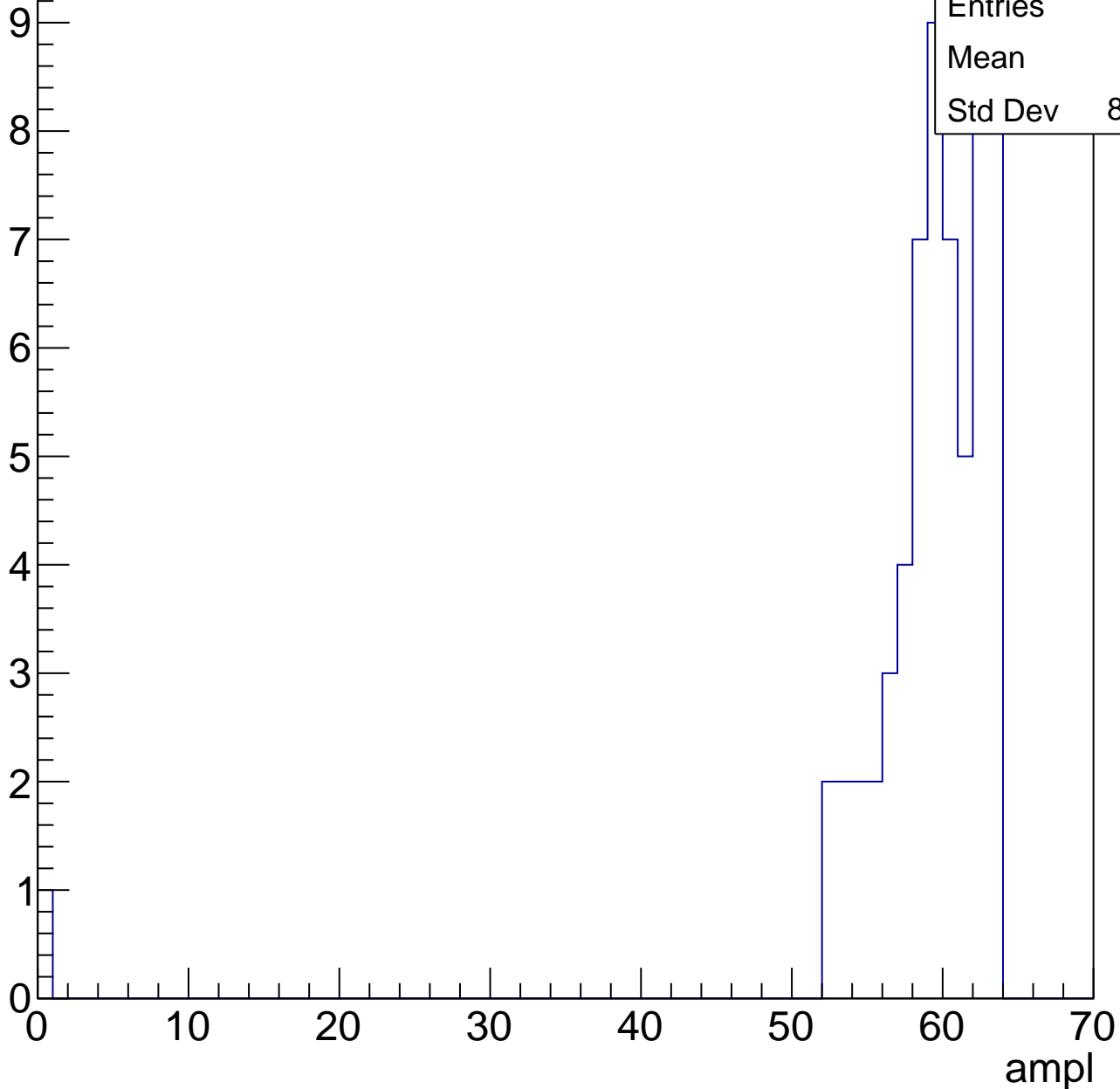
Entries	56
Mean	53.82
Std Dev	3.285



B1L103S, U10-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

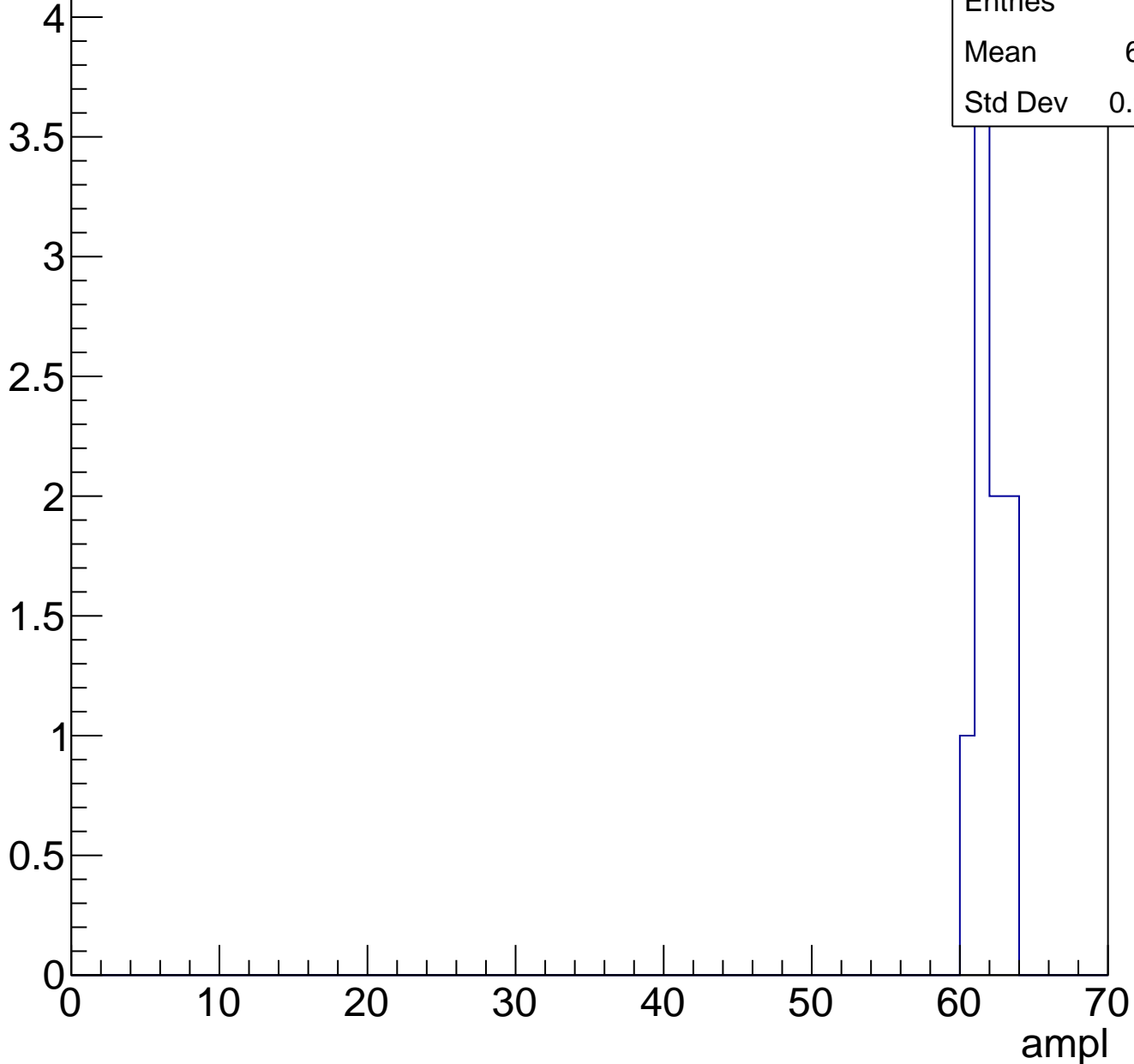
Entry



B1L103S, U10-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch63, adc0

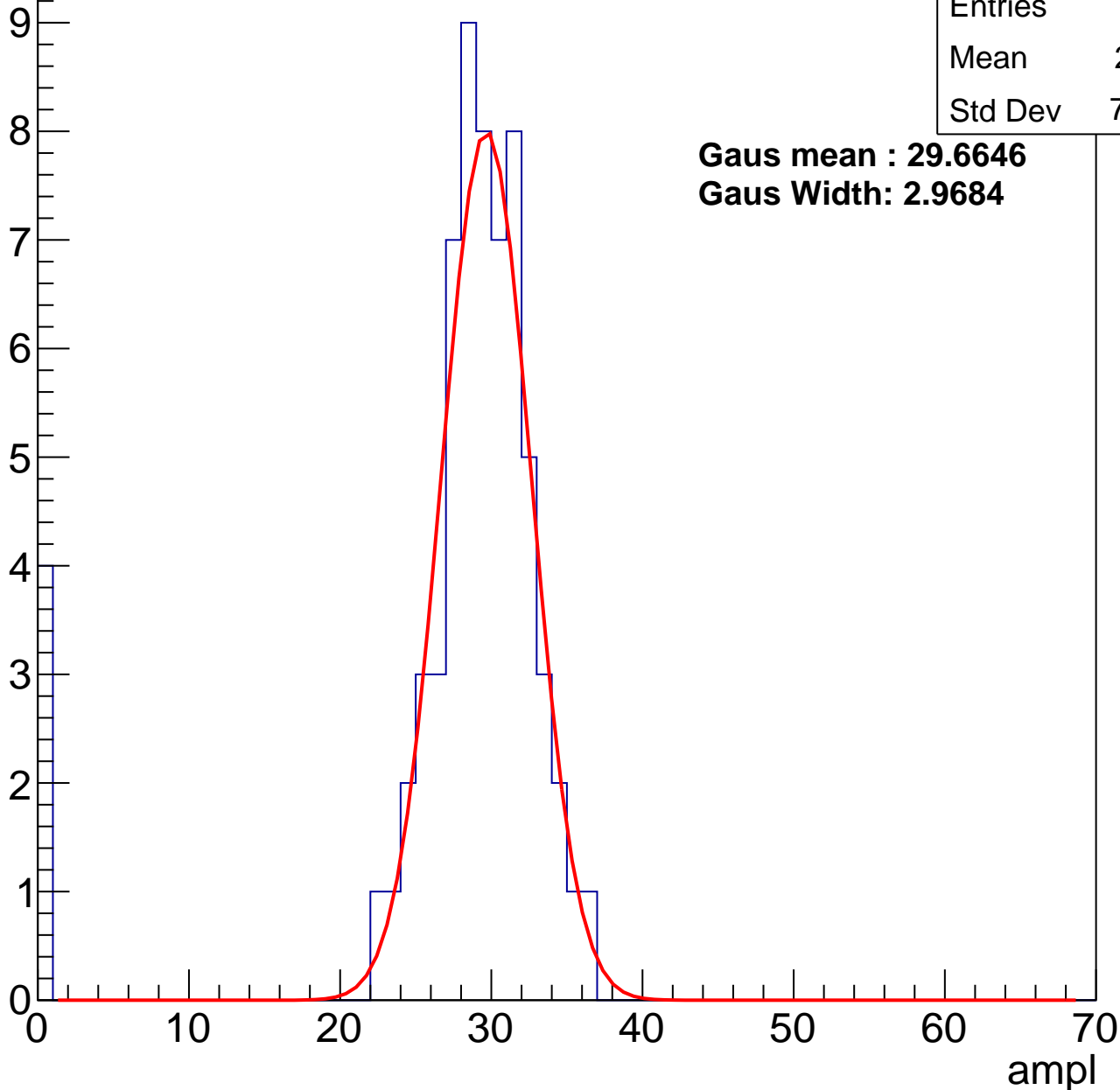
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	27.31
Std Dev	7.536

Gaus mean : 29.6646

Gaus Width: 2.9684



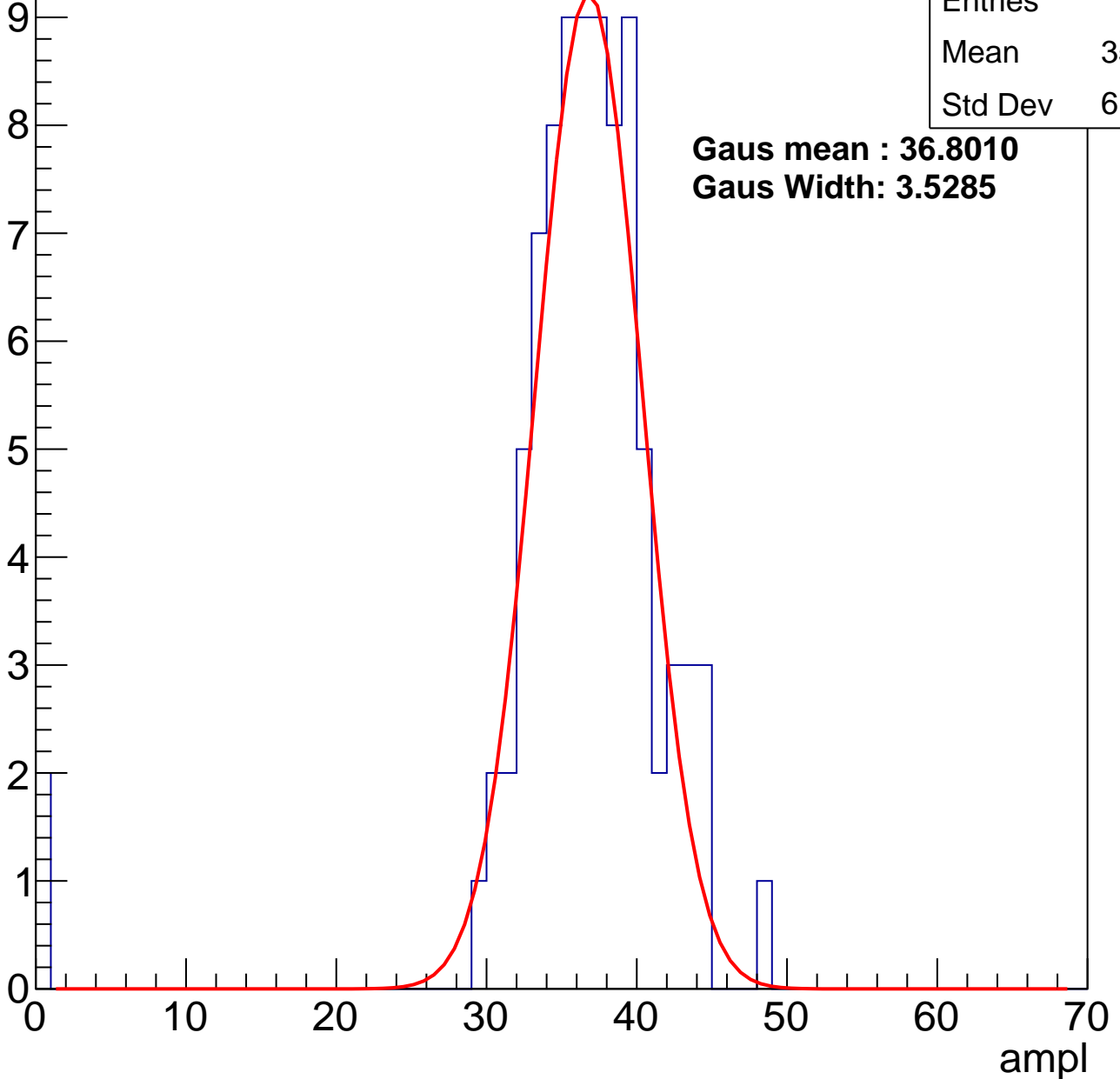
B1L103S, U10-ch63, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	35.89
Std Dev	6.562

Gaus mean : 36.8010
Gaus Width: 3.5285



B1L103S, U10-ch63, adc2

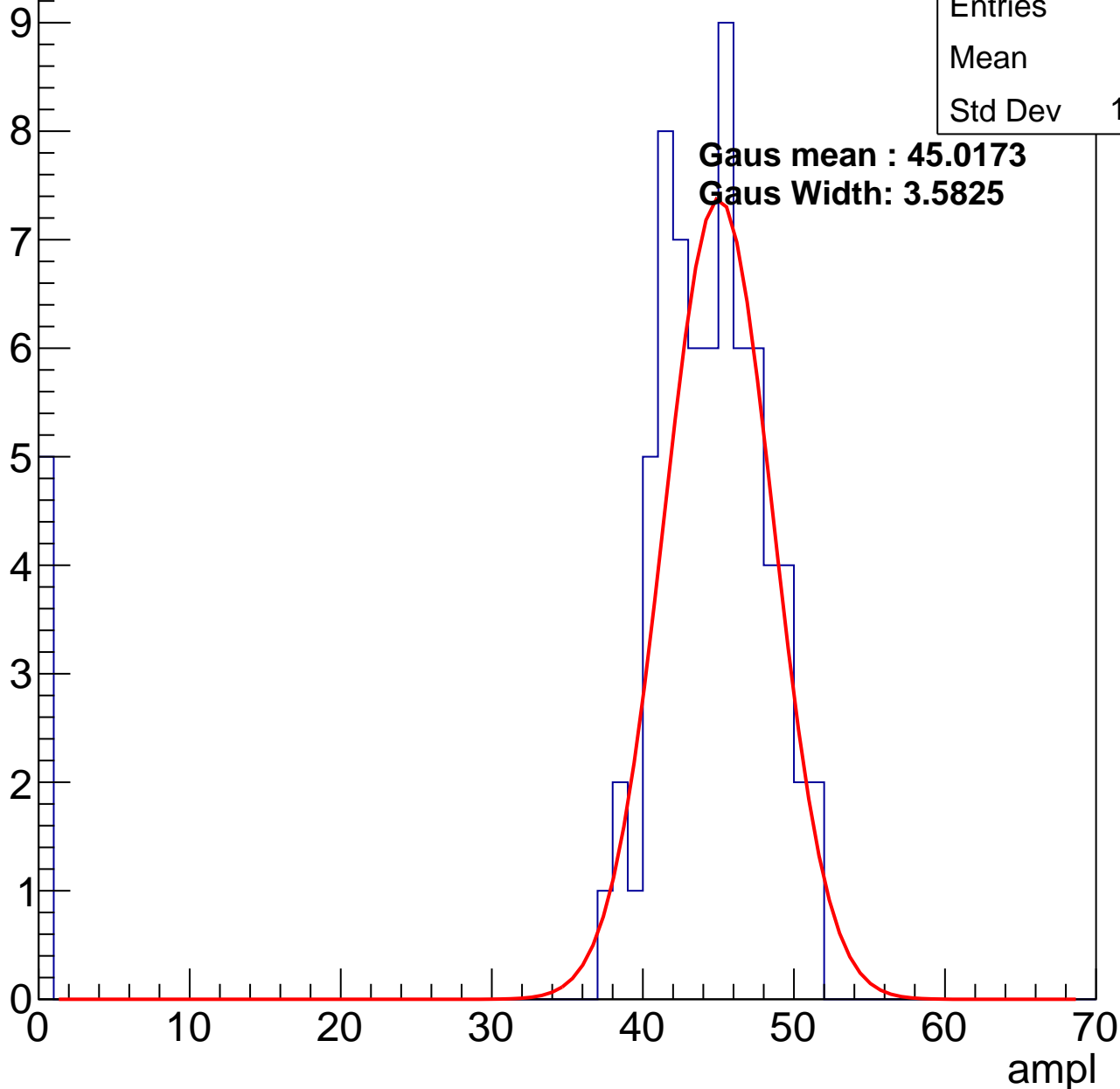
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	41.2
Std Dev	11.54

Gaus mean : 45.0173

Gaus Width: 3.5825

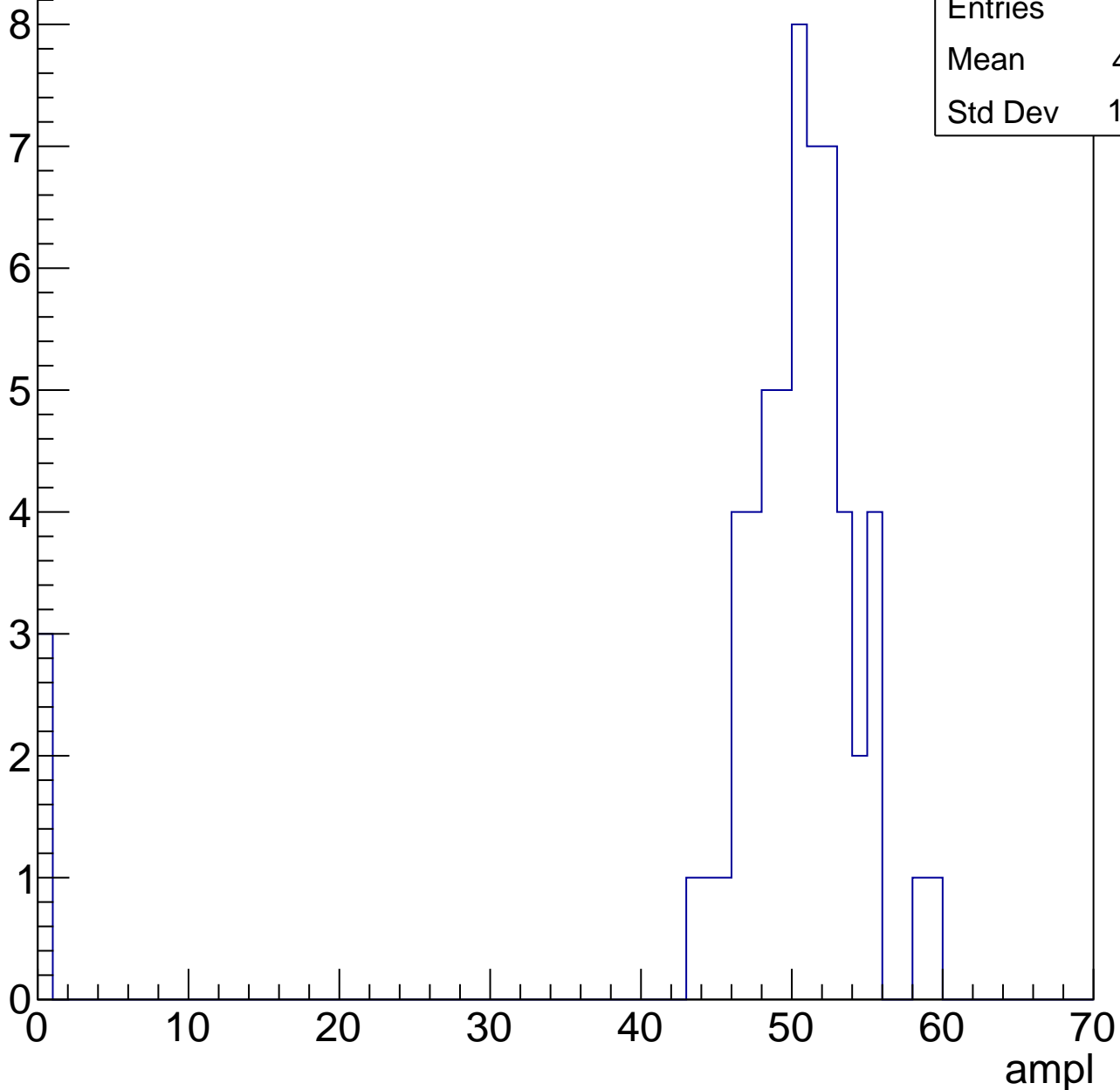


B1L103S, U10-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.71
Std Dev	11.58

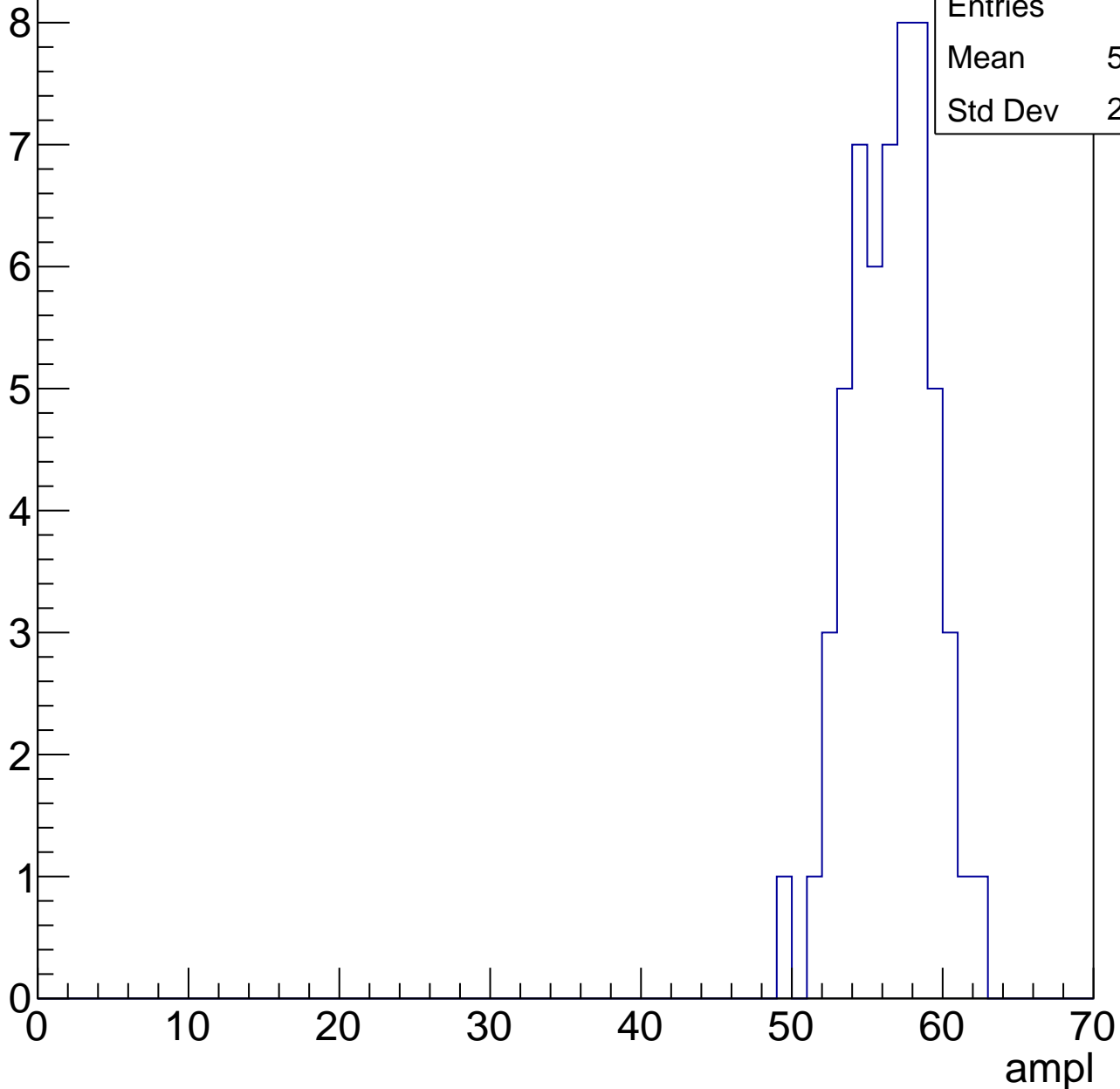


B1L103S, U10-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	56.05
Std Dev	2.655

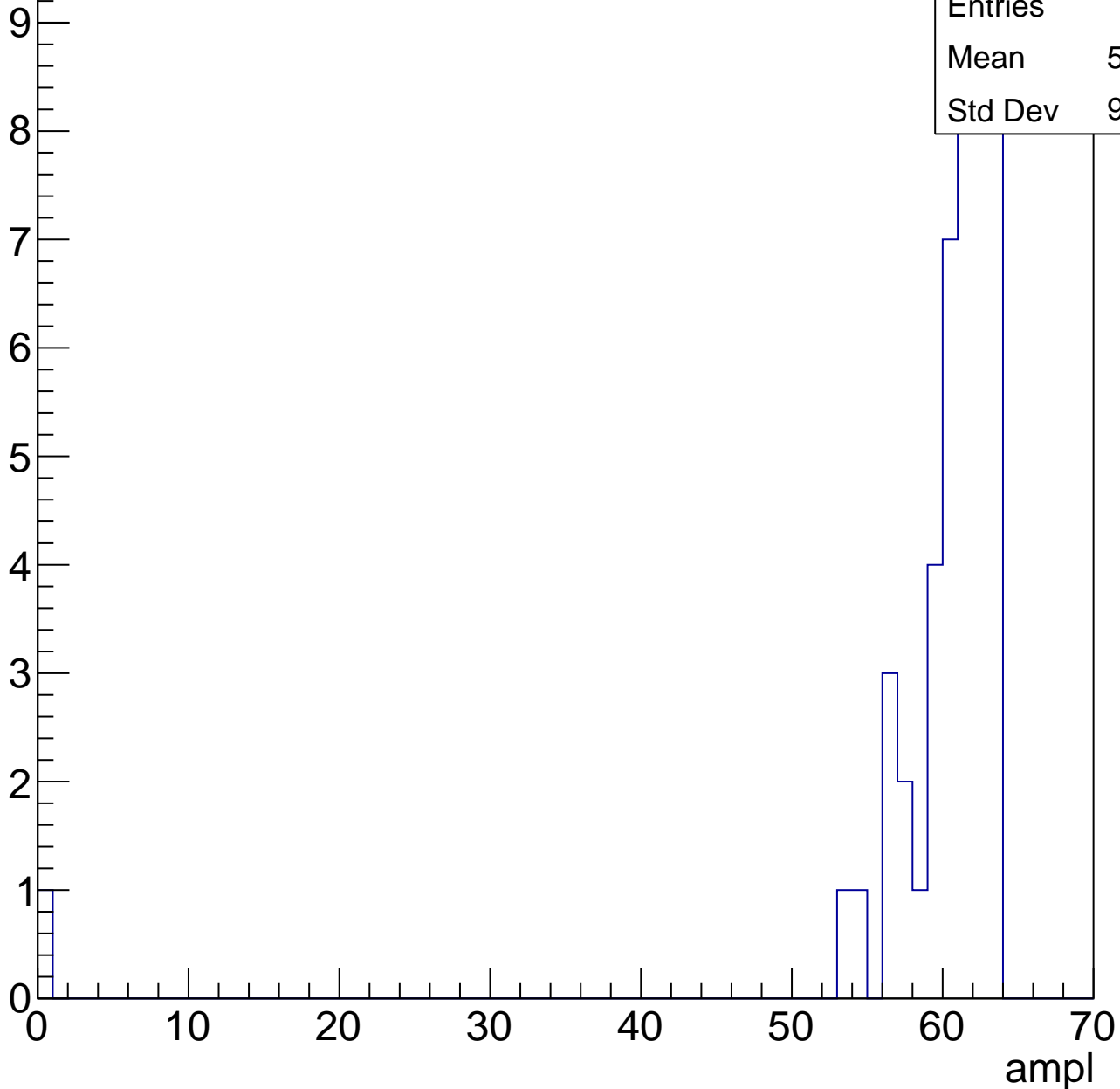


B1L103S, U10-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.93
Std Dev	9.214



B1L103S, U10-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch64, adc0

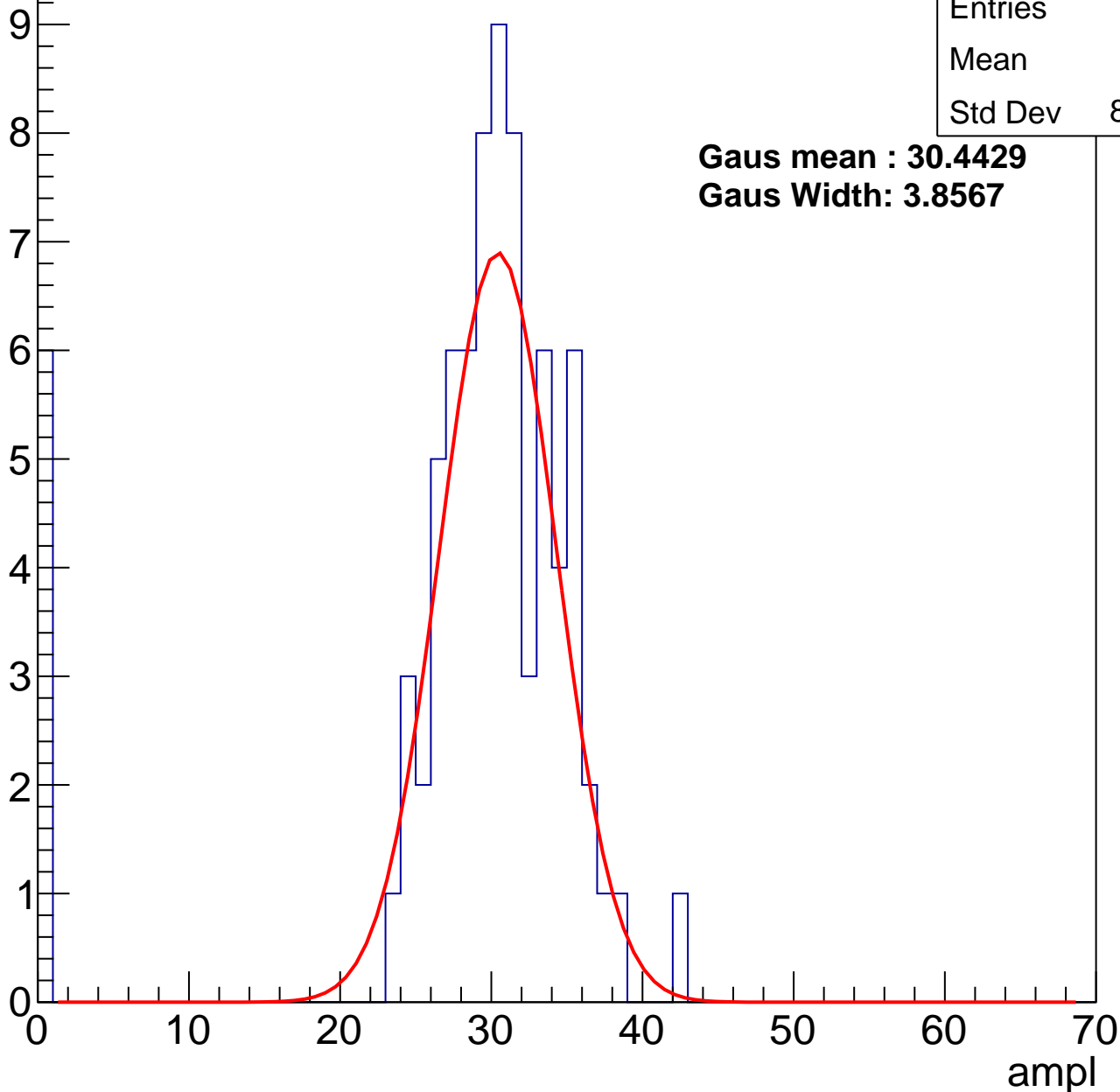
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	28
Std Dev	8.827

Gaus mean : 30.4429

Gaus Width: 3.8567



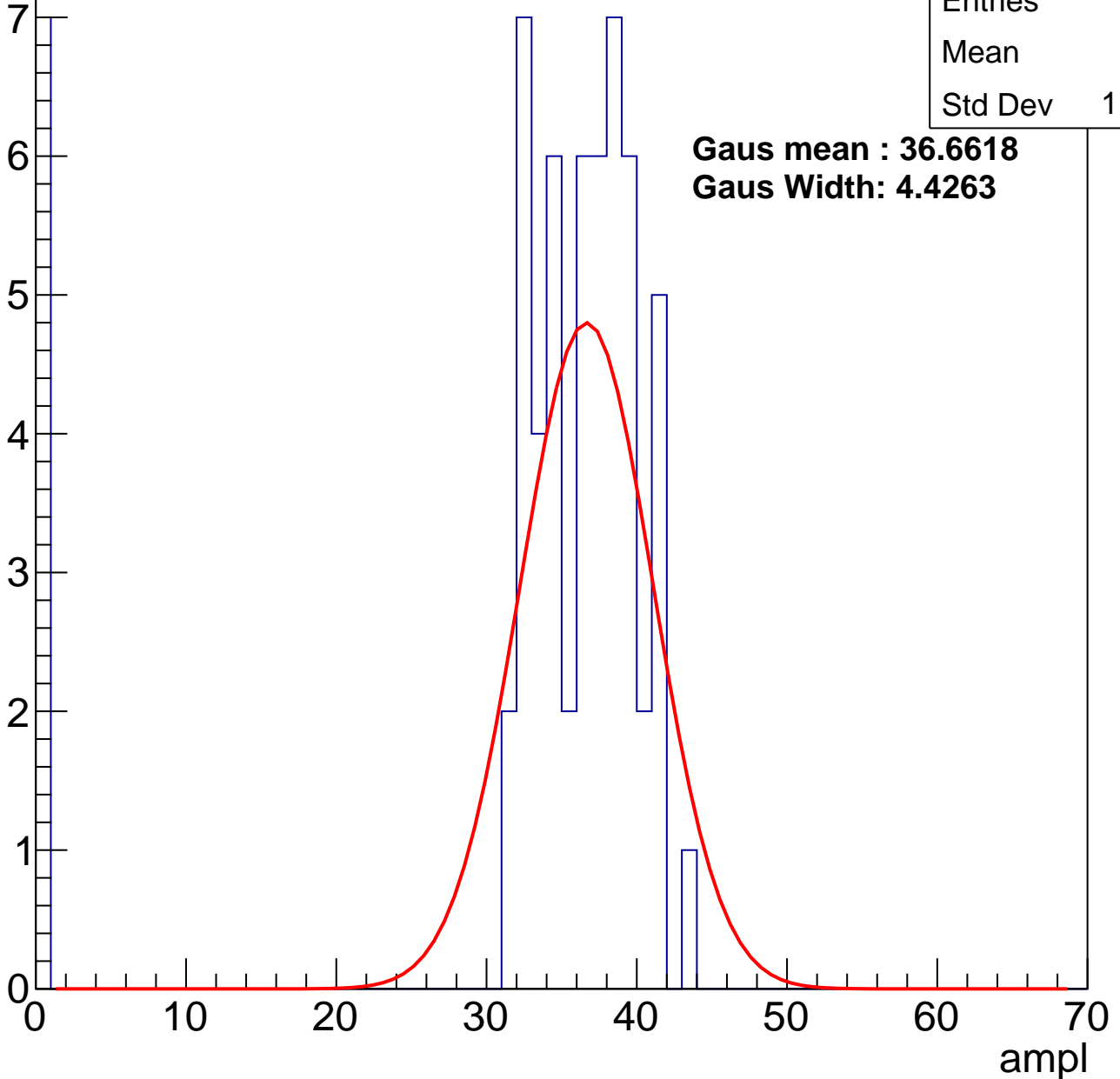
B1L103S, U10-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	32.1
Std Dev	11.92

Gaus mean : 36.6618
Gaus Width: 4.4263



B1L103S, U10-ch64, adc2

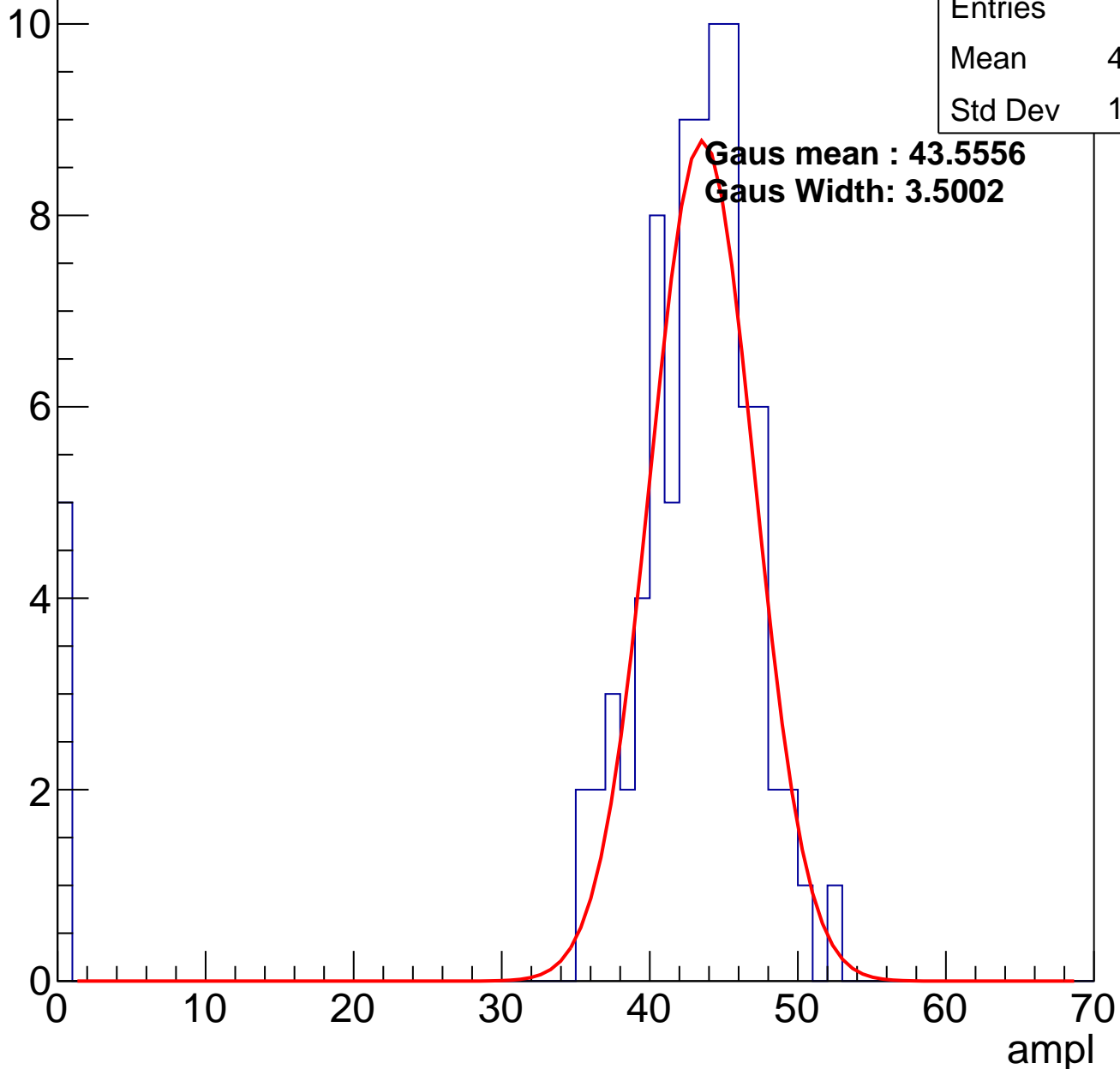
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	40.45
Std Dev	10.55

Gaus mean : 43.5556

Gaus Width: 3.5002

Entry

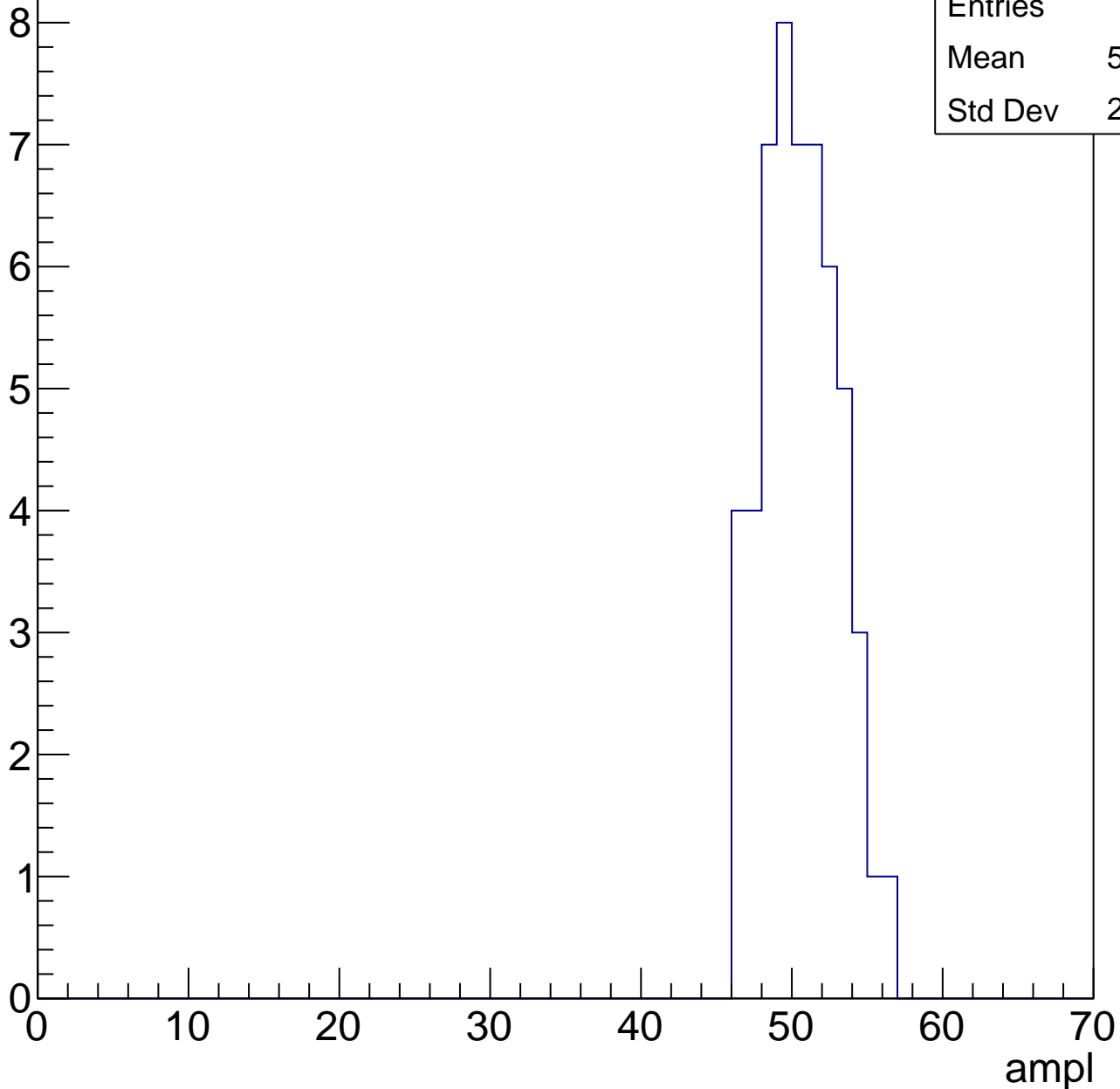


B1L103S, U10-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	50.13
Std Dev	2.457

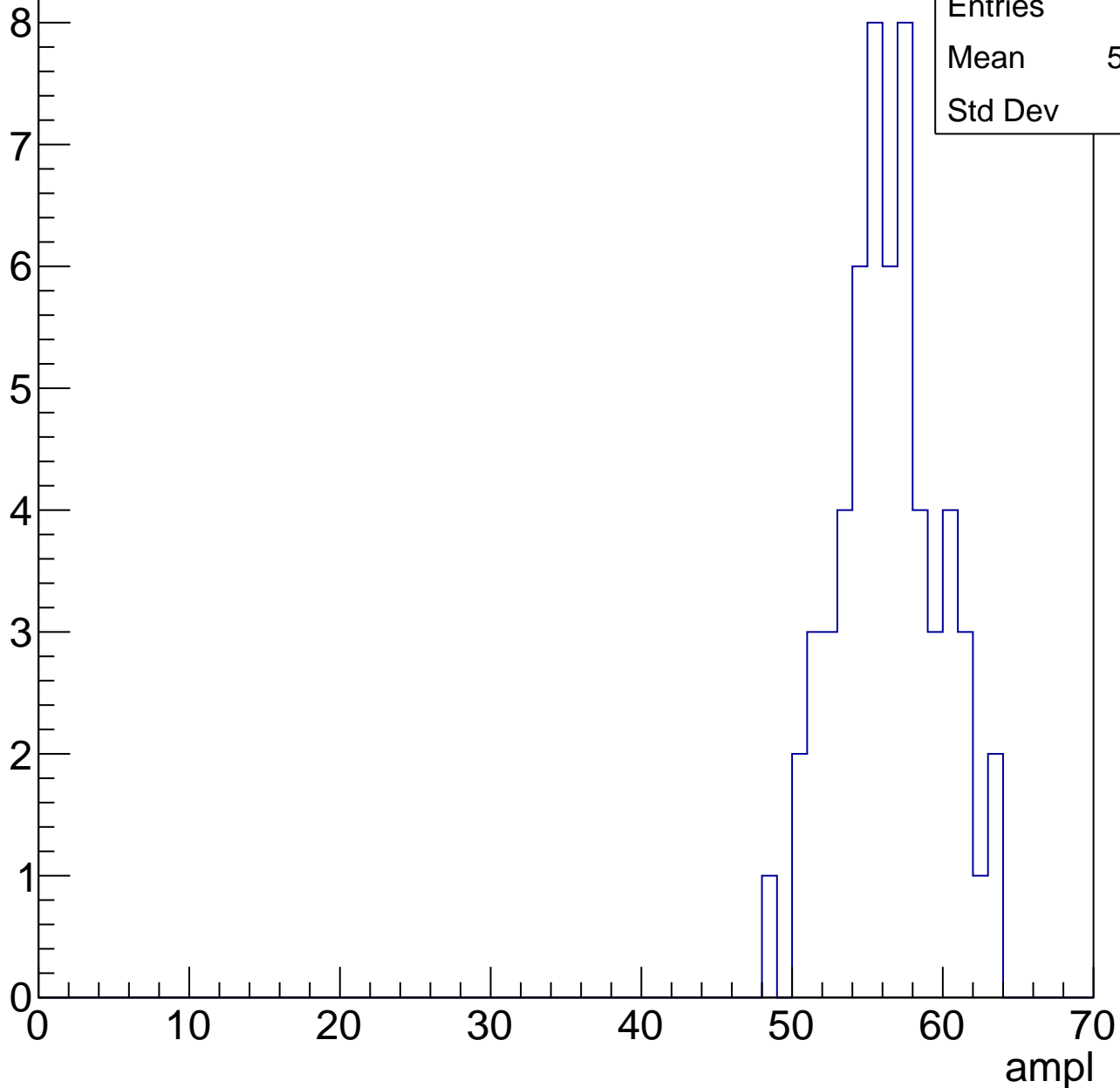


B1L103S, U10-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	55.95
Std Dev	3.35

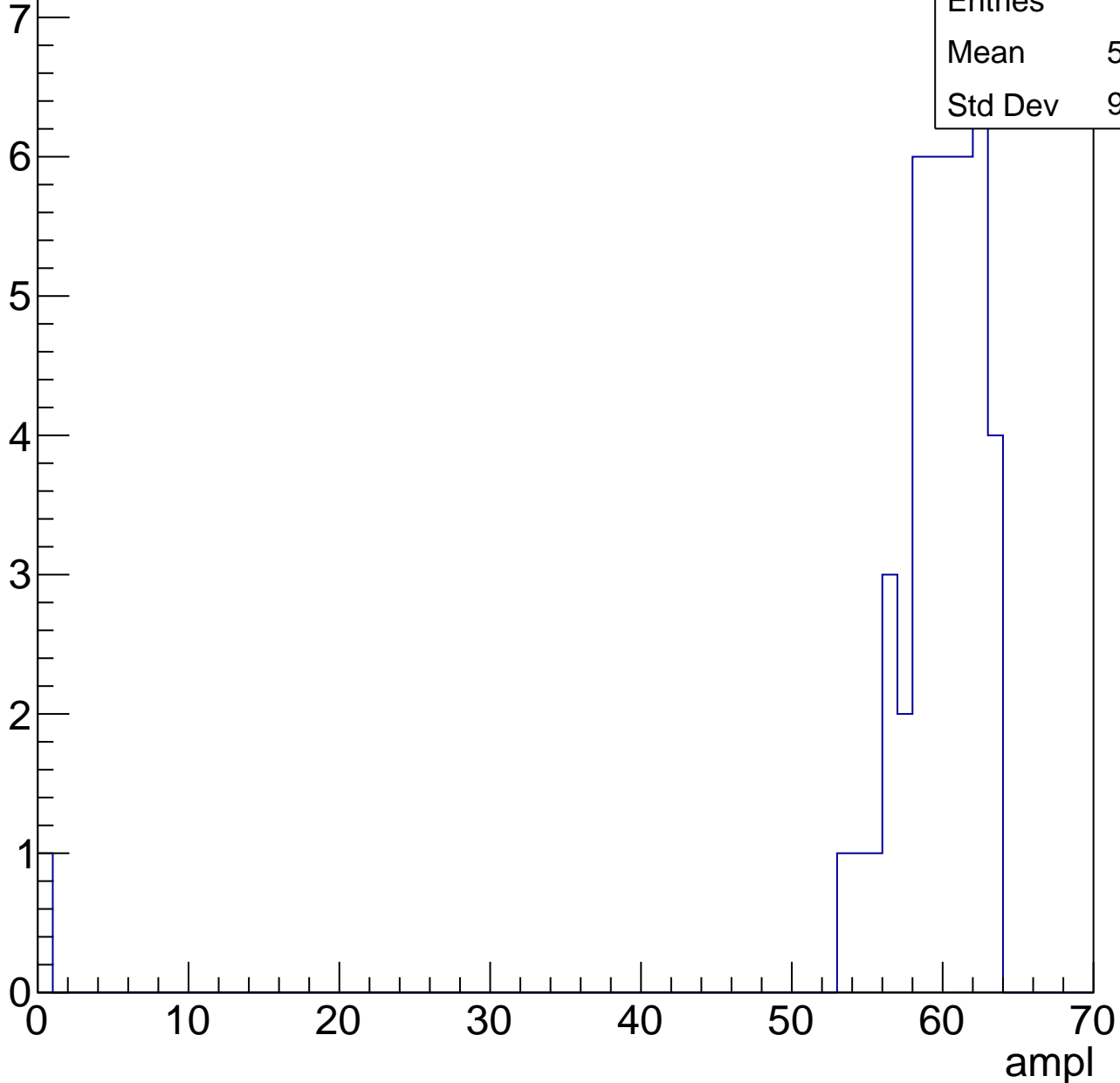


B1L103S, U10-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

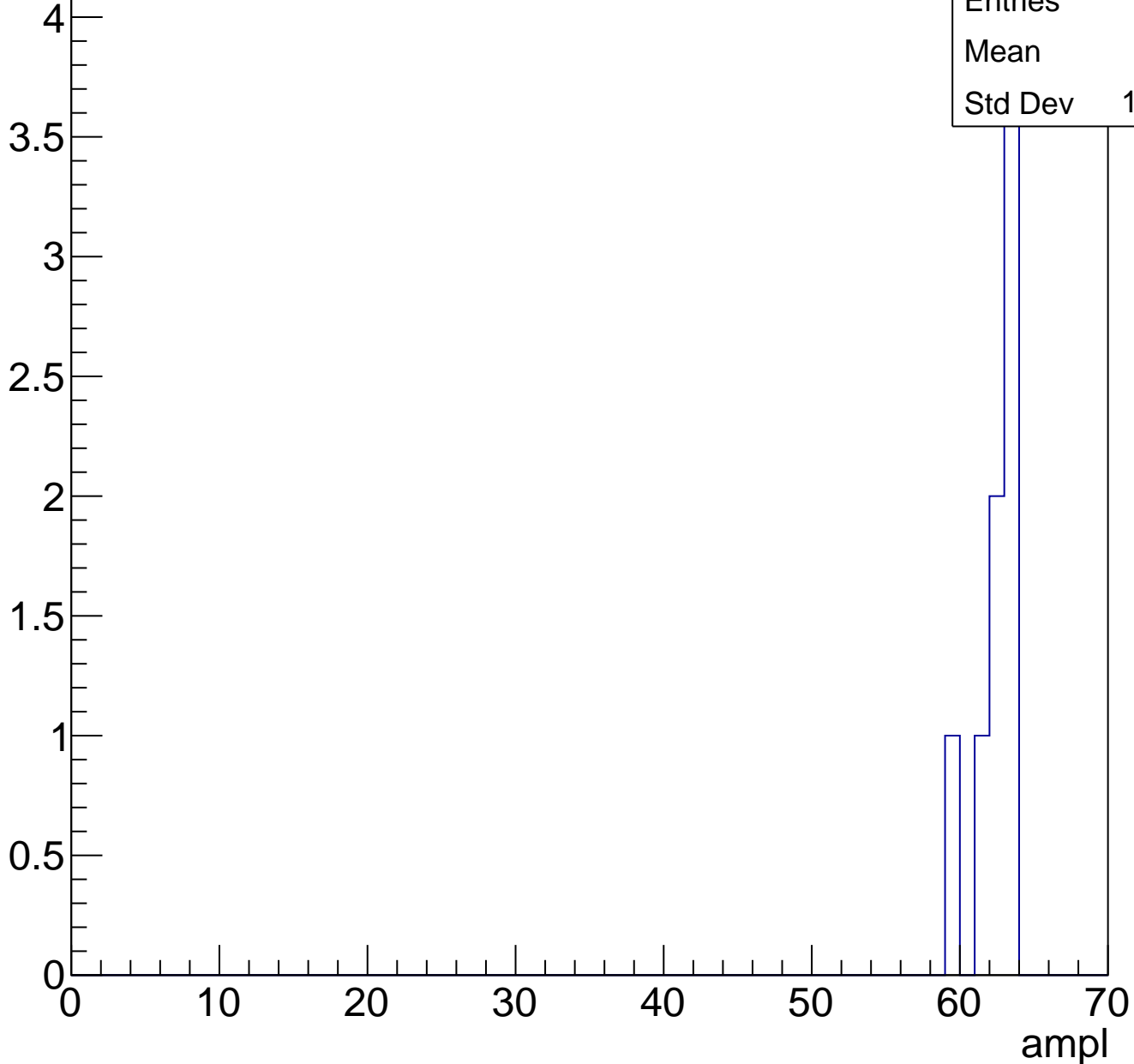
Entries	44
Mean	58.14
Std Dev	9.199



B1L103S, U10-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	8
Mean	62
Std Dev	1.323

B1L103S, U10-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch65, adc0

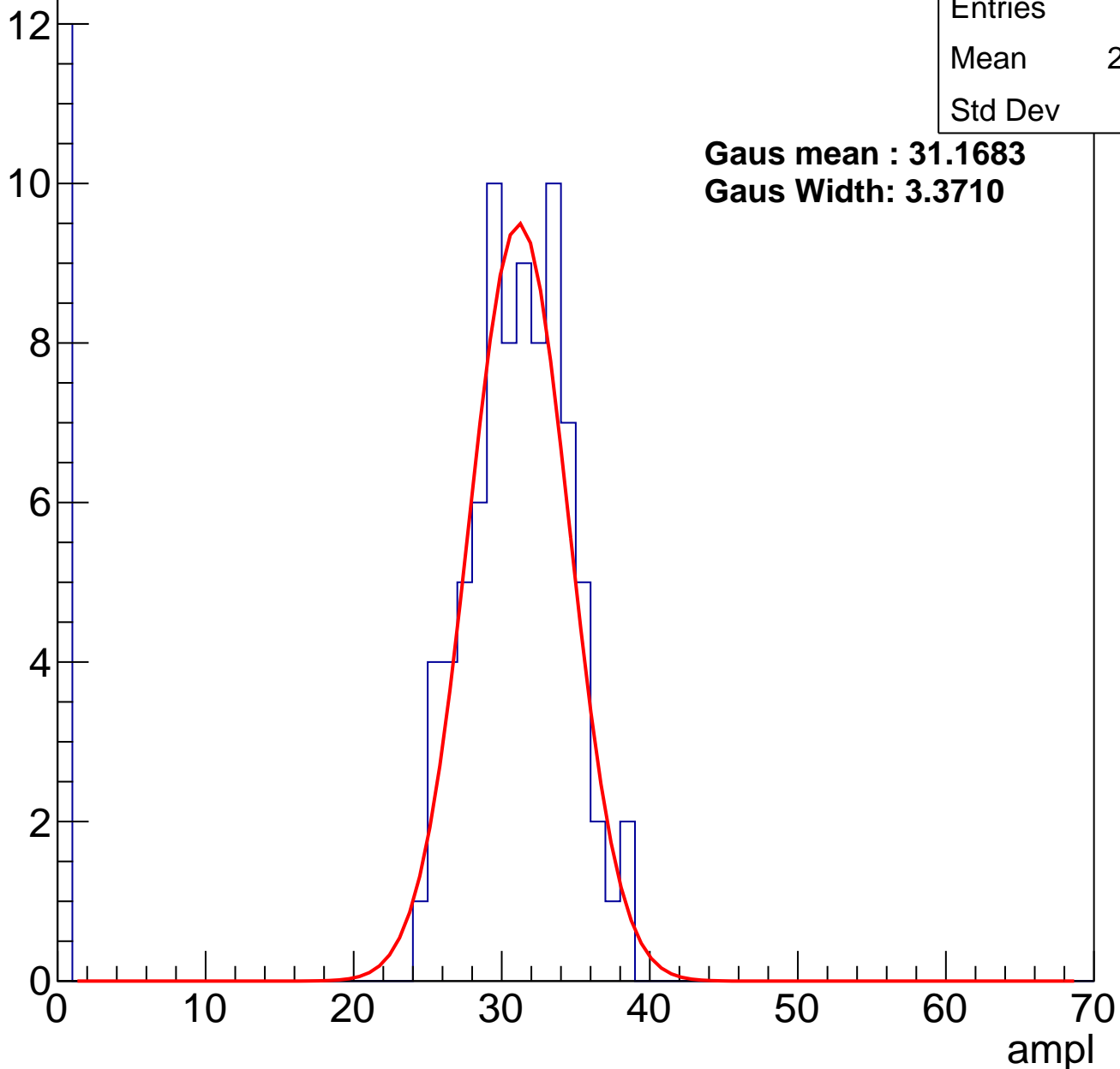
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	26.85
Std Dev	10.7

Gaus mean : 31.1683

Gaus Width: 3.3710

Entry



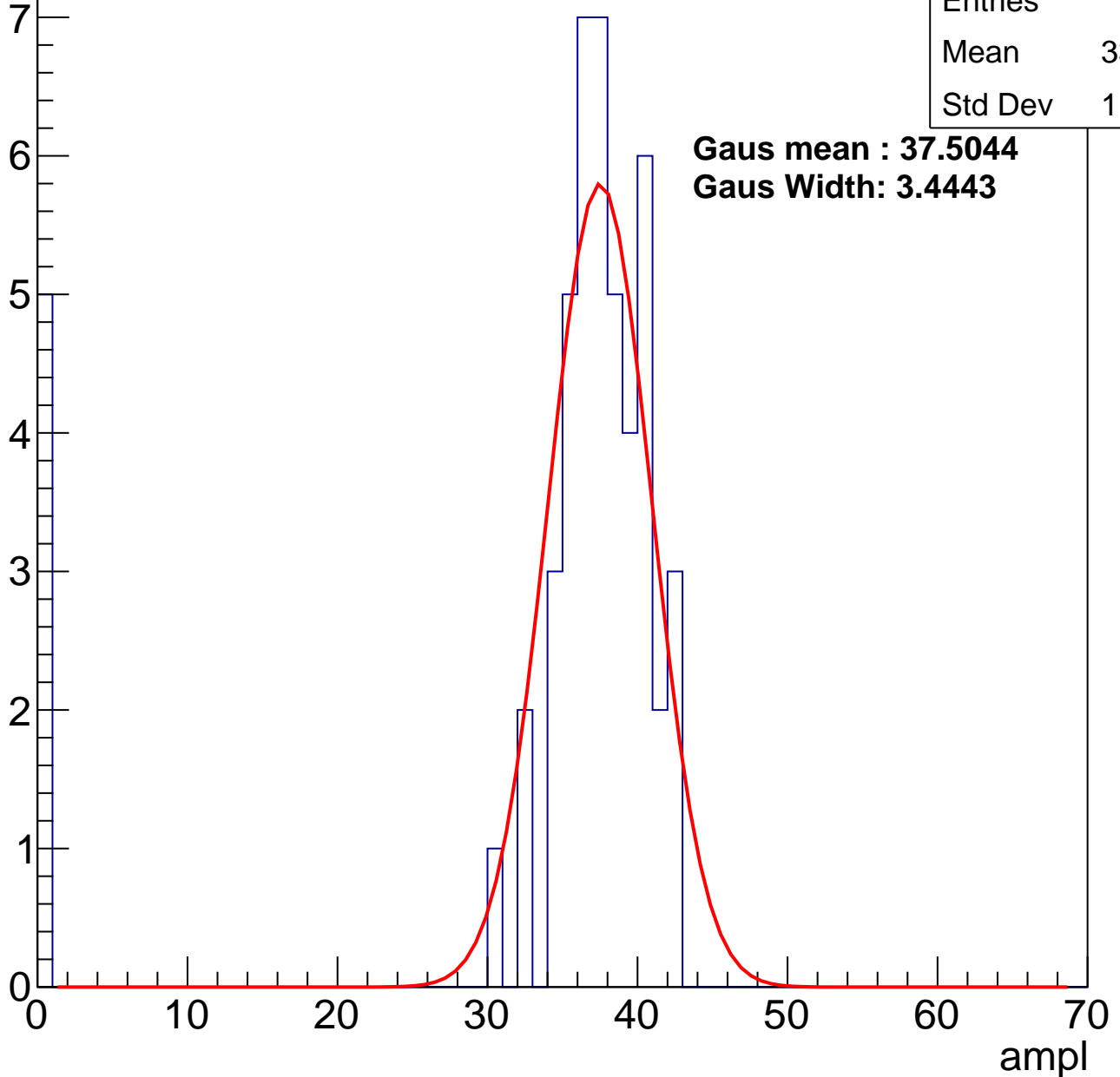
B1L103S, U10-ch65, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

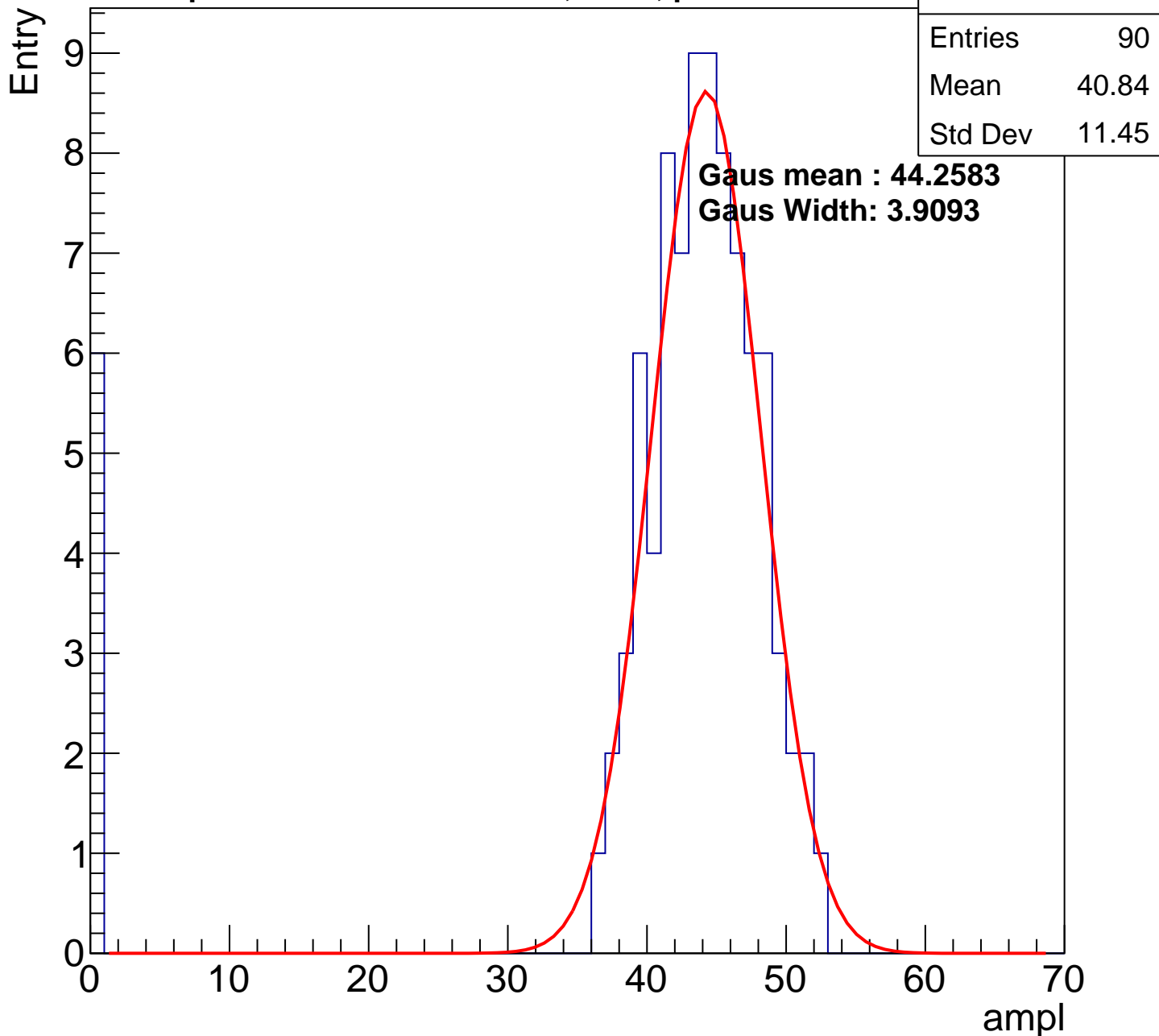
Entries	50
Mean	33.52
Std Dev	11.47

Gaus mean : 37.5044
Gaus Width: 3.4443



B1L103S, U10-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

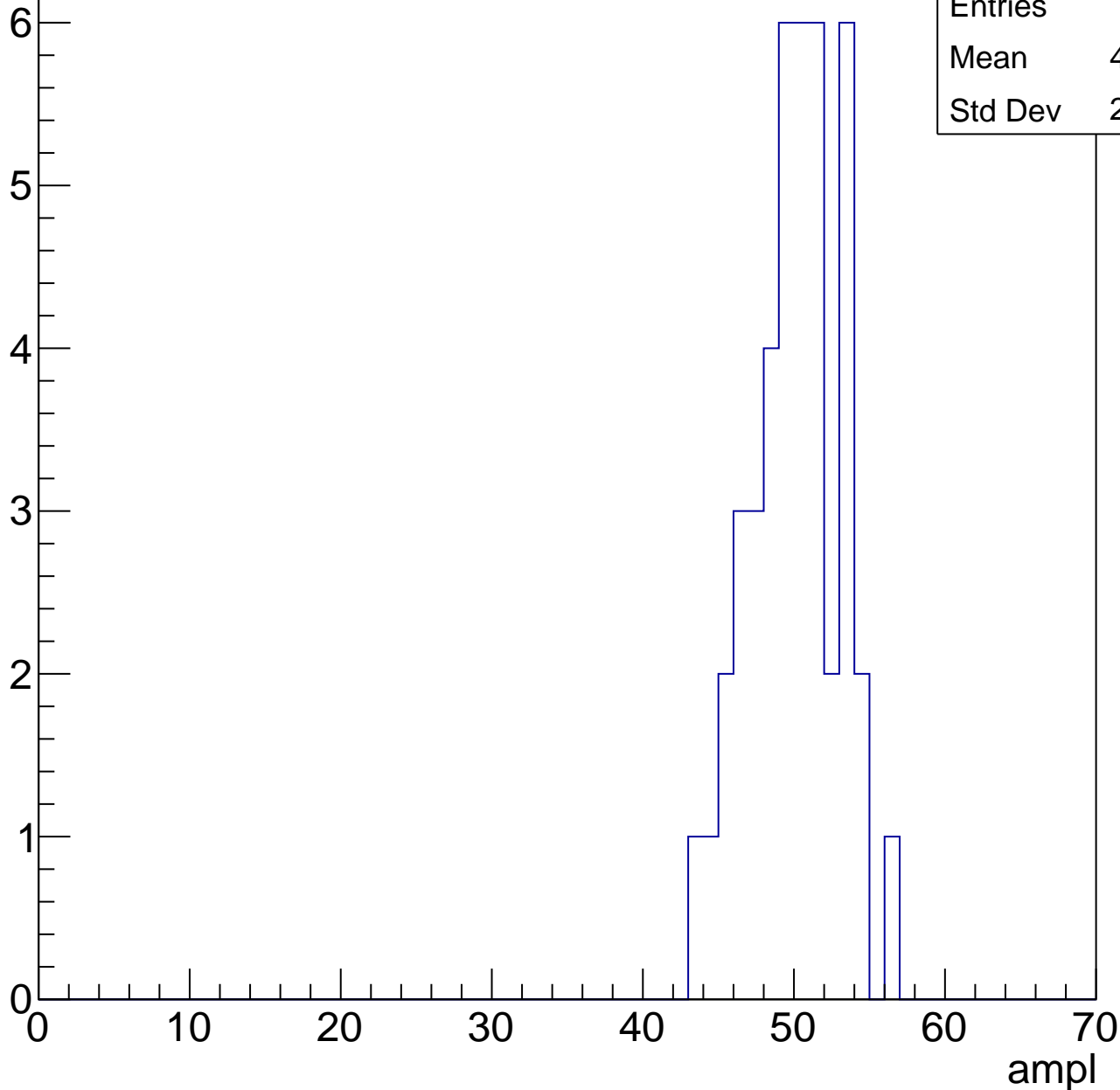


B1L103S, U10-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

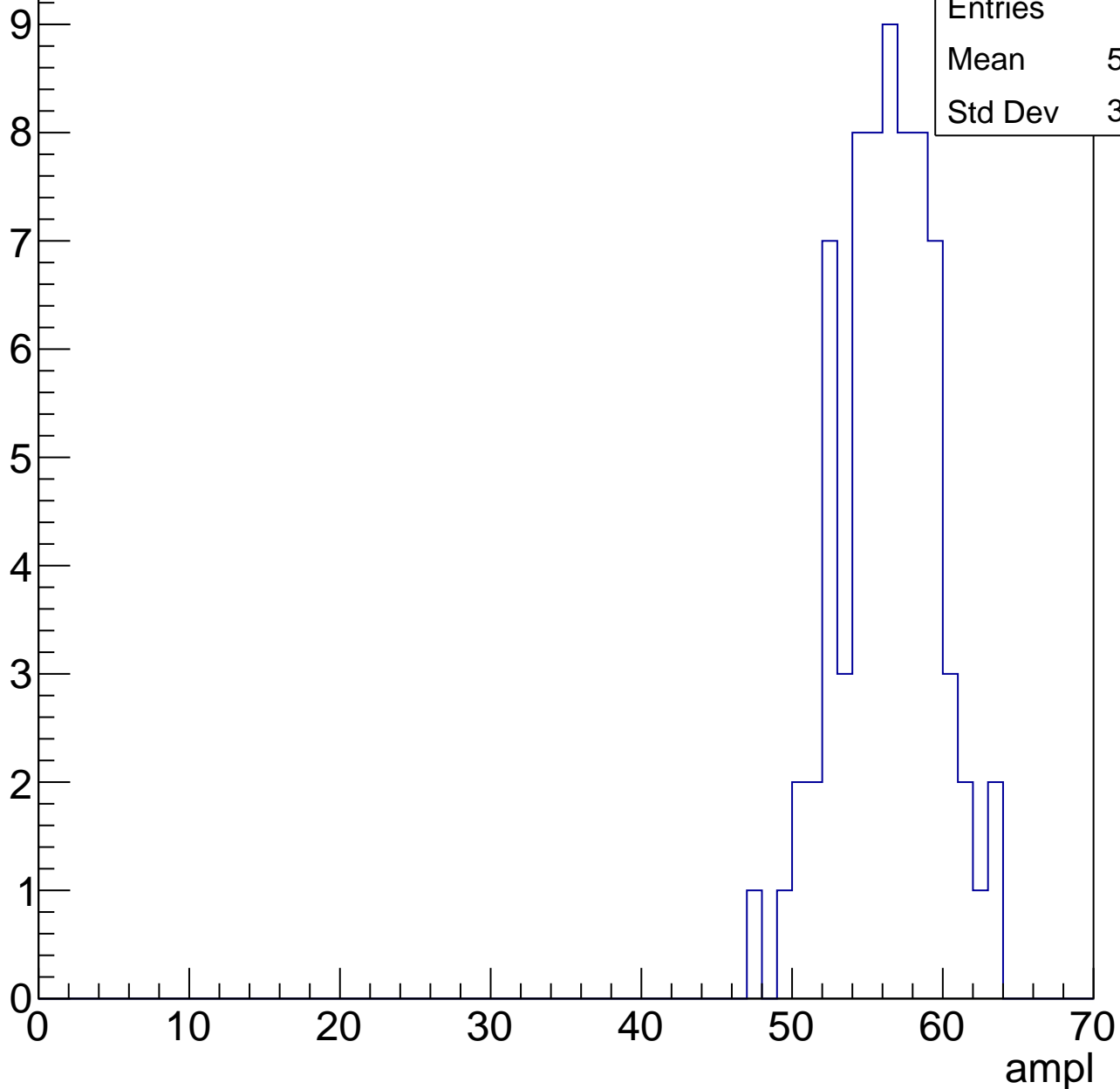
Entries	43
Mean	49.63
Std Dev	2.902



B1L103S, U10-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

60.12

Std Dev

2.471

0

10

20

30

40

50

60

ampl

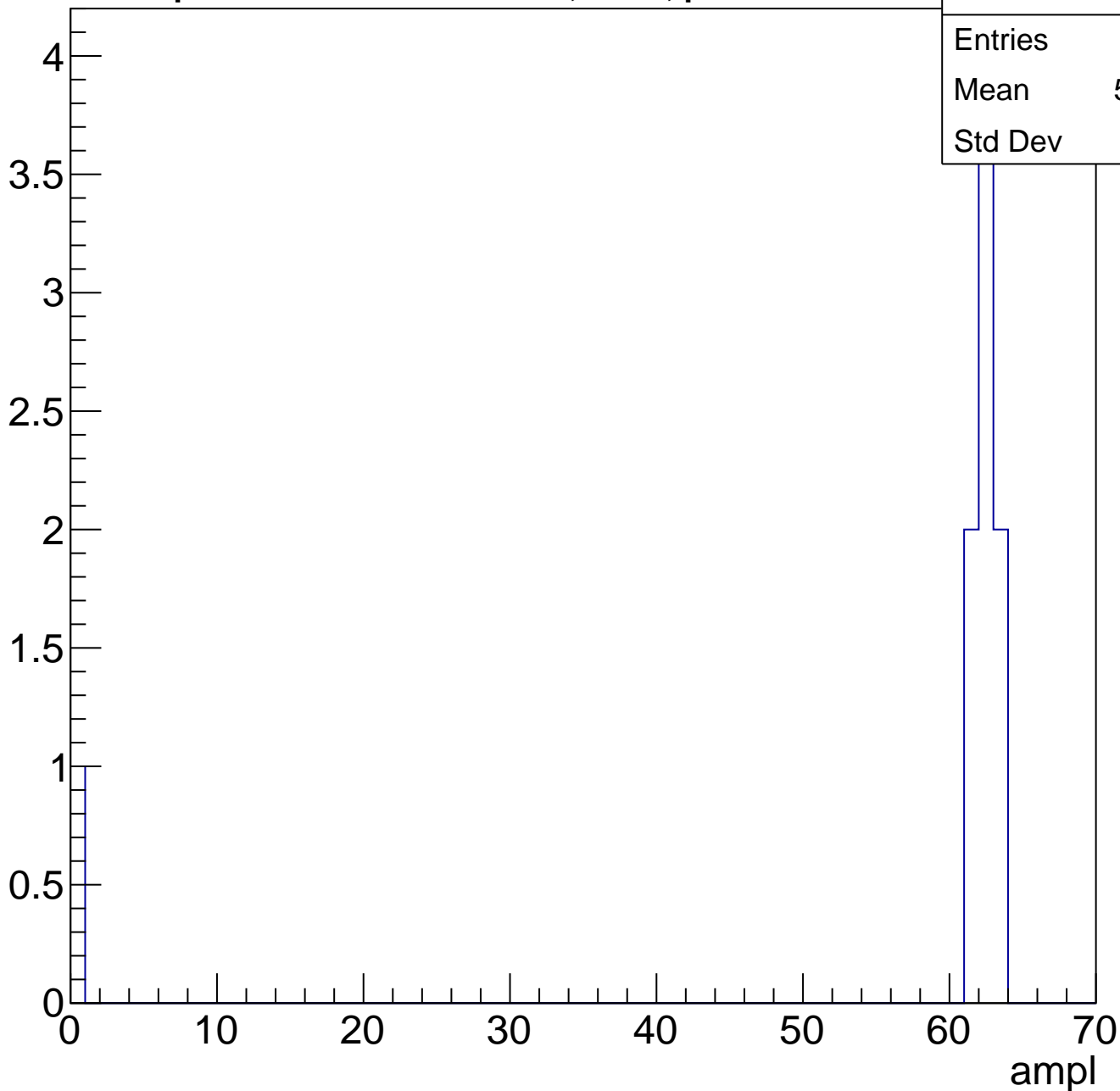
70

Entries	34
Mean	60.12
Std Dev	2.471

B1L103S, U10-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U10-ch66, adc0

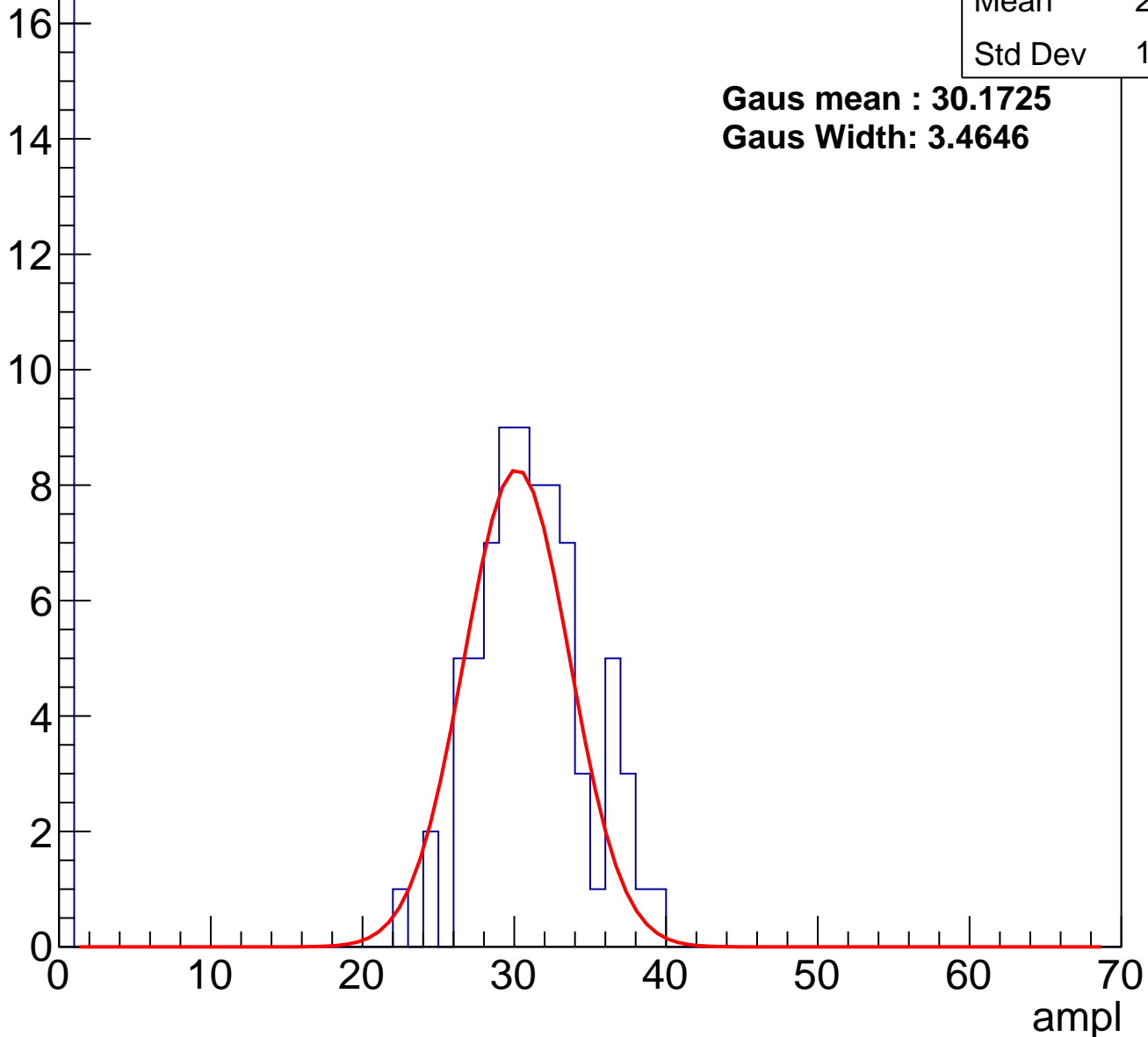
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	25.02
Std Dev	12.32

Gaus mean : 30.1725

Gaus Width: 3.4646

Entry



B1L103S, U10-ch66, adc1

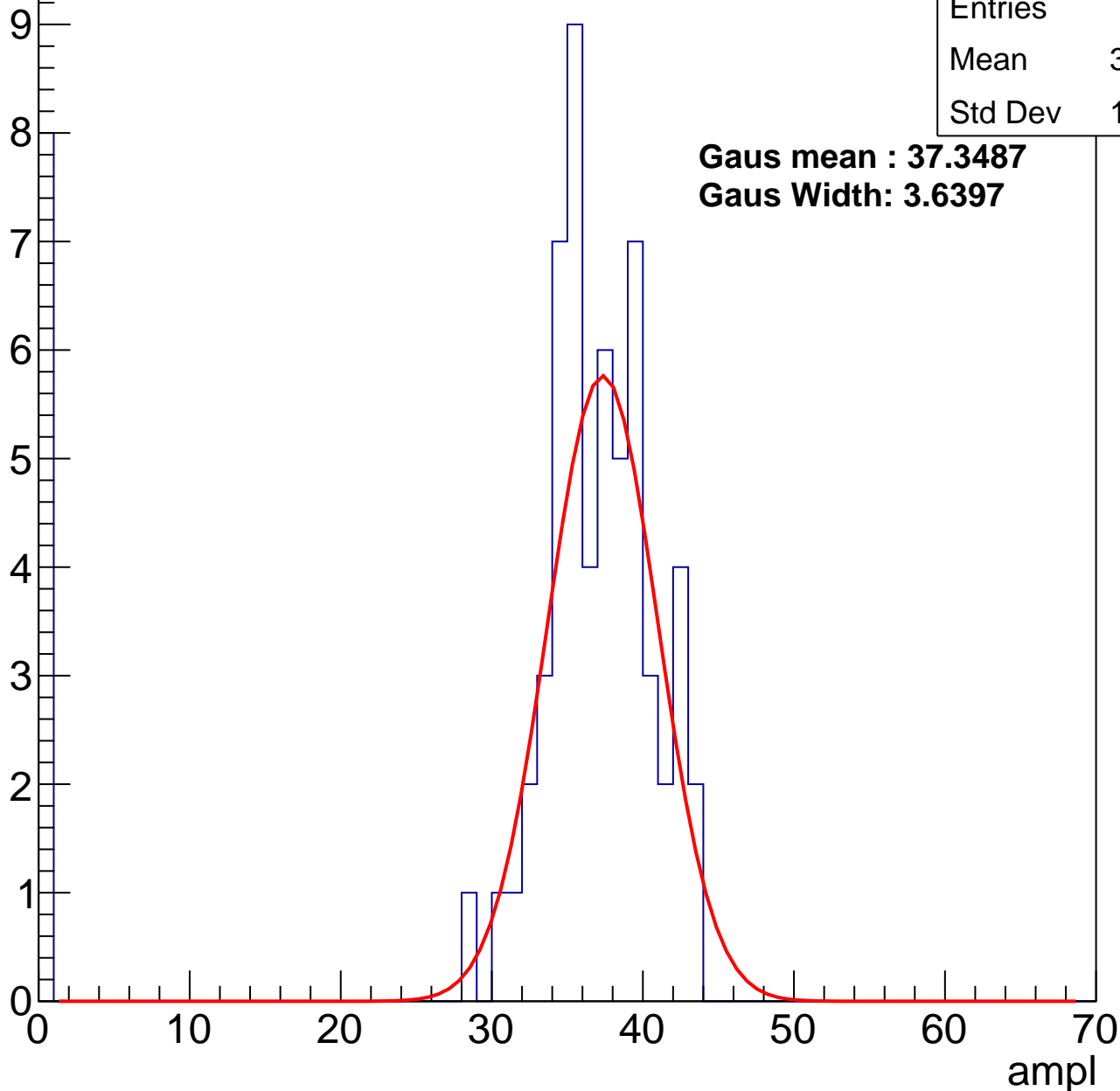
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	32.15
Std Dev	12.44

Gaus mean : 37.3487

Gaus Width: 3.6397



B1L103S, U10-ch66, adc2

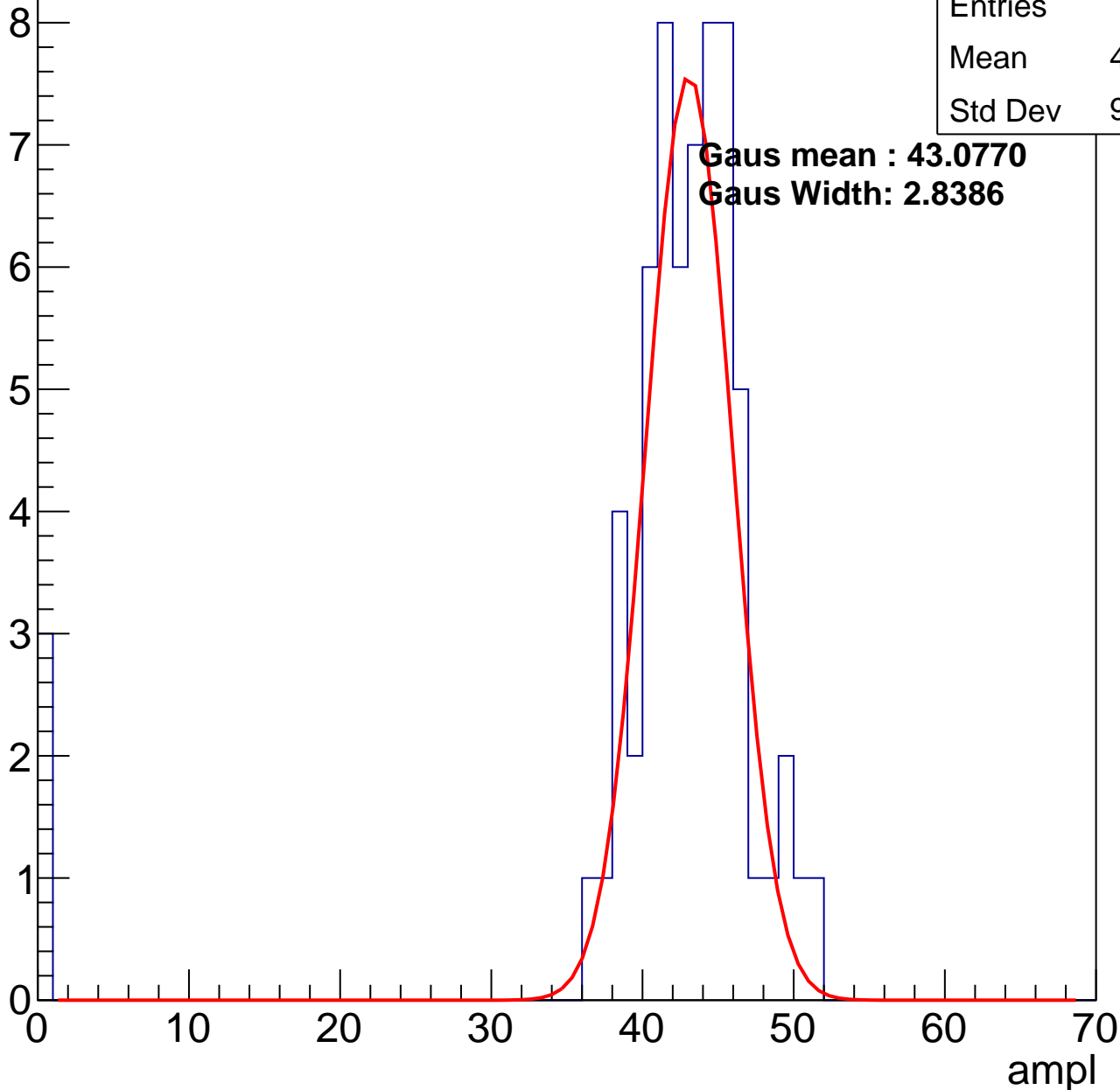
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.92
Std Dev	9.516

Gaus mean : 43.0770

Gaus Width: 2.8386

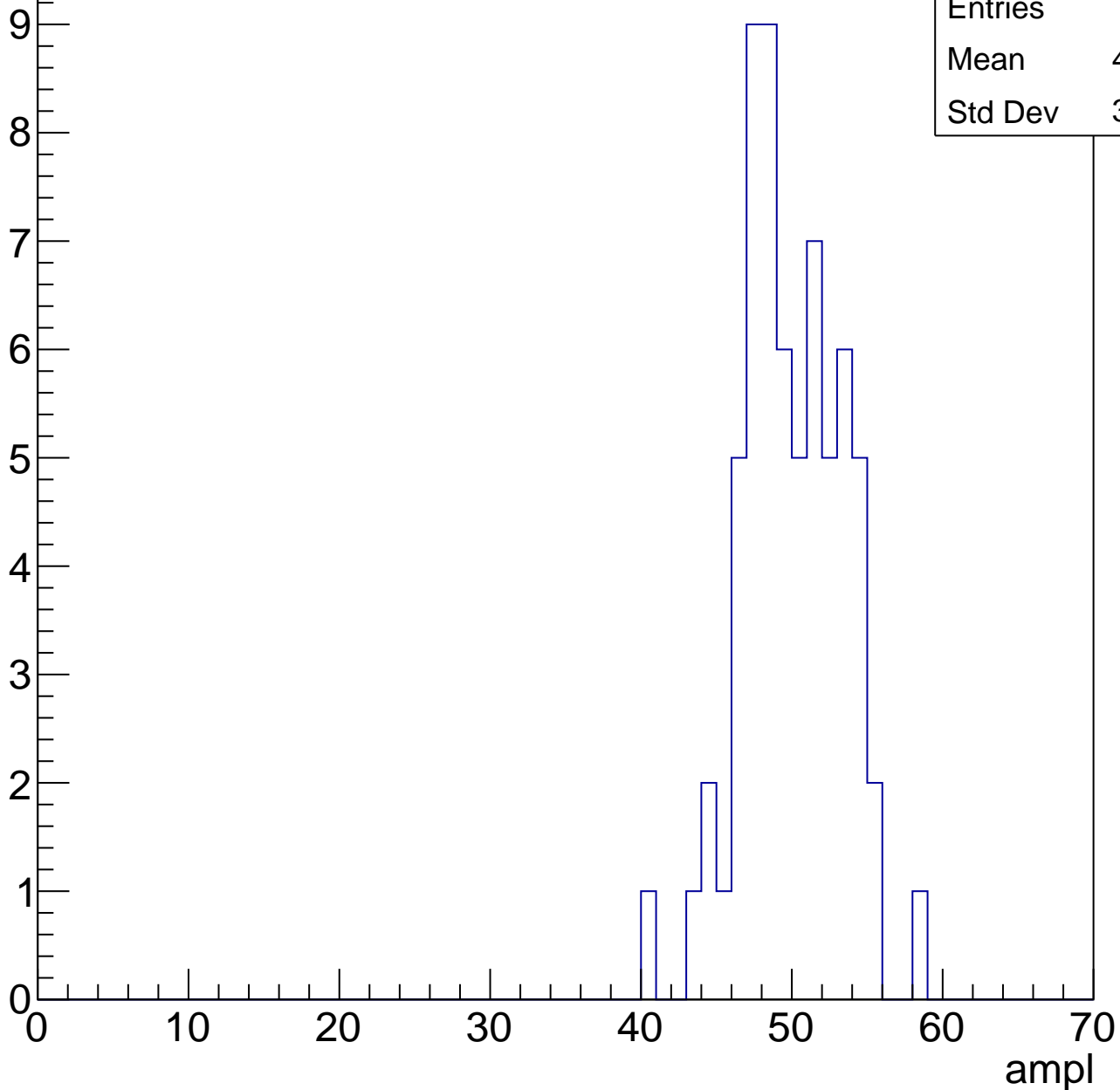


B1L103S, U10-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	49.51
Std Dev	3.301

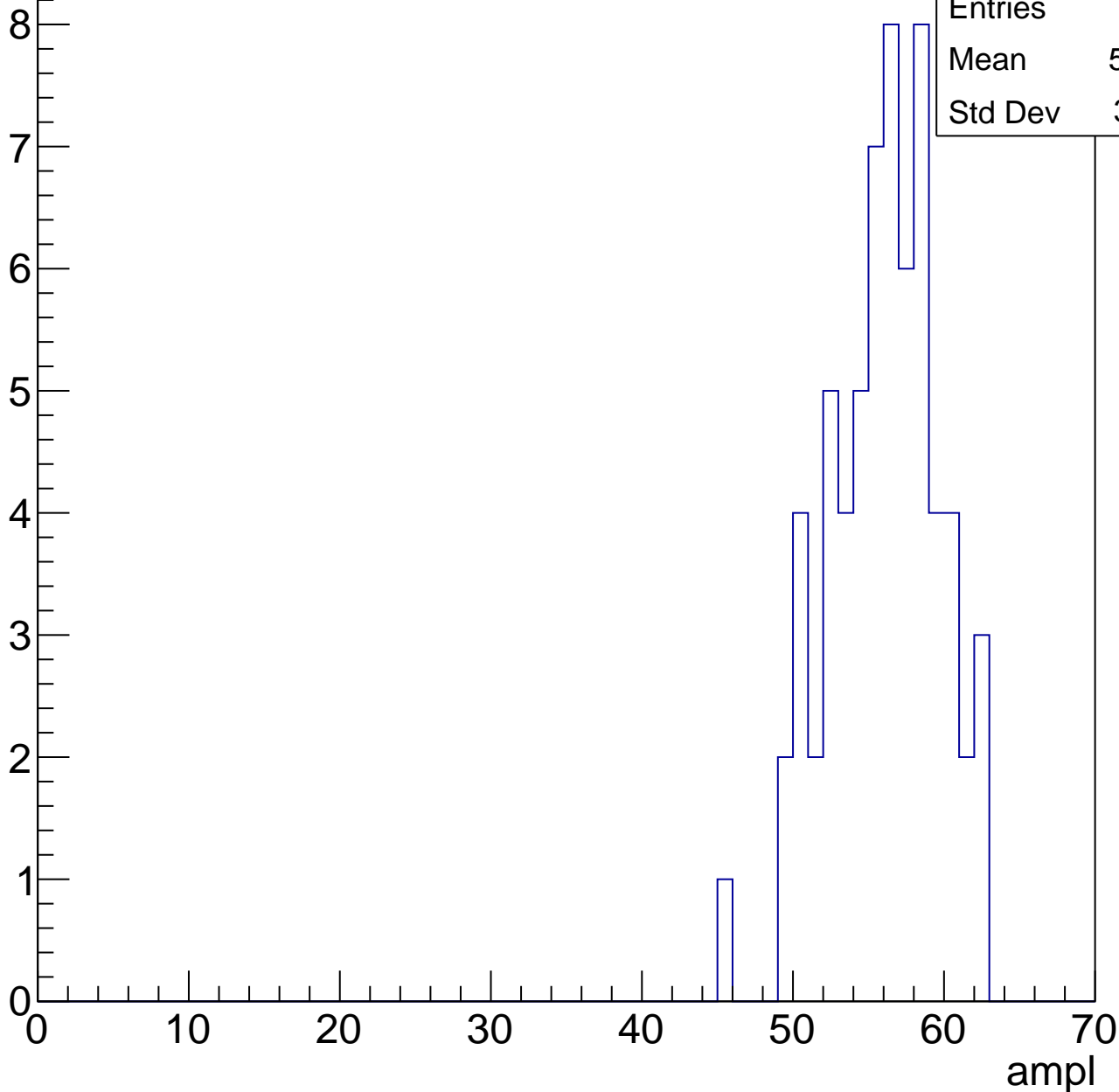


B1L103S, U10-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.54
Std Dev	3.591

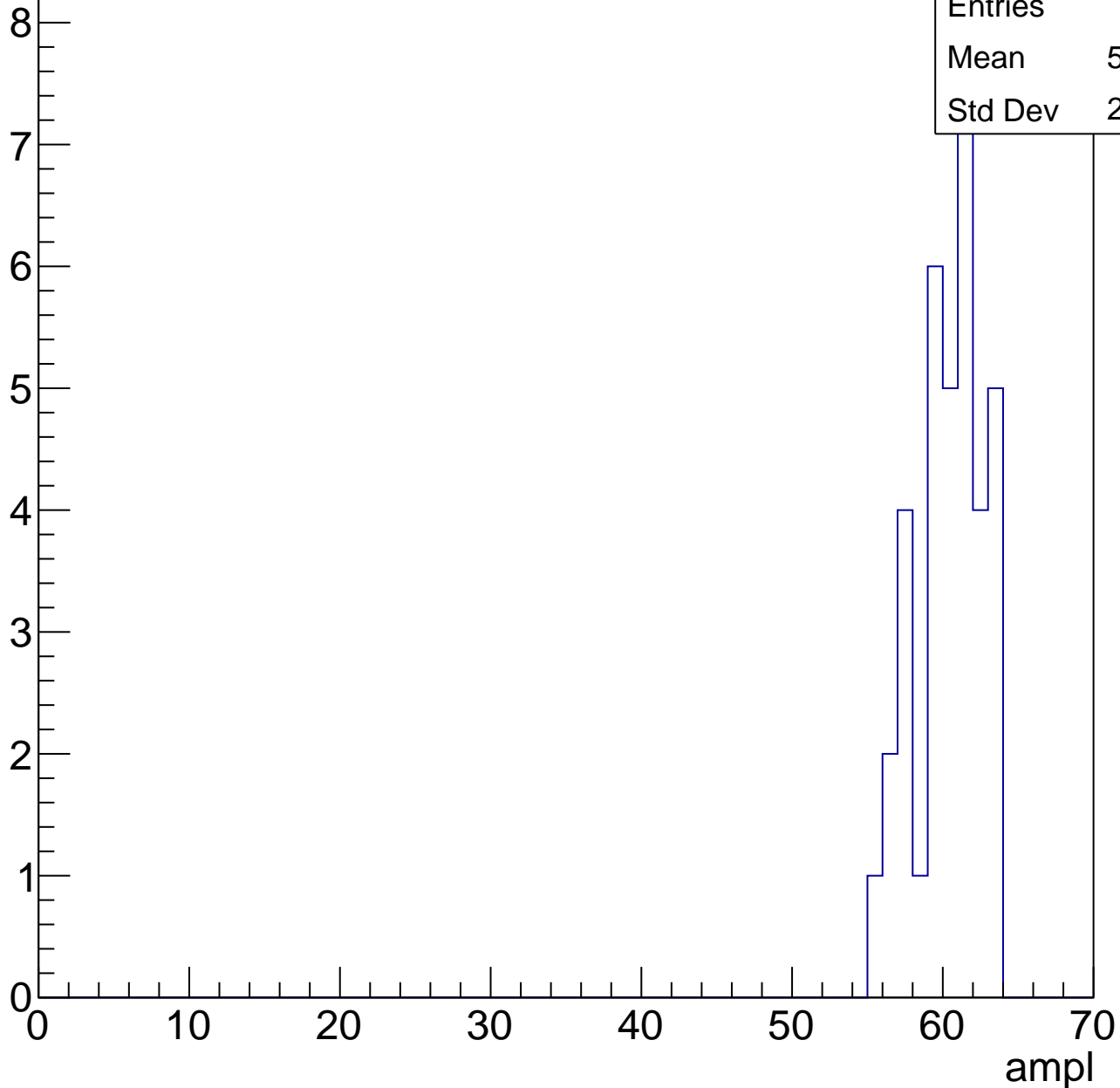


B1L103S, U10-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	59.94
Std Dev	2.185

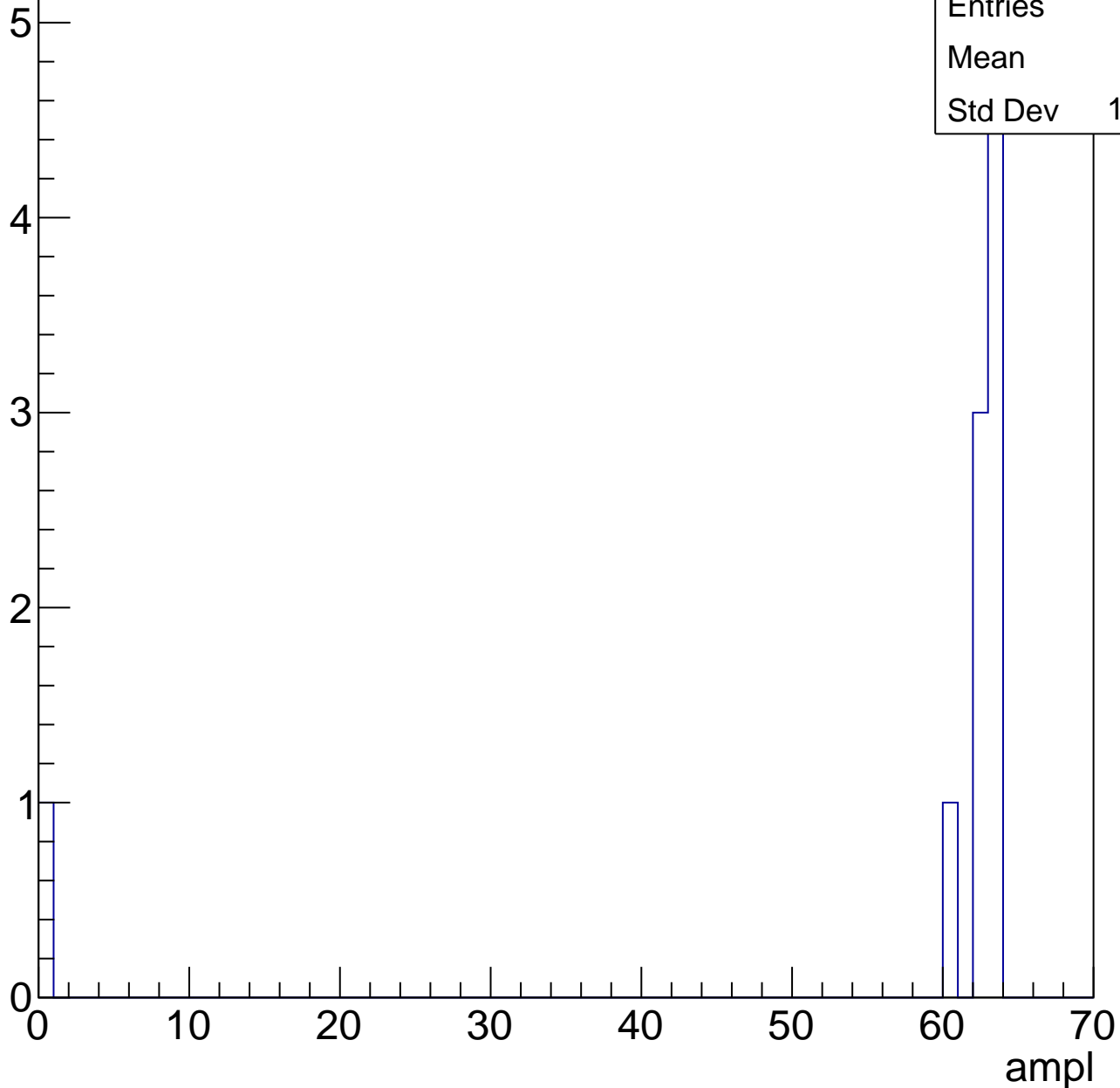


B1L103S, U10-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	56.1
Std Dev	18.72



B1L103S, U10-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch67, adc0

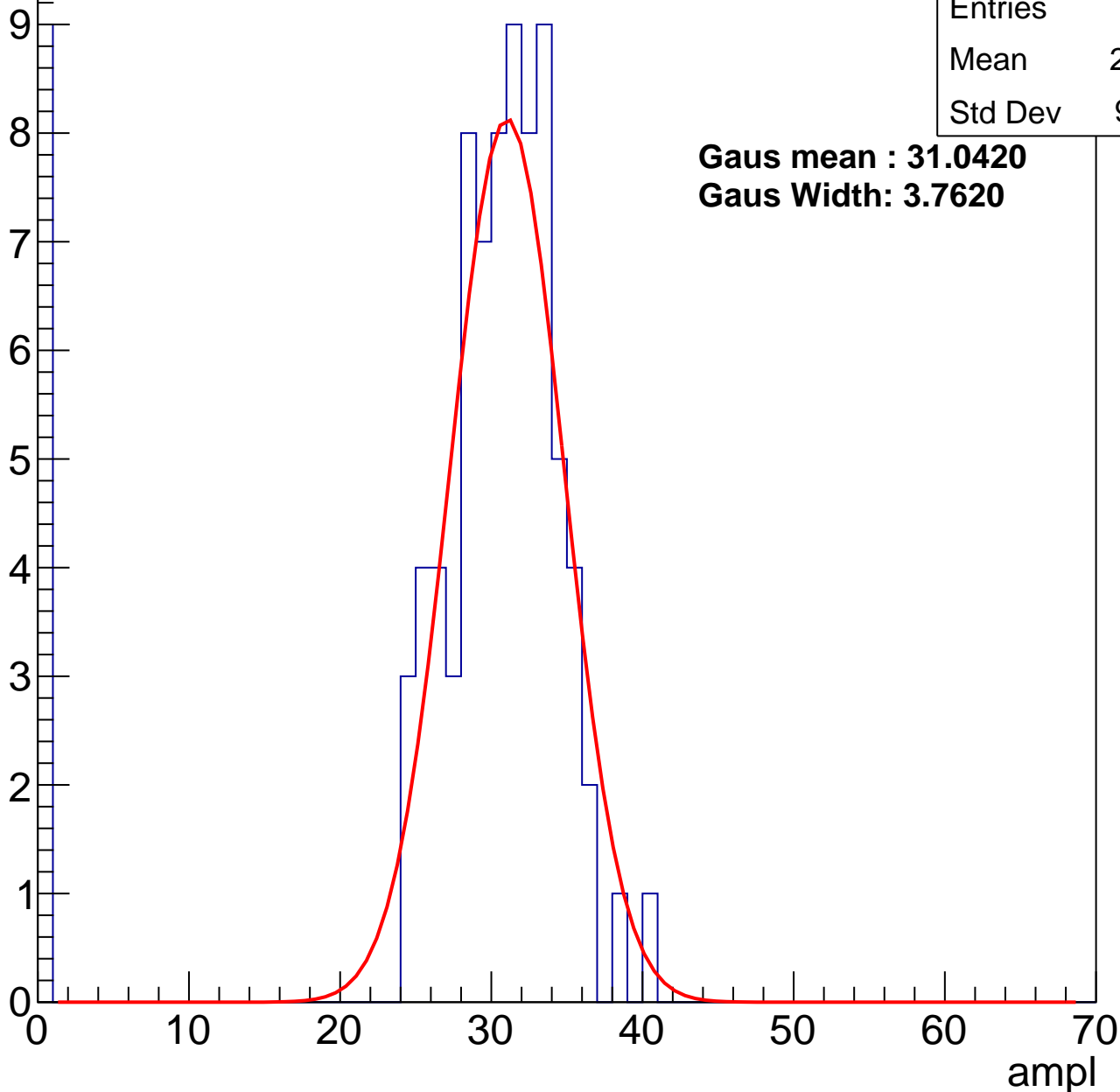
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	27.25
Std Dev	9.901

Gaus mean : 31.0420

Gaus Width: 3.7620



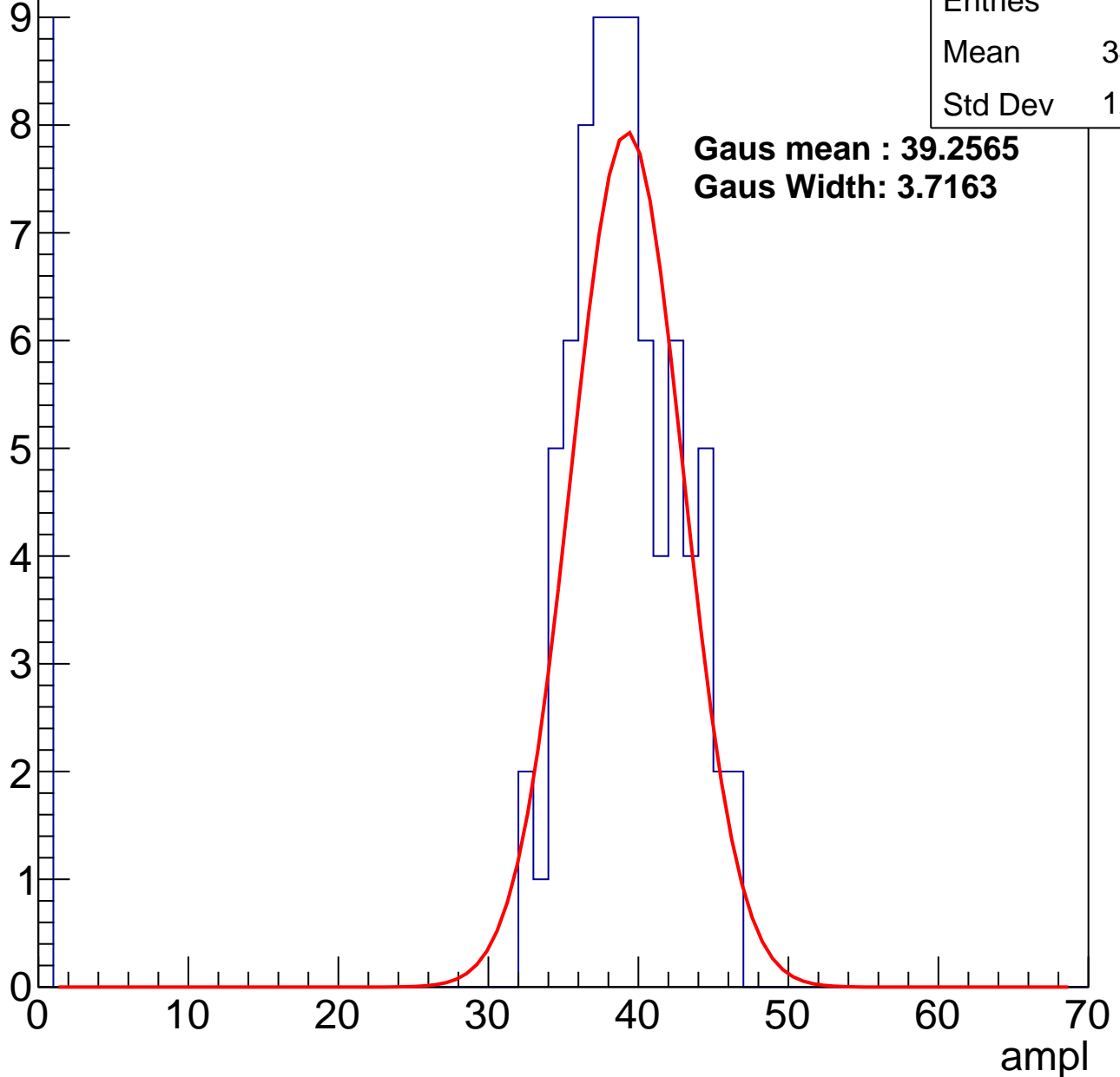
B1L103S, U10-ch67, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	34.72
Std Dev	12.23

Gaus mean : 39.2565
Gaus Width: 3.7163



B1L103S, U10-ch67, adc2

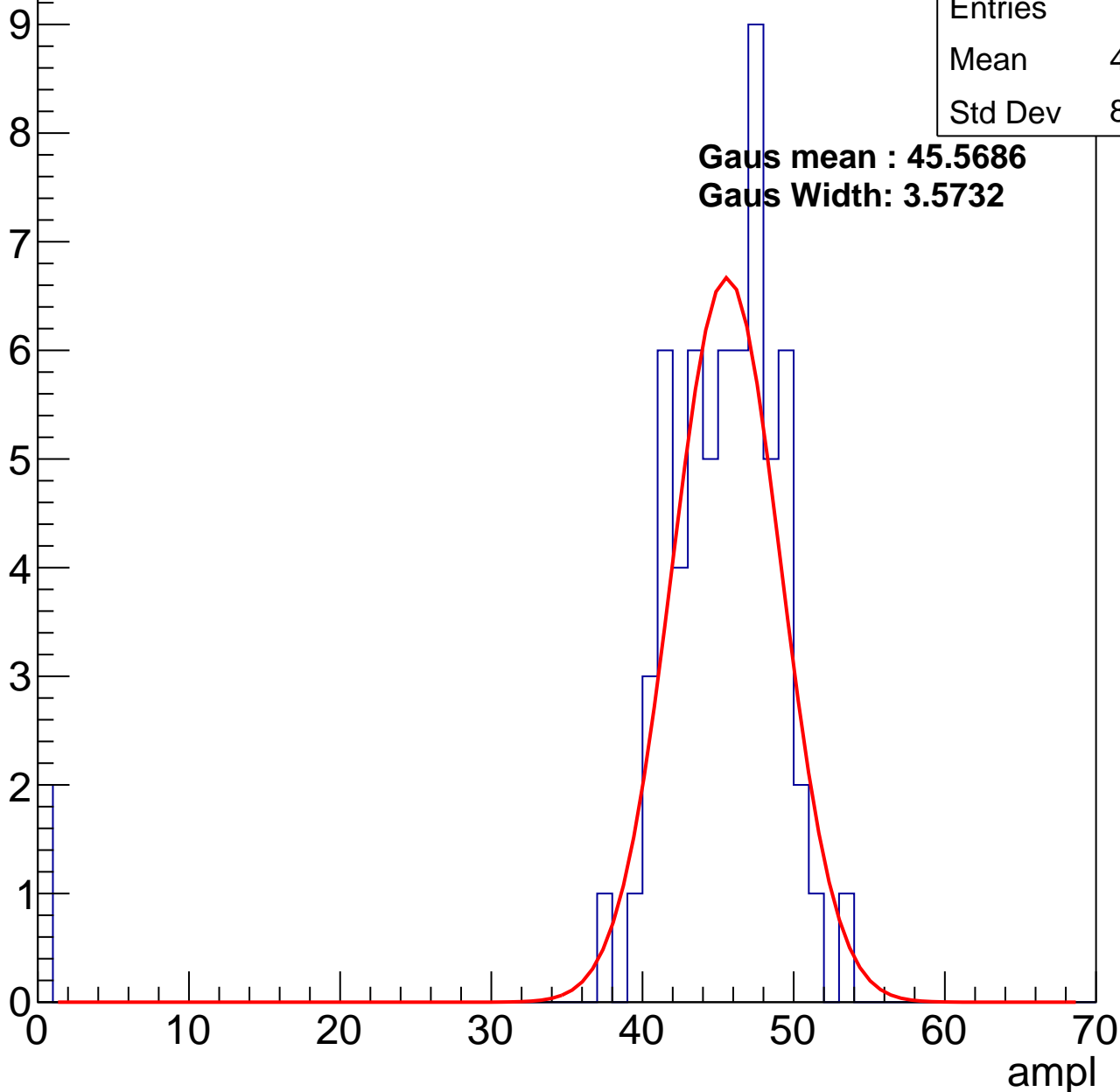
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	43.67
Std Dev	8.484

Gaus mean : 45.5686

Gaus Width: 3.5732

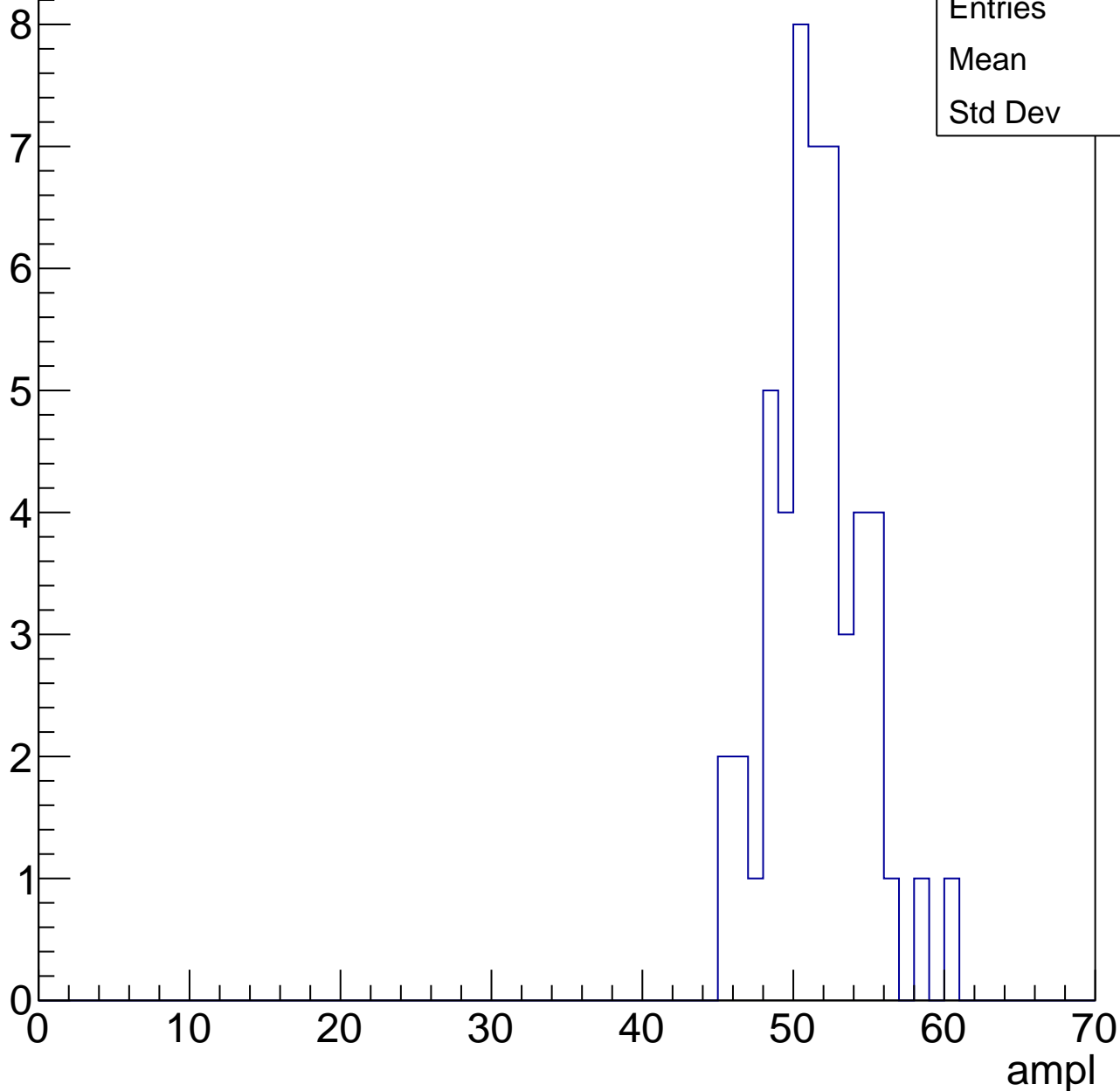


B1L103S, U10-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	51.1
Std Dev	3.1

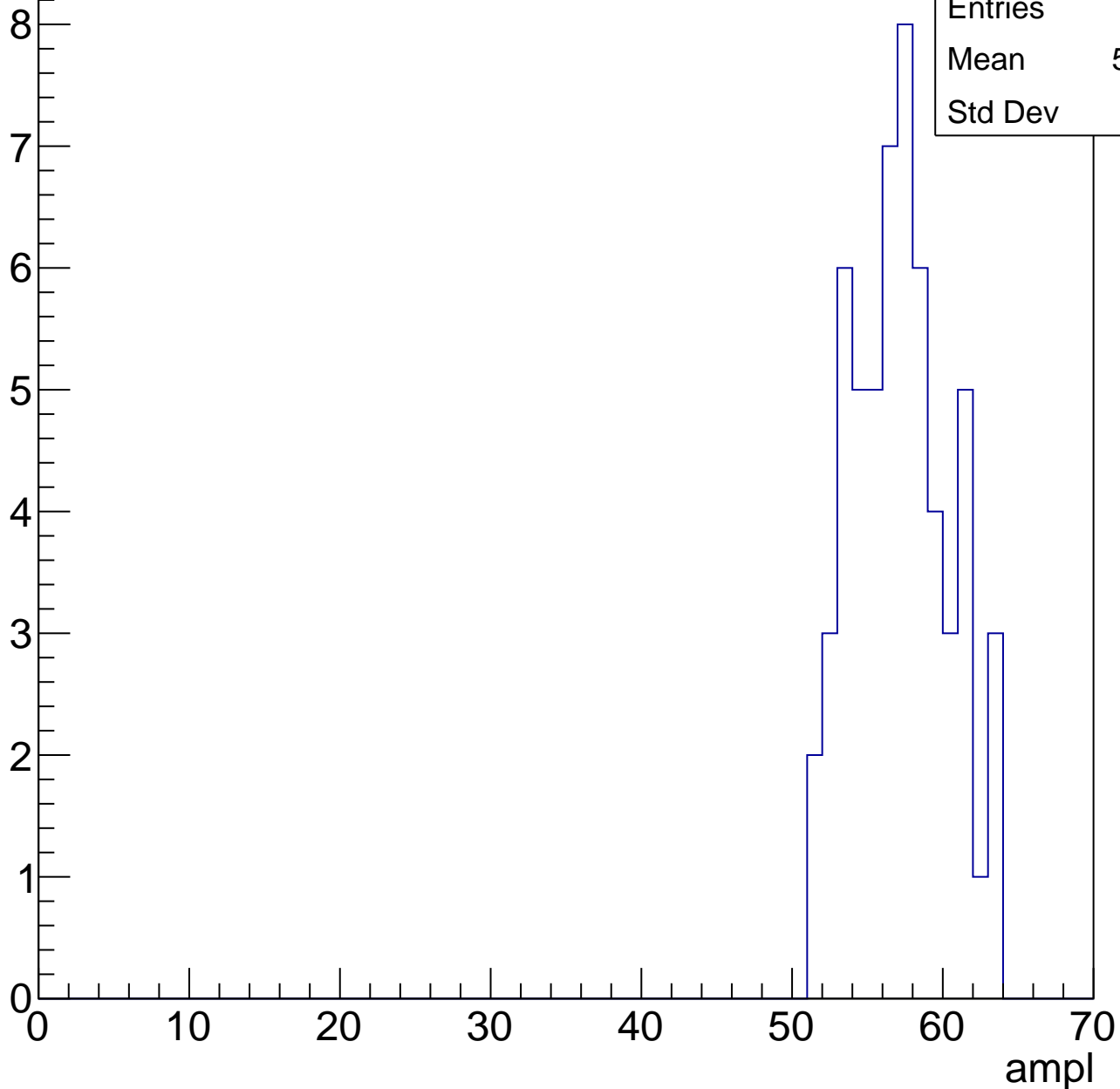


B1L103S, U10-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.71
Std Dev	3.14

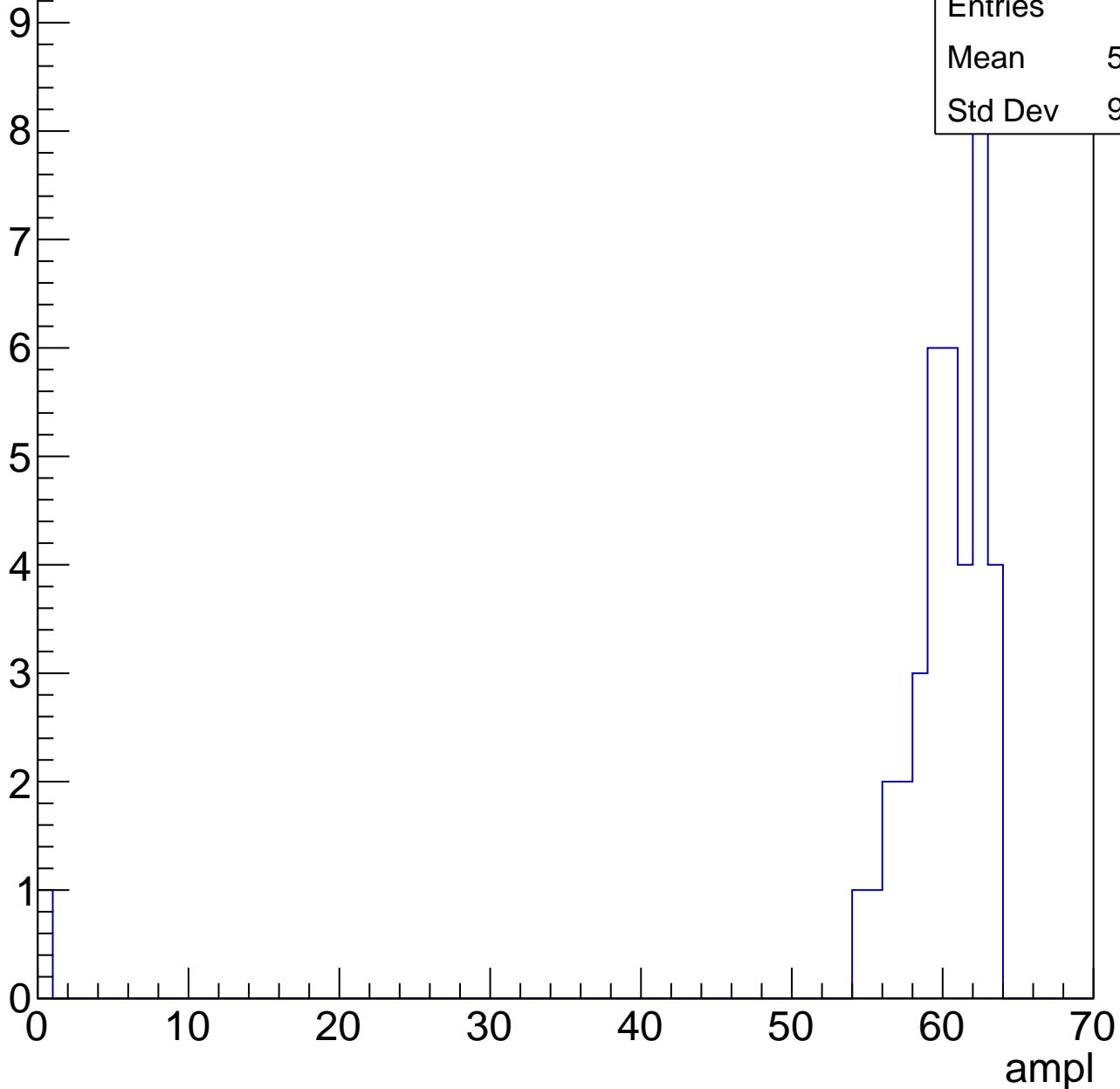


B1L103S, U10-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.38
Std Dev	9.744



B1L103S, U10-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch68, adc0

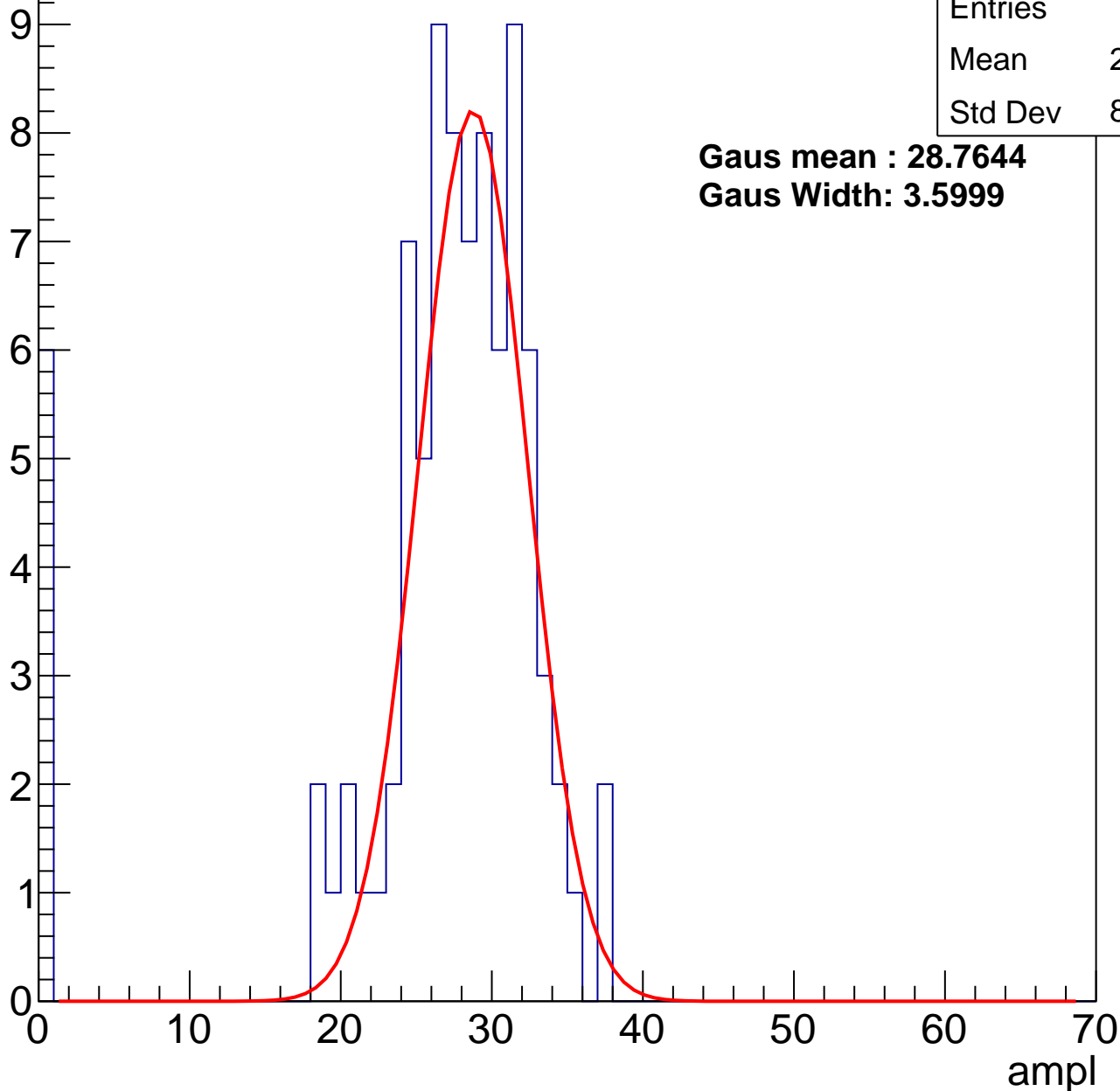
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	25.93
Std Dev	8.005

Gaus mean : 28.7644

Gaus Width: 3.5999



B1L103S, U10-ch68, adc1

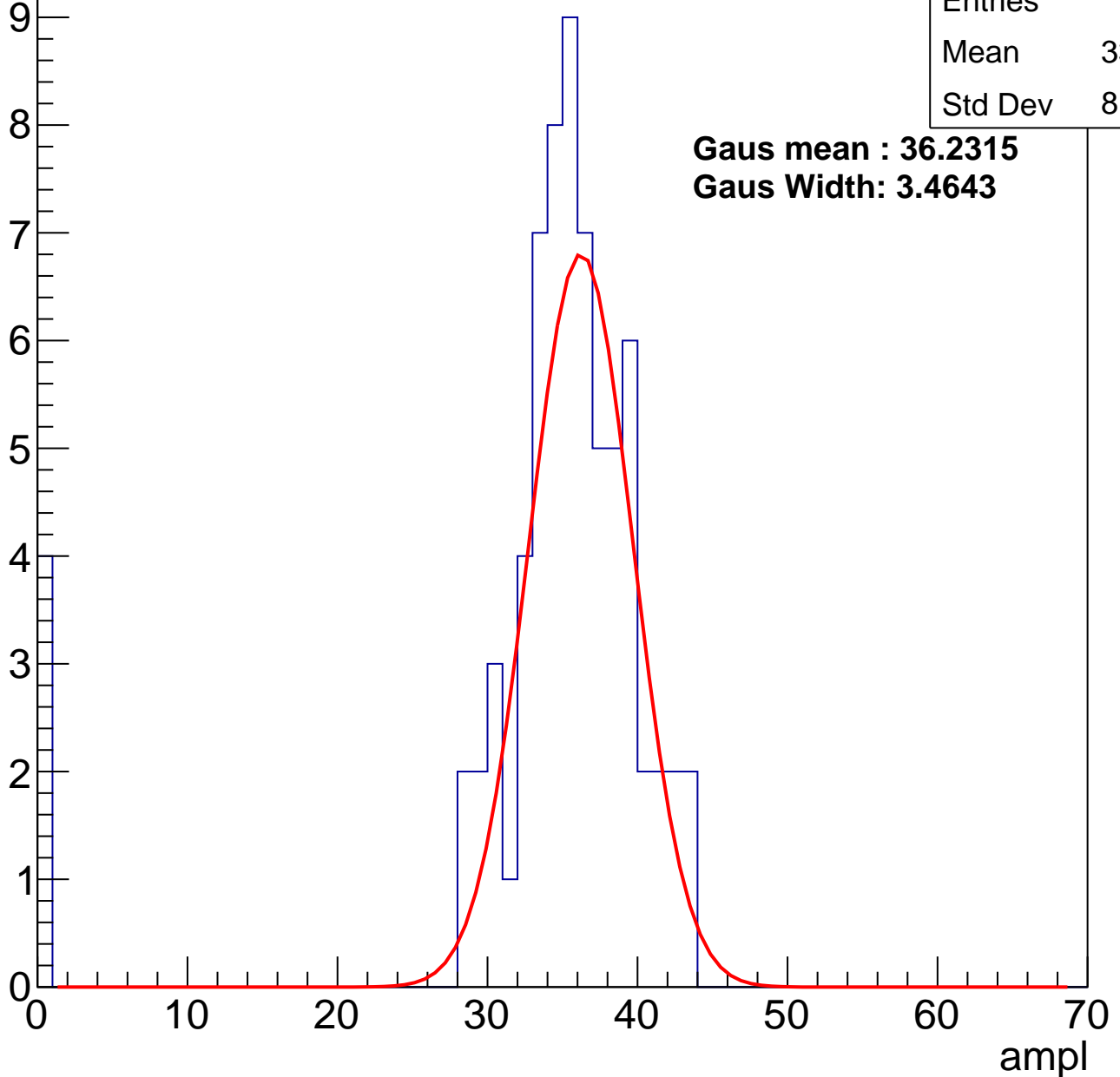
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.44
Std Dev	8.862

Gaus mean : 36.2315

Gaus Width: 3.4643



B1L103S, U10-ch68, adc2

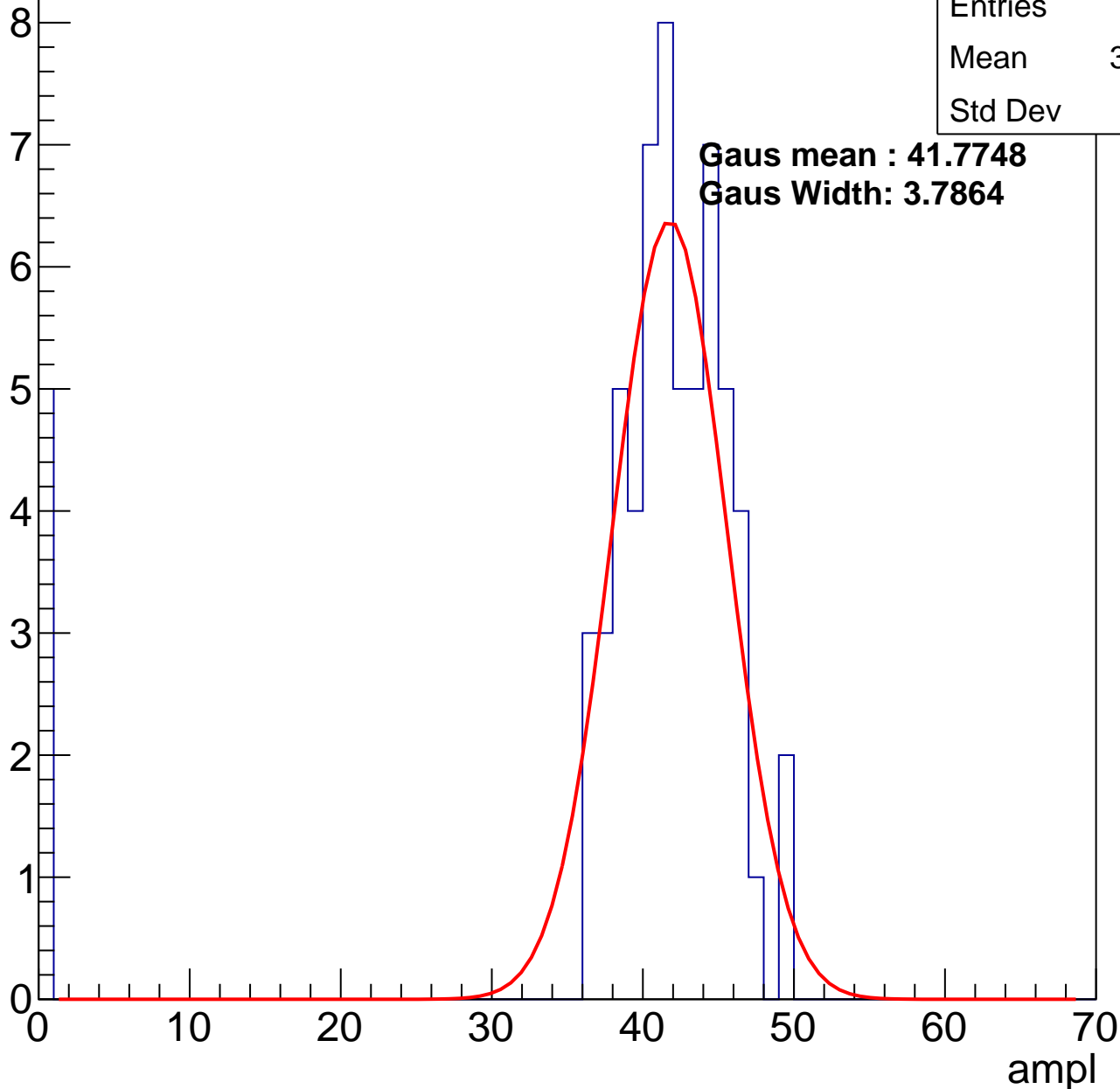
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.44
Std Dev	11.6

Gaus mean : 41.7748

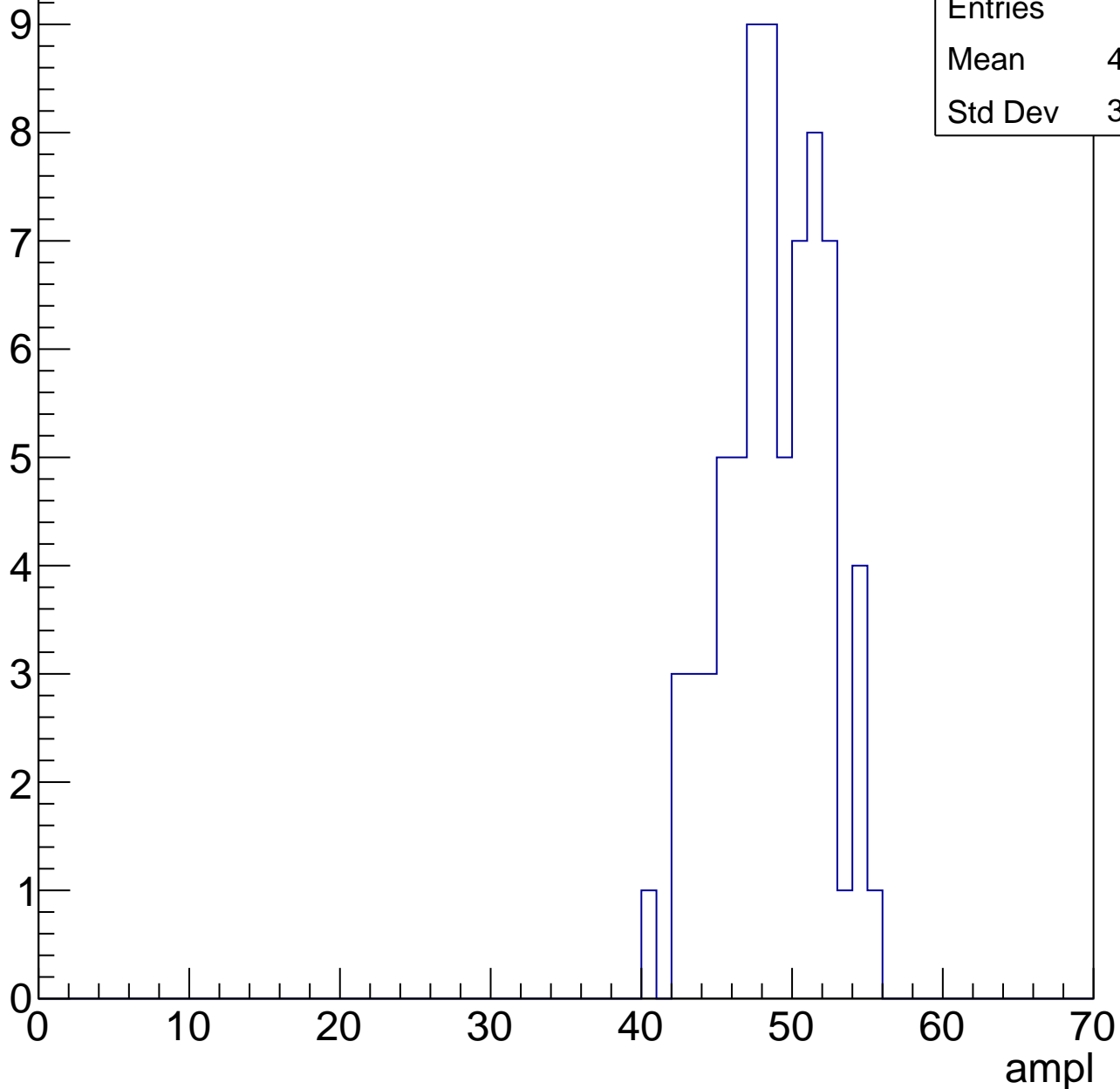
Gaus Width: 3.7864



B1L103S, U10-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



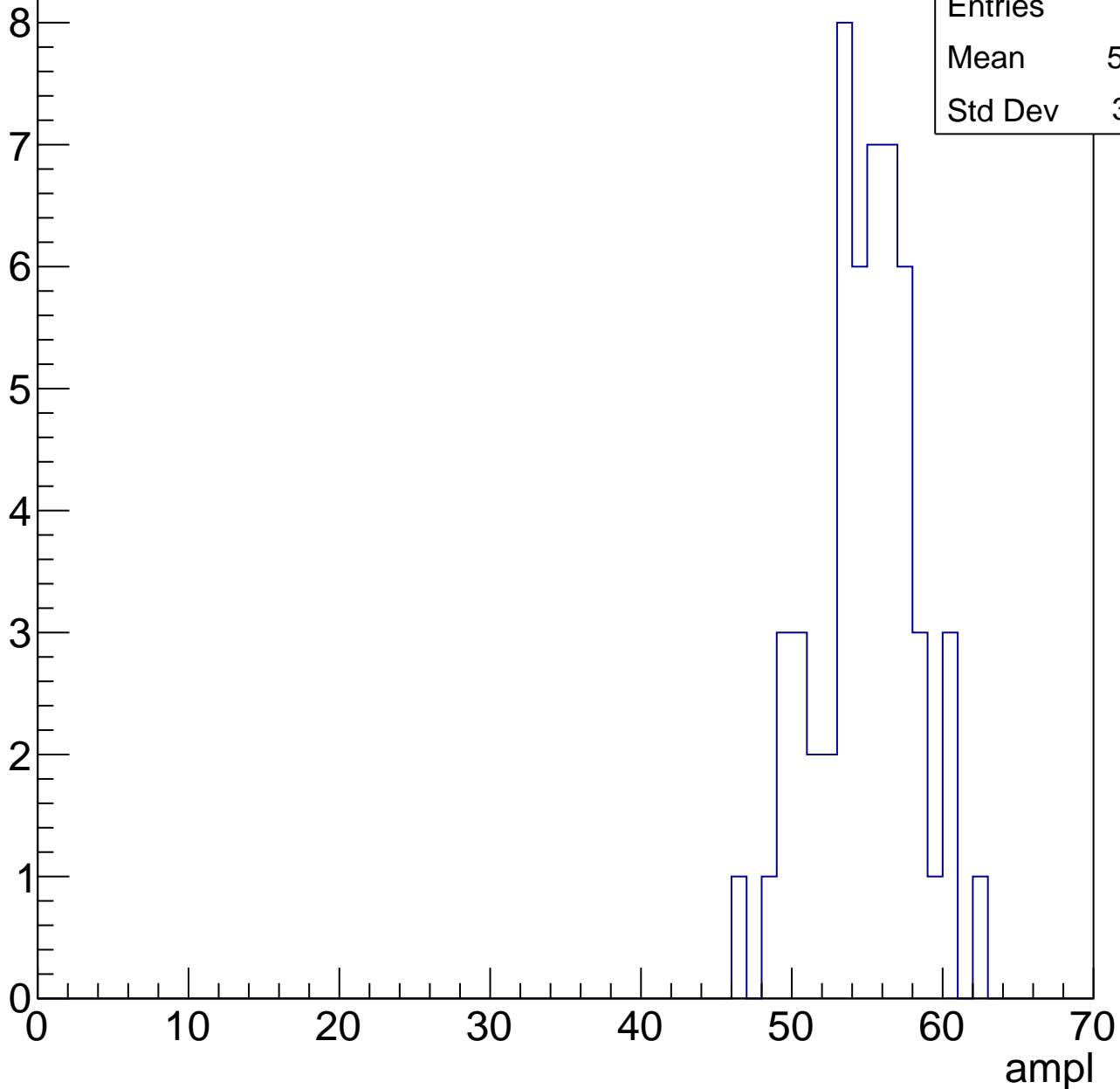
Entries	71
Mean	48.28
Std Dev	3.353

B1L103S, U10-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.43
Std Dev	3.281

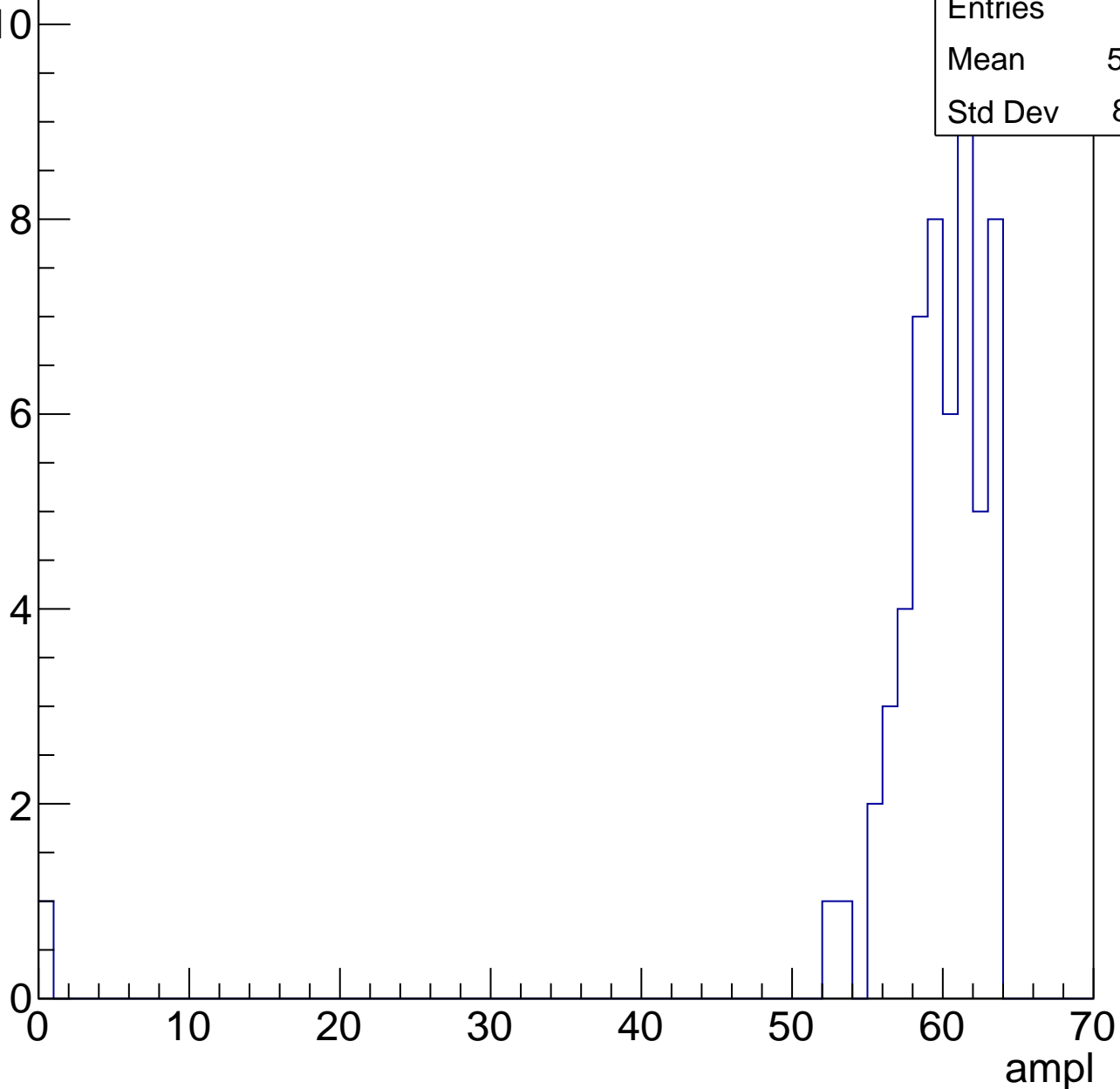


B1L103S, U10-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

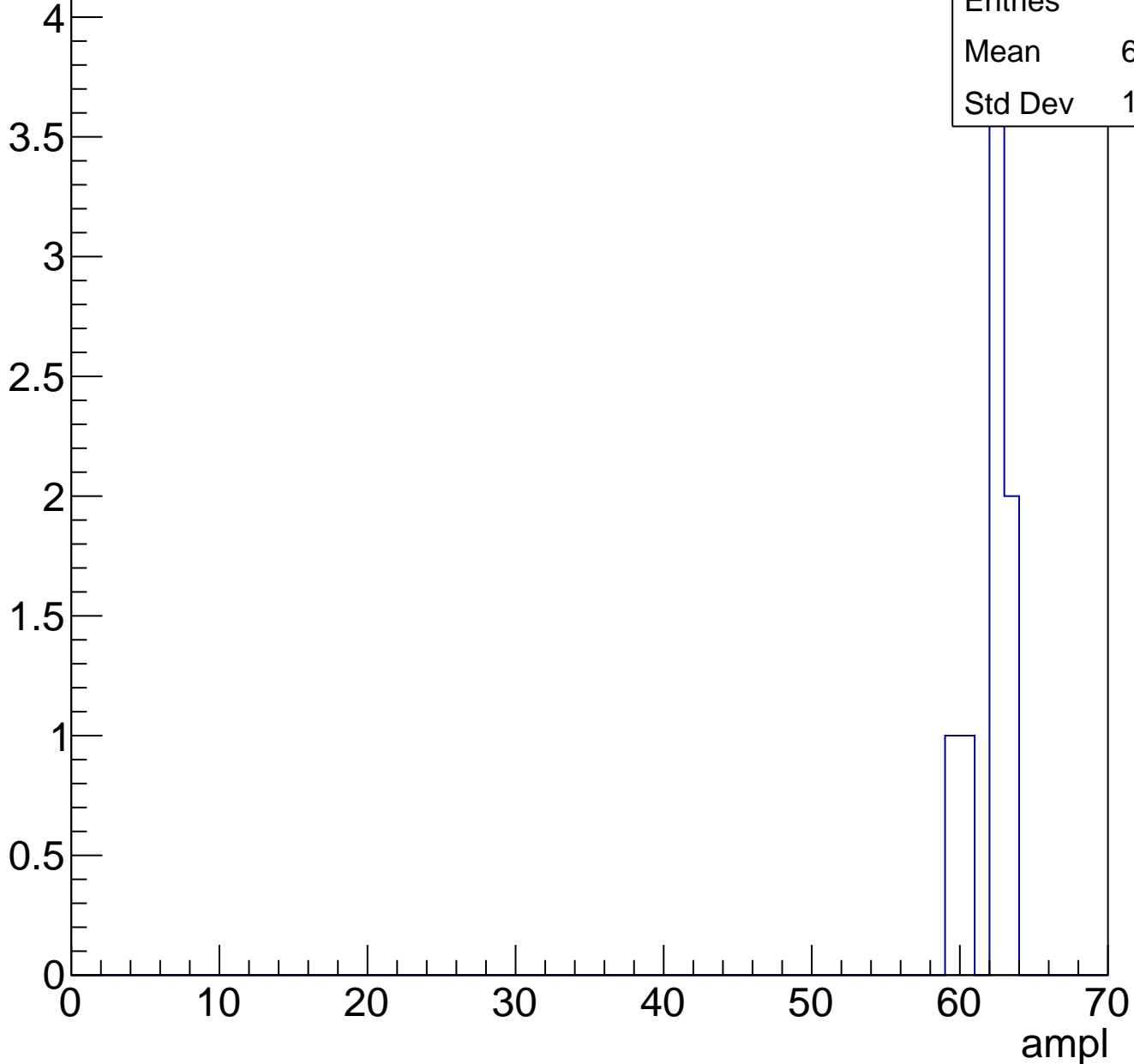
Entries	56
Mean	58.45
Std Dev	8.291



B1L103S, U10-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch69, adc0

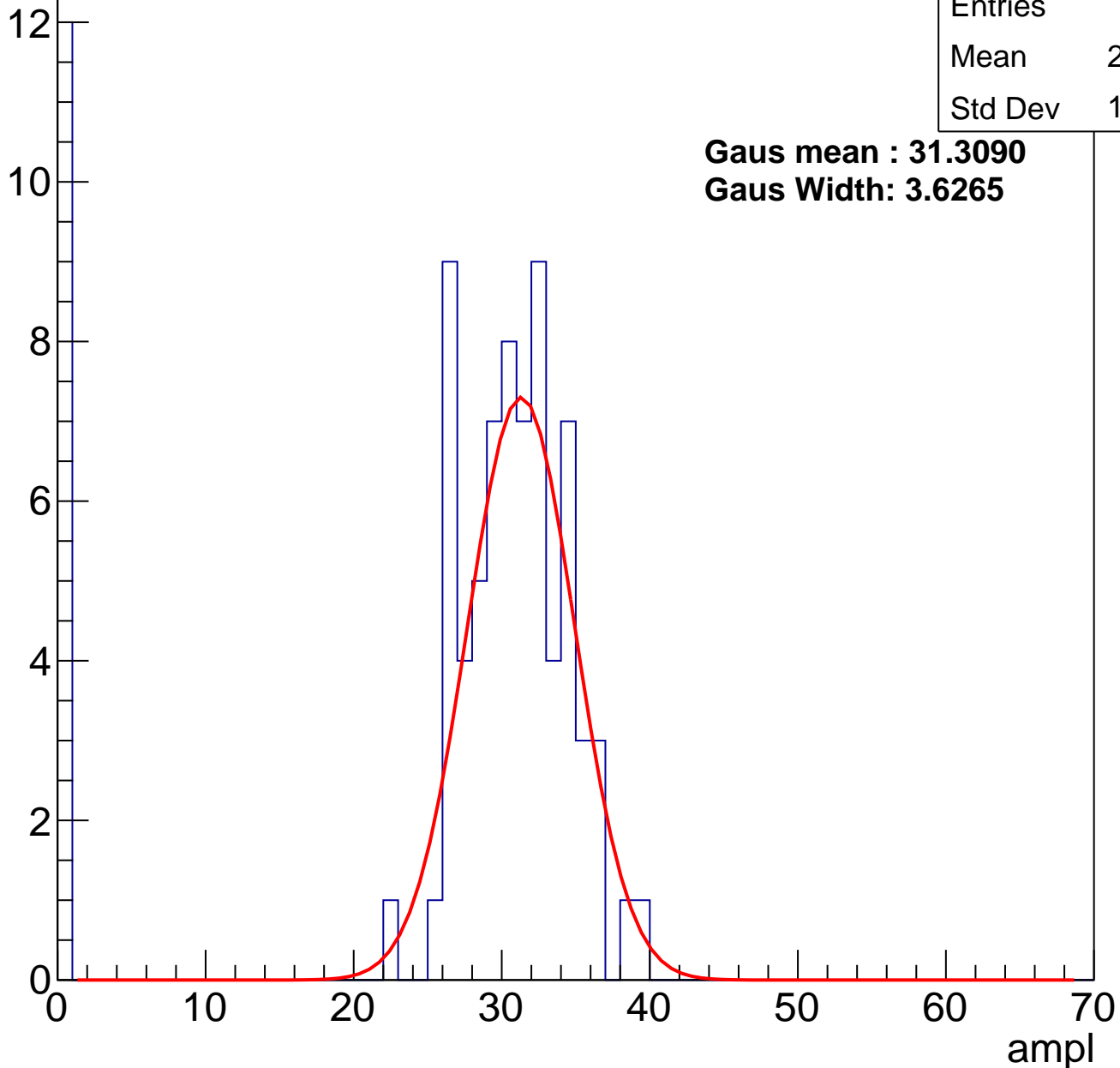
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	26.06
Std Dev	11.23

Gaus mean : 31.3090

Gaus Width: 3.6265

Entry



B1L103S, U10-ch69, adc1

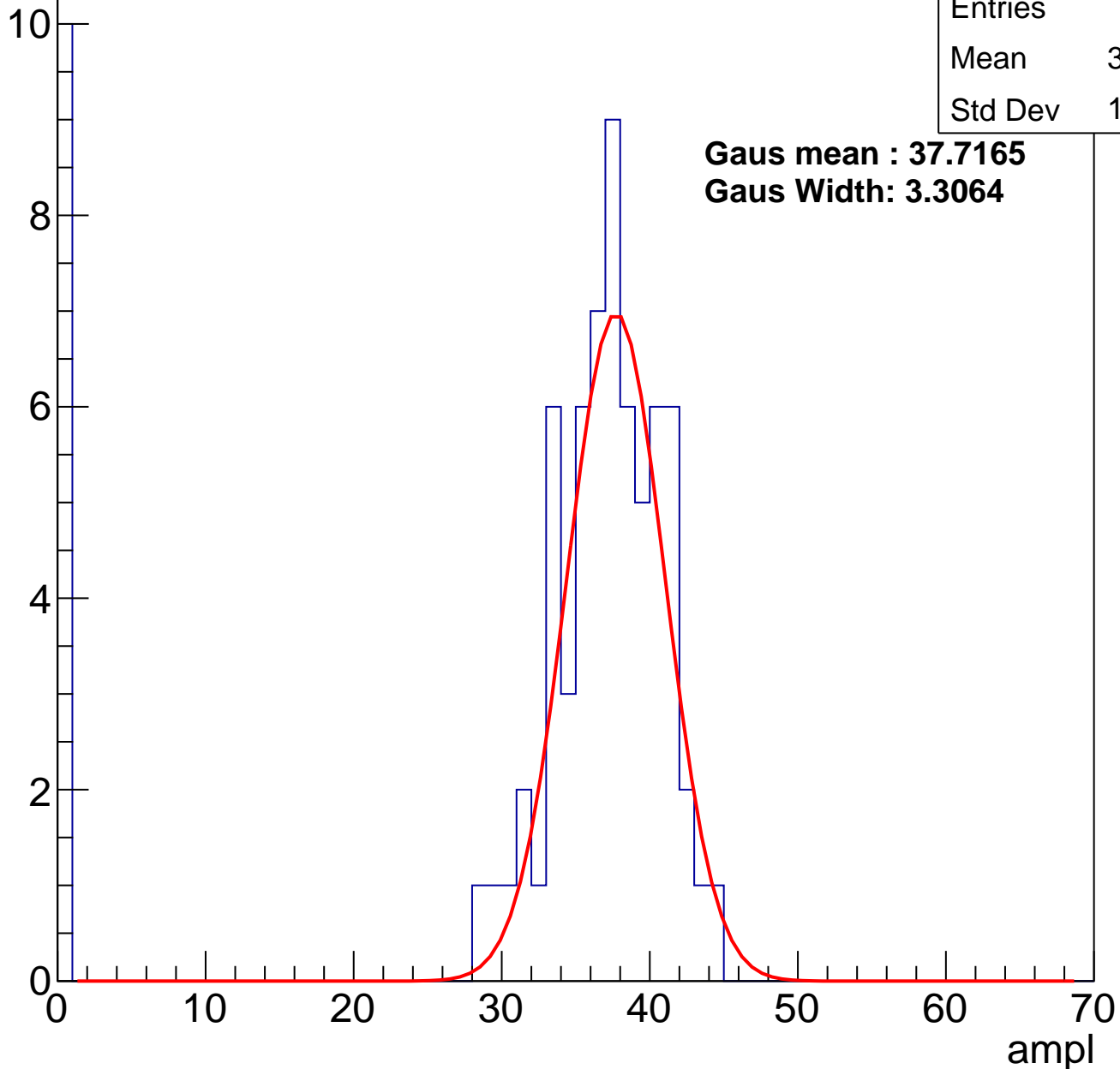
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.84
Std Dev	12.98

Gaus mean : 37.7165

Gaus Width: 3.3064

Entry



B1L103S, U10-ch69, adc2

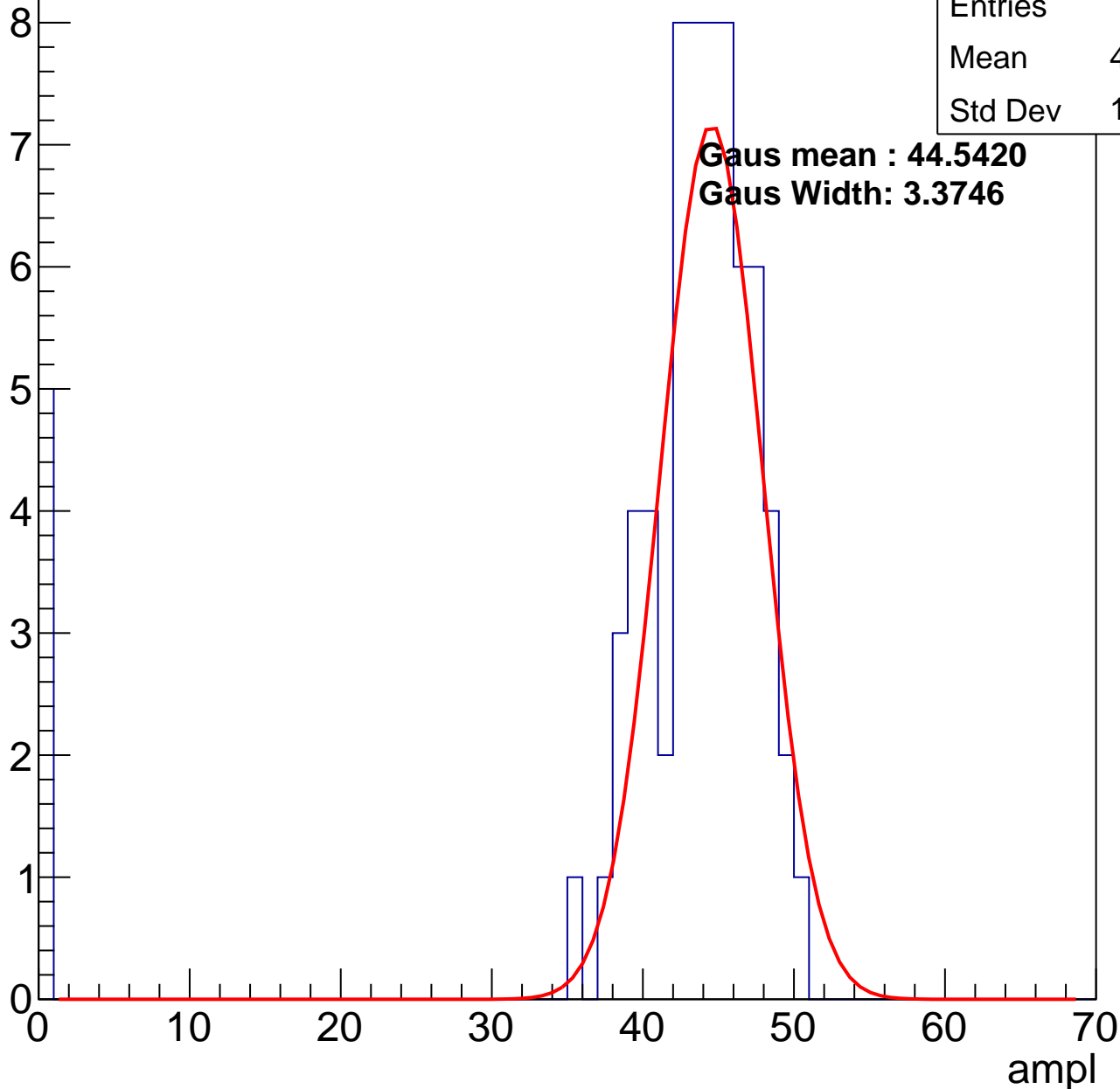
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	40.48
Std Dev	11.56

Gaus mean : 44.5420

Gaus Width: 3.3746

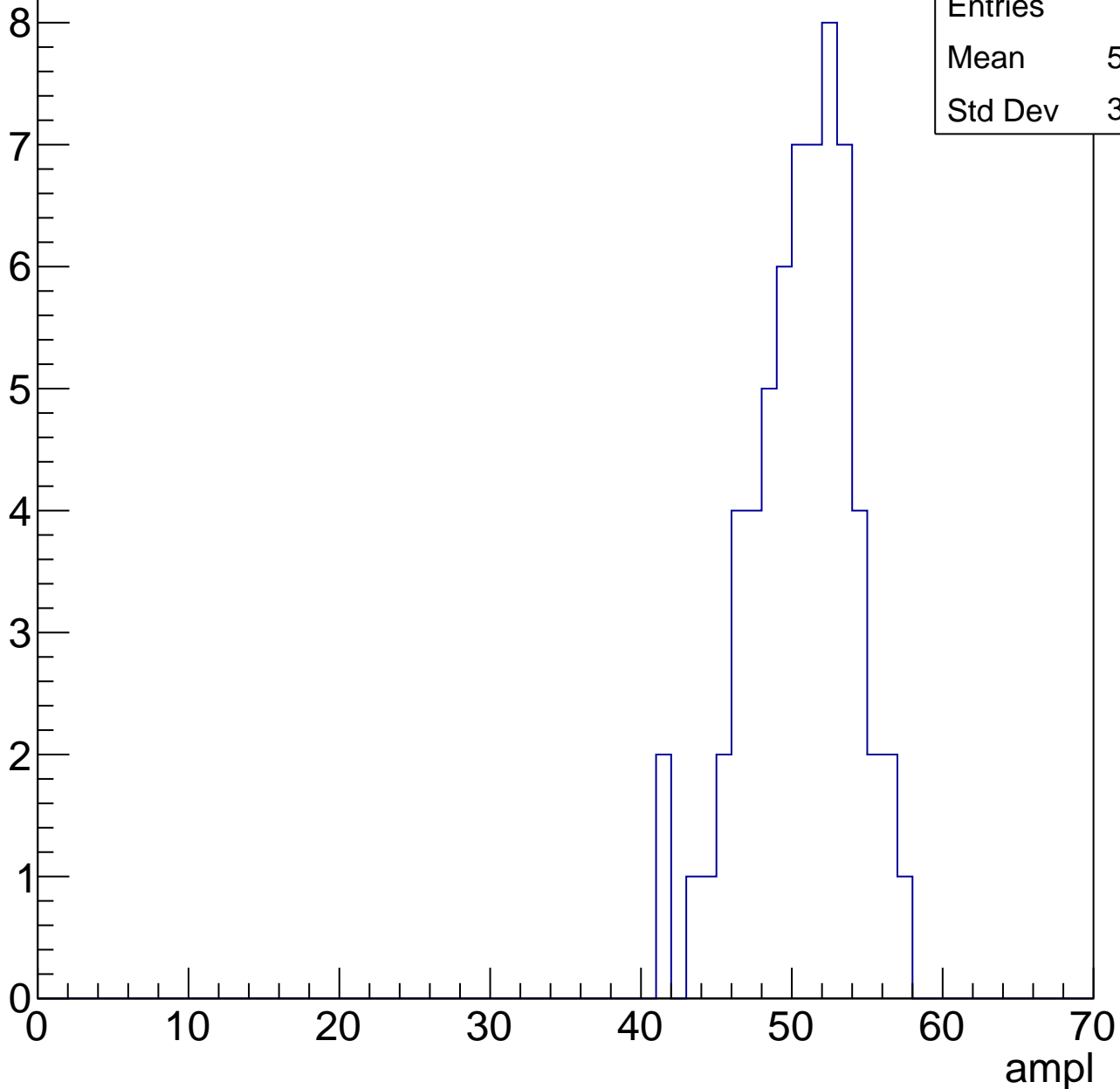


B1L103S, U10-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	50.06
Std Dev	3.473

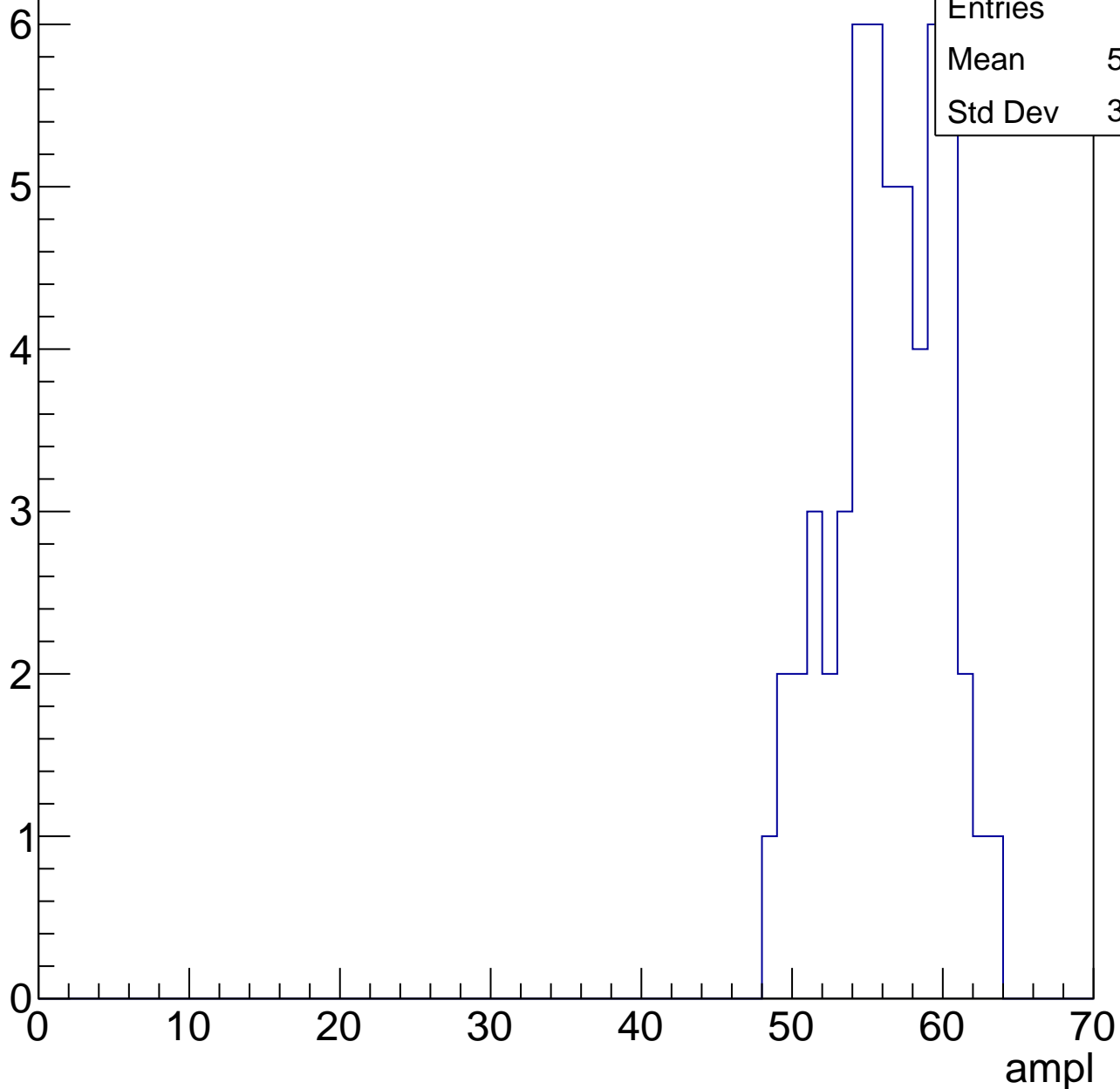


B1L103S, U10-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.89
Std Dev	3.576

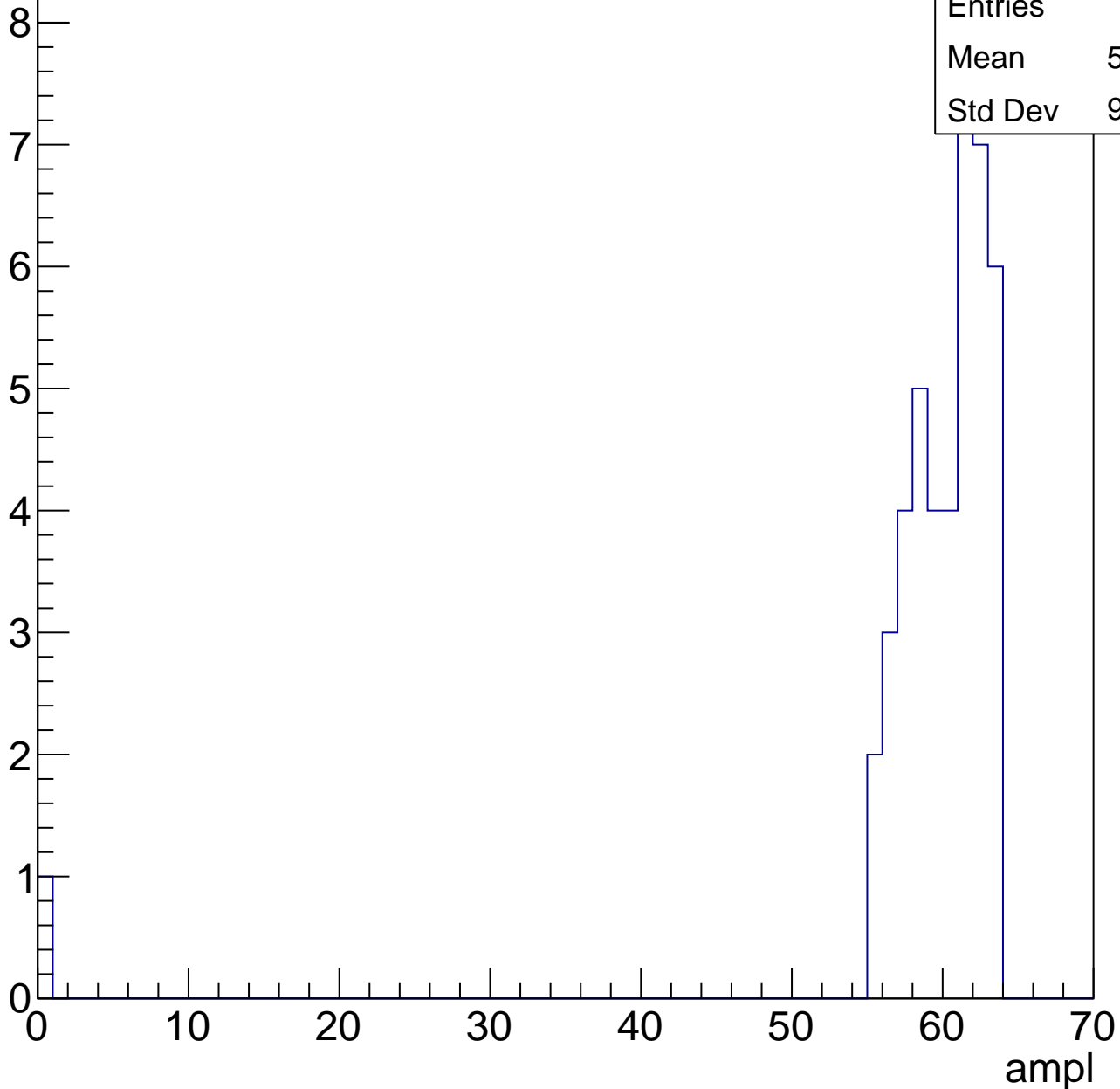


B1L103S, U10-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

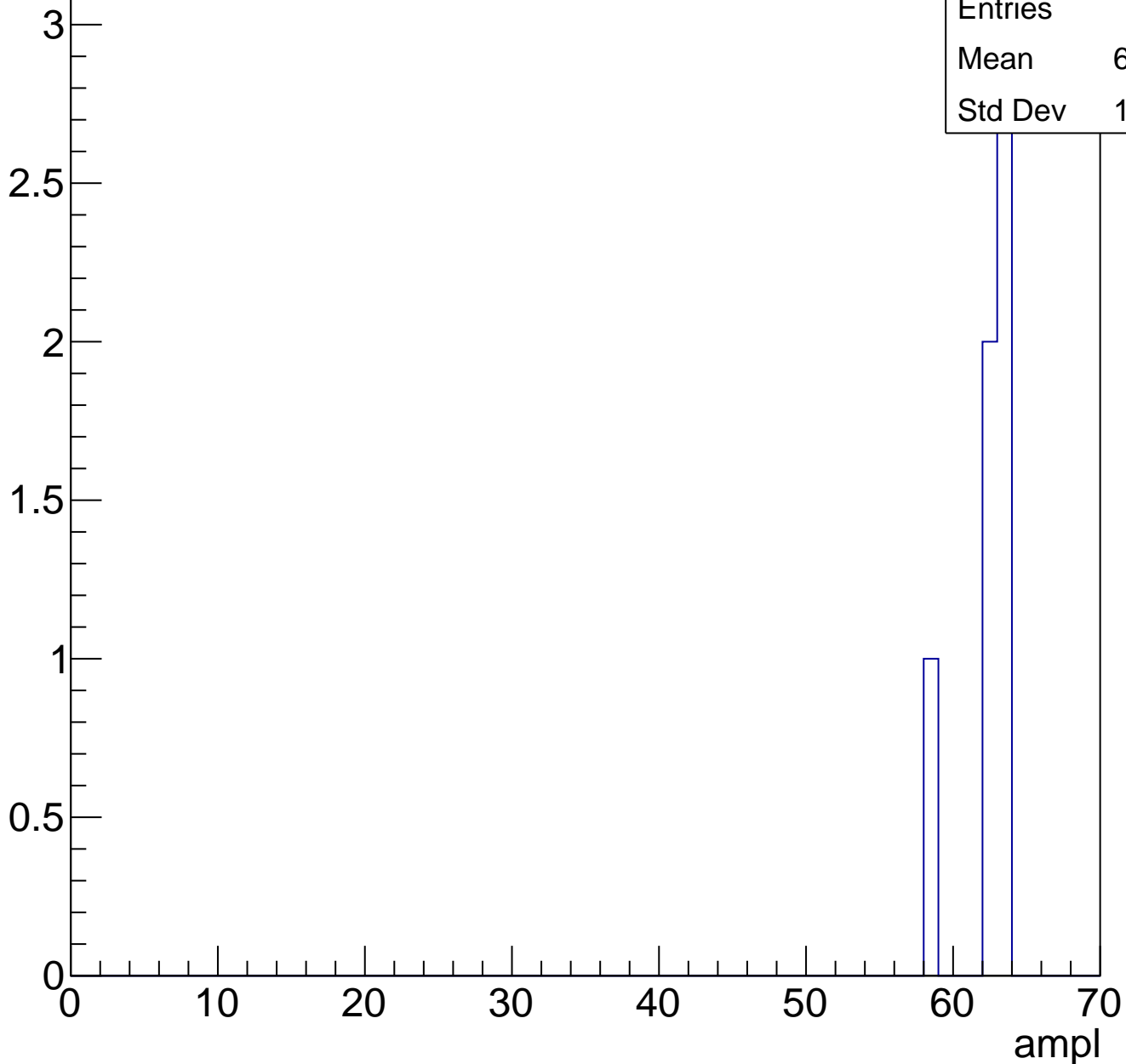
Entries	44
Mean	58.45
Std Dev	9.223



B1L103S, U10-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

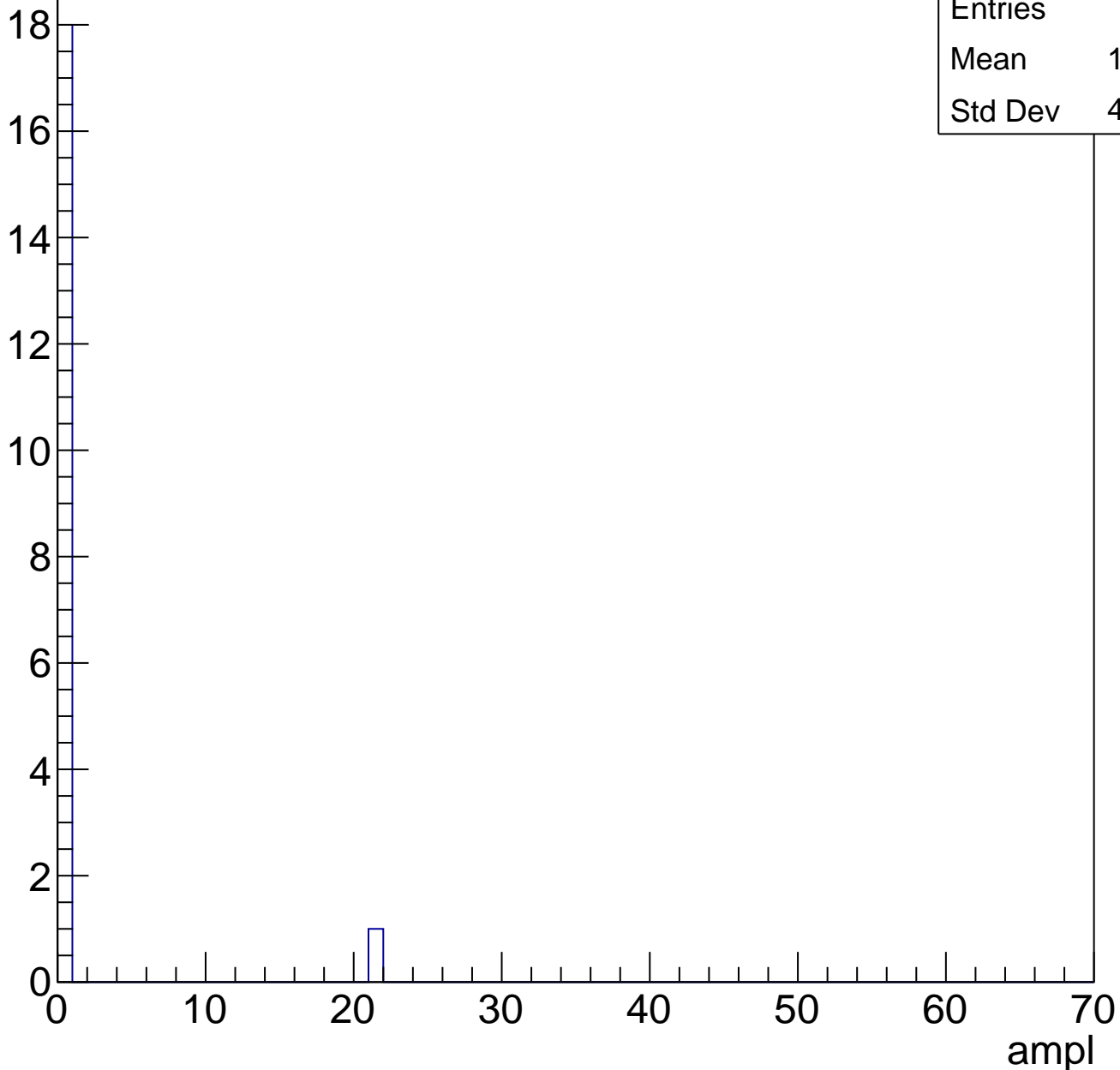


B1L103S, U10-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U10-ch70, adc0

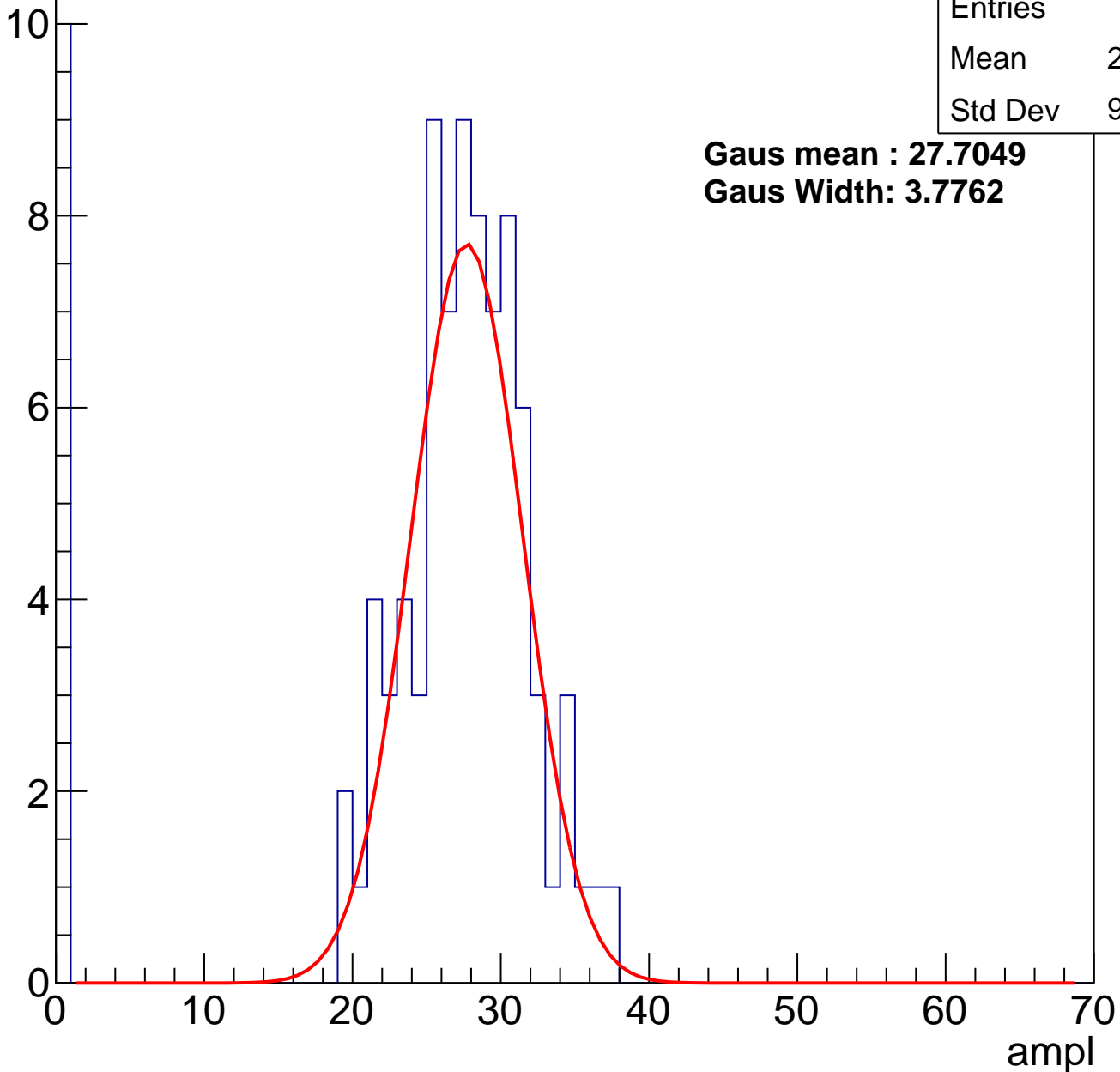
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.33
Std Dev	9.304

Gaus mean : 27.7049

Gaus Width: 3.7762

Entry



B1L103S, U10-ch70, adc1

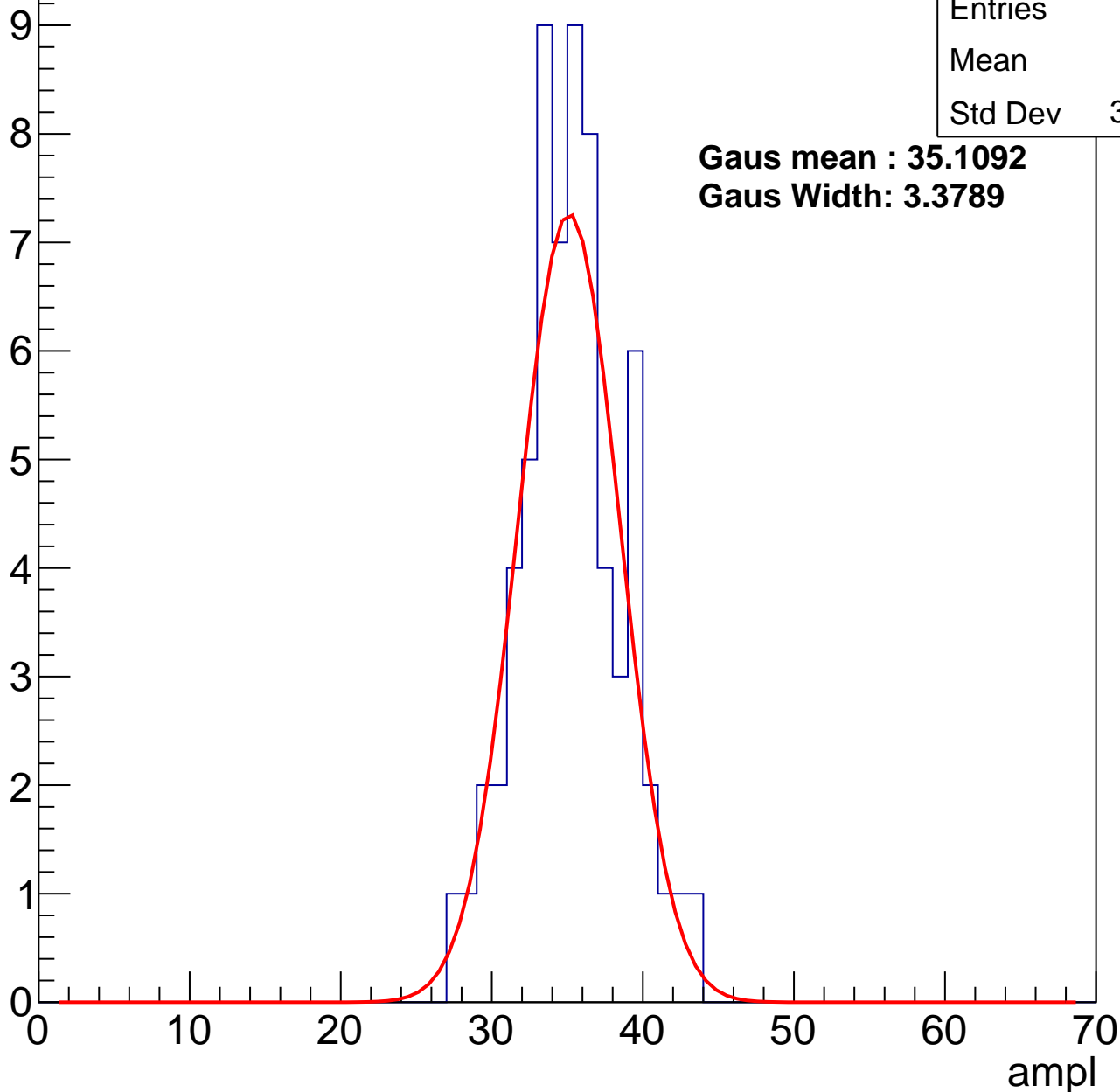
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.8
Std Dev	3.327

Gaus mean : 35.1092

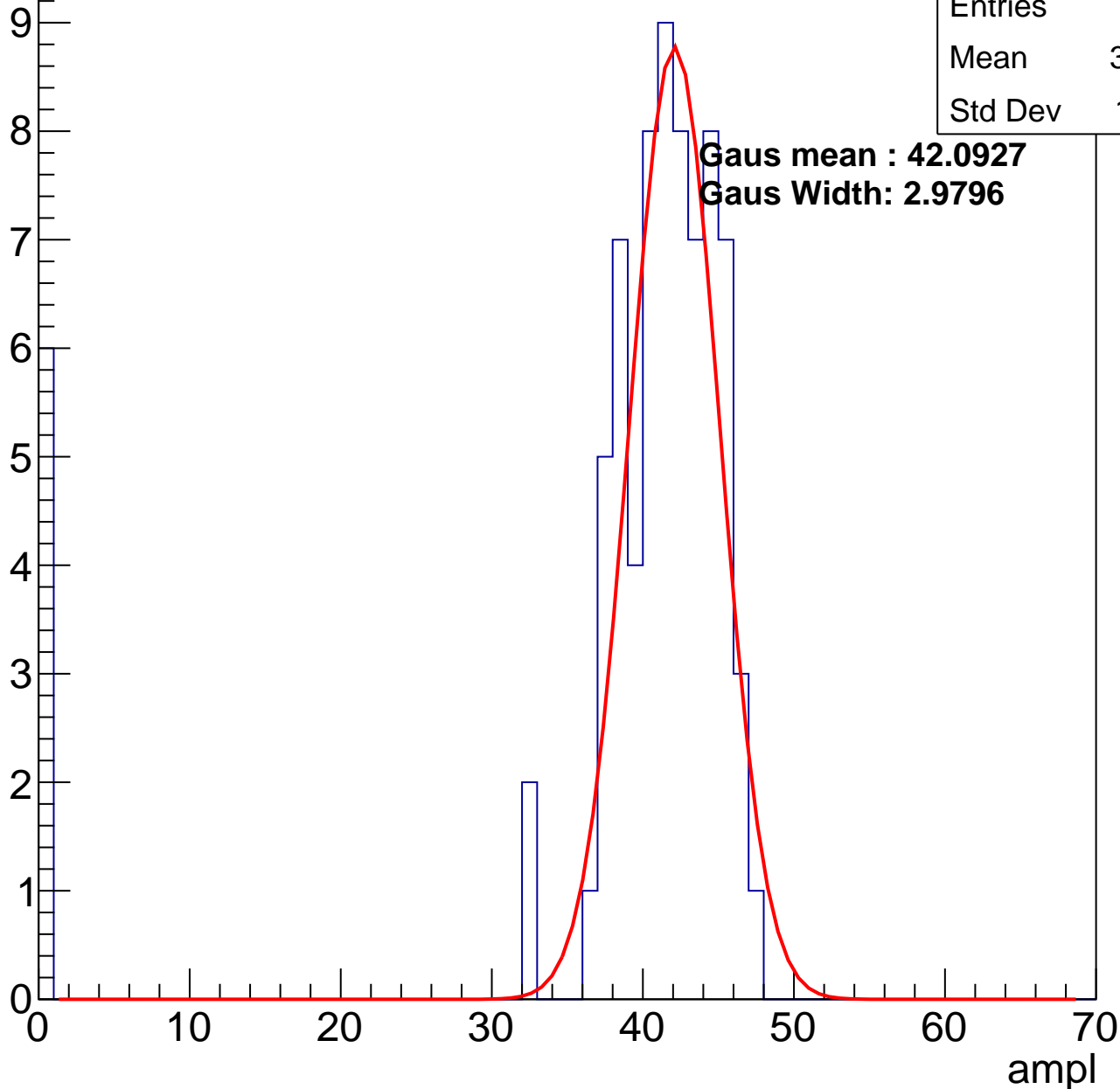
Gaus Width: 3.3789



B1L103S, U10-ch70, adc2

calib_packv5_041523_1651.root, FC#0, port C2

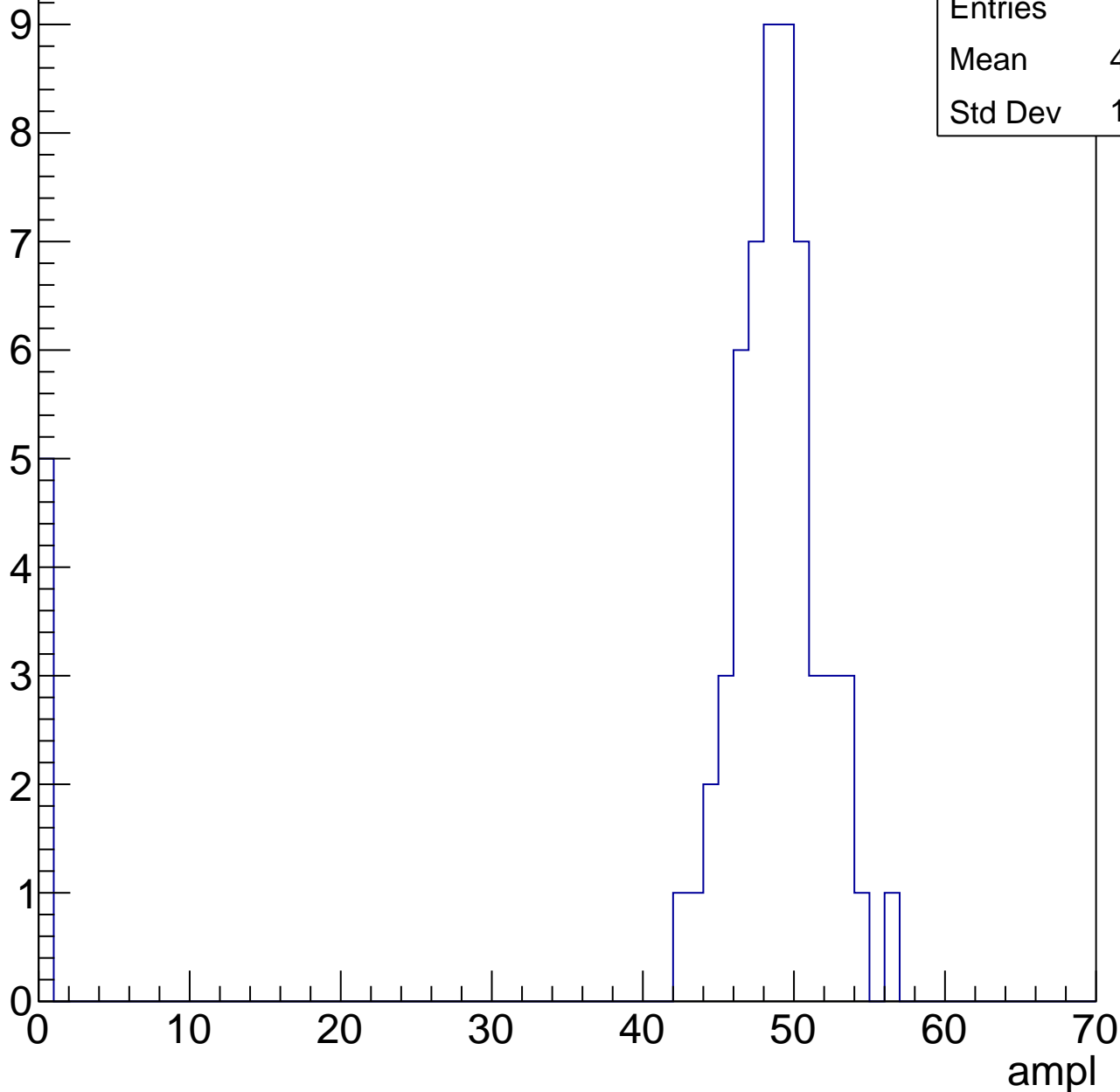
Entry



B1L103S, U10-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

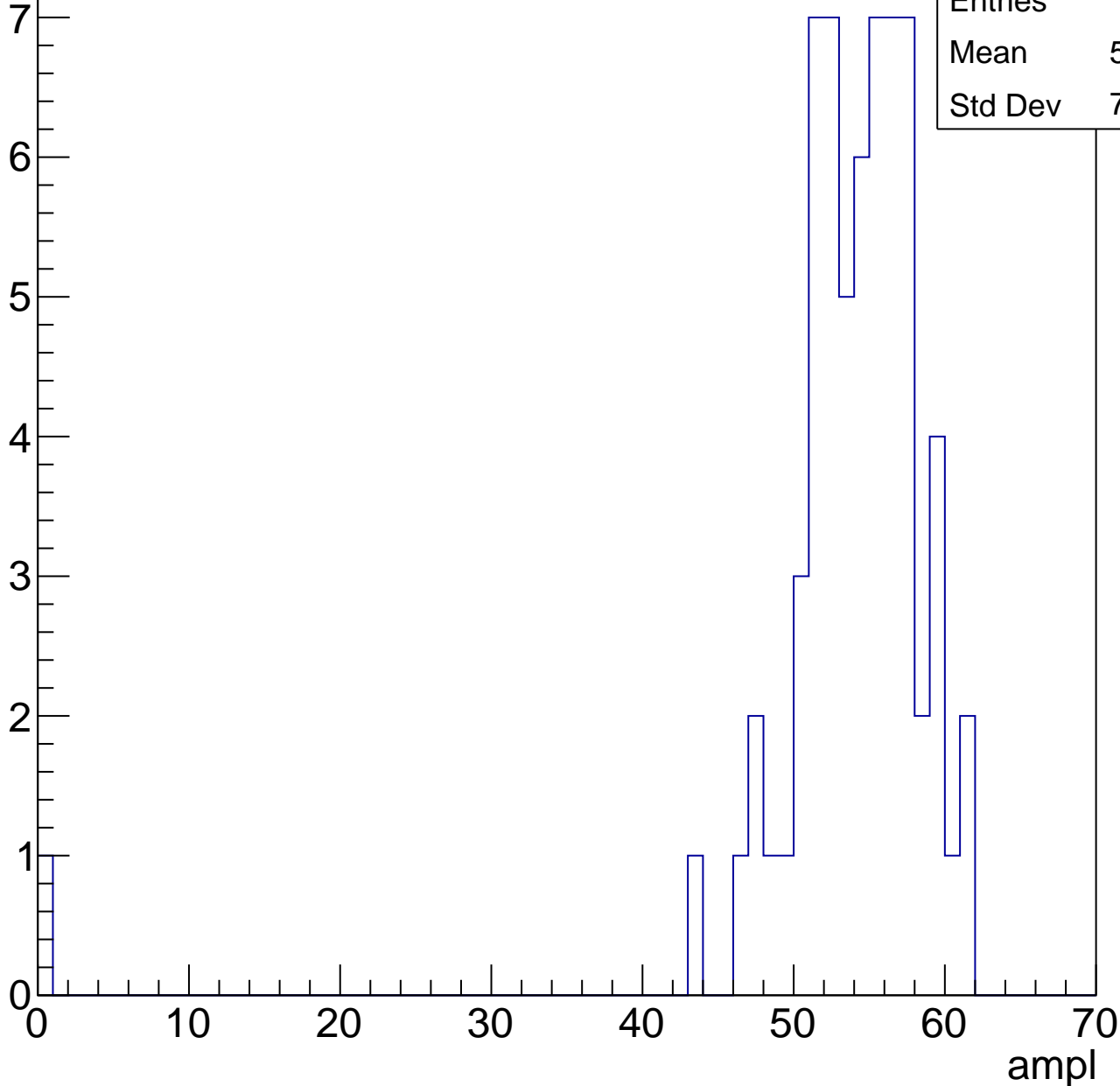


B1L103S, U10-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

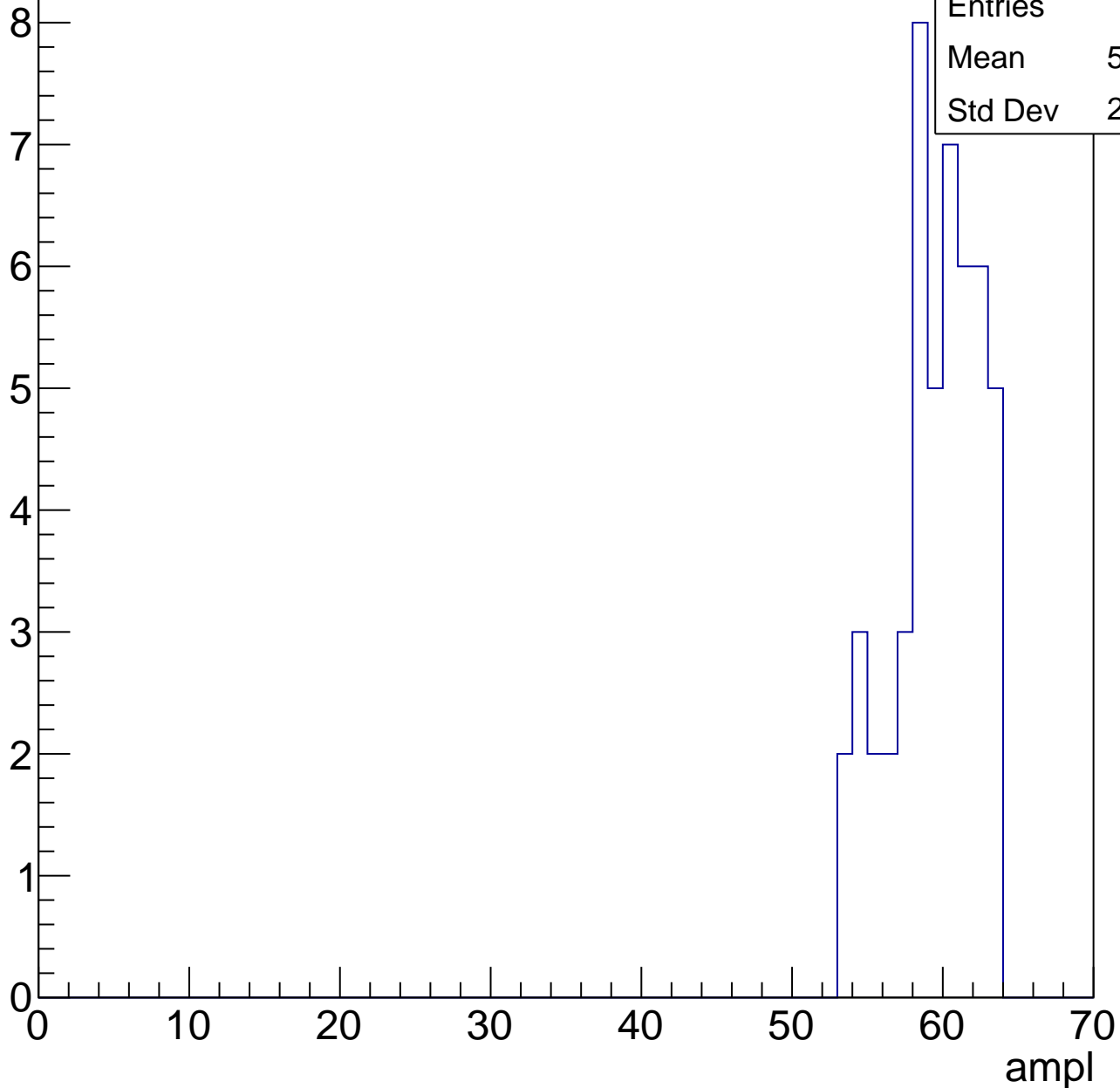
Entries	65
Mean	53.08
Std Dev	7.558



B1L103S, U10-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



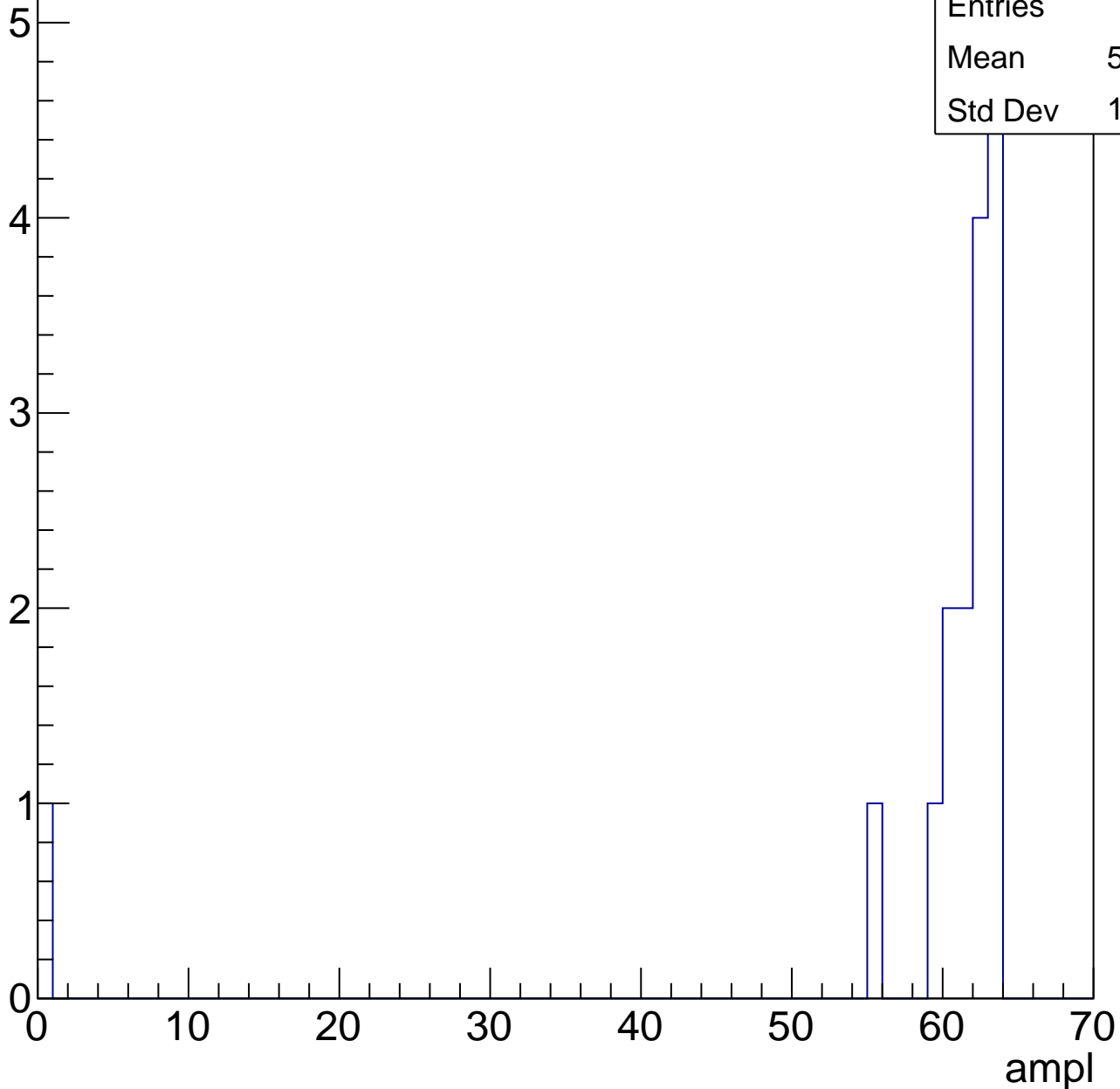
Entries	49
Mean	59.04
Std Dev	2.792

B1L103S, U10-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.44
Std Dev	14.97



B1L103S, U10-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	21.69
Std Dev	13.33

Gaus mean : 29.7695

Gaus Width: 3.6266

Entry

25

20

15

10

5

0

0

10

20

30

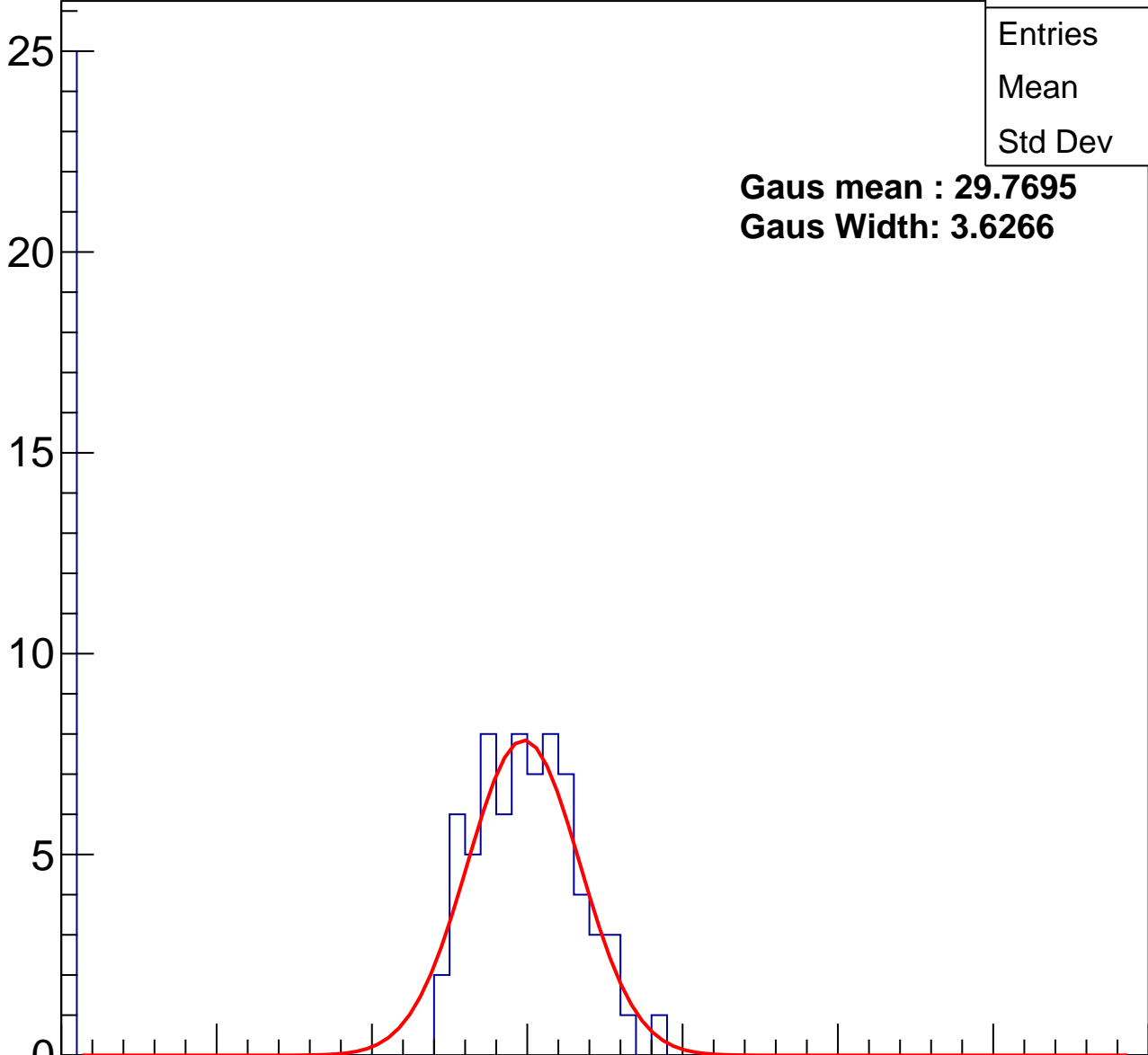
40

50

60

70

ampl



B1L103S, U10-ch71, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	30.77
Std Dev	13.8

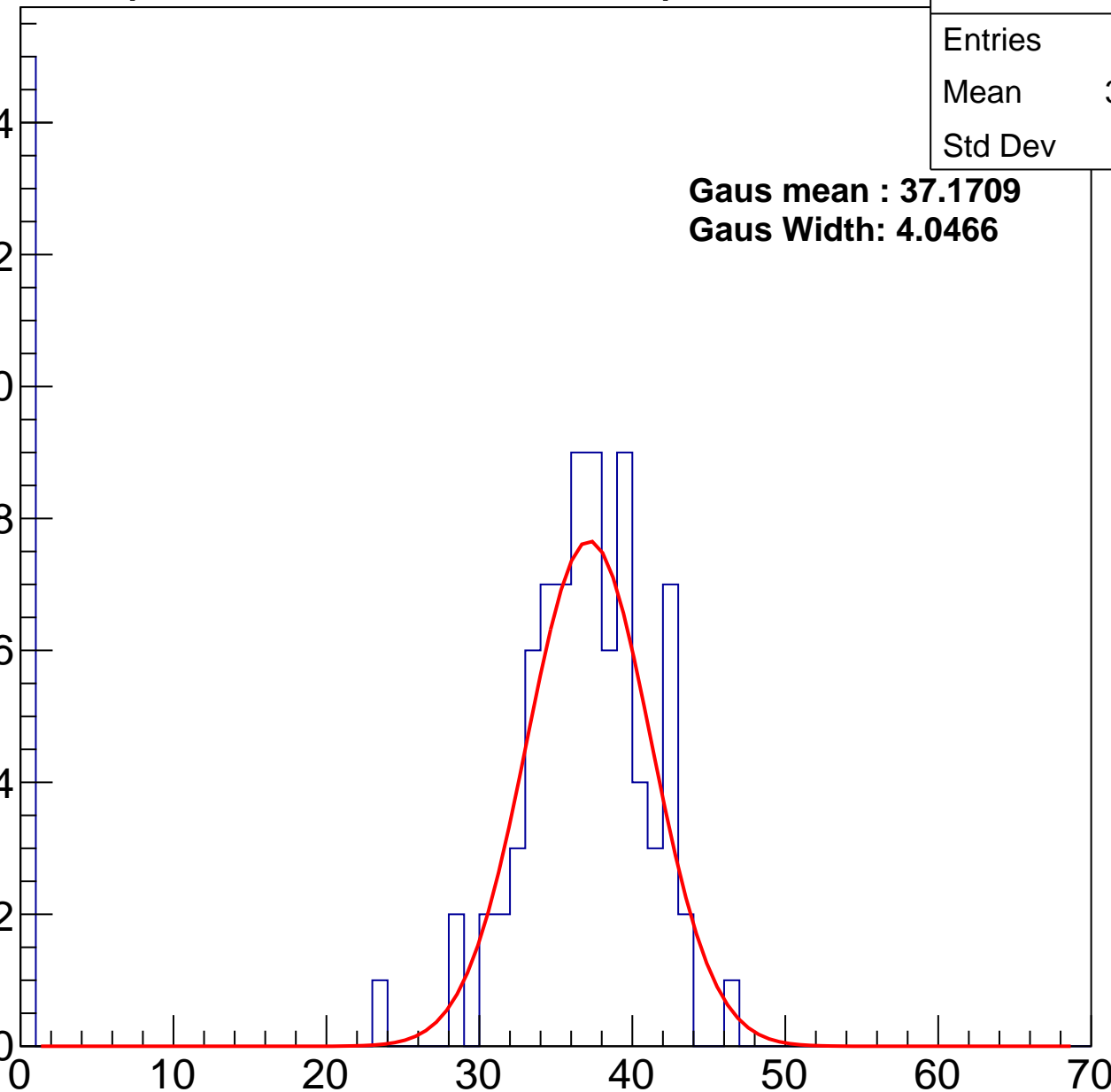
Gaus mean : 37.1709

Gaus Width: 4.0466

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U10-ch71, adc2

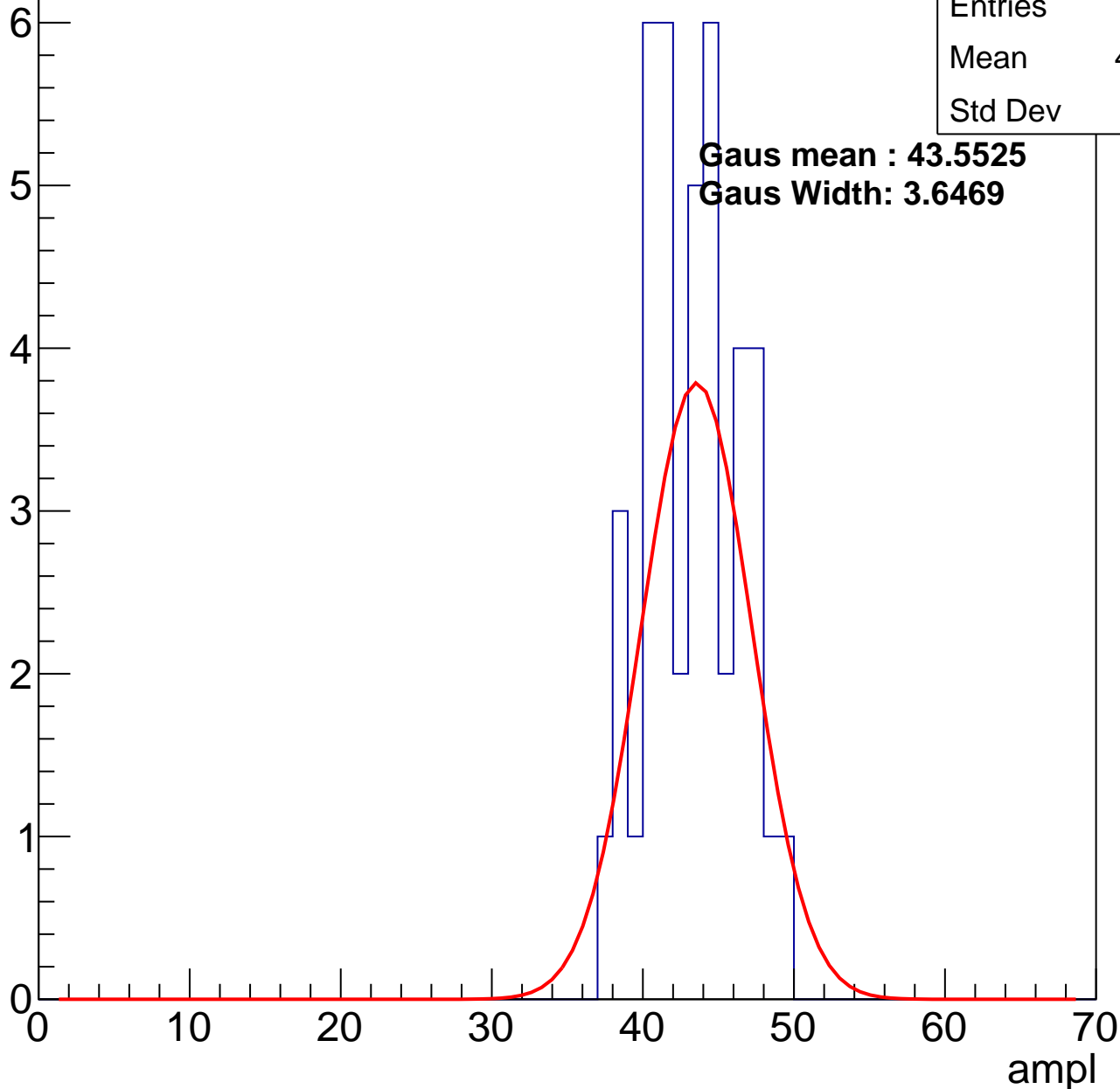
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	42.81
Std Dev	3.01

Gaus mean : 43.5525

Gaus Width: 3.6469

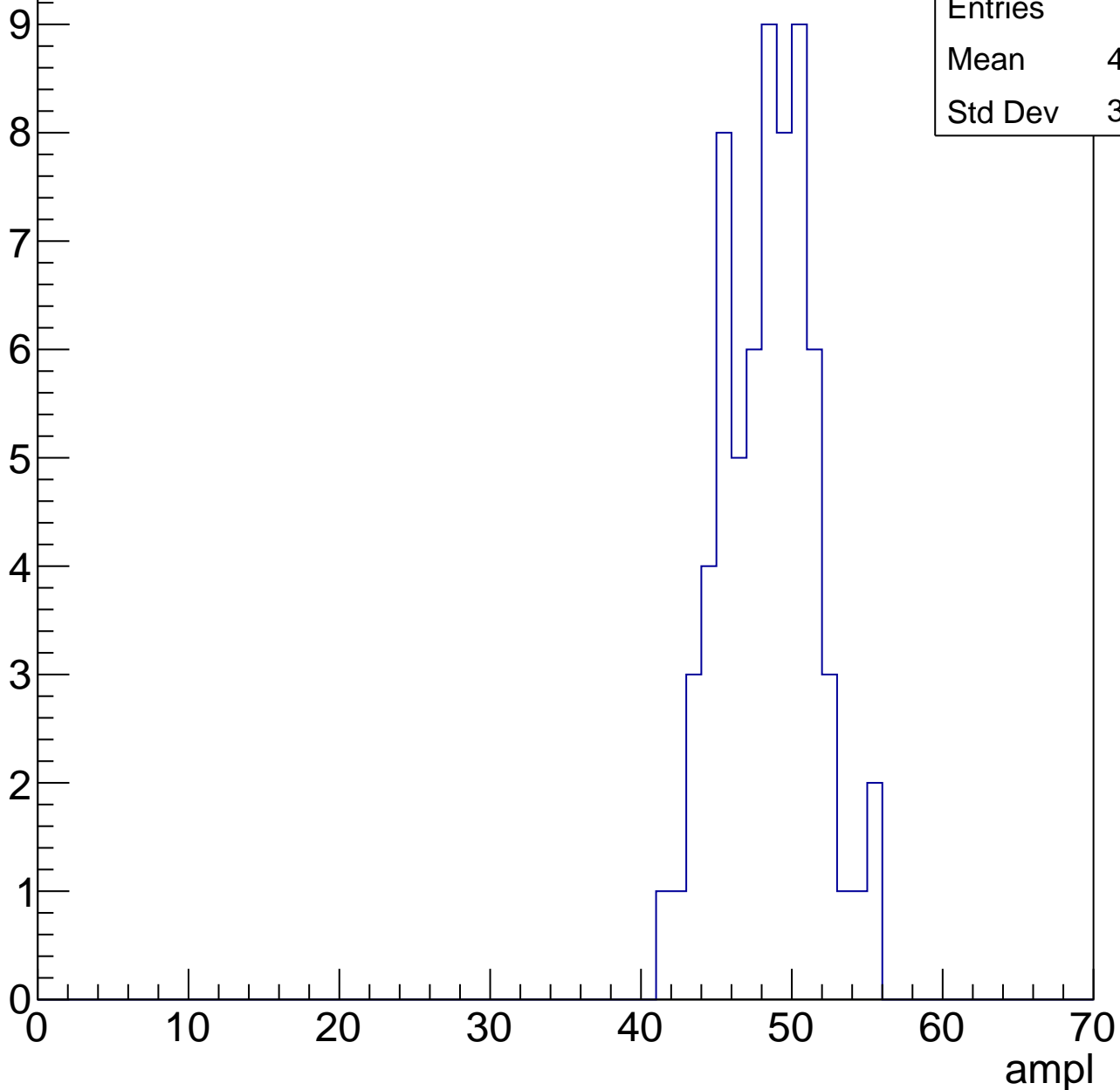


B1L103S, U10-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.96
Std Dev	3.059

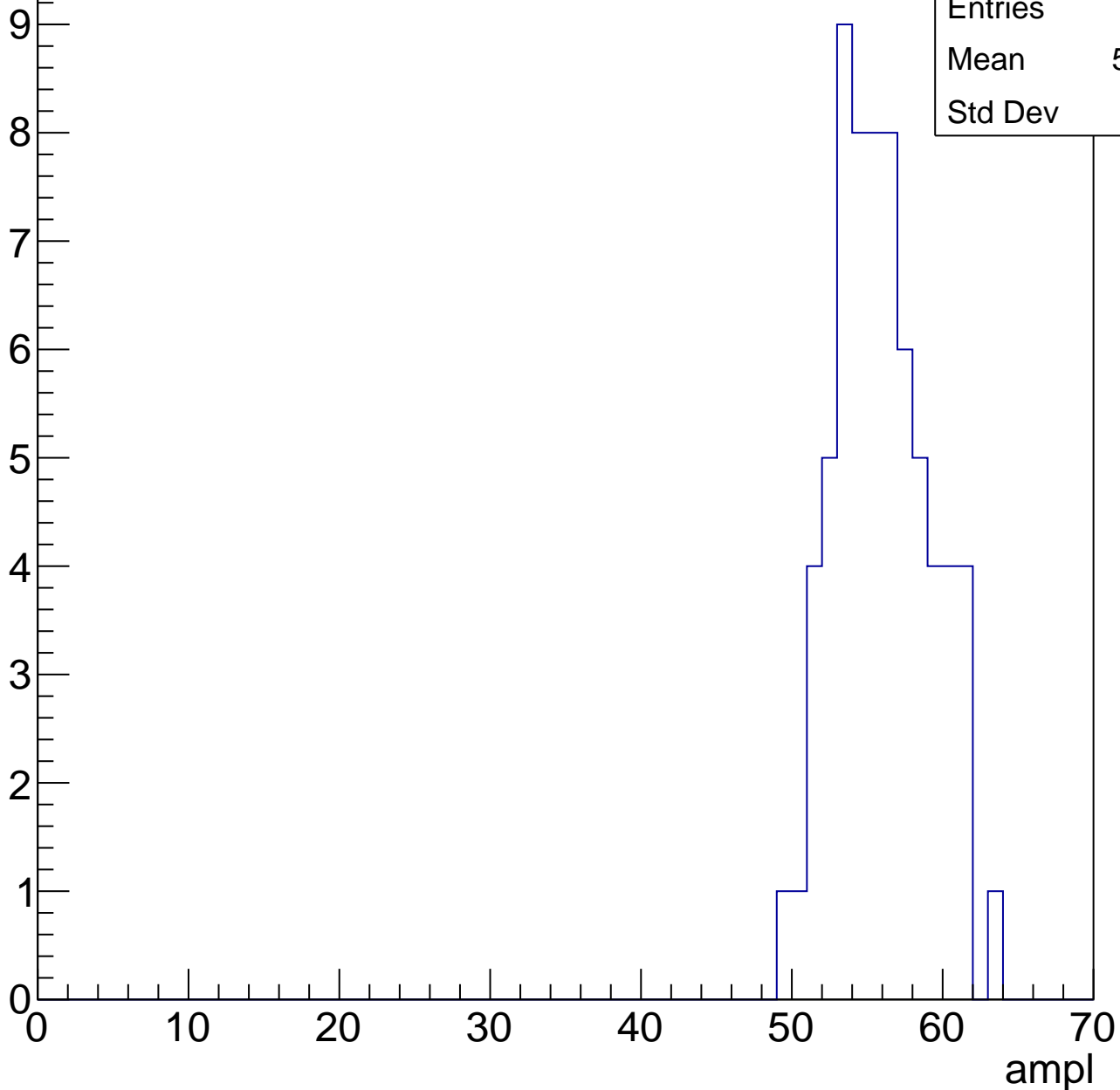


B1L103S, U10-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	55.51
Std Dev	3.08

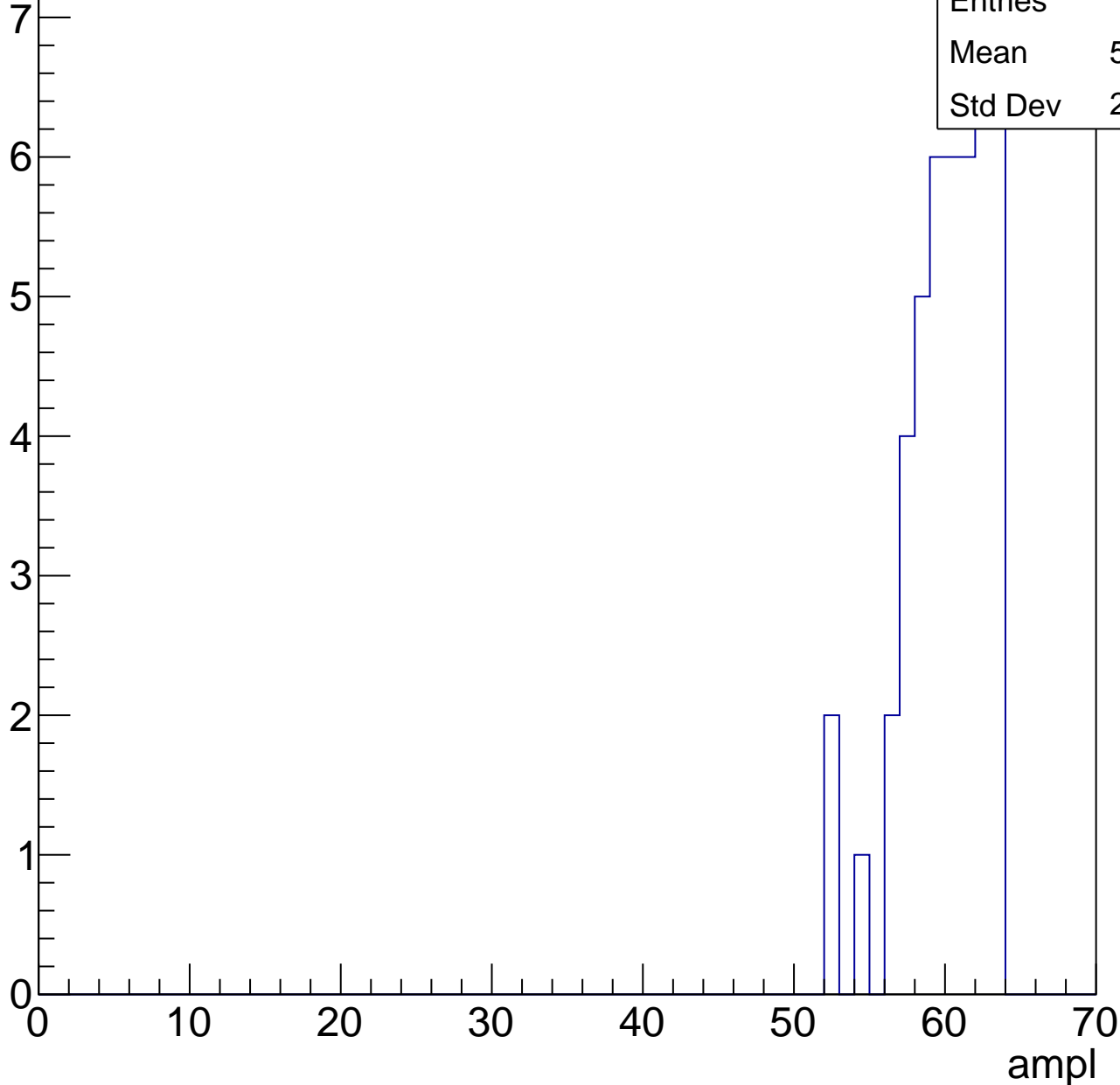


B1L103S, U10-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

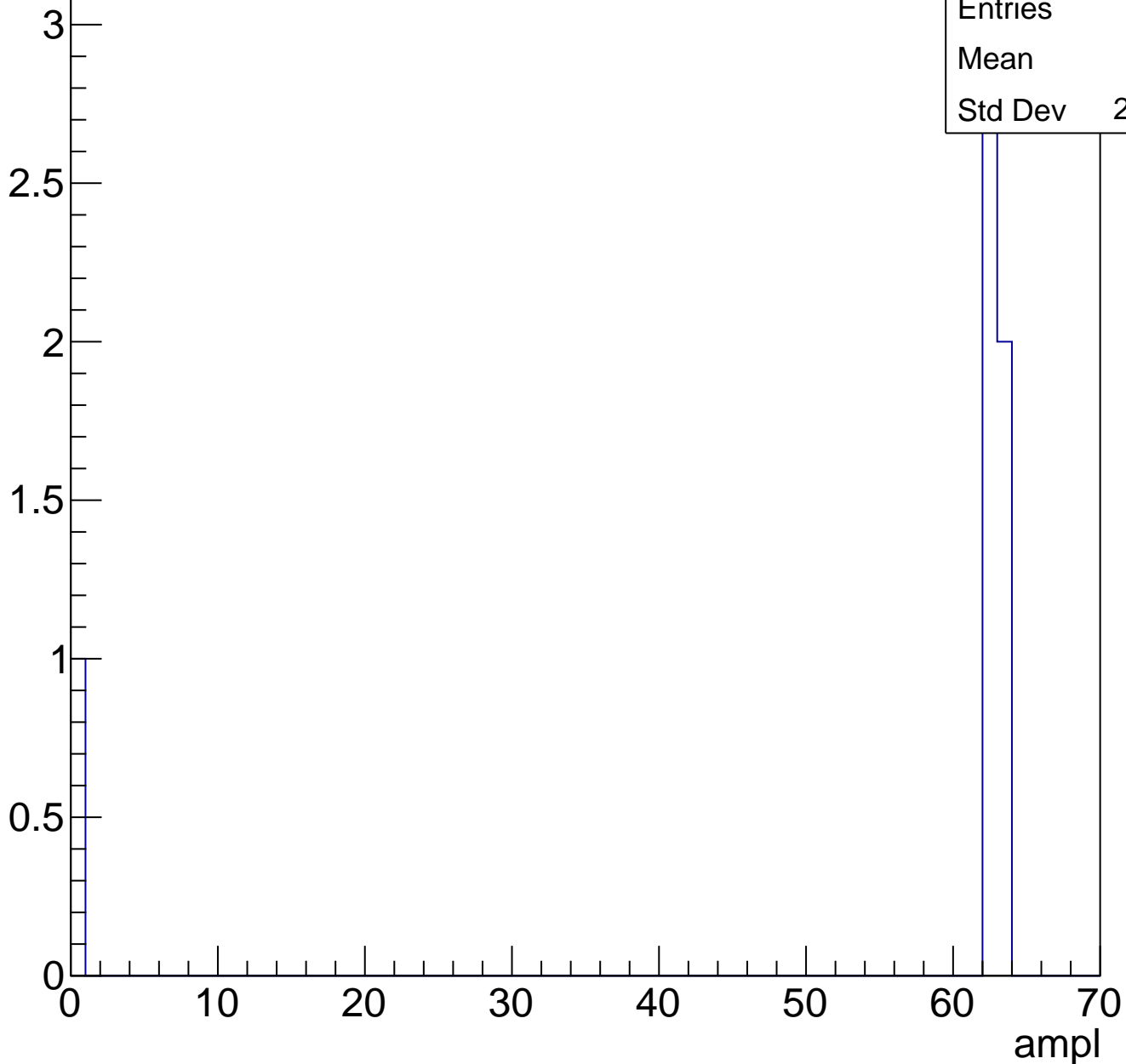
Entries	46
Mean	59.63
Std Dev	2.753



B1L103S, U10-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



B1L103S, U10-ch72, adc0

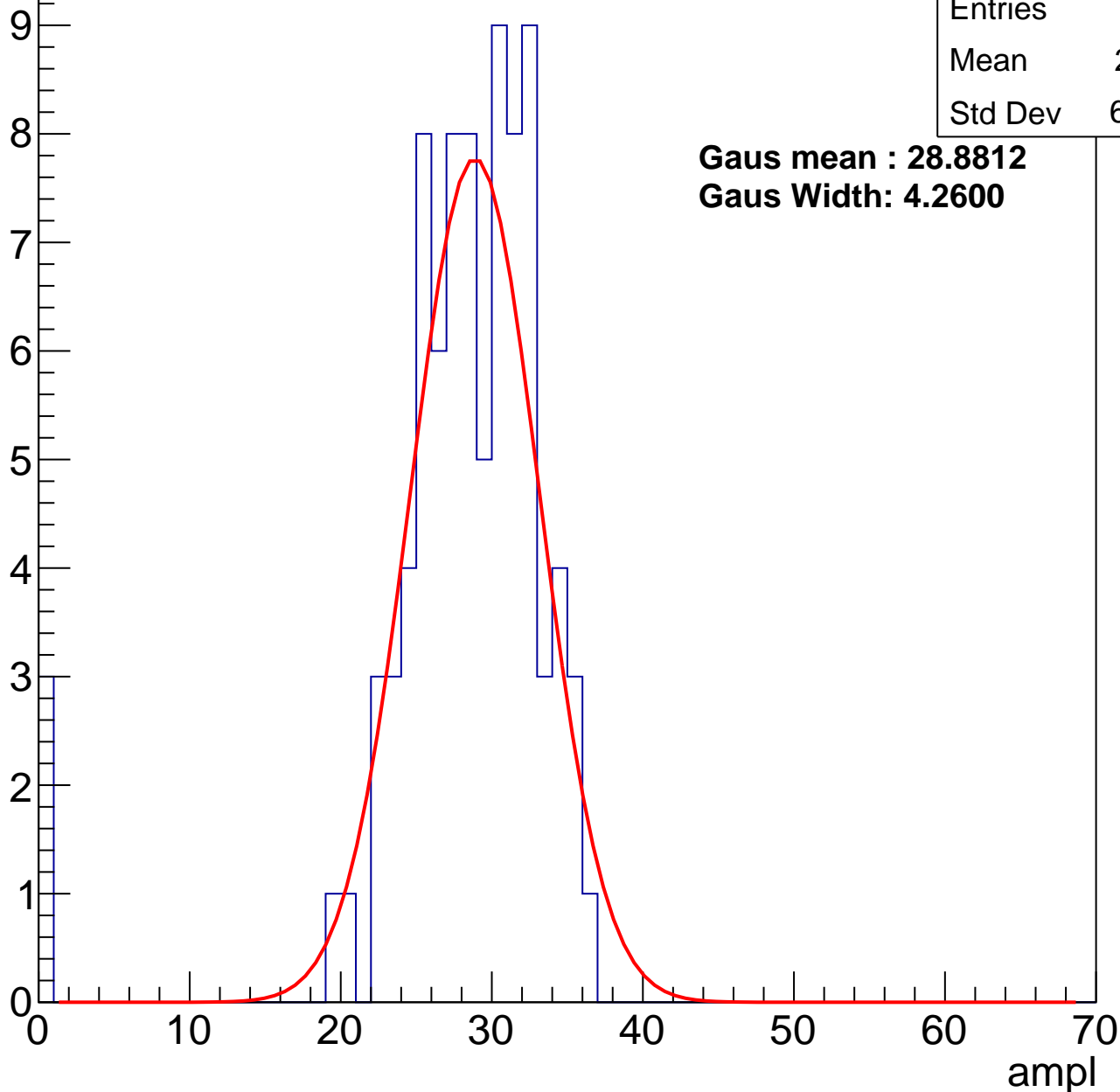
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	27.51
Std Dev	6.355

Gaus mean : 28.8812

Gaus Width: 4.2600



B1L103S, U10-ch72, adc1

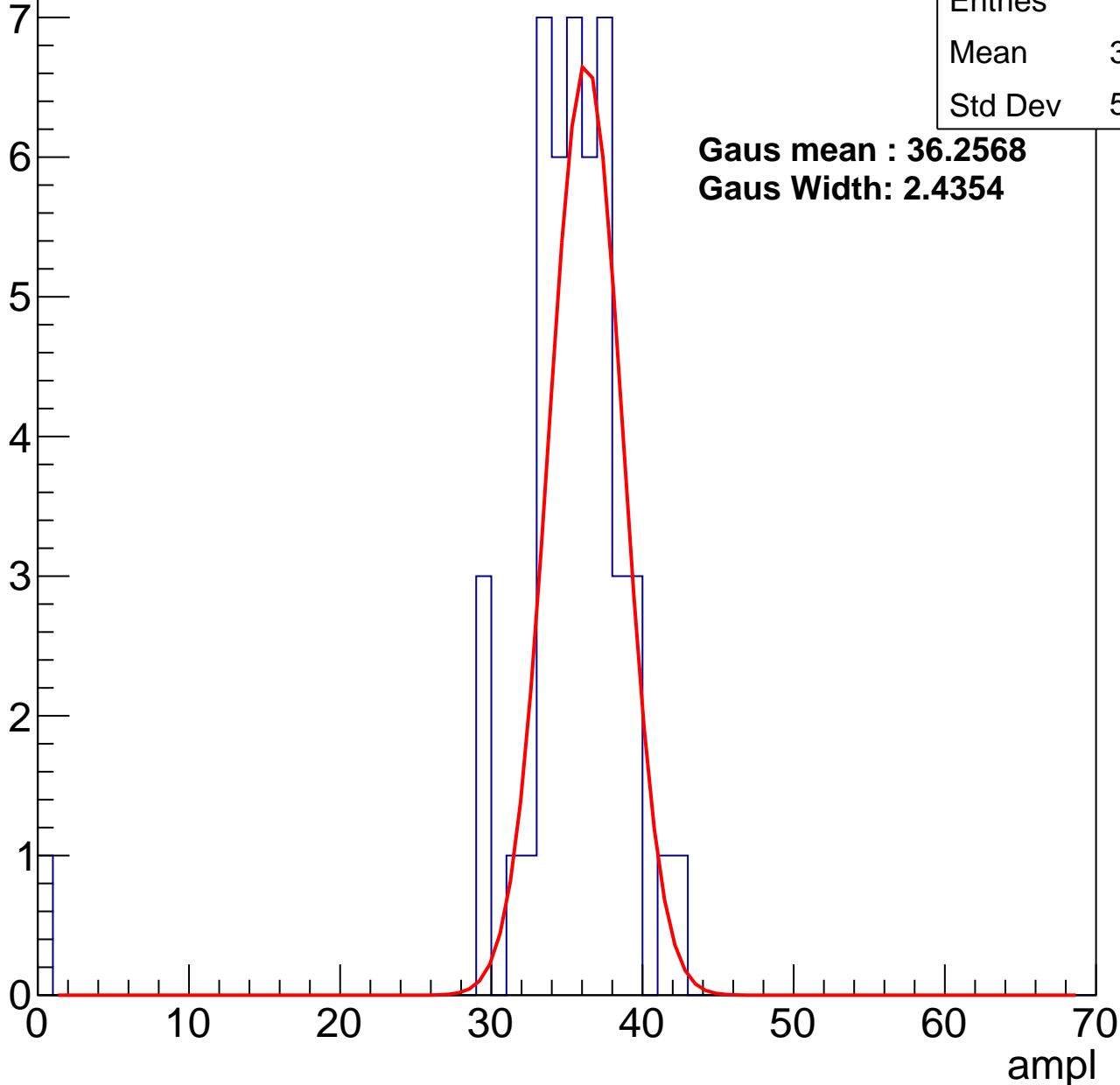
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	34.45
Std Dev	5.783

Gaus mean : 36.2568

Gaus Width: 2.4354



B1L103S, U10-ch72, adc2

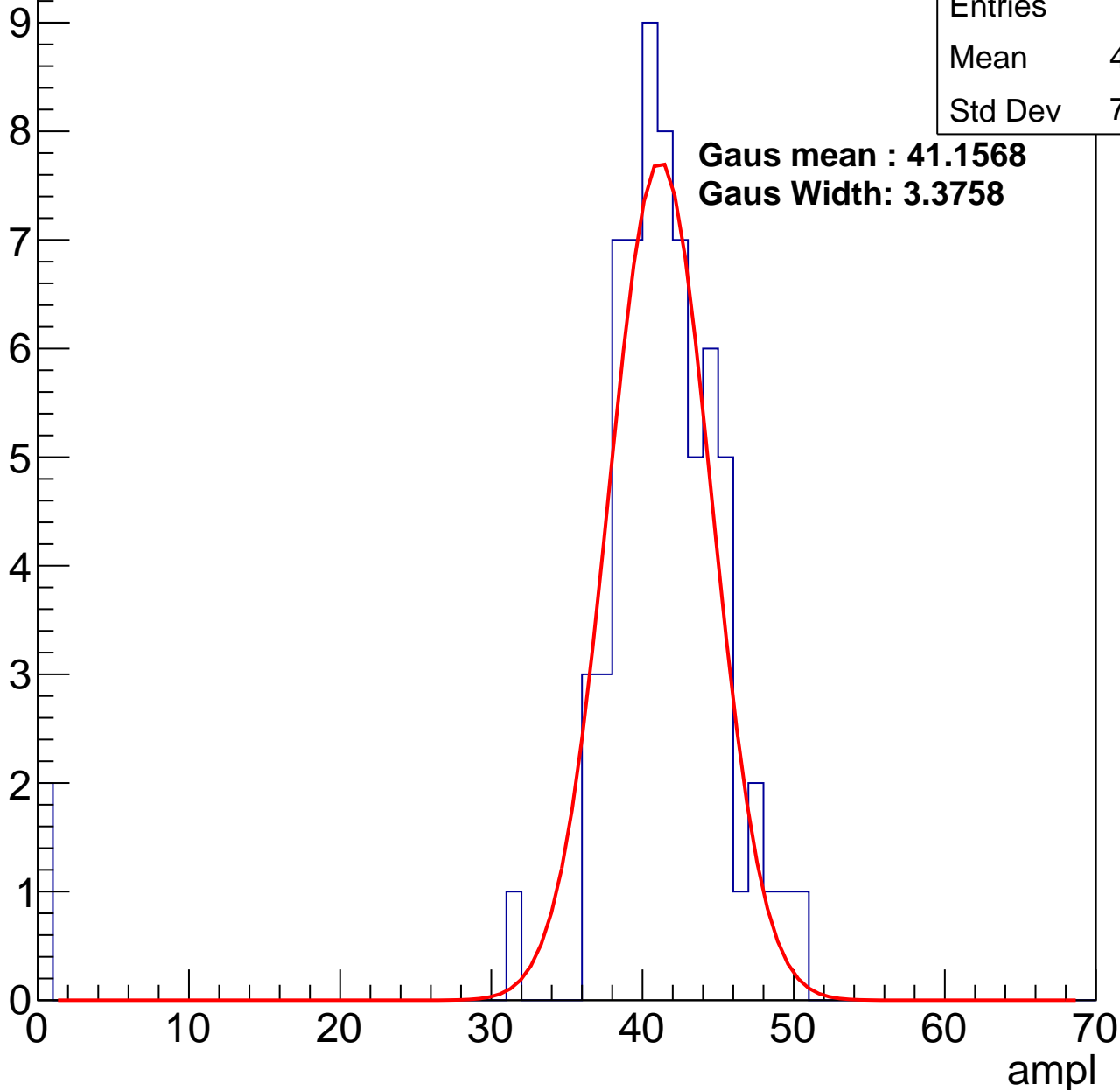
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	40.03
Std Dev	7.682

Gaus mean : 41.1568

Gaus Width: 3.3758

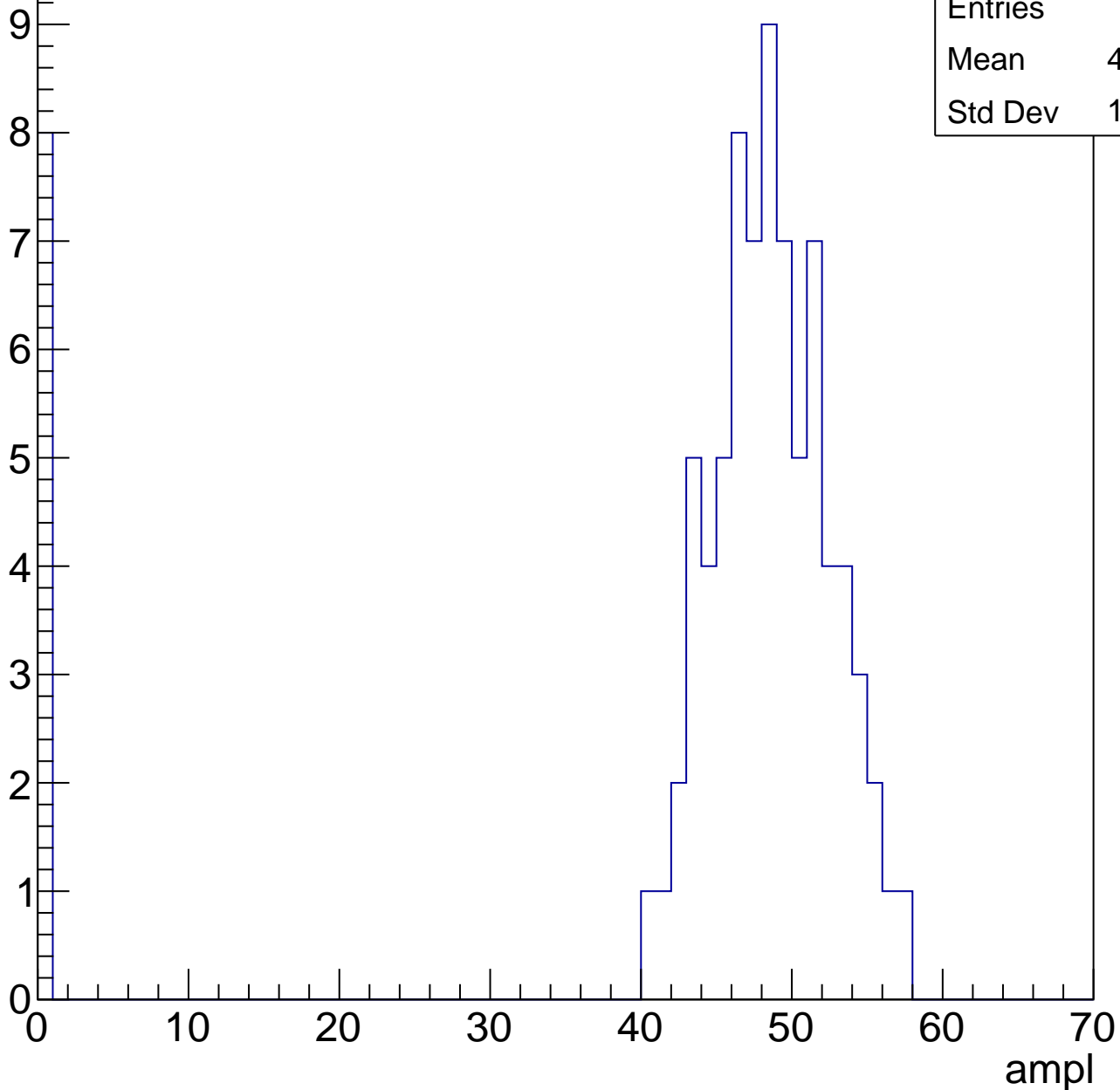


B1L103S, U10-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	43.63
Std Dev	14.59

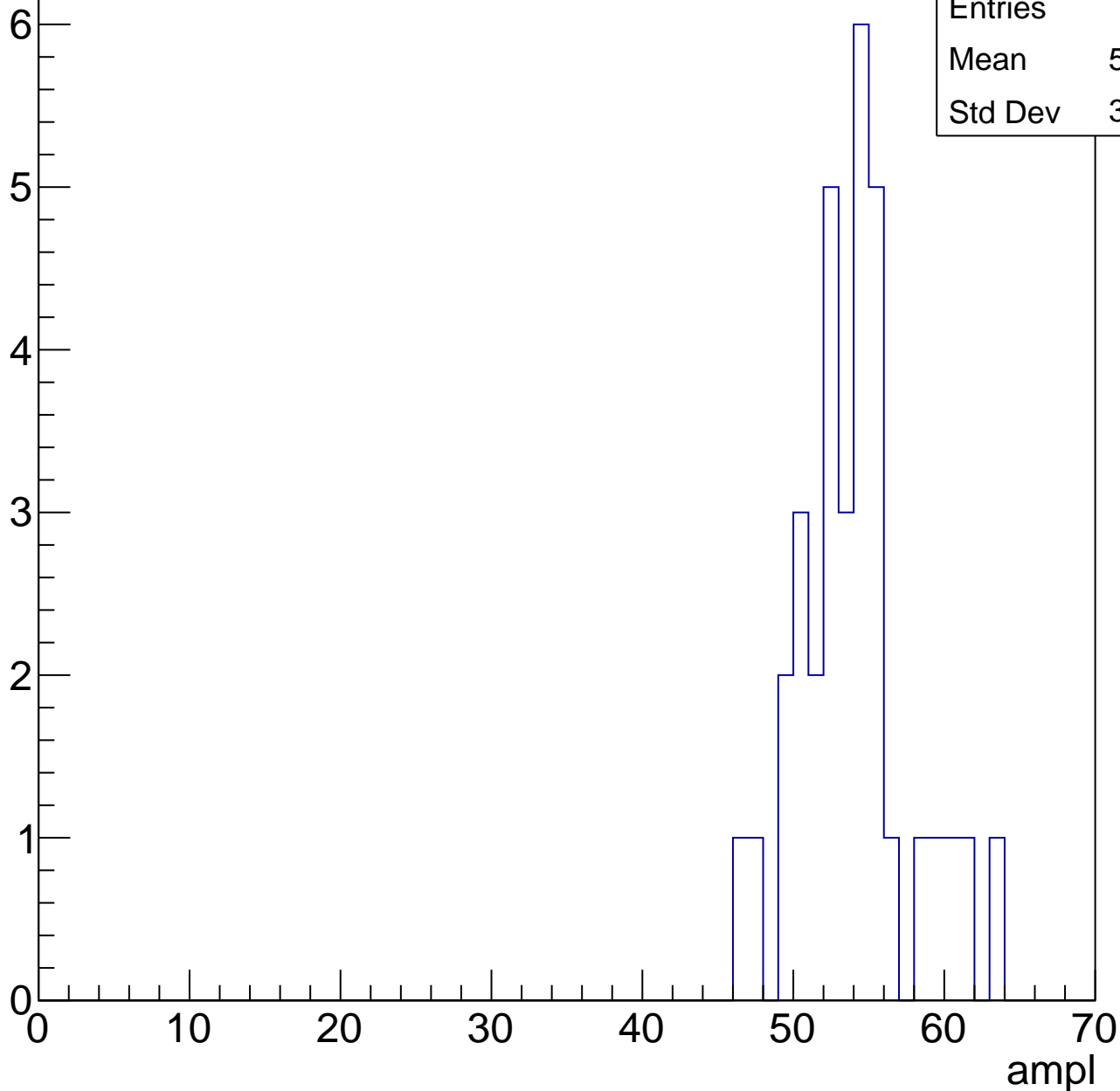


B1L103S, U10-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	53.47
Std Dev	3.672



B1L103S, U10-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

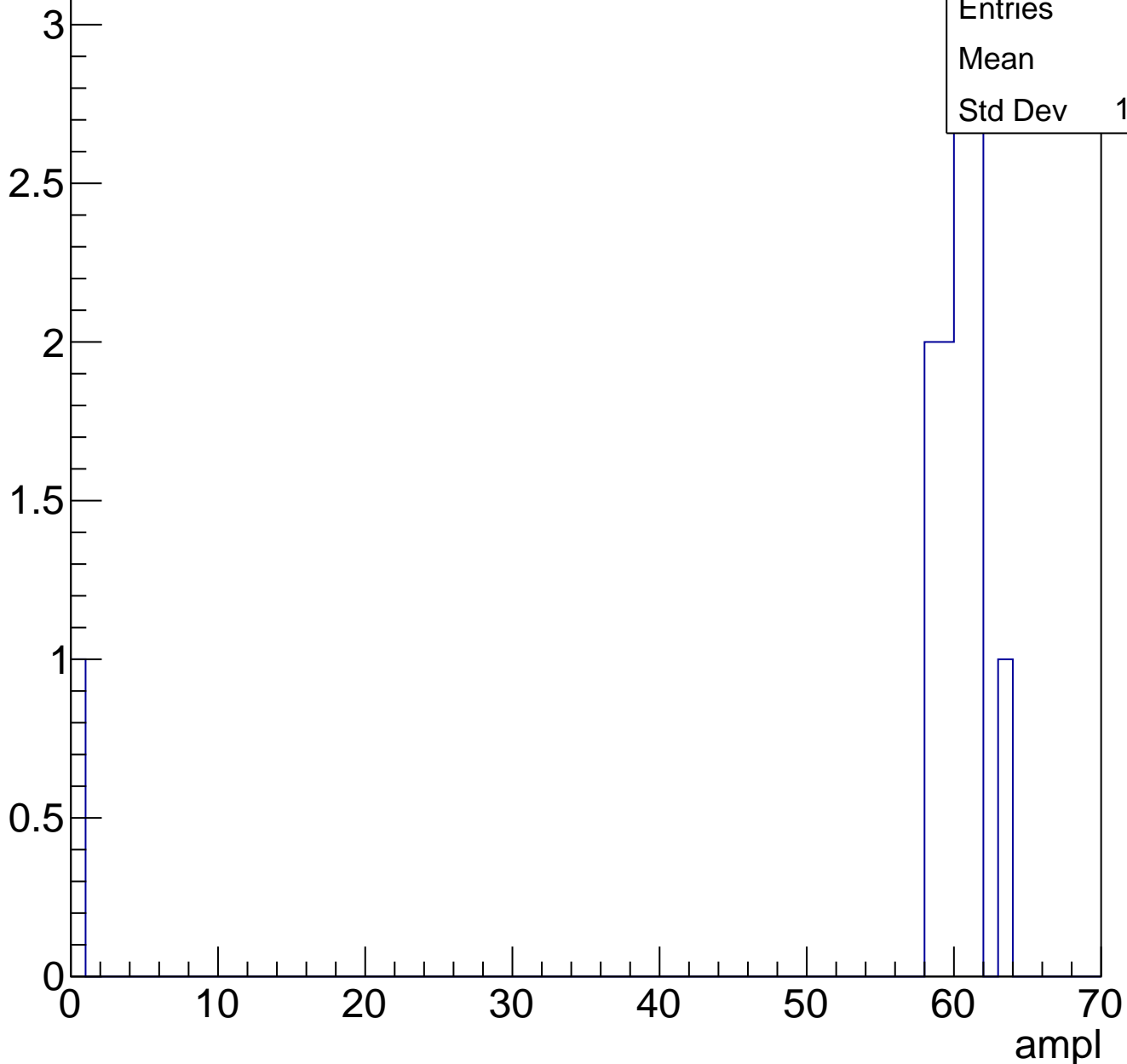
ampl

Entries	71
Mean	58.61
Std Dev	3.2

B1L103S, U10-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	12
Mean	55
Std Dev	16.64

B1L103S, U10-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch73, adc0

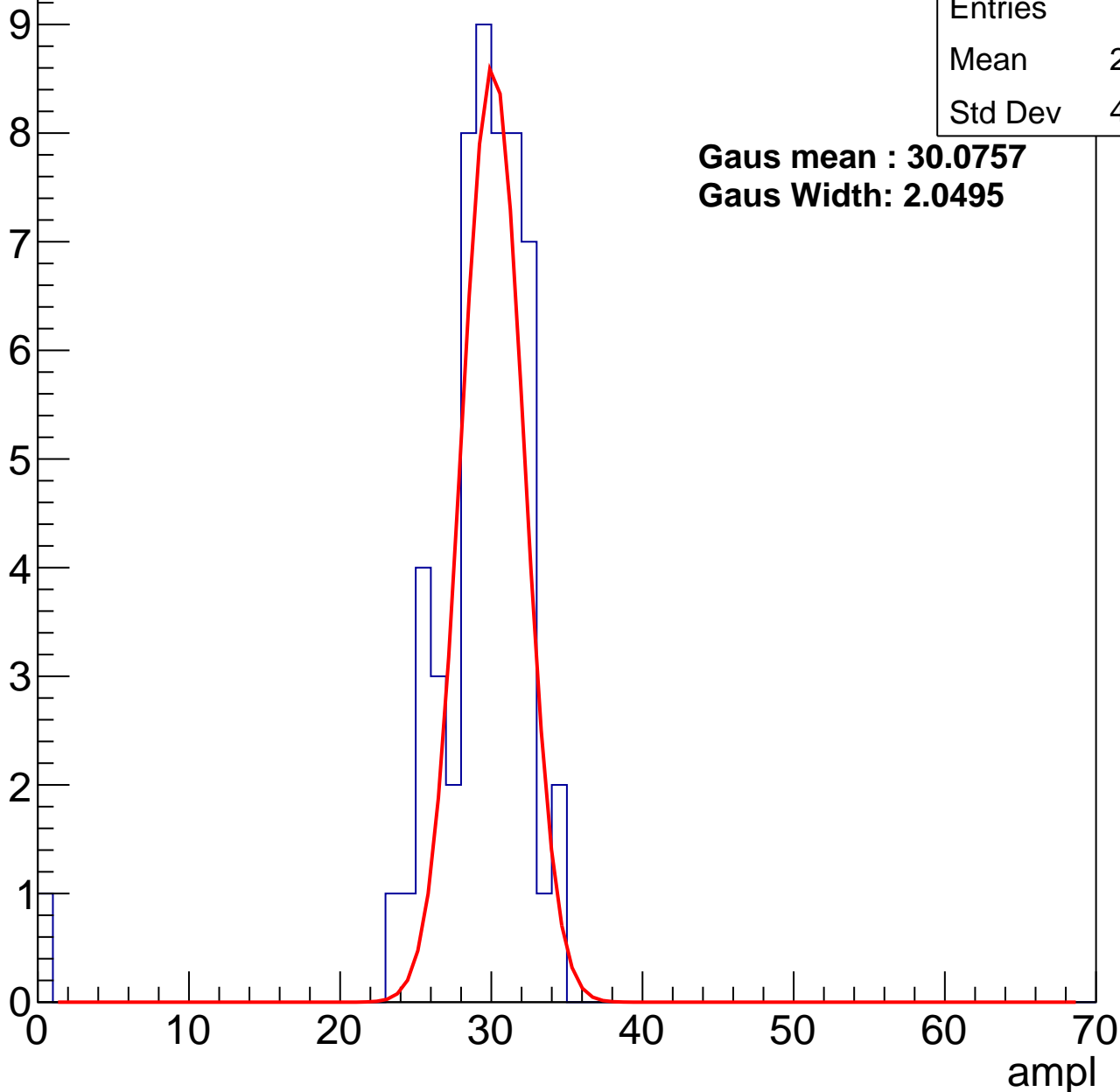
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	28.67
Std Dev	4.616

Gaus mean : 30.0757

Gaus Width: 2.0495



B1L103S, U10-ch73, adc1

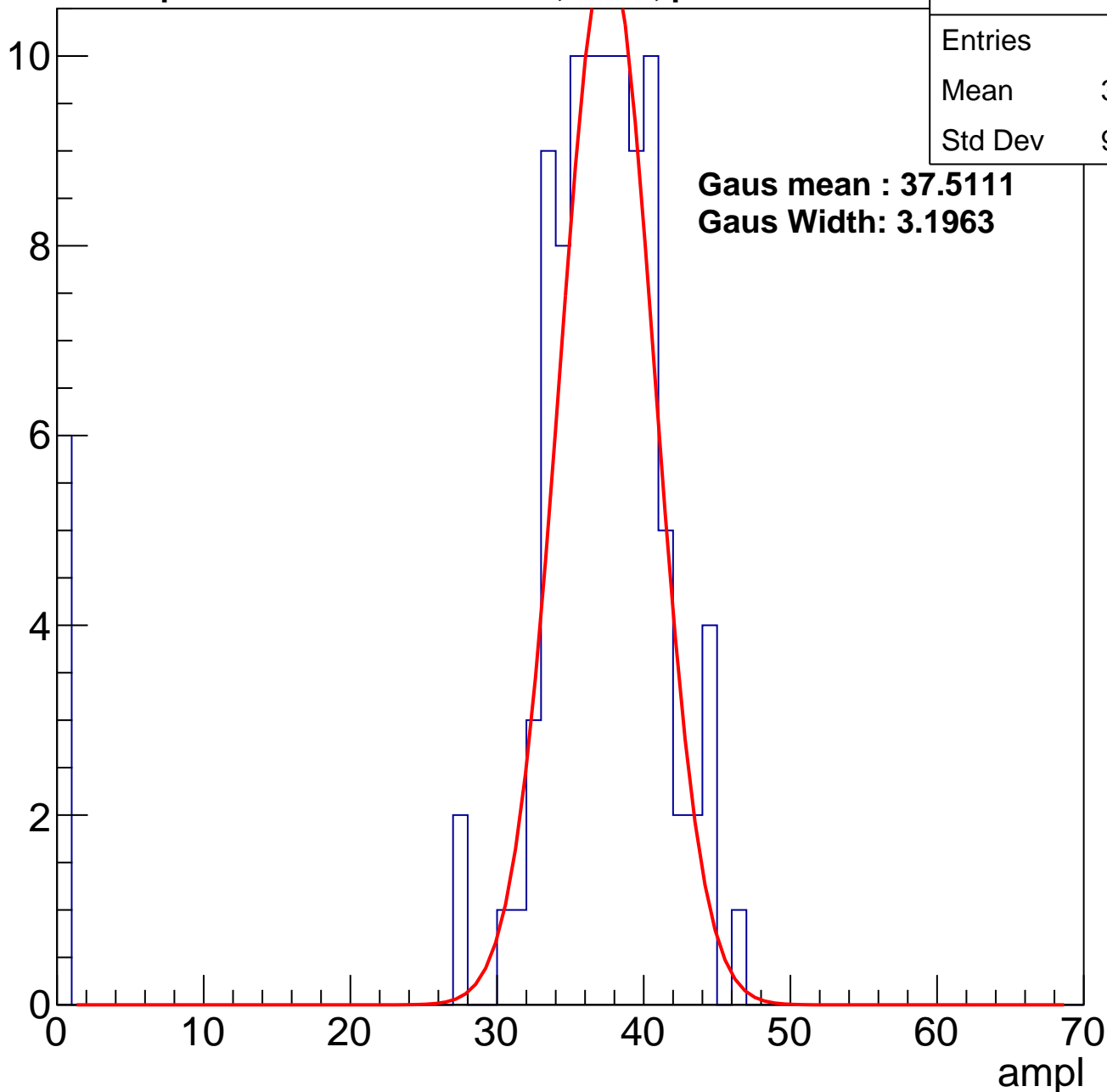
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	34.83
Std Dev	9.333

Gaus mean : 37.5111

Gaus Width: 3.1963

Entry



B1L103S, U10-ch73, adc2

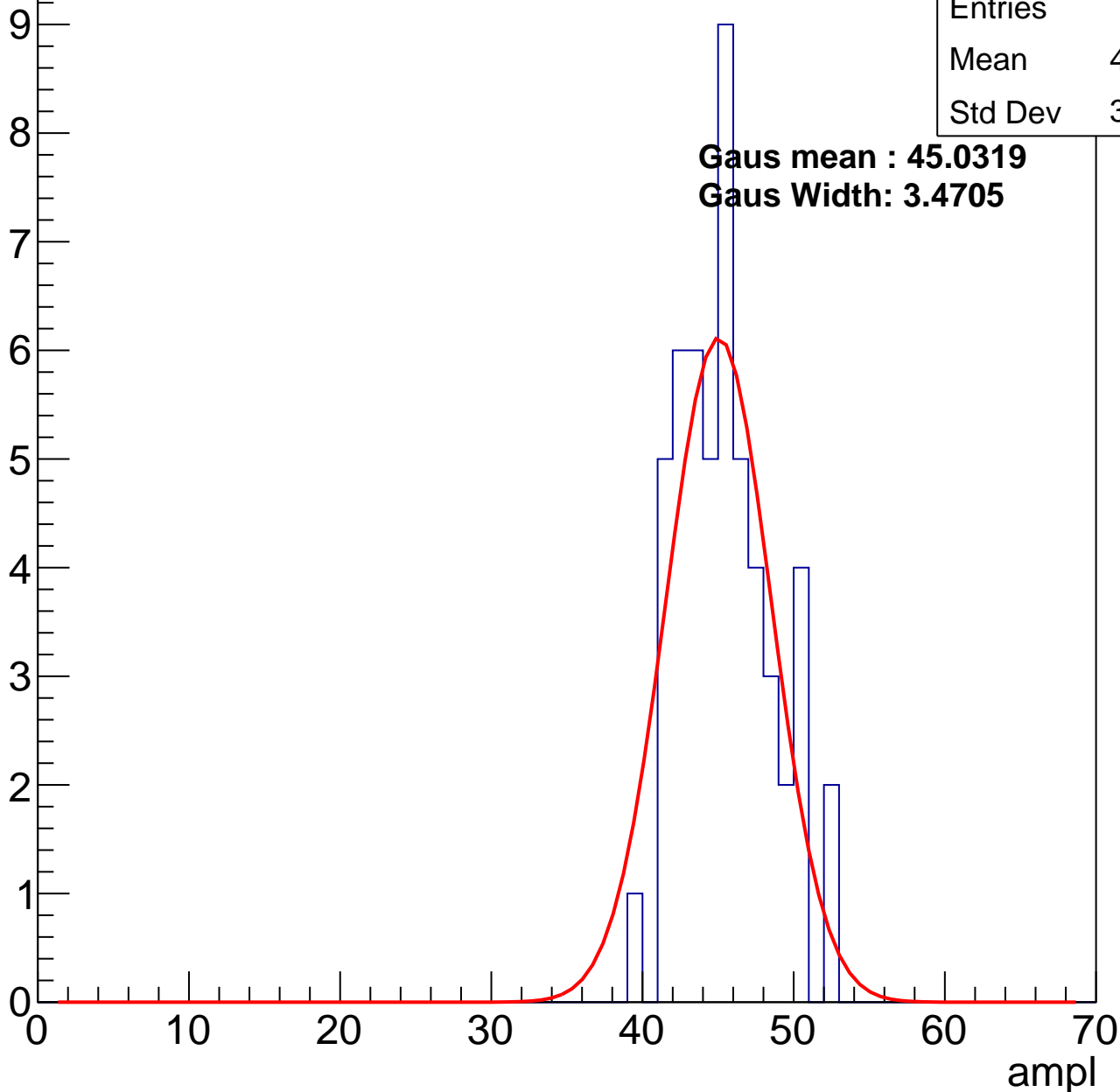
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	45.06
Std Dev	3.028

Gaus mean : 45.0319

Gaus Width: 3.4705

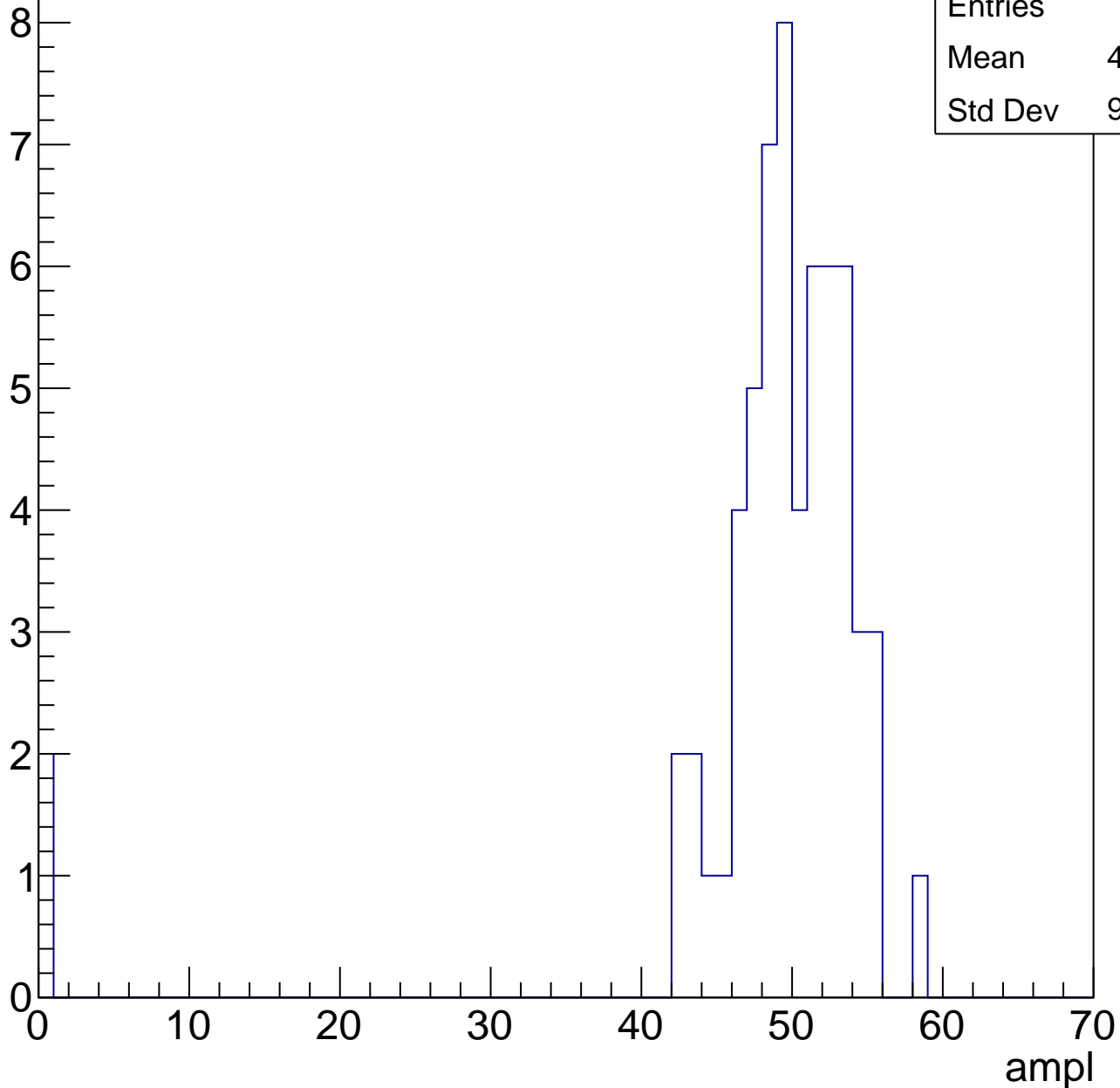


B1L103S, U10-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.98
Std Dev	9.457

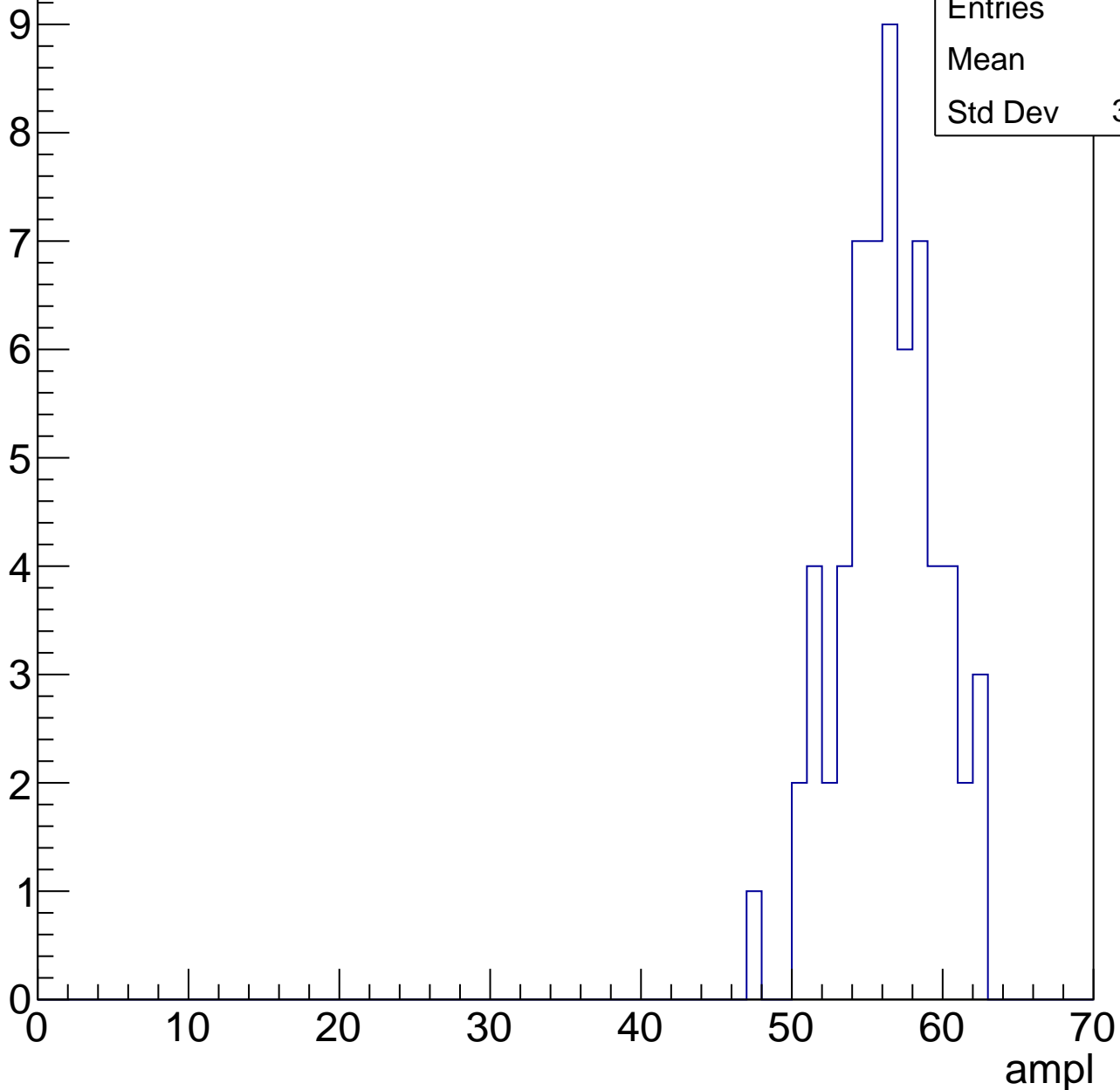


B1L103S, U10-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.9
Std Dev	3.231

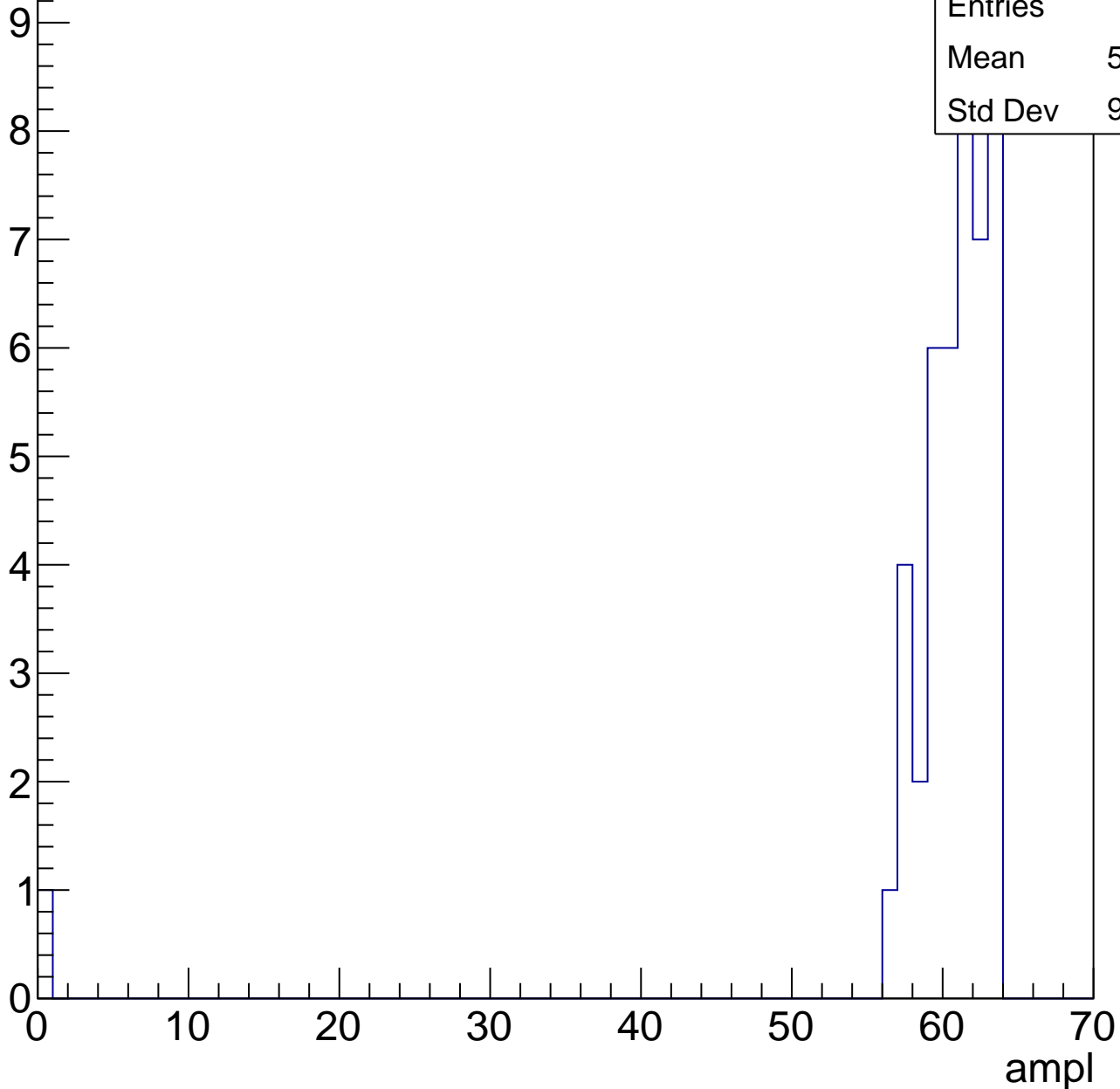


B1L103S, U10-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	59.16
Std Dev	9.234



B1L103S, U10-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch74, adc0

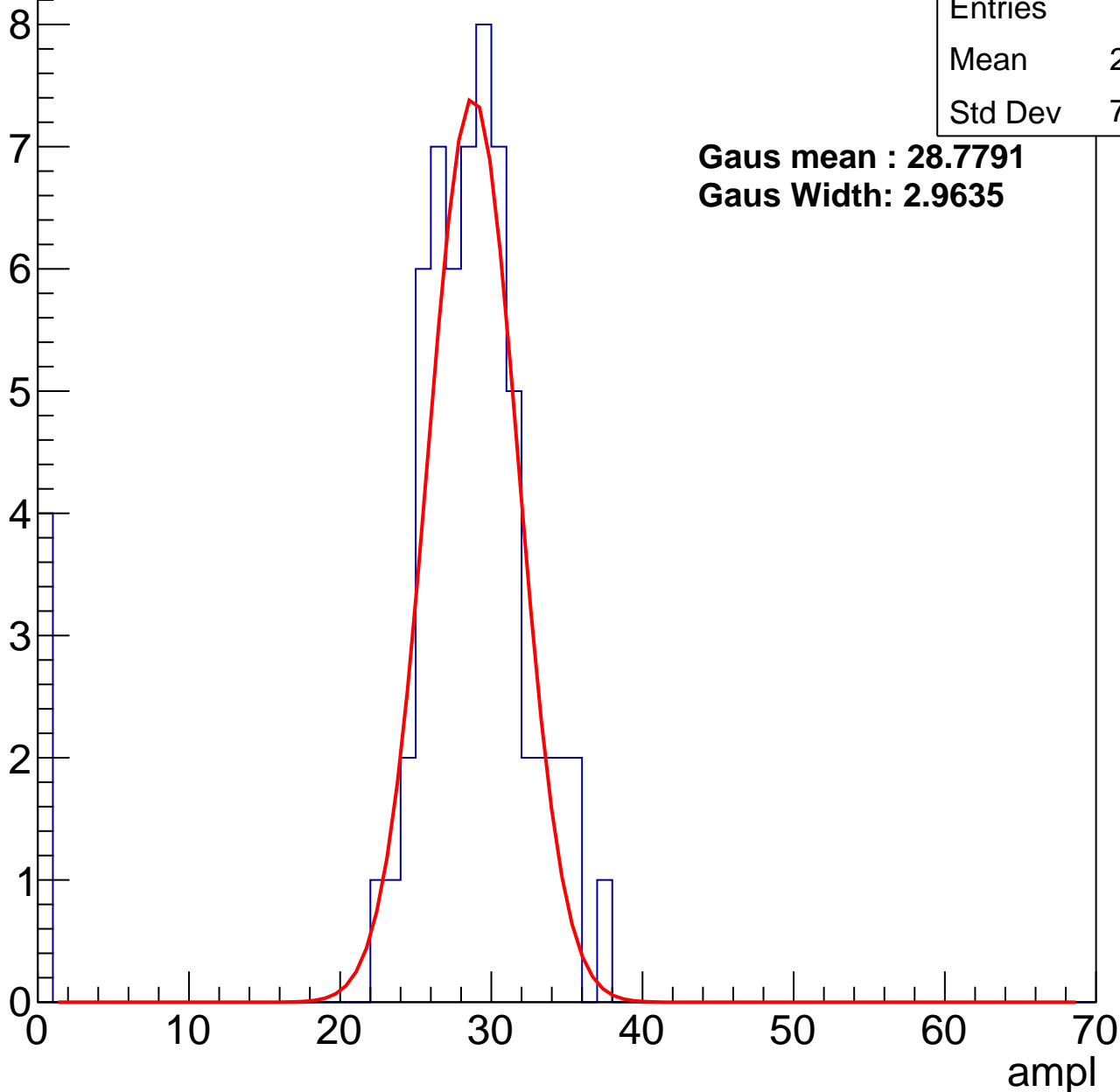
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	26.75
Std Dev	7.593

Gaus mean : 28.7791

Gaus Width: 2.9635



B1L103S, U10-ch74, adc1

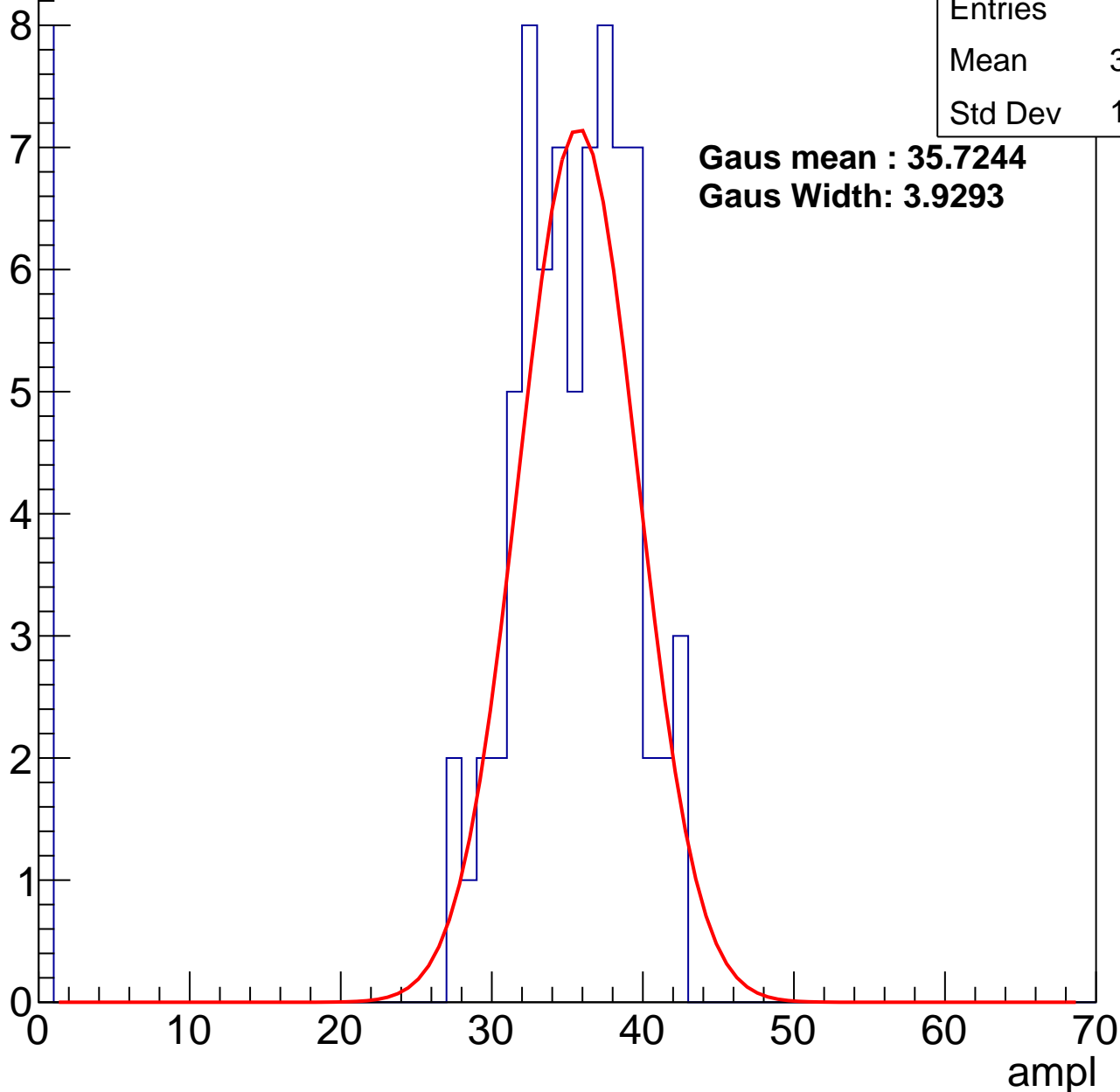
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	31.67
Std Dev	10.96

Gaus mean : 35.7244

Gaus Width: 3.9293



B1L103S, U10-ch74, adc2

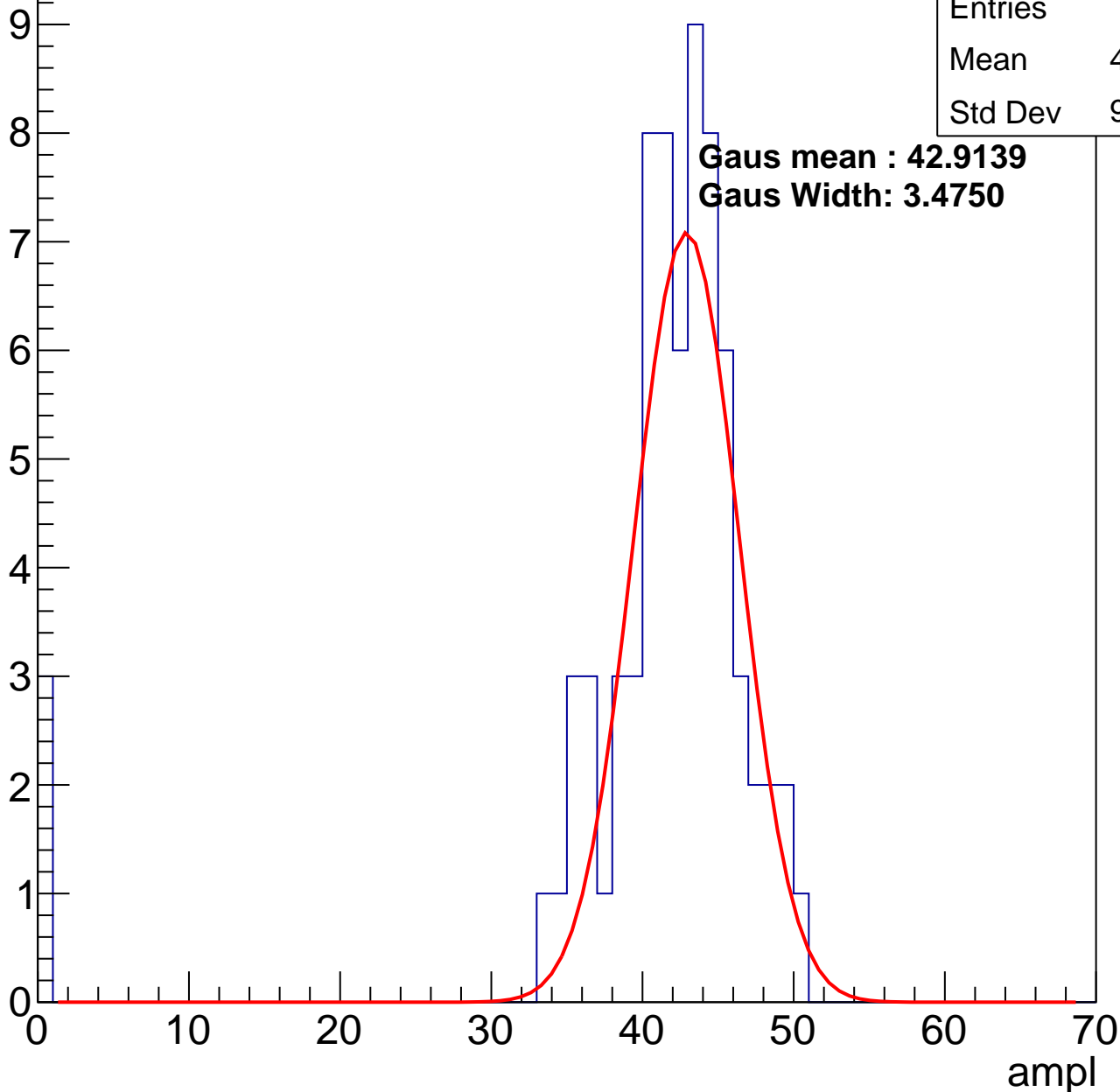
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.18
Std Dev	9.079

Gaus mean : 42.9139

Gaus Width: 3.4750

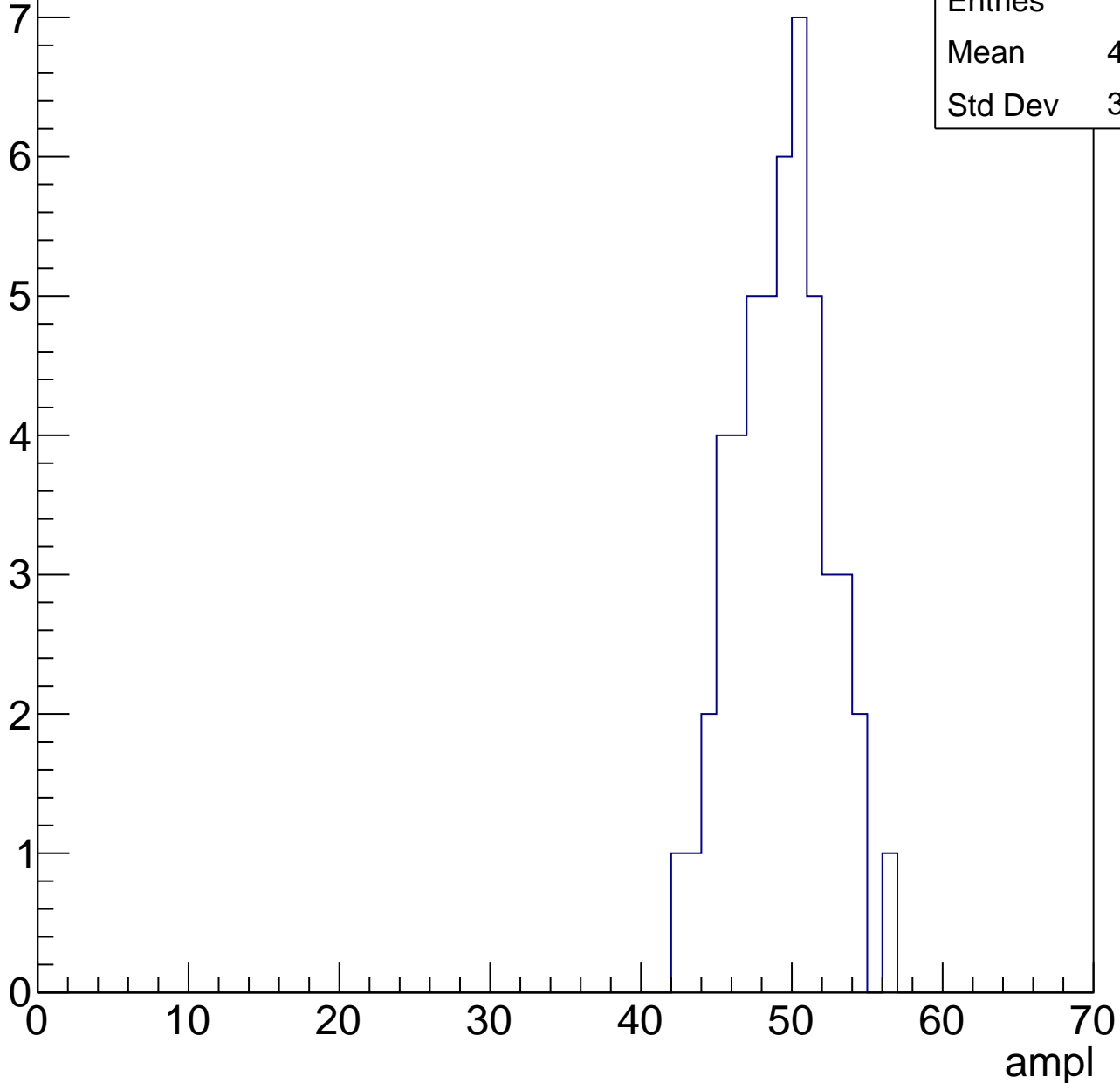


B1L103S, U10-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	48.78
Std Dev	3.059

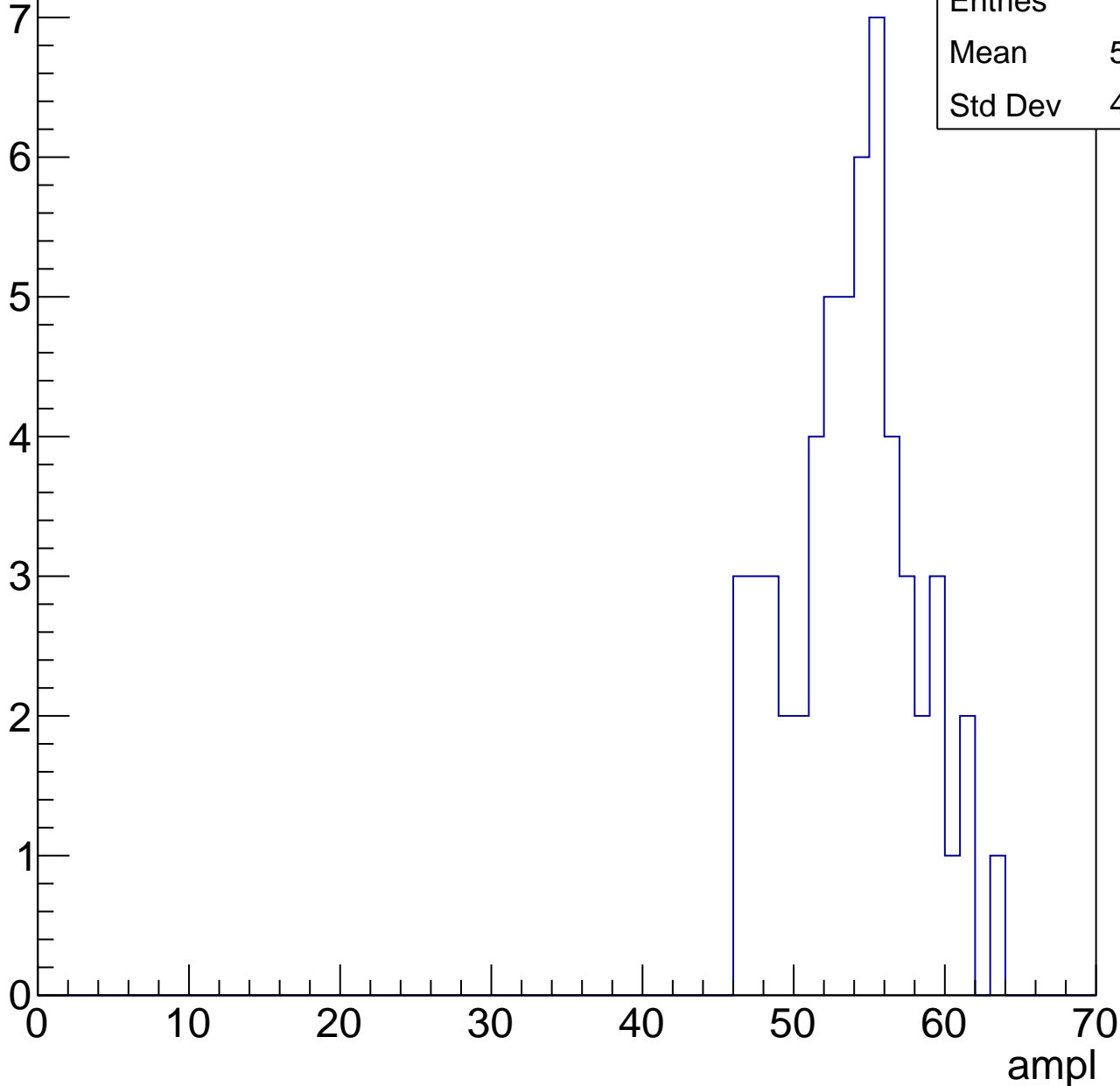


B1L103S, U10-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.43
Std Dev	4.105

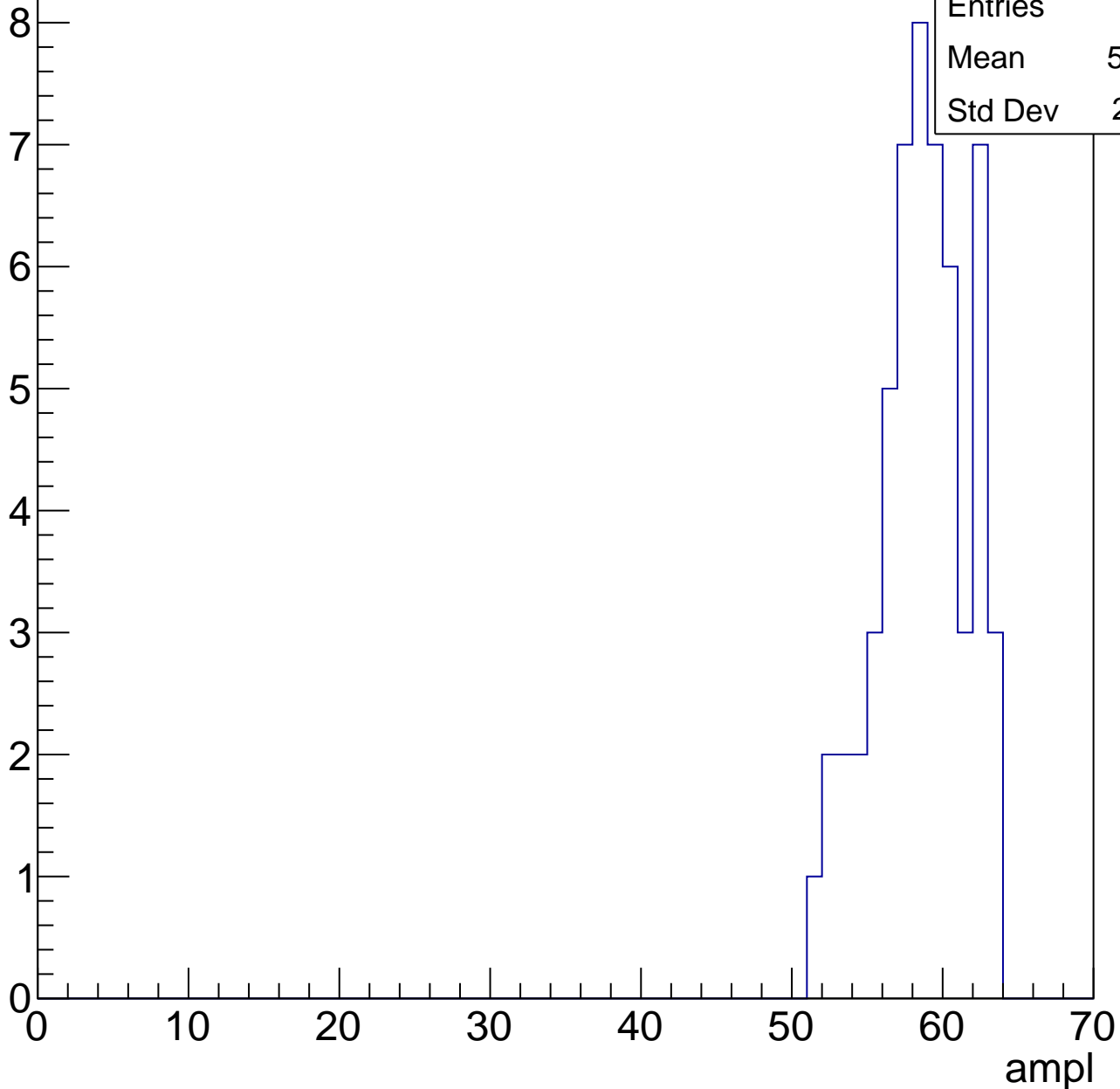


B1L103S, U10-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.14
Std Dev	2.991

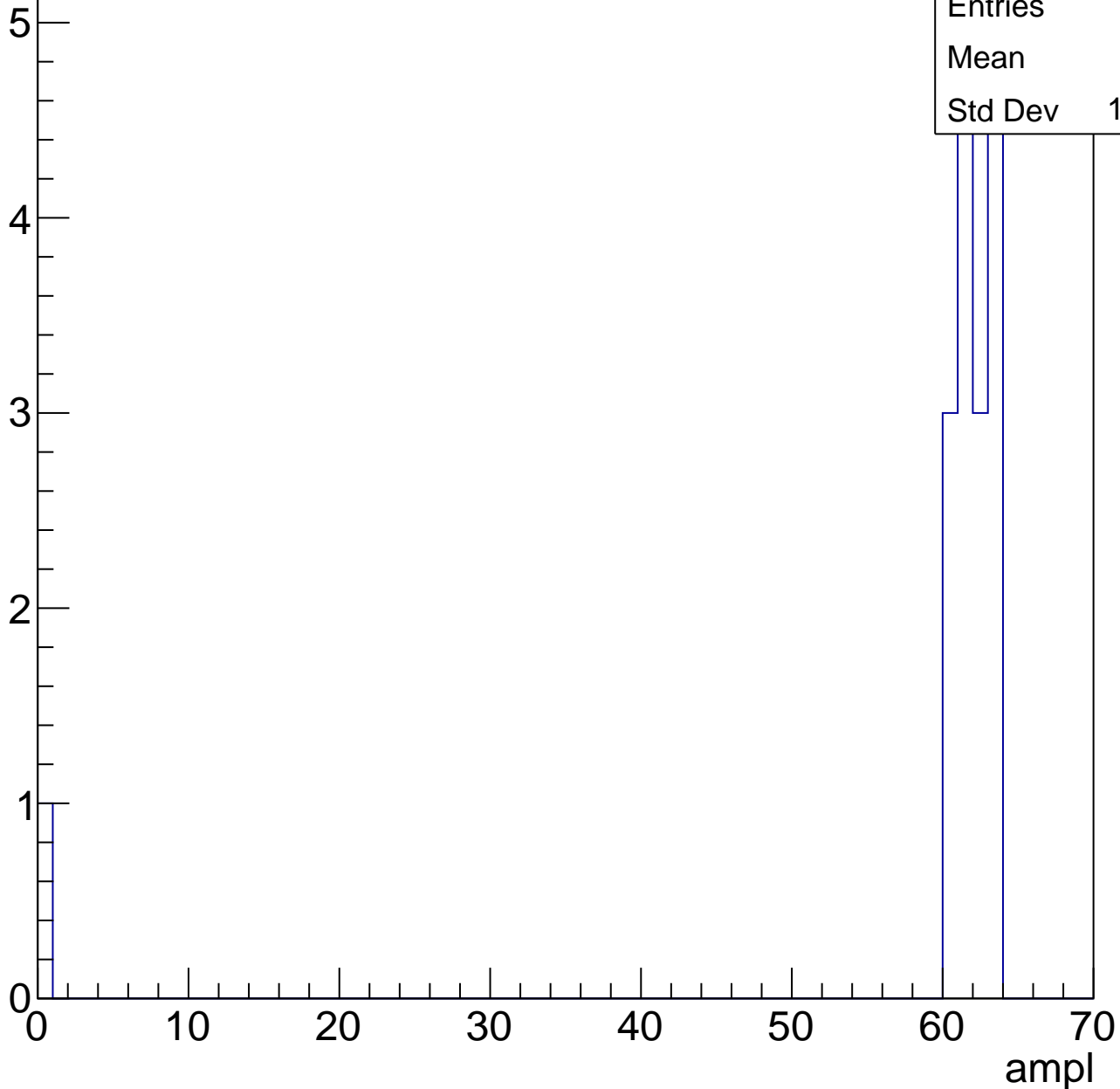


B1L103S, U10-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	58
Std Dev	14.54



B1L103S, U10-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

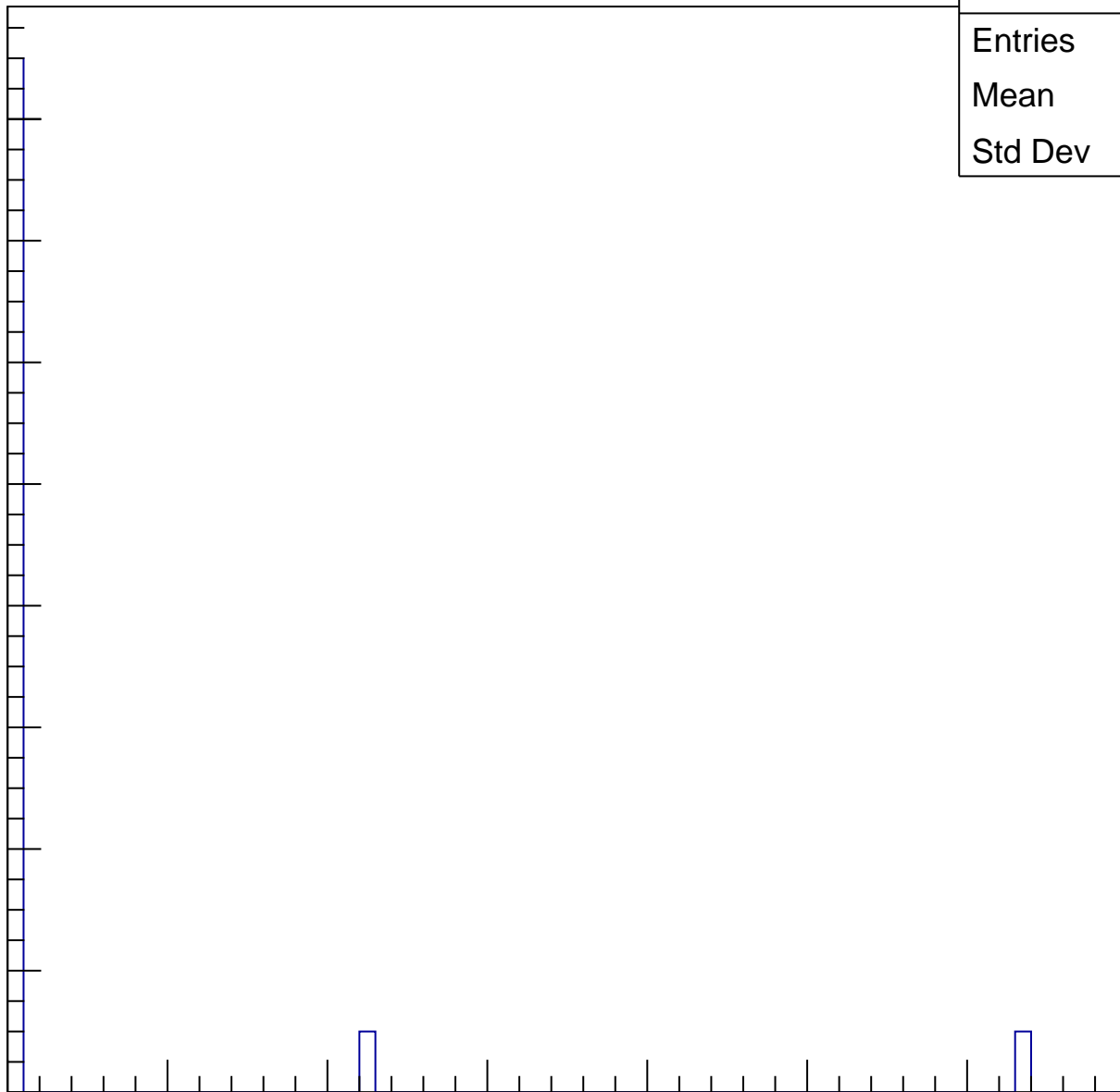
Entries	19
Mean	4.474
Std Dev	14.64

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch75, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.36
Std Dev	10.64

Gaus mean : 29.5278

Gaus Width: 2.6712

Entry

10

8

6

4

2

0

0

10

20

30

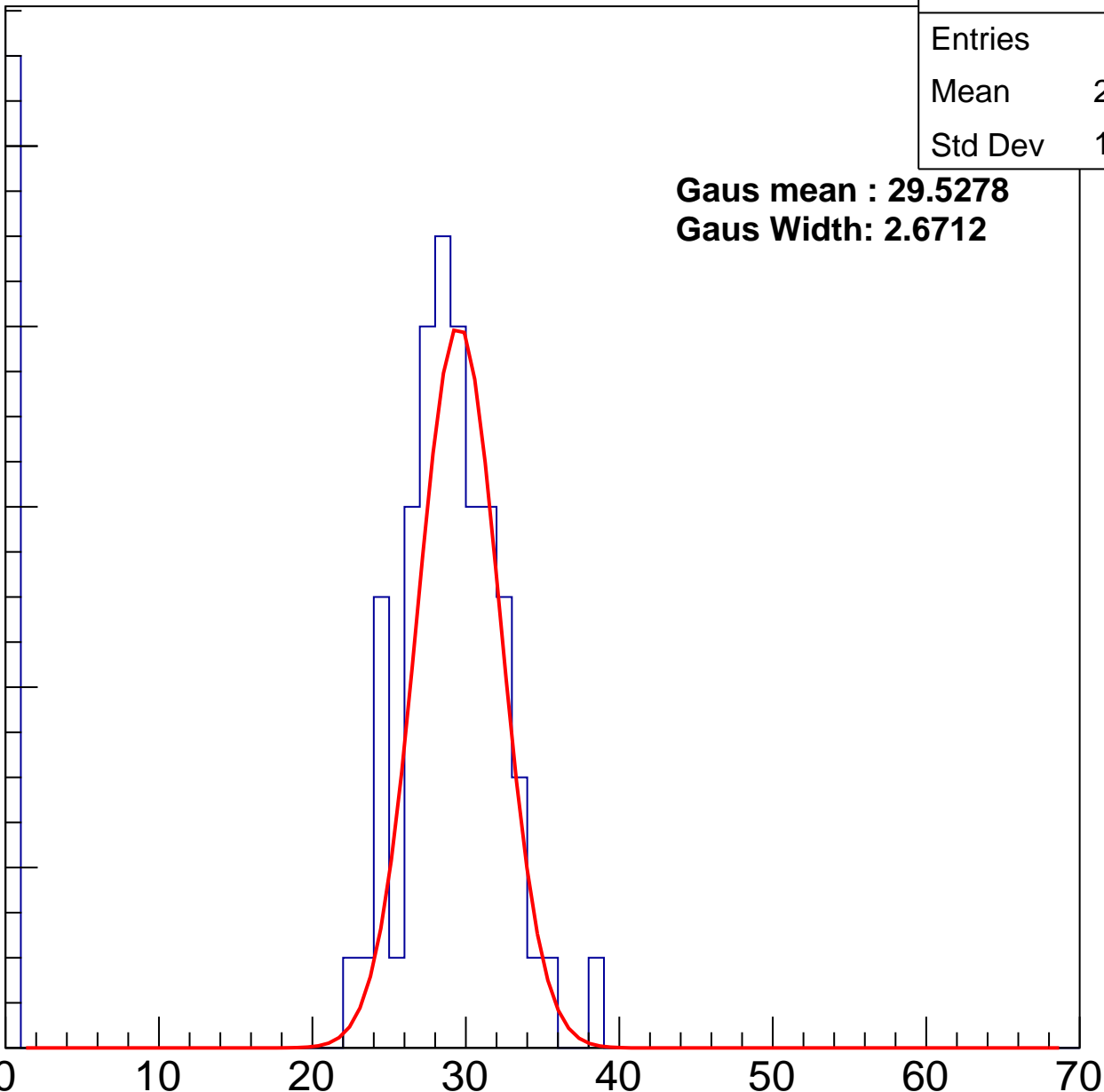
40

50

60

70

ampl



B1L103S, U10-ch75, adc1

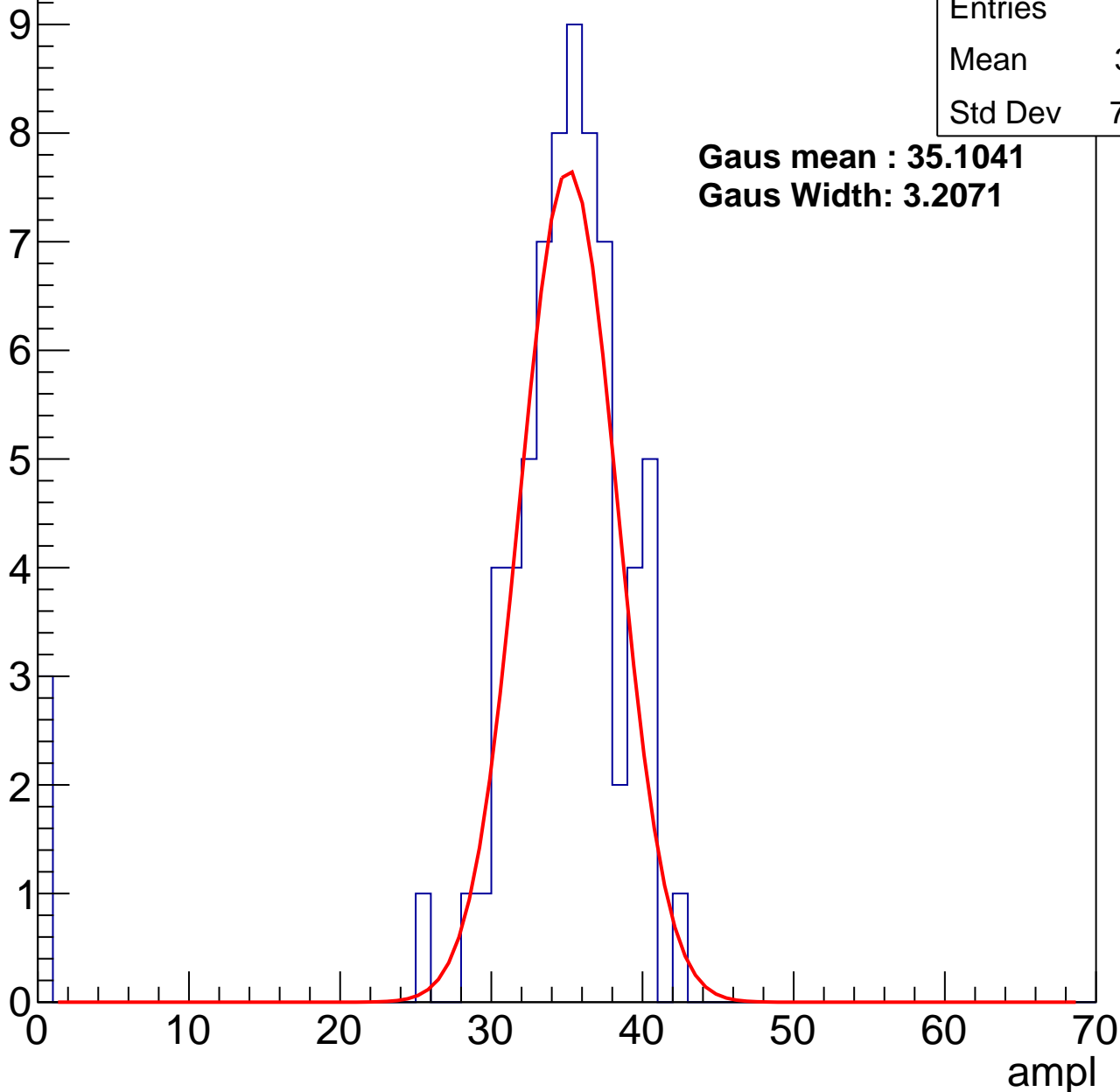
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.21
Std Dev	7.722

Gaus mean : 35.1041

Gaus Width: 3.2071



B1L103S, U10-ch75, adc2

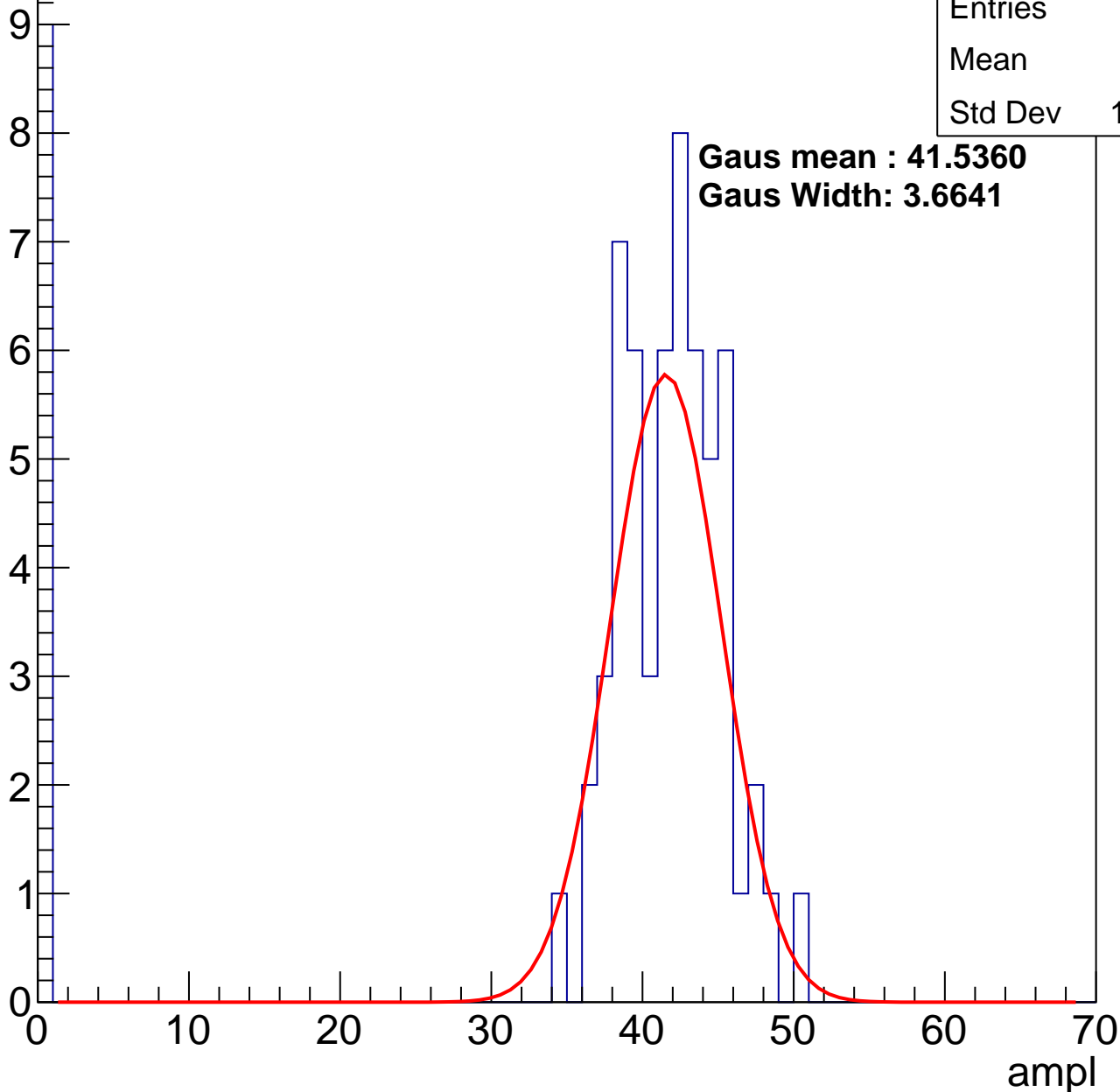
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.9
Std Dev	14.47

Gaus mean : 41.5360

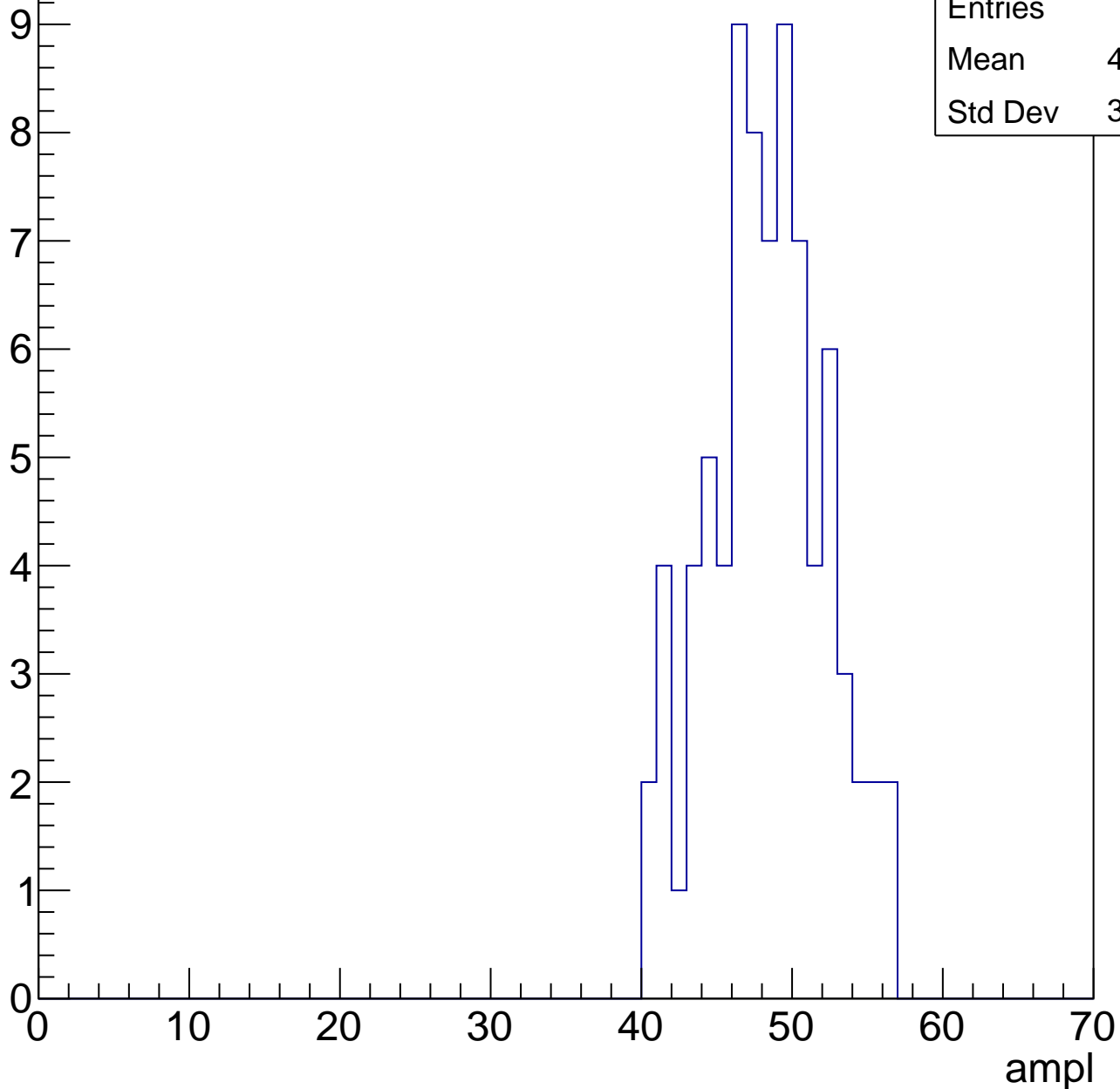
Gaus Width: 3.6641



B1L103S, U10-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

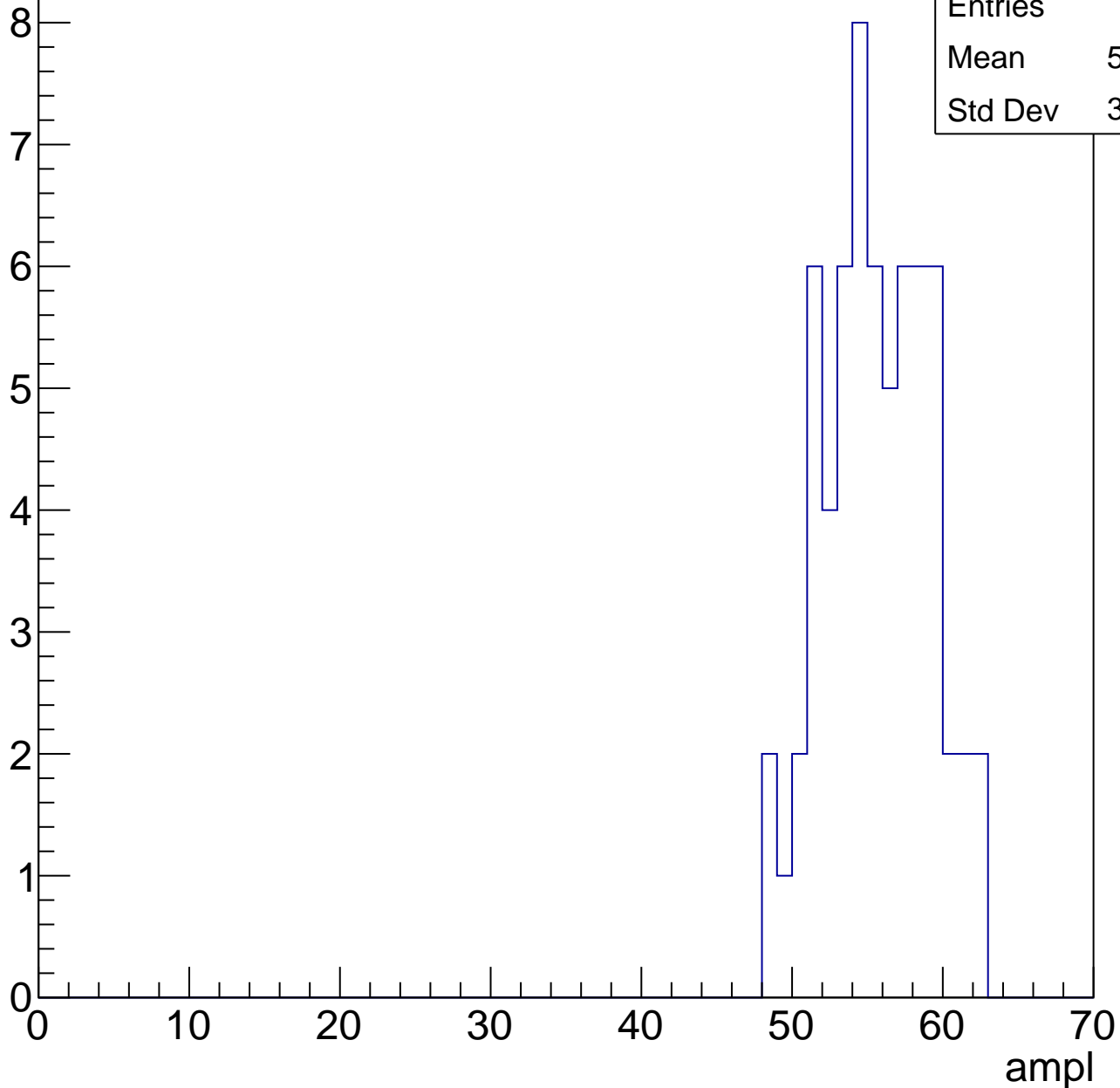


B1L103S, U10-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

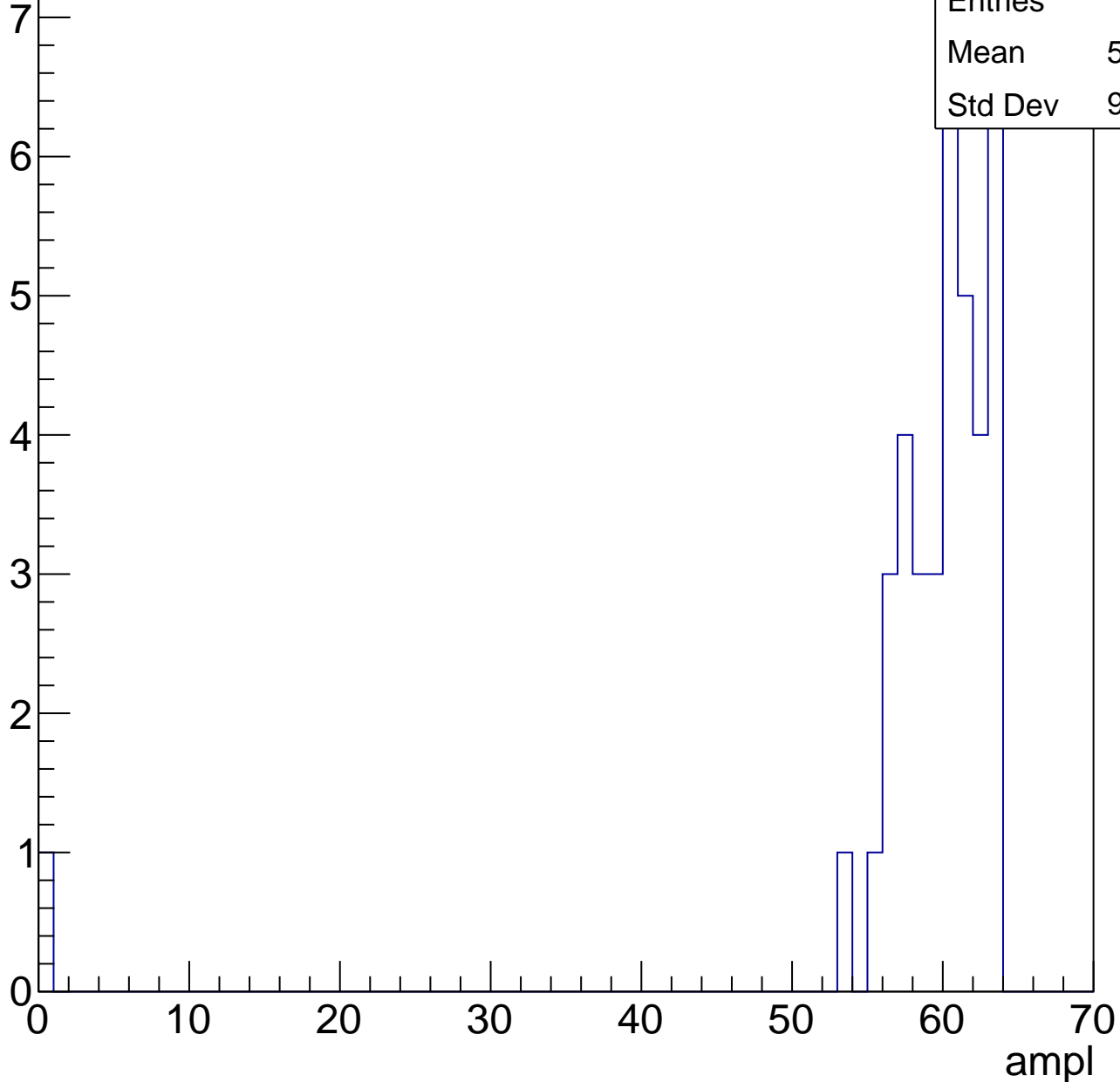
Entries	64
Mean	55.14
Std Dev	3.414



B1L103S, U10-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

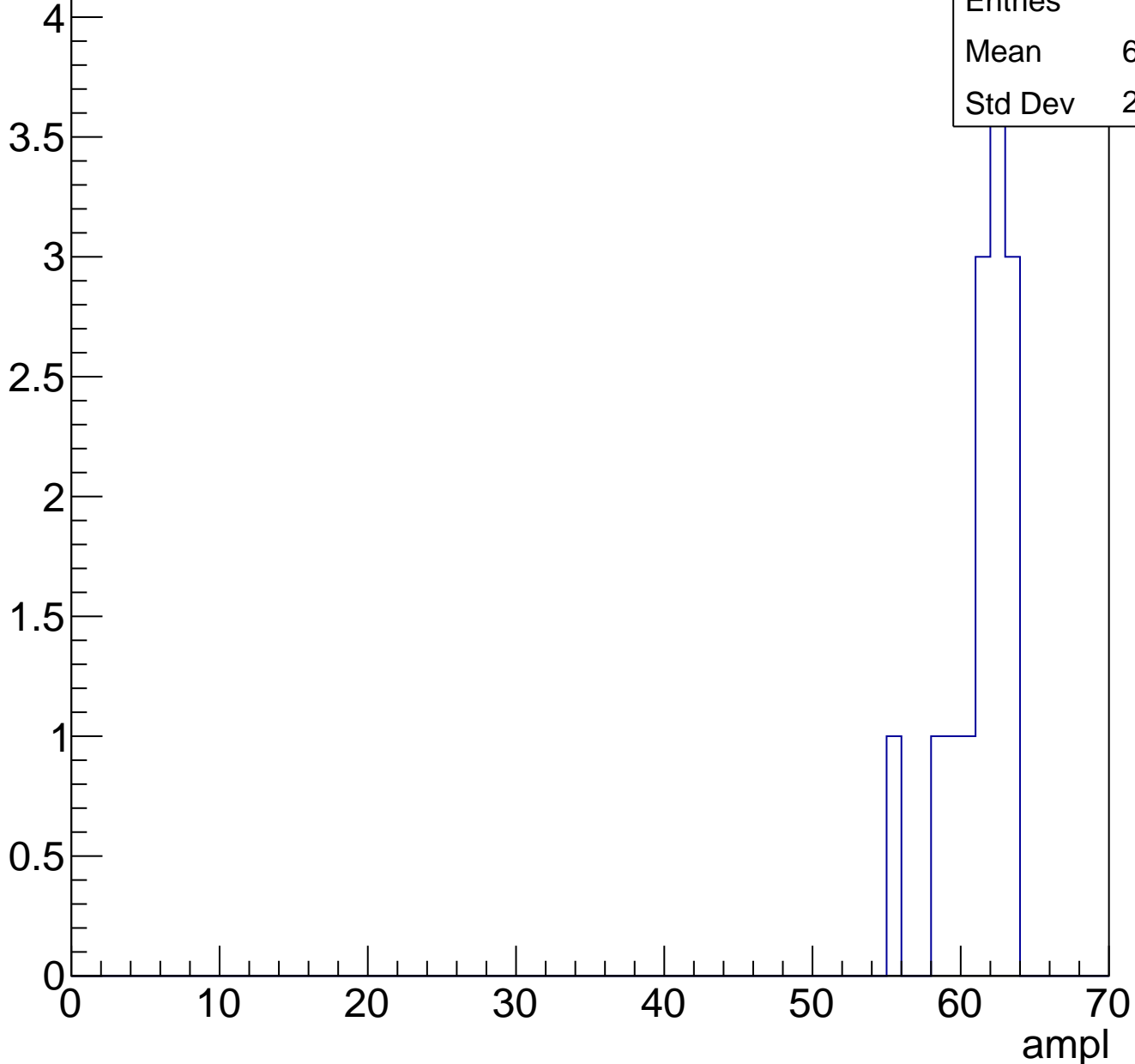
Entry



B1L103S, U10-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	14
Mean	60.86
Std Dev	2.167

B1L103S, U10-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

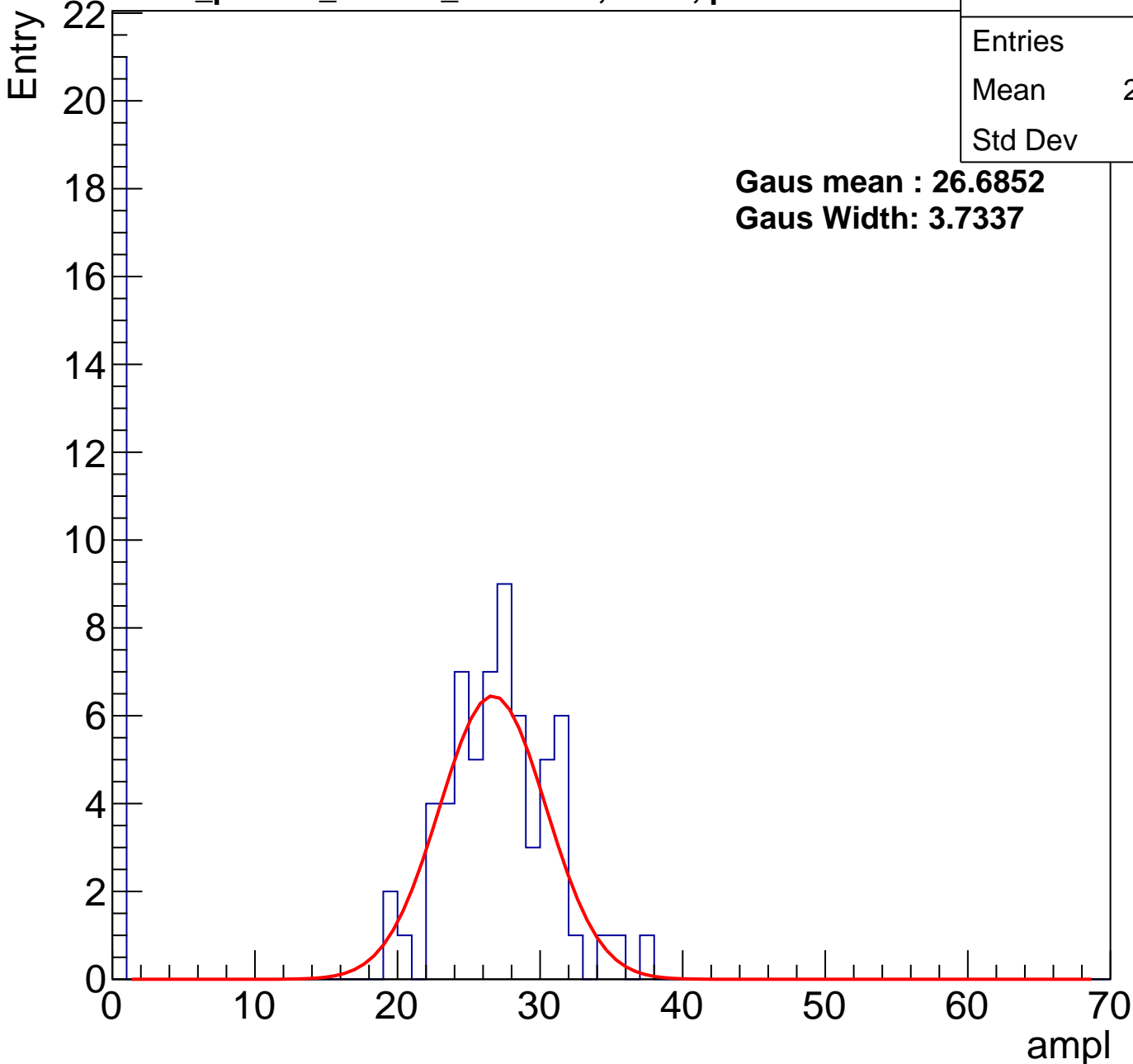
B1L103S, U10-ch76, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	20.06
Std Dev	12

Gaus mean : 26.6852

Gaus Width: 3.7337



B1L103S, U10-ch76, adc1

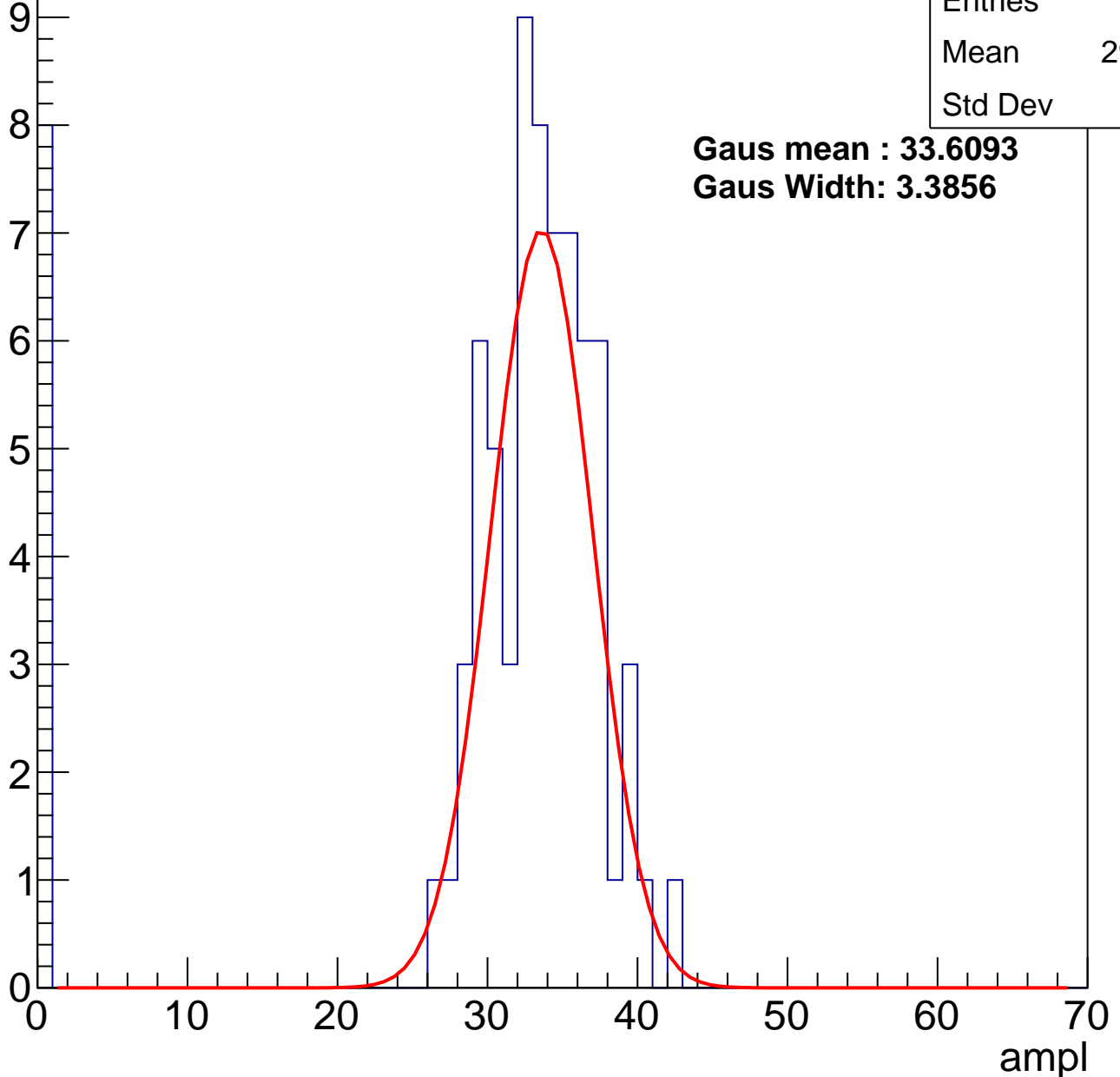
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	29.79
Std Dev	10.7

Gaus mean : 33.6093

Gaus Width: 3.3856



B1L103S, U10-ch76, adc2

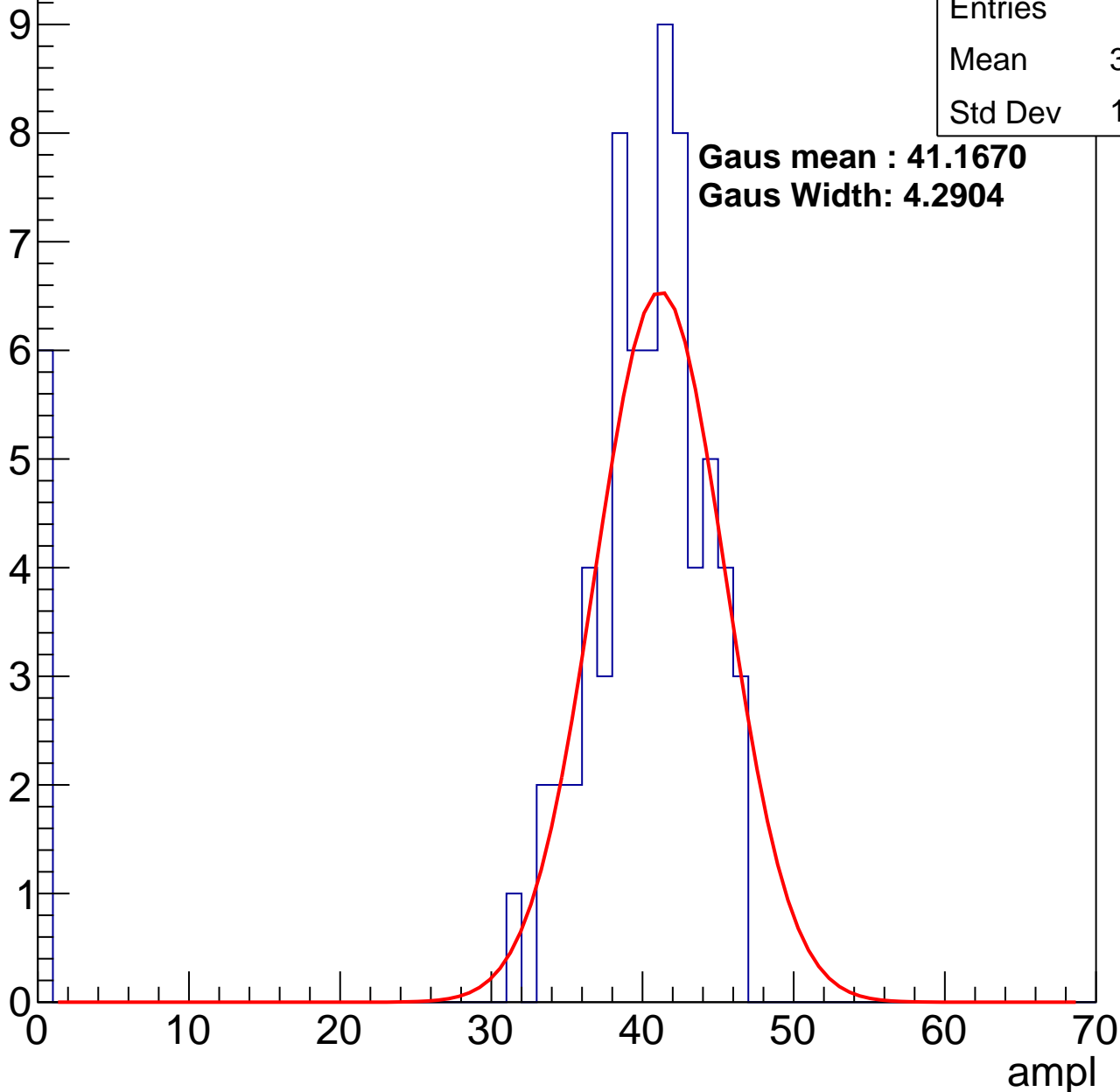
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.75
Std Dev	11.48

Gaus mean : 41.1670

Gaus Width: 4.2904

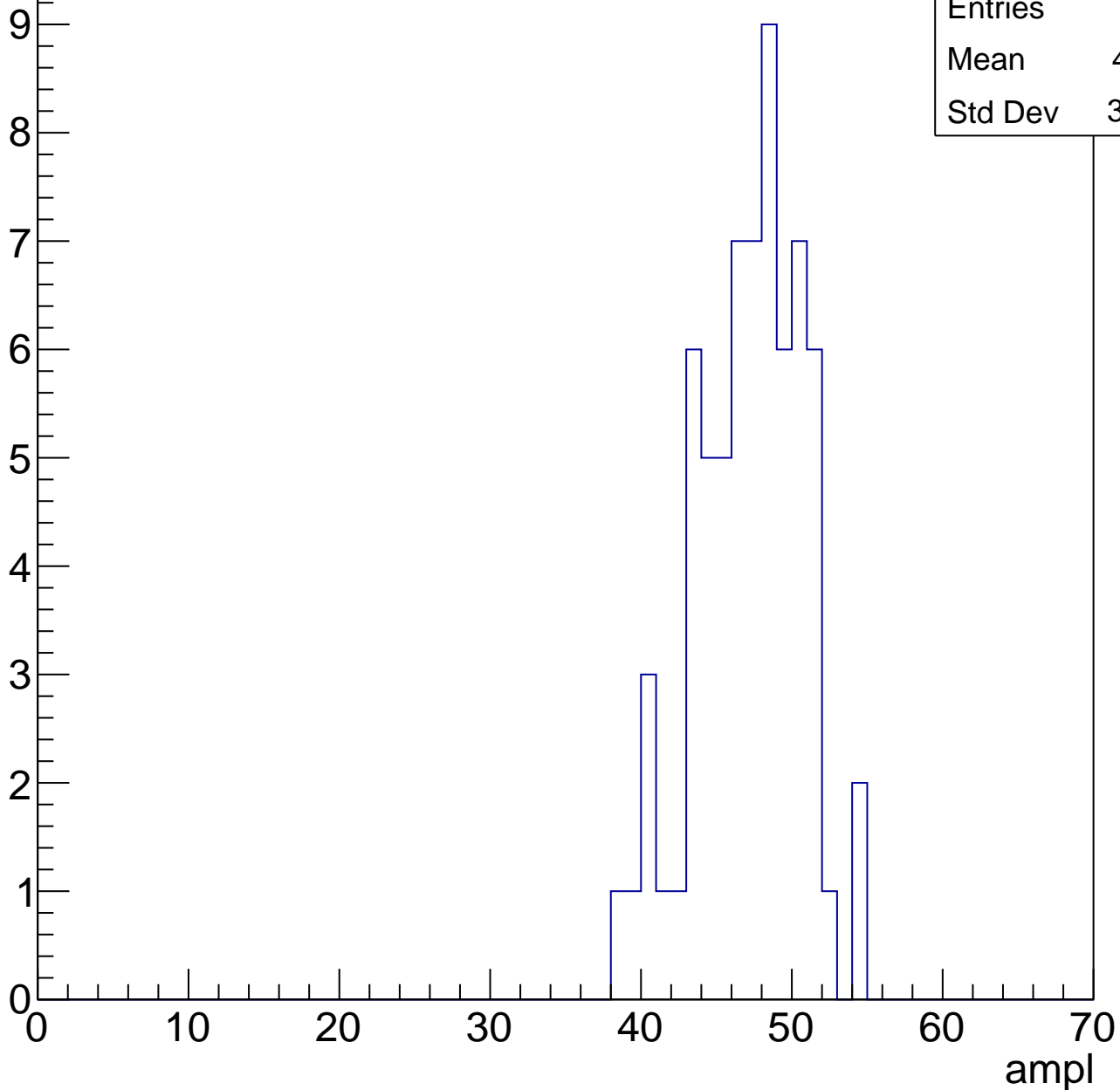


B1L103S, U10-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

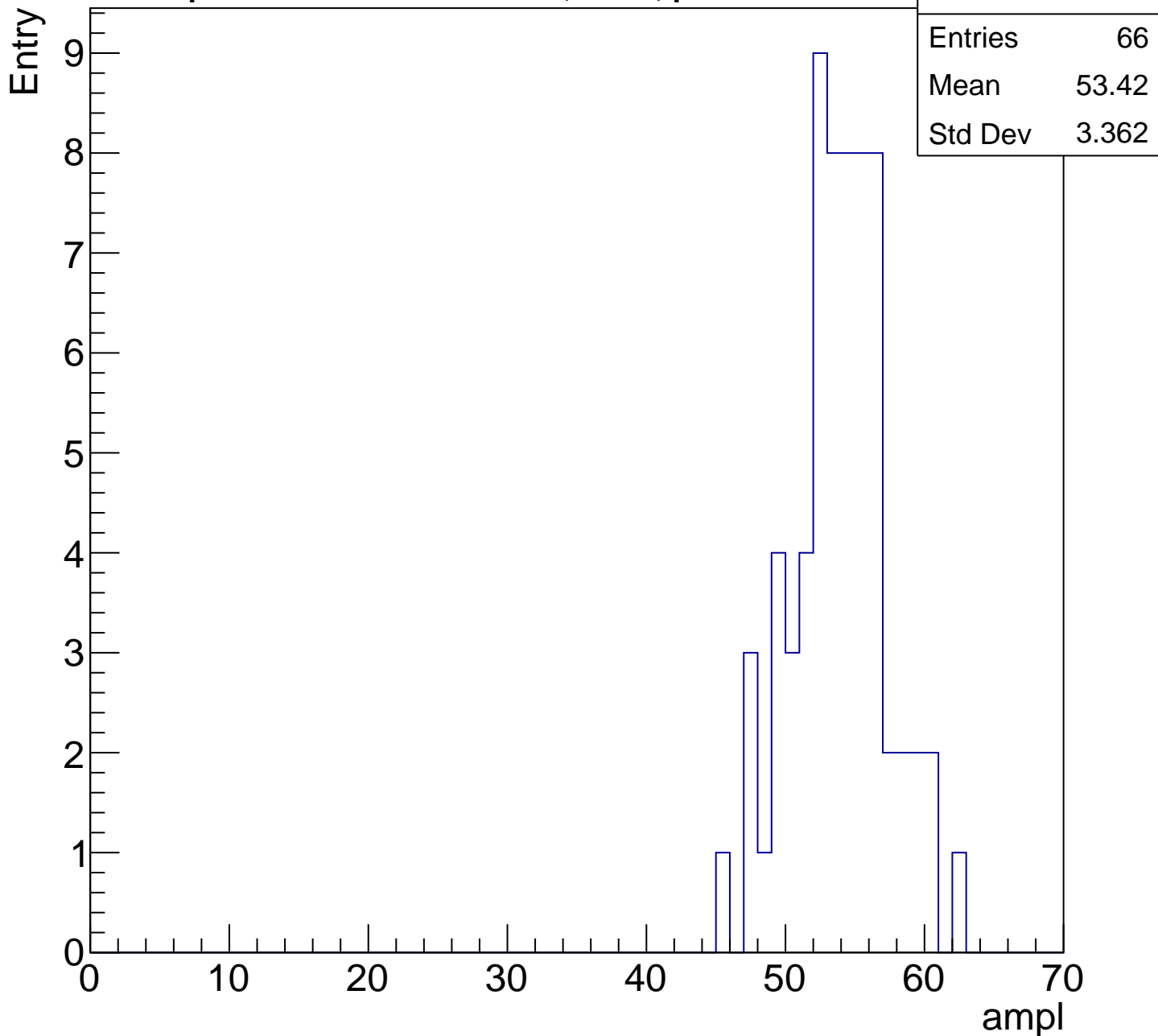
Entry

Entries	68
Mean	46.71
Std Dev	3.494



B1L103S, U10-ch76, adc4

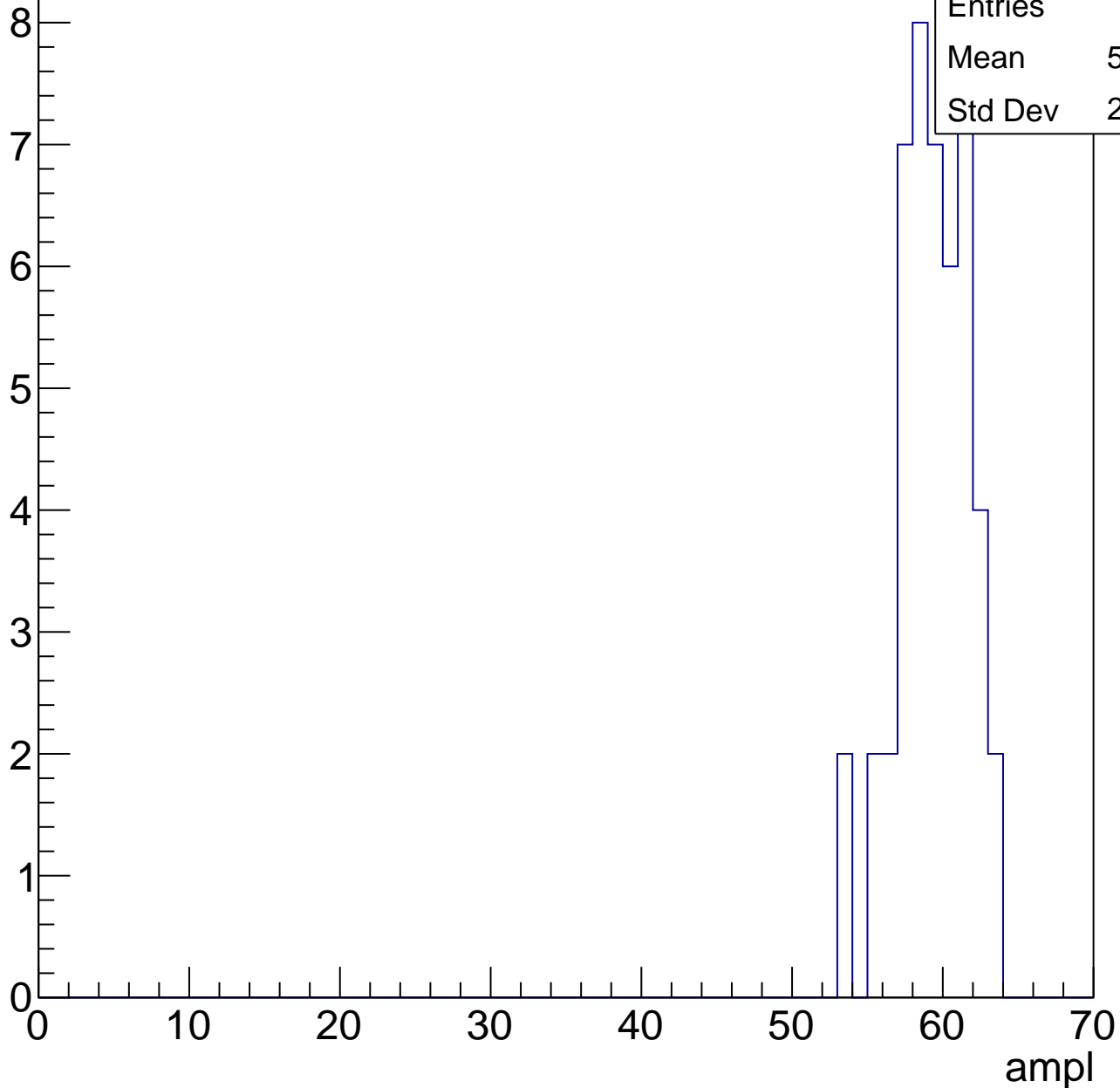
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

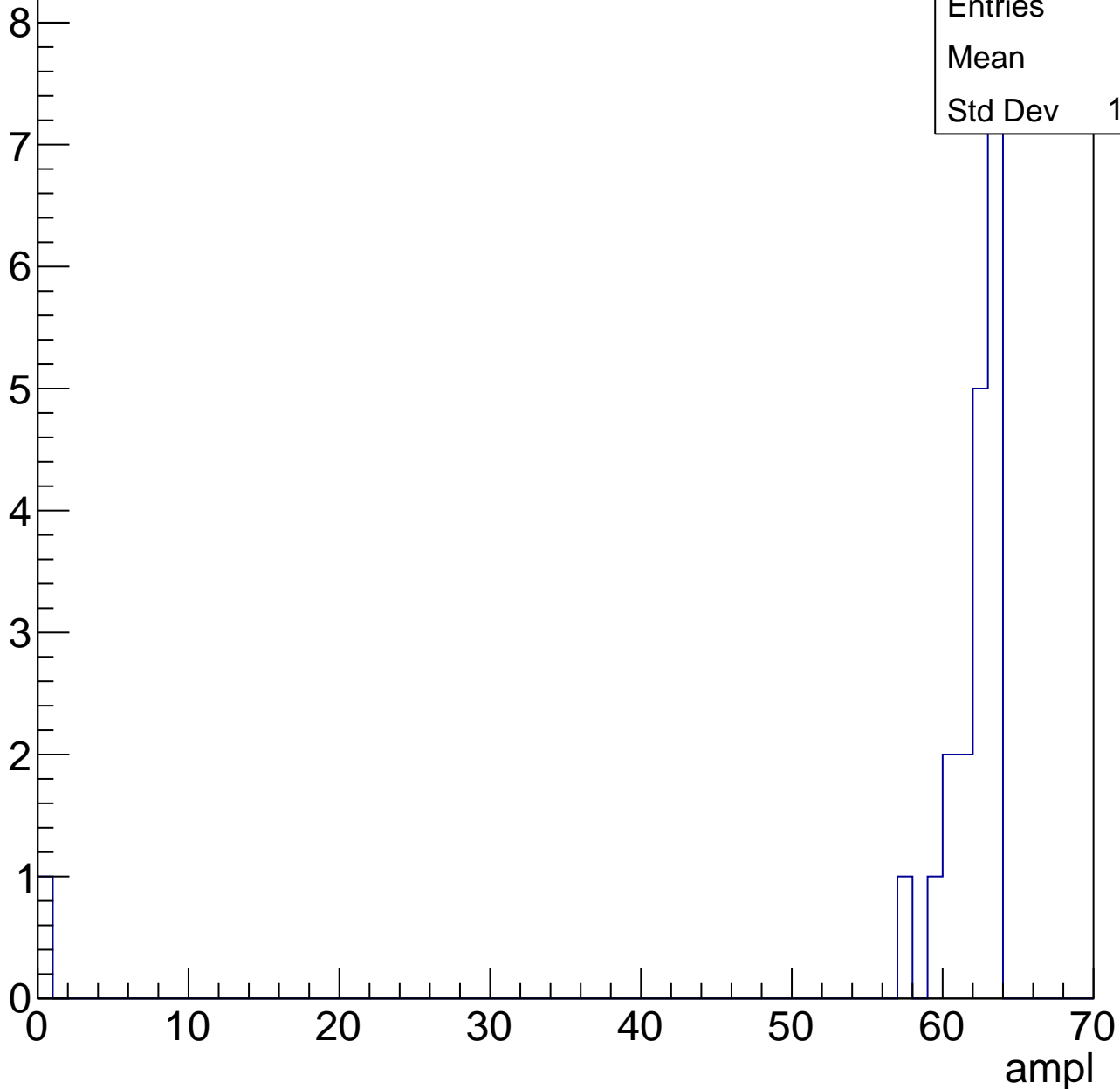


B1L103S, U10-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.6
Std Dev	13.54



B1L103S, U10-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch77, adc0

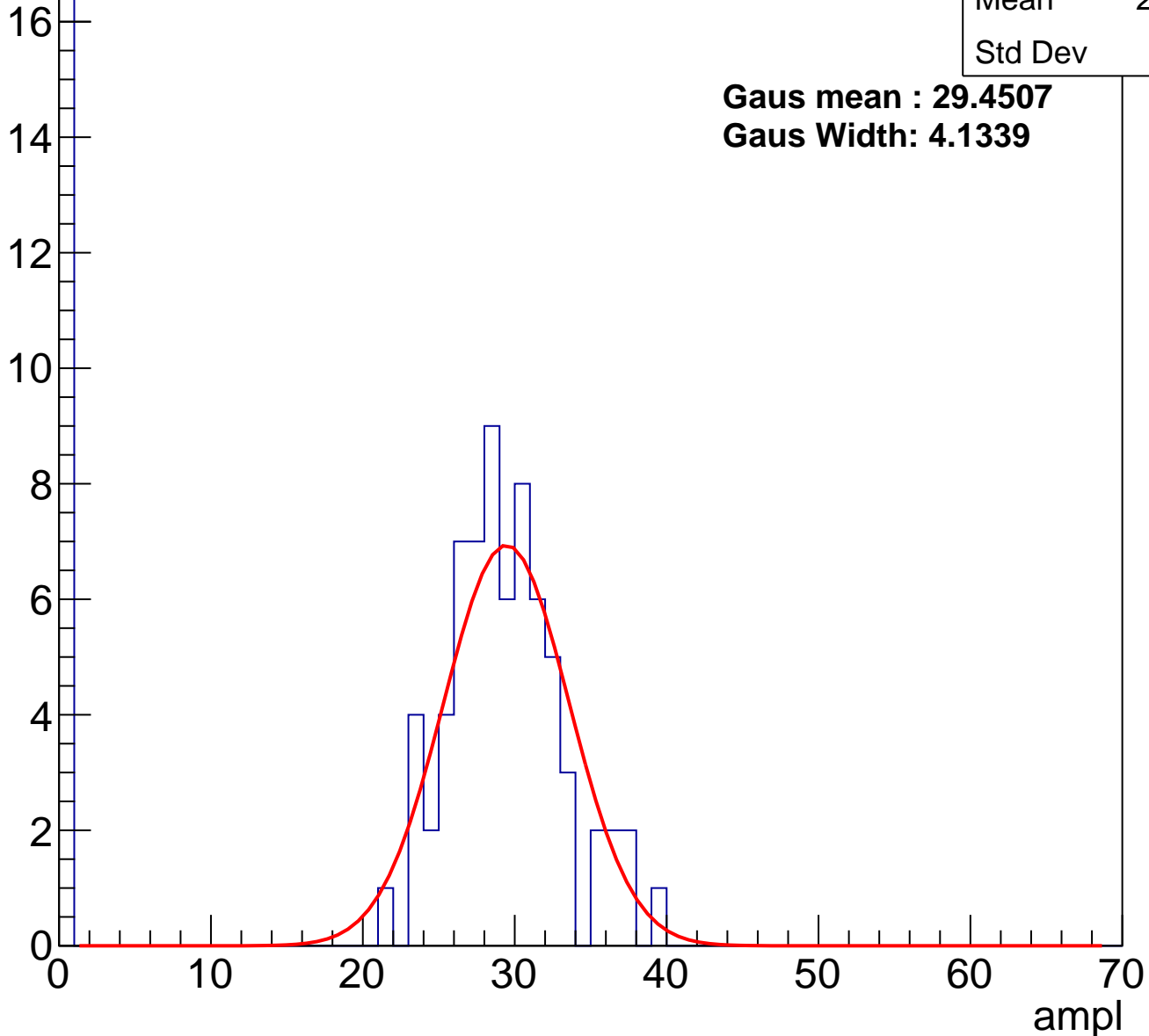
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.23
Std Dev	12

Gaus mean : 29.4507

Gaus Width: 4.1339

Entry



B1L103S, U10-ch77, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	30.21
Std Dev	13.25

Gaus mean : 35.7669

Gaus Width: 3.6390

Entry

12

10

8

6

4

2

0

0

10

20

30

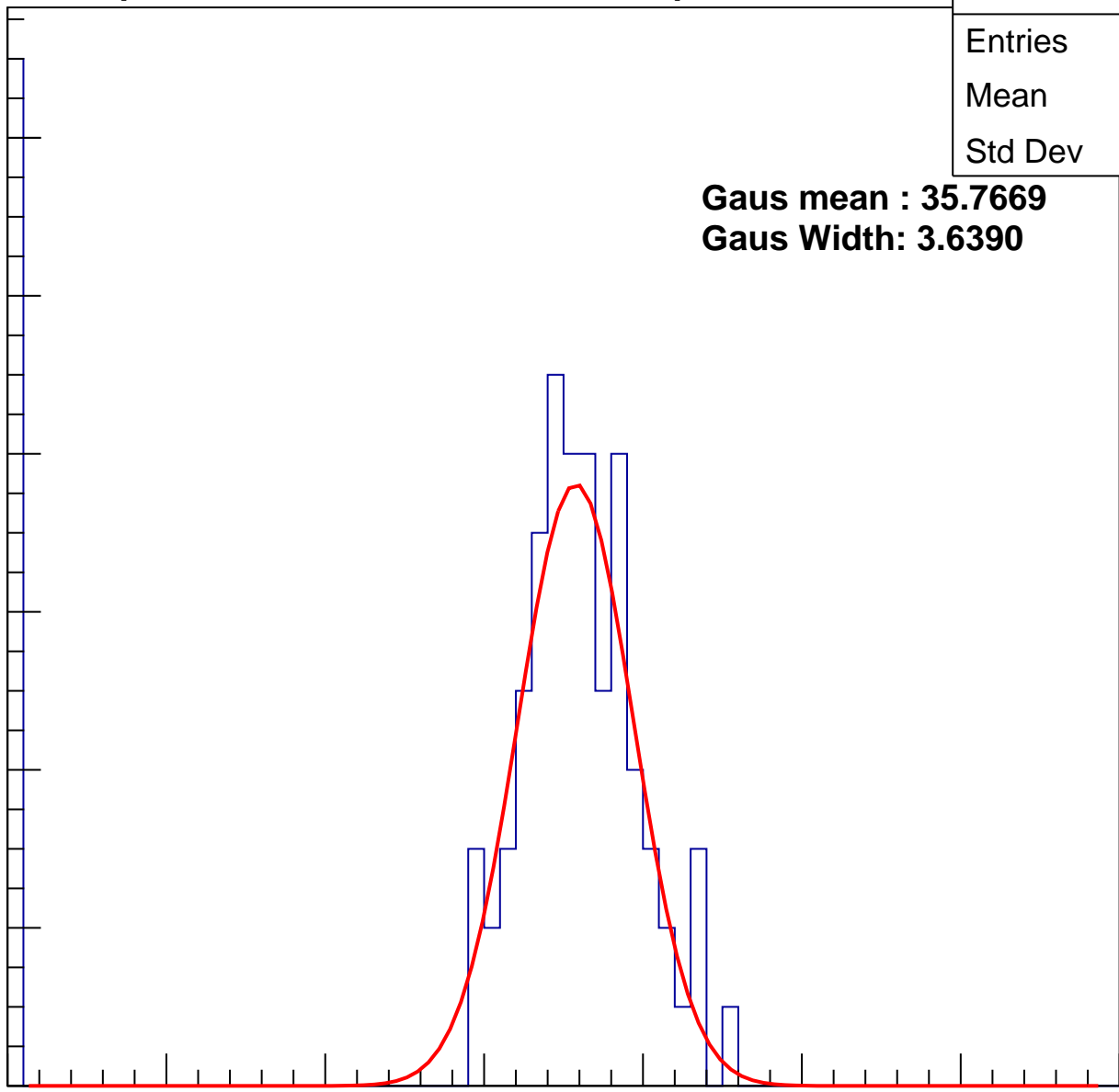
40

50

60

70

ampl



B1L103S, U10-ch77, adc2

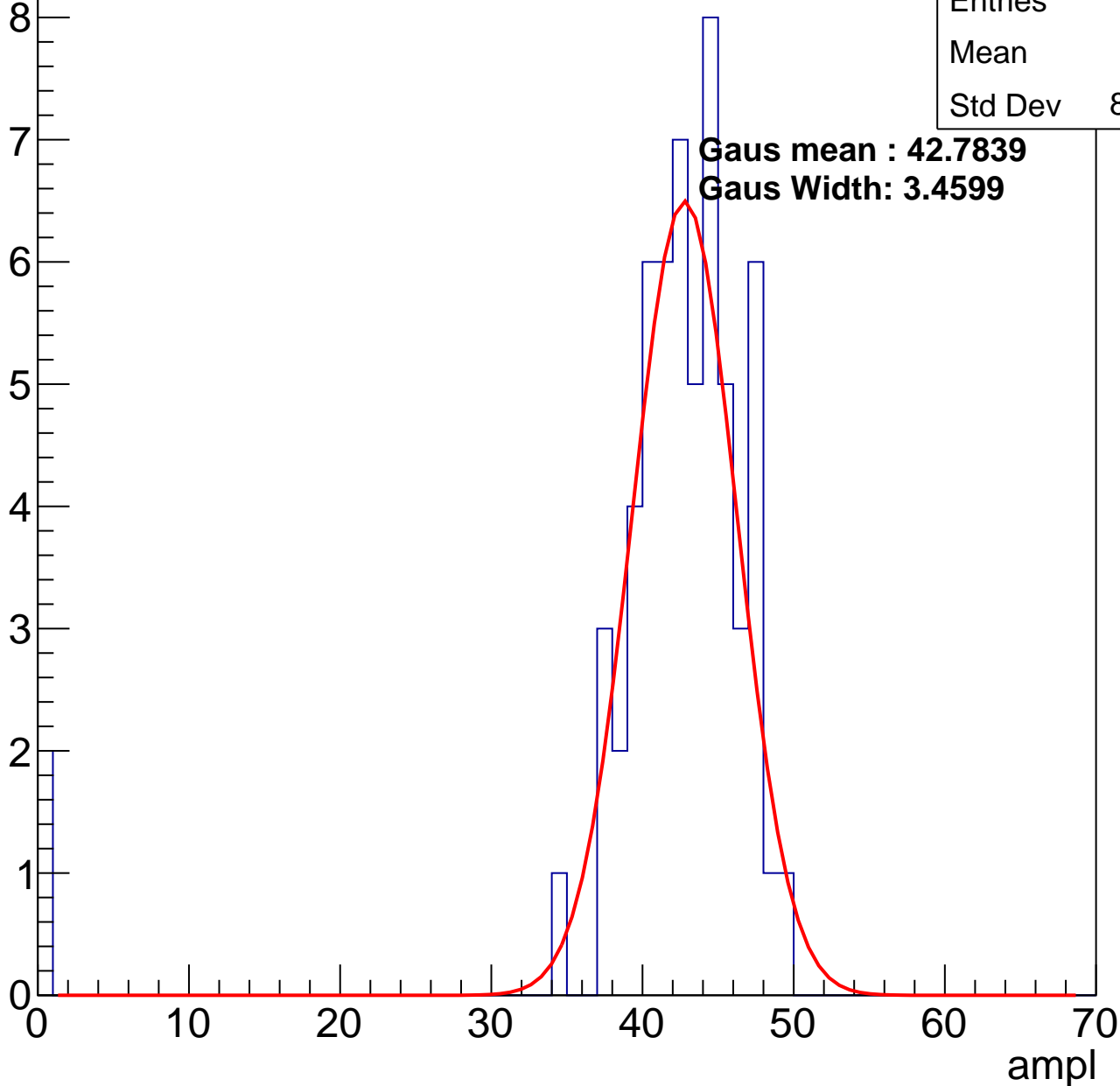
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	41.1
Std Dev	8.248

Gaus mean : 42.7839

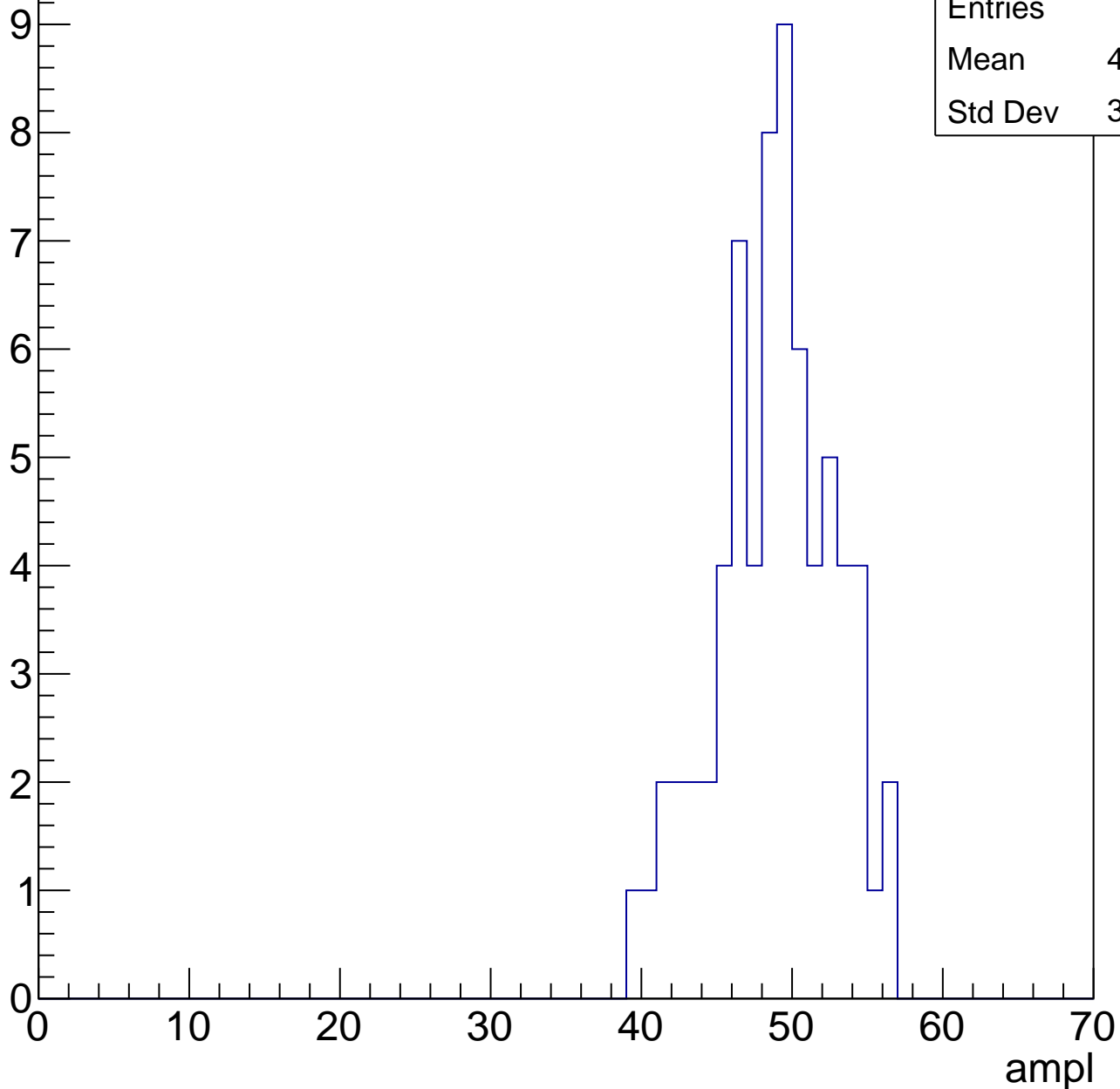
Gaus Width: 3.4599



B1L103S, U10-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



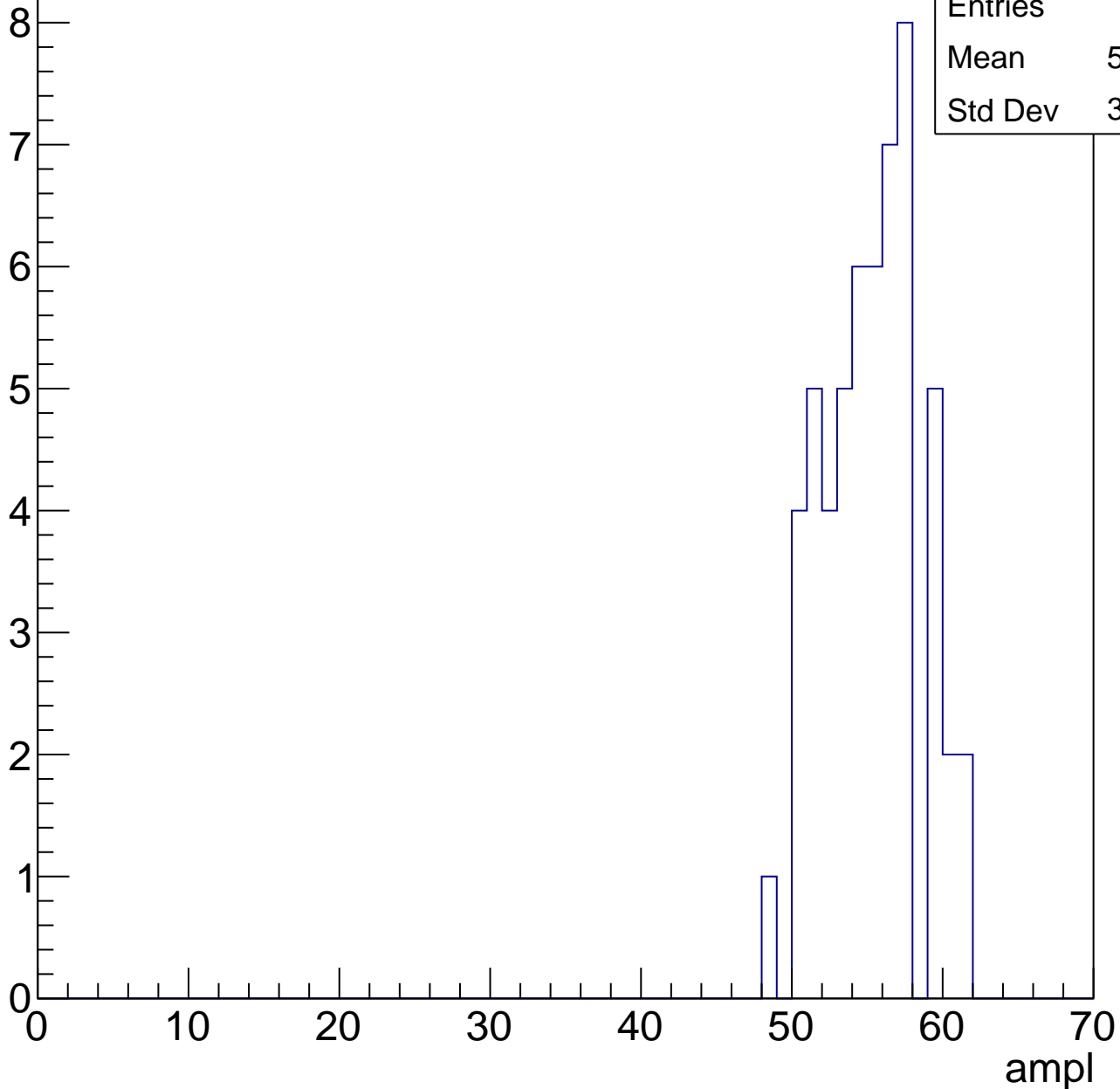
Entries	68
Mean	48.43
Std Dev	3.878

B1L103S, U10-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.82
Std Dev	3.105

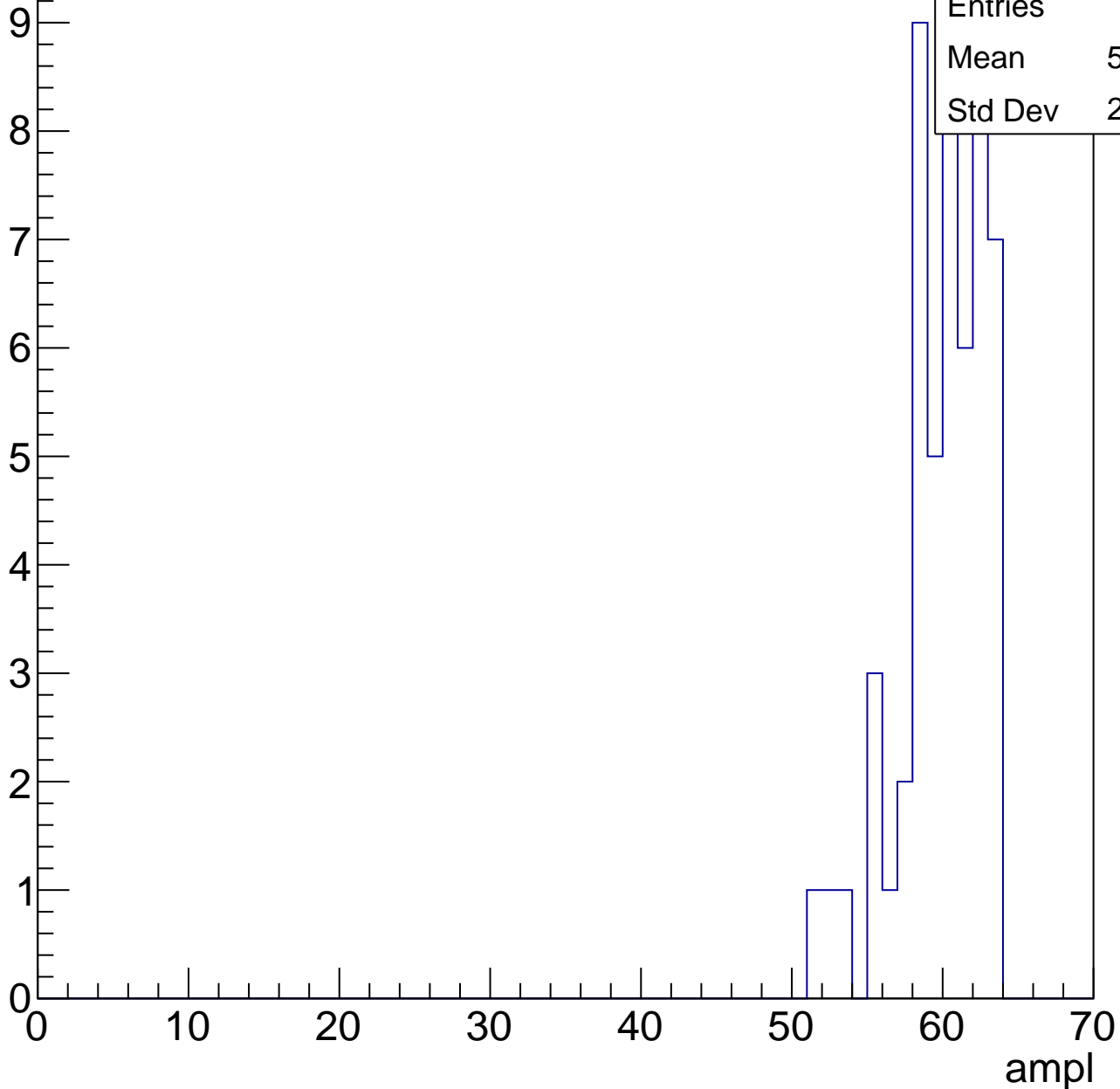


B1L103S, U10-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

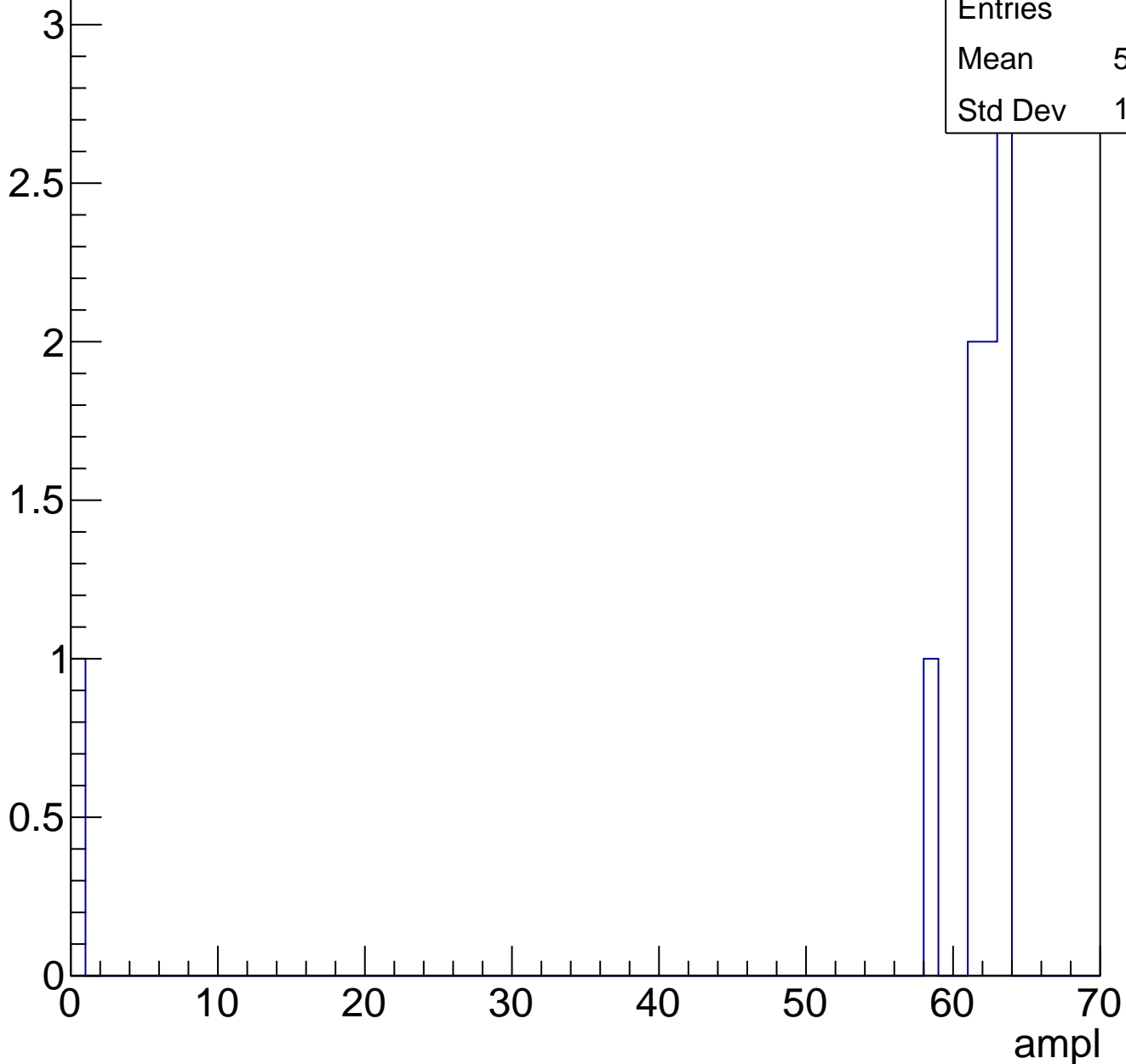
Entries	52
Mean	59.44
Std Dev	2.872



B1L103S, U10-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

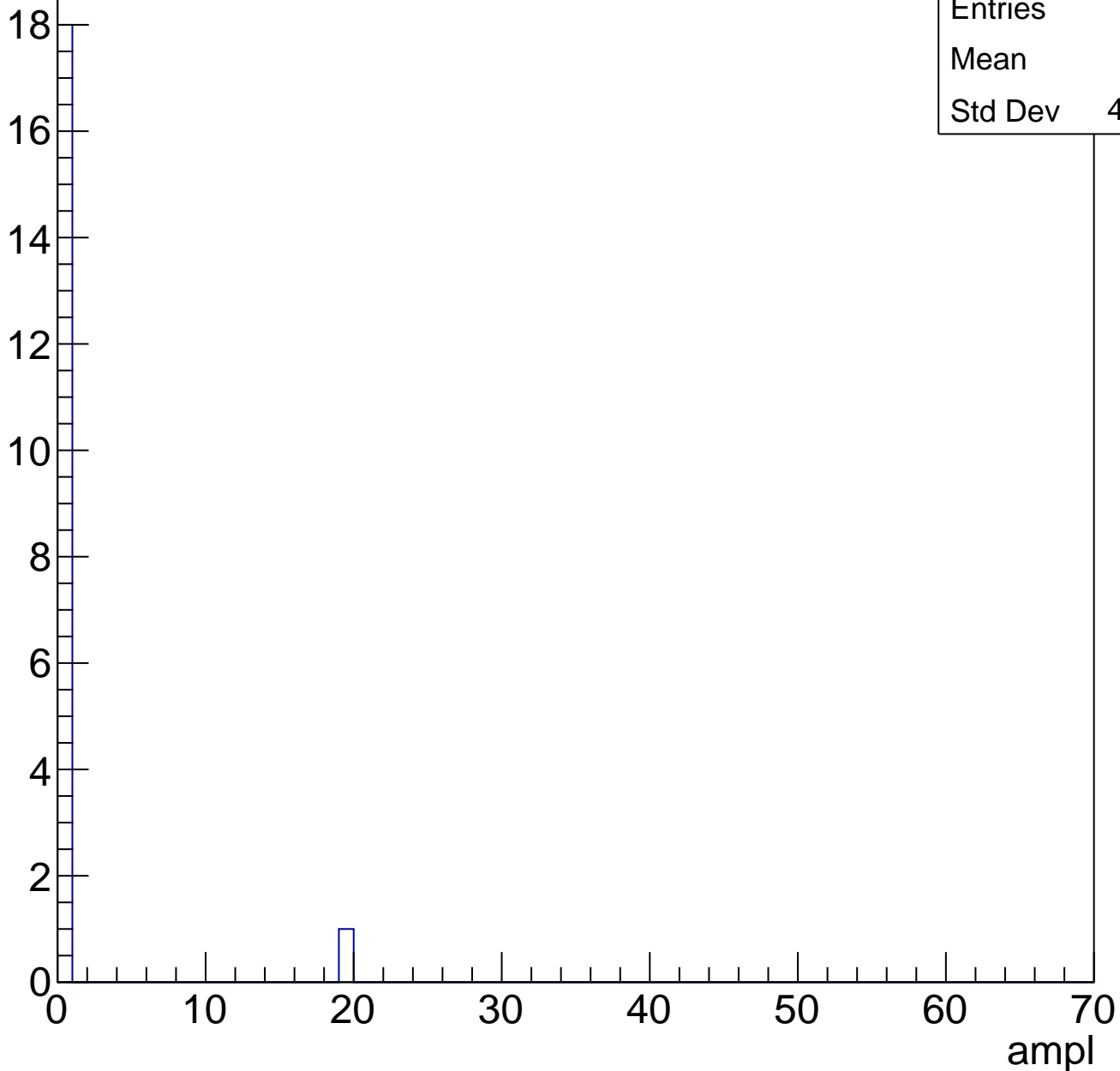
Entry



B1L103S, U10-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1
Std Dev	4.243

B1L103S, U10-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.59
Std Dev	11.52

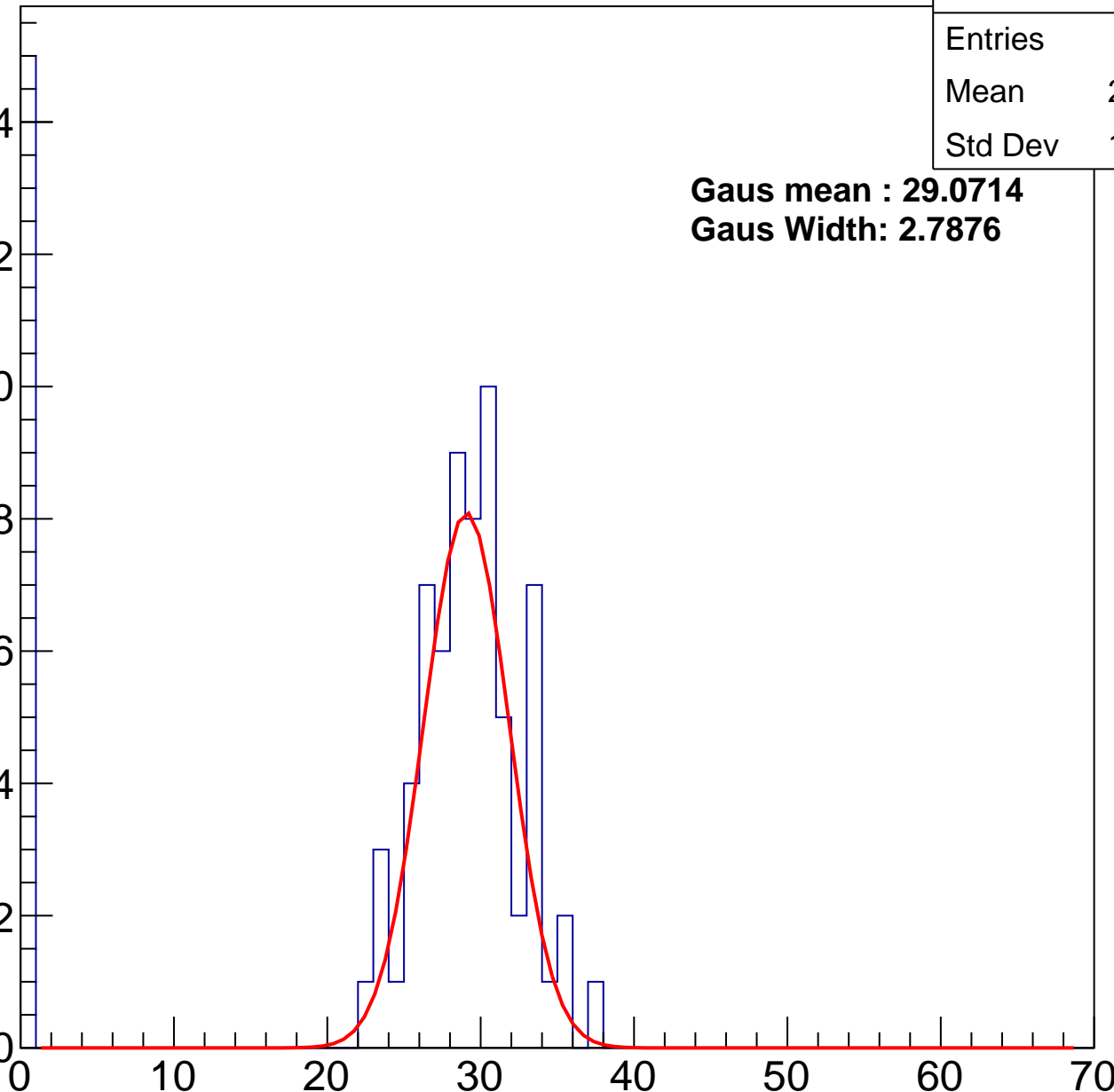
Gaus mean : 29.0714

Gaus Width: 2.7876

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U10-ch78, adc1

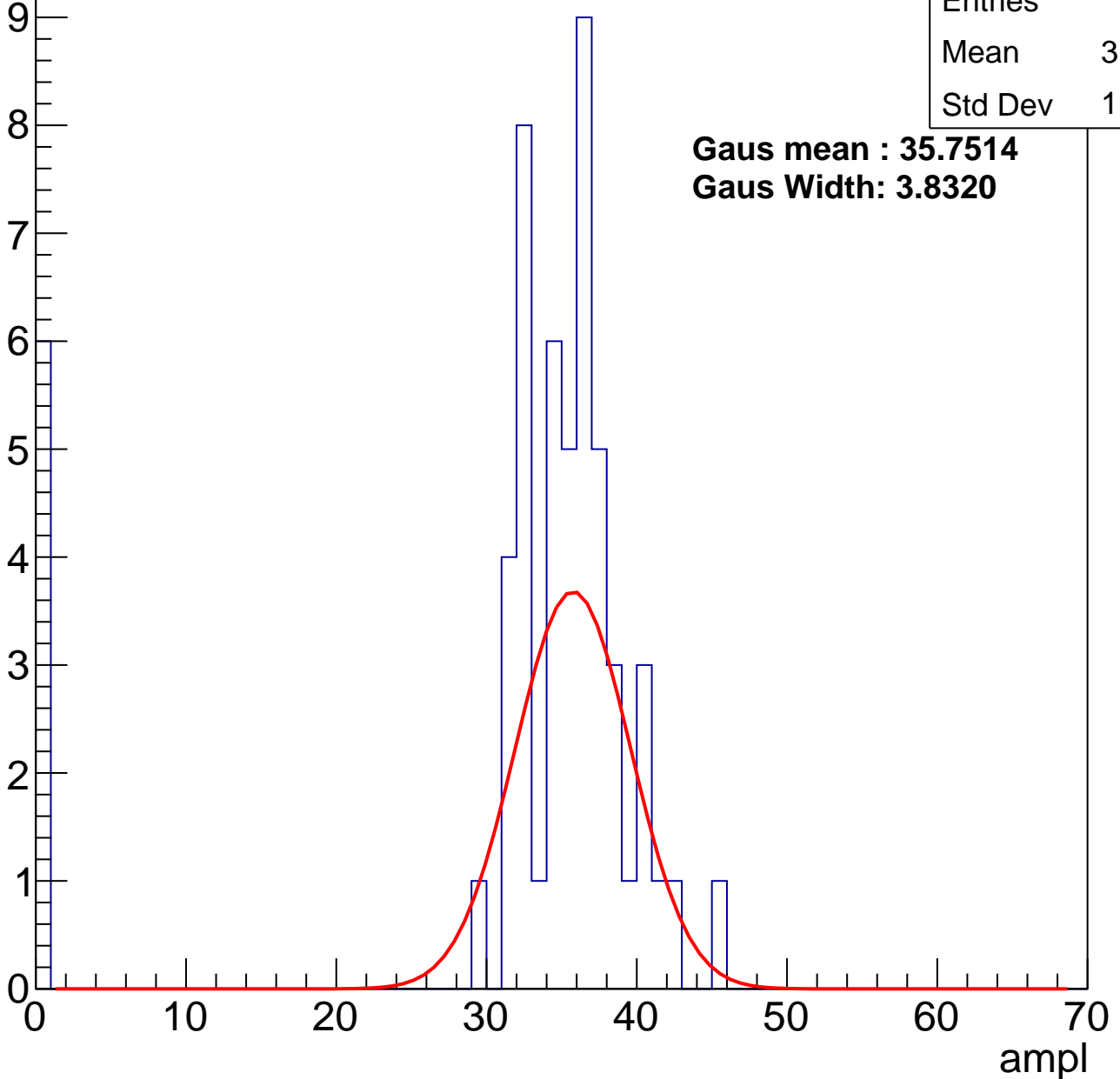
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	31.47
Std Dev	11.43

Gaus mean : 35.7514

Gaus Width: 3.8320



B1L103S, U10-ch78, adc2

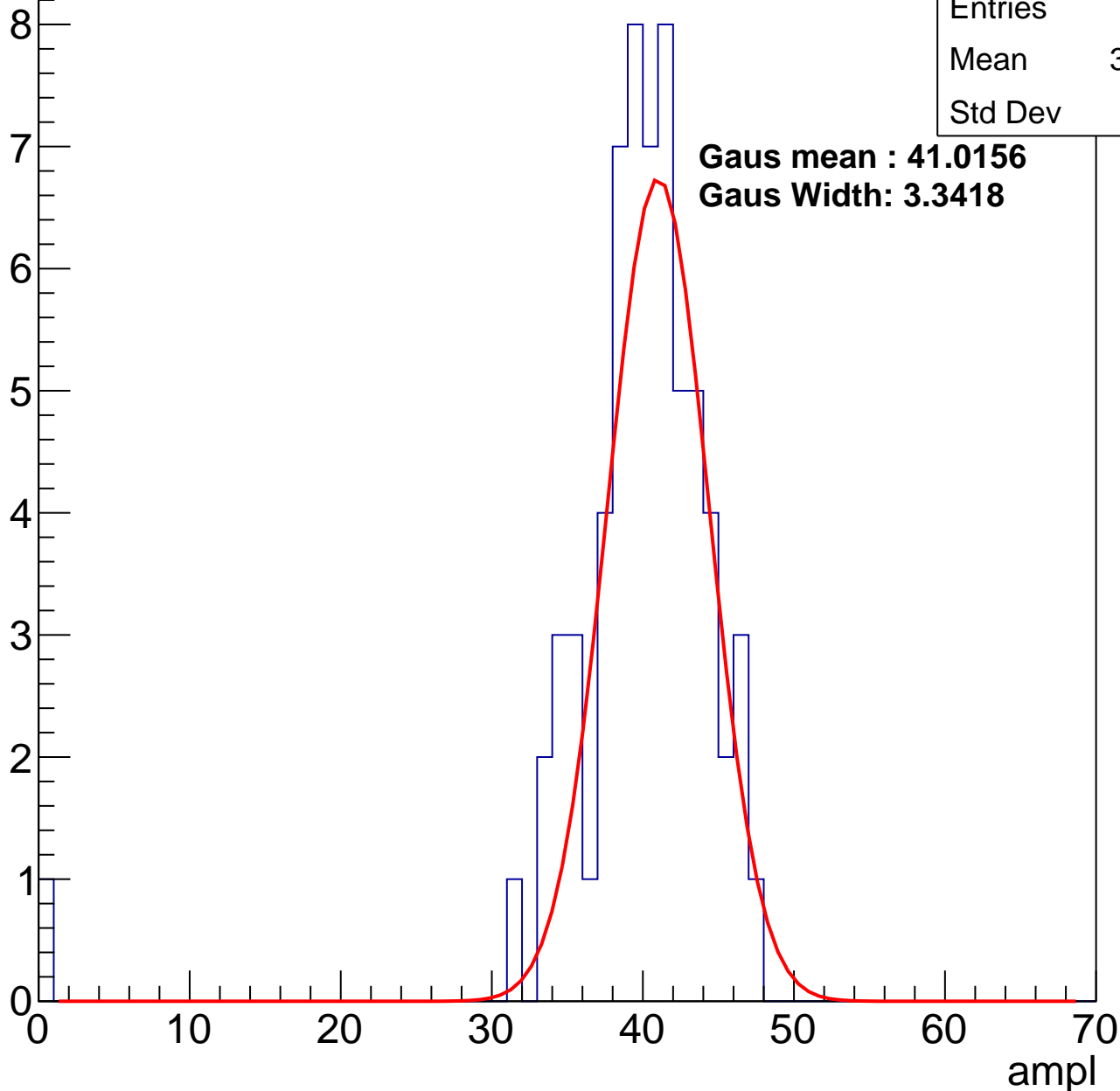
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.23
Std Dev	6.02

Gaus mean : 41.0156

Gaus Width: 3.3418

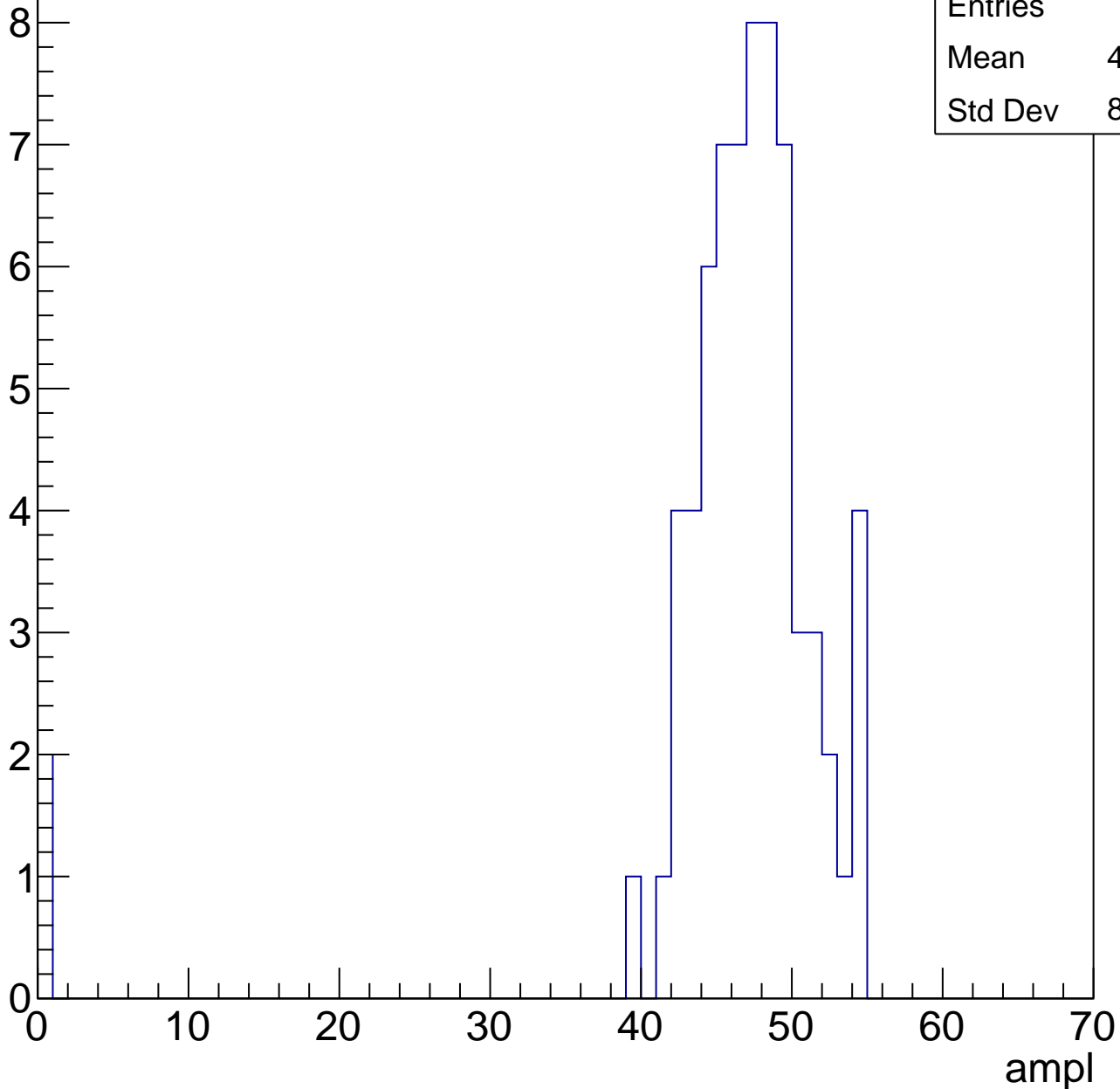


B1L103S, U10-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.59
Std Dev	8.599

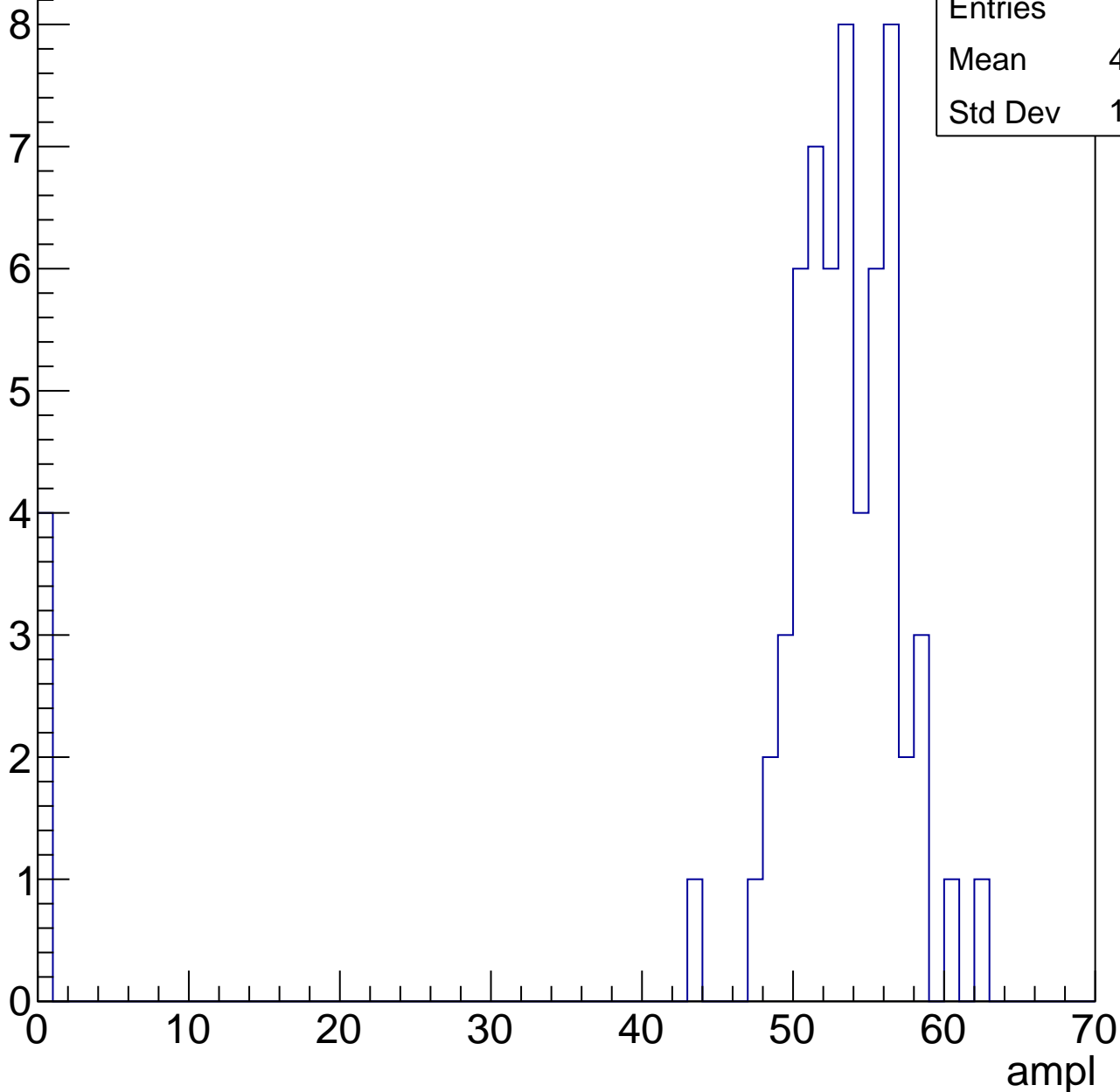


B1L103S, U10-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.68
Std Dev	13.33



B1L103S, U10-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	53
Mean	56.98
Std Dev	8.41

Entry

10

8

6

4

2

0

0

10

20

30

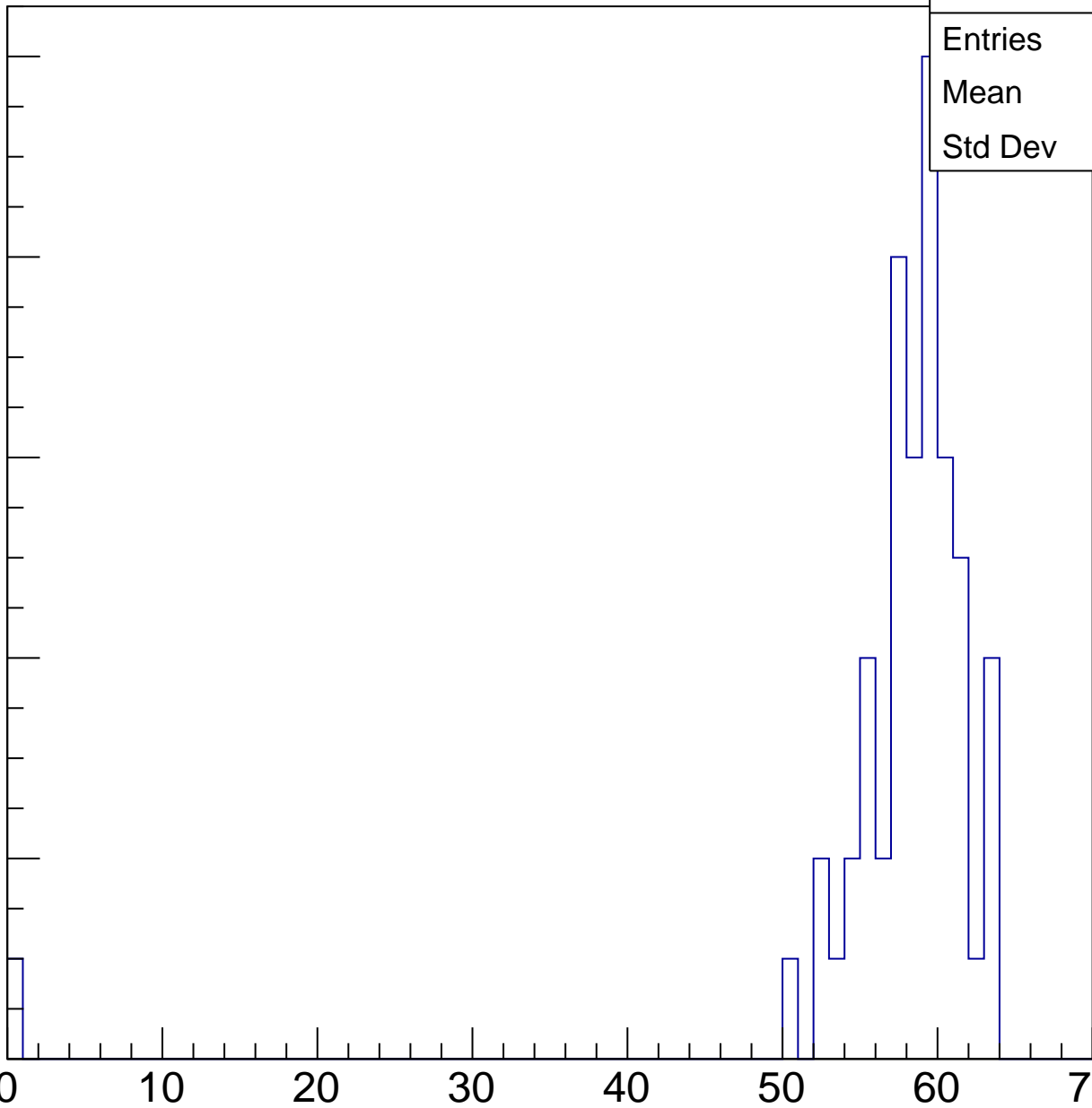
40

50

60

70

ampl

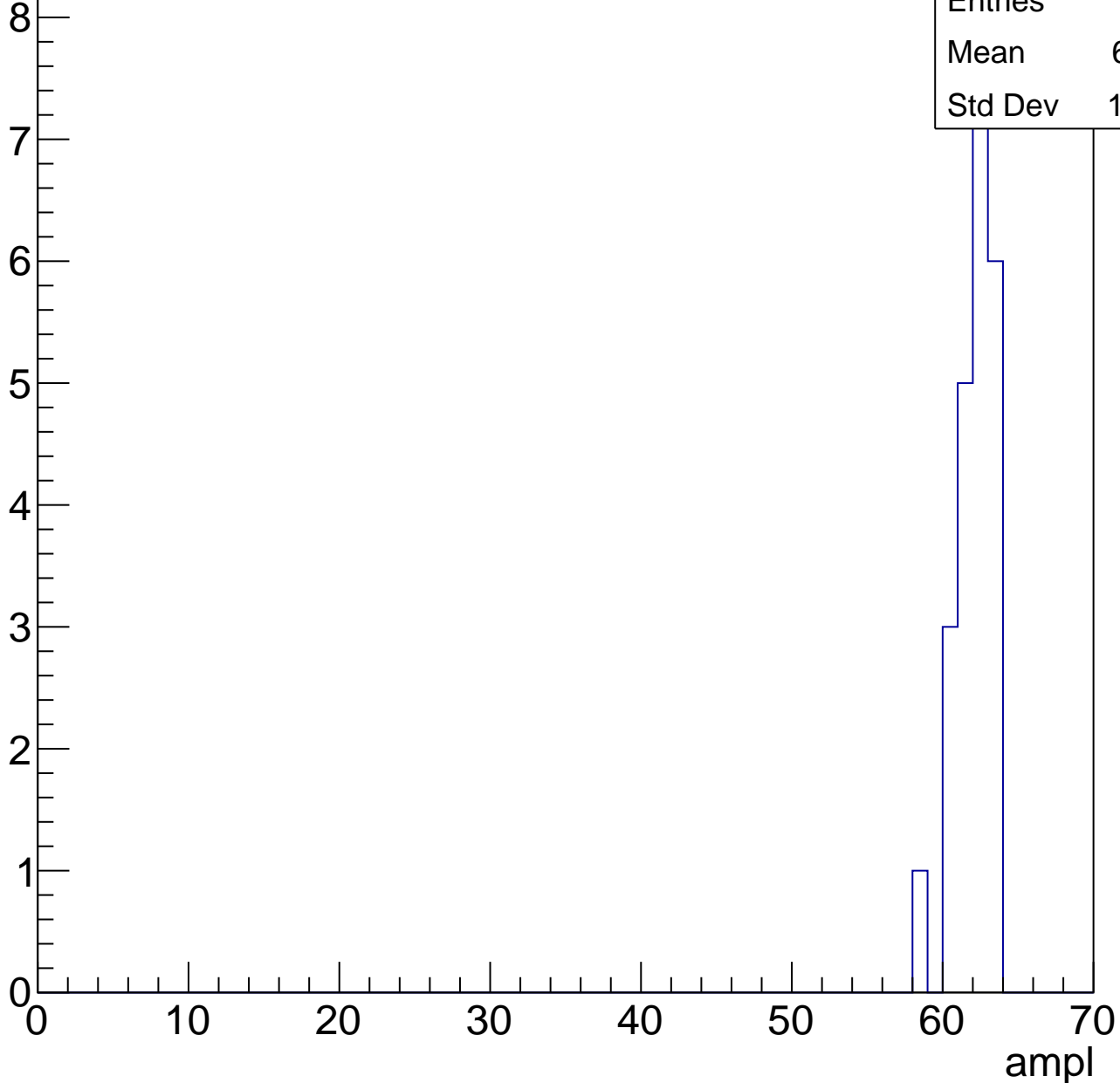


B1L103S, U10-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	61.61
Std Dev	1.242



B1L103S, U10-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.111
Std Dev	4.581

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch79, adc0

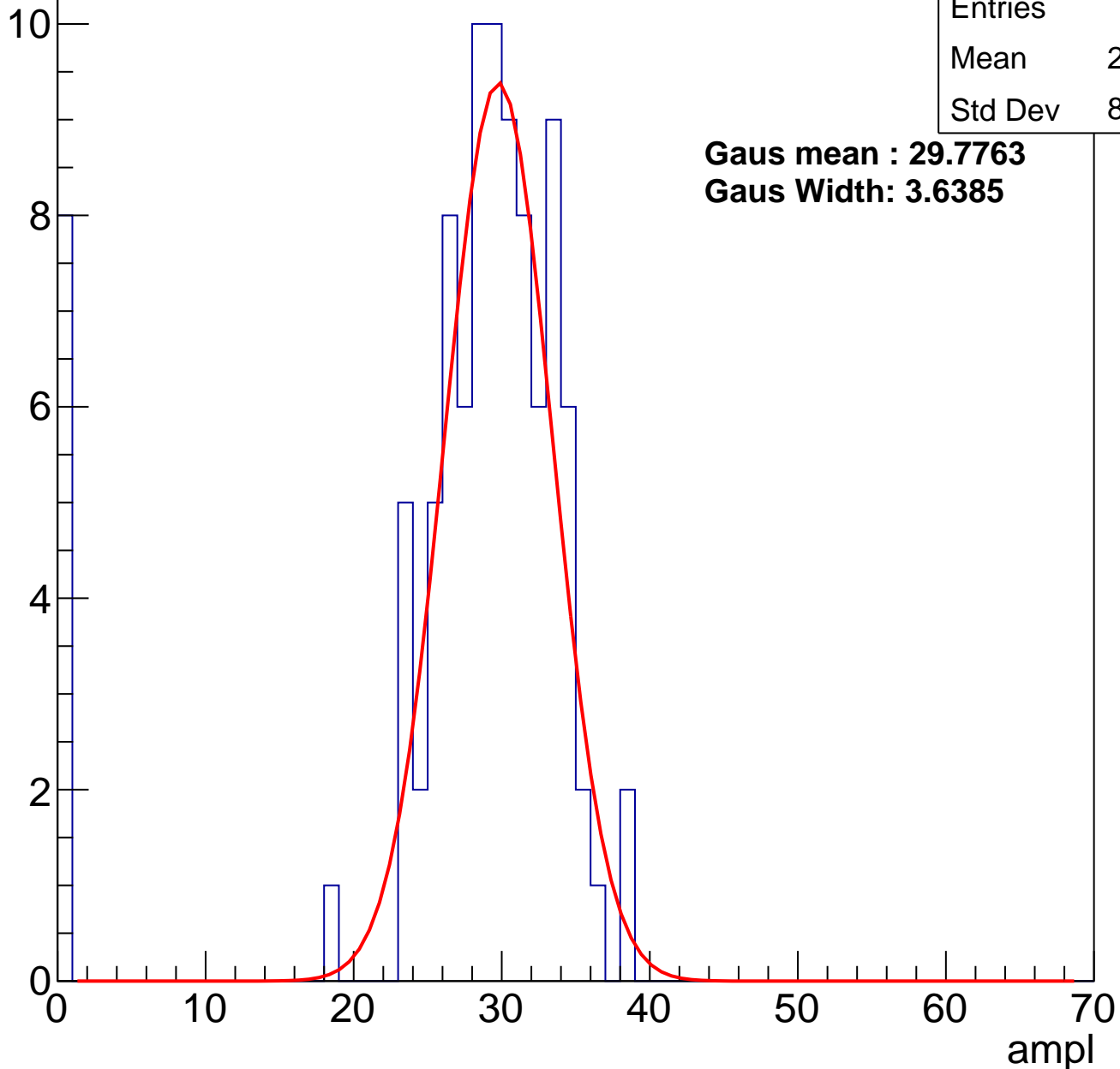
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	26.93
Std Dev	8.758

Gaus mean : 29.7763

Gaus Width: 3.6385

Entry



B1L103S, U10-ch79, adc1

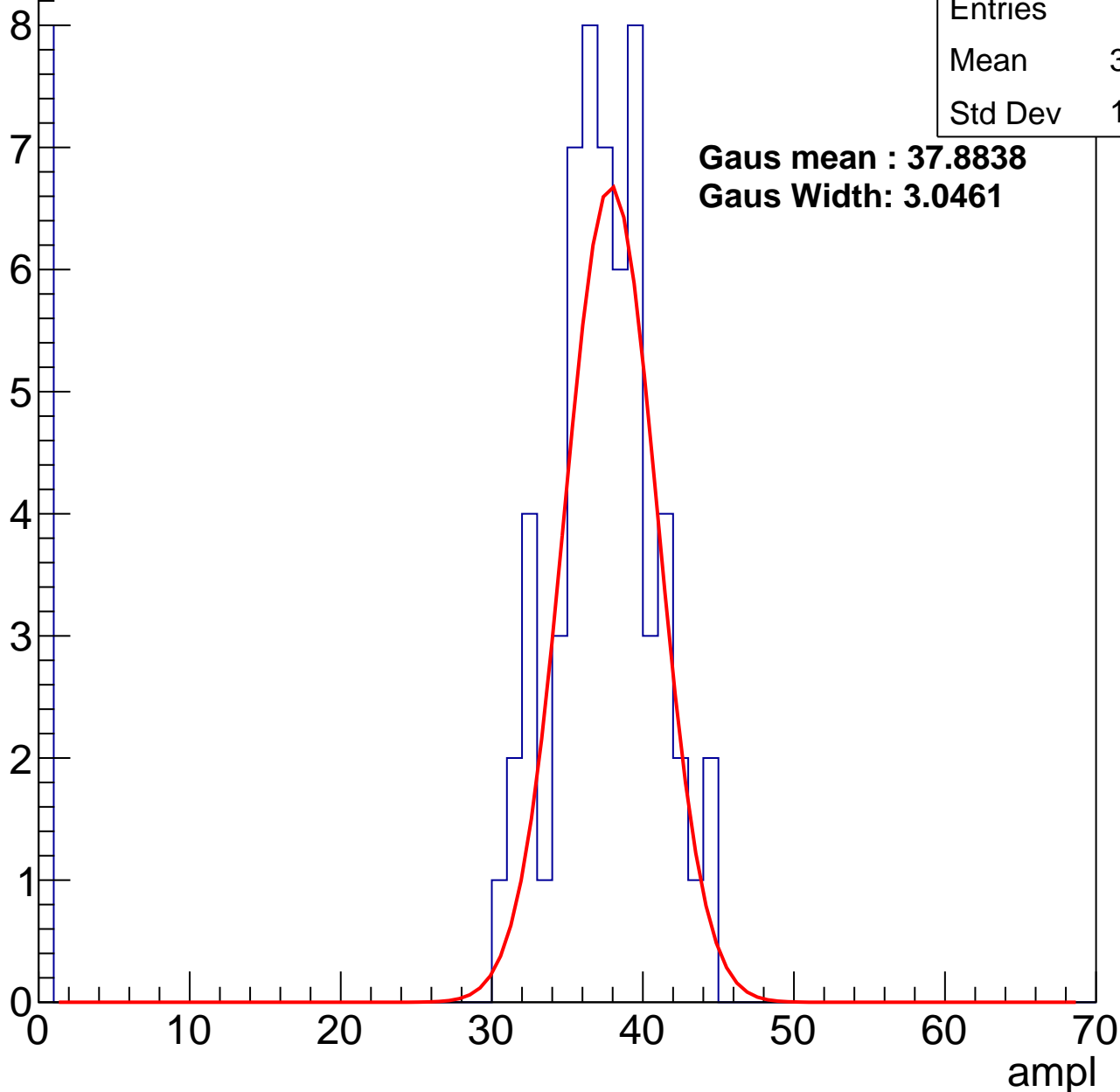
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.63
Std Dev	12.39

Gaus mean : 37.8838

Gaus Width: 3.0461



B1L103S, U10-ch79, adc2

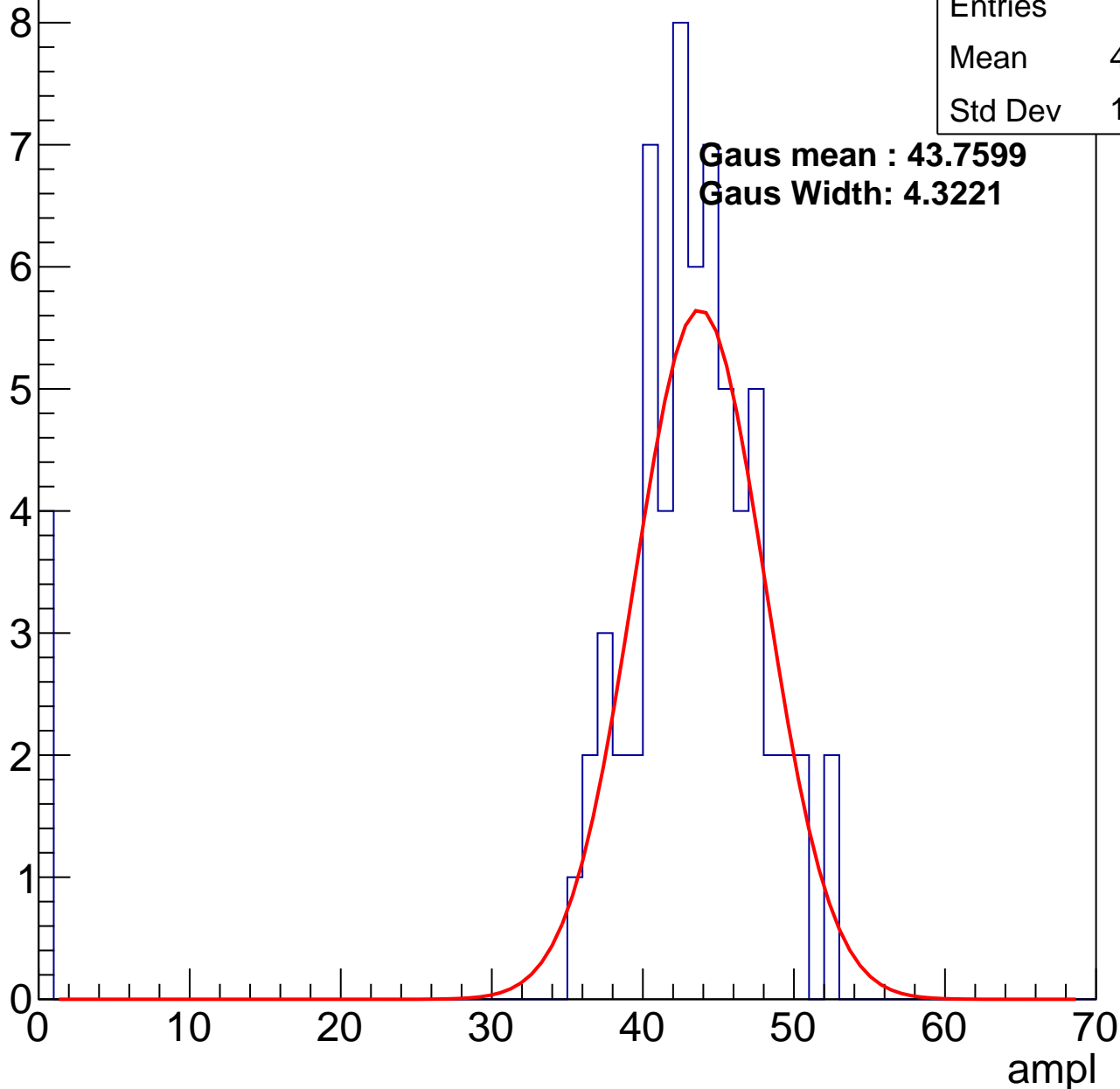
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.59
Std Dev	10.82

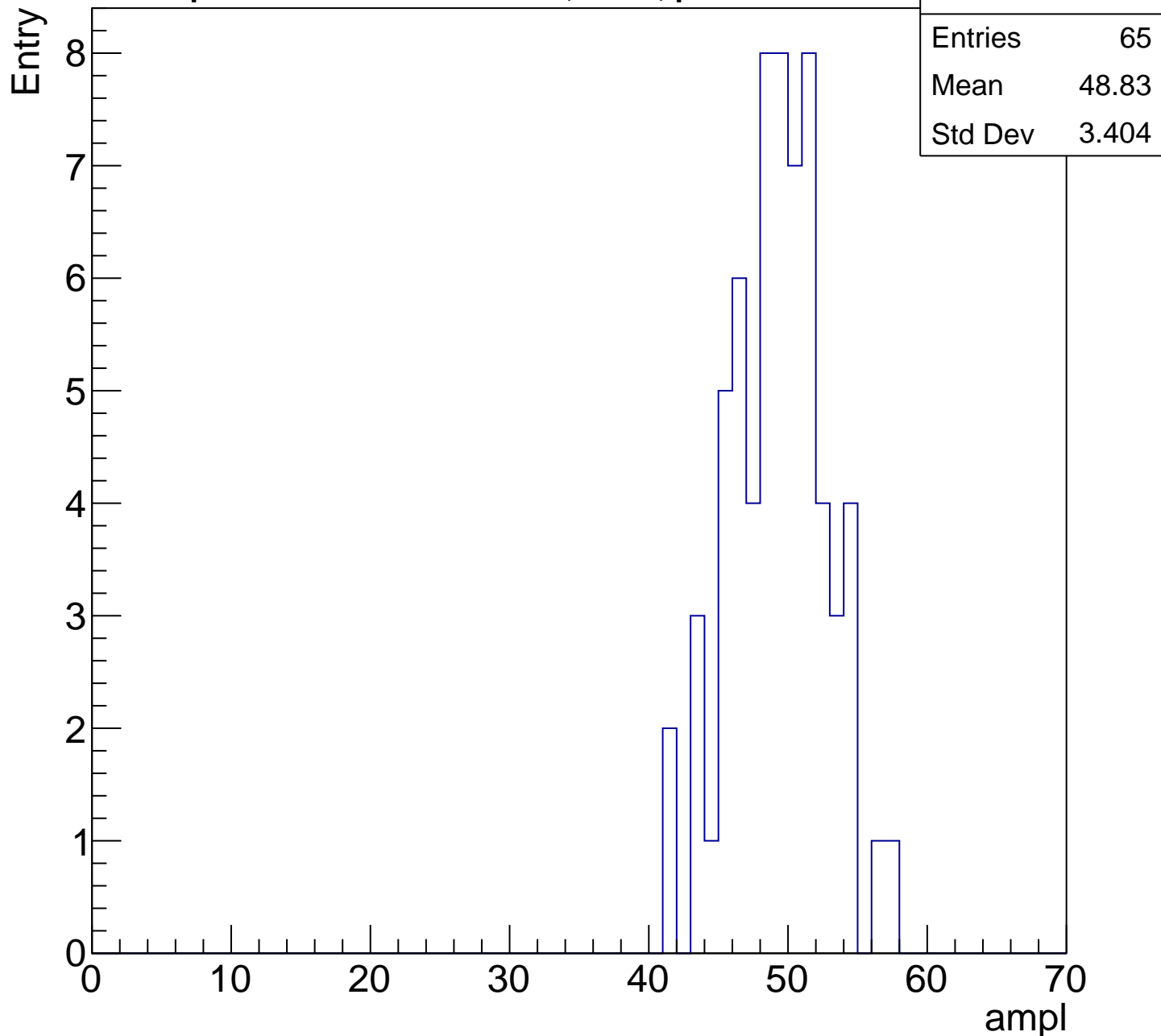
Gaus mean : 43.7599

Gaus Width: 4.3221



B1L103S, U10-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

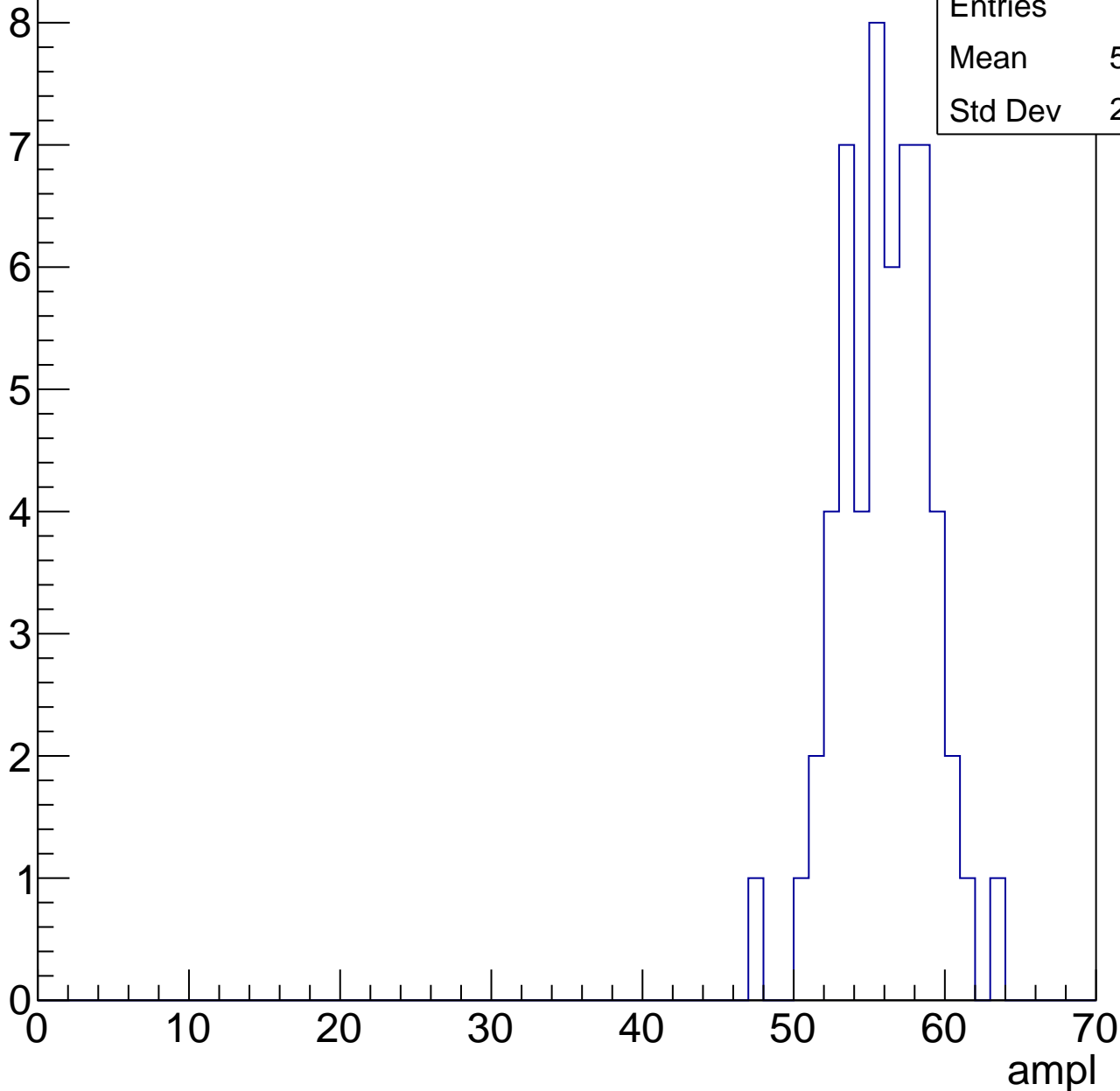


B1L103S, U10-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.55
Std Dev	2.965

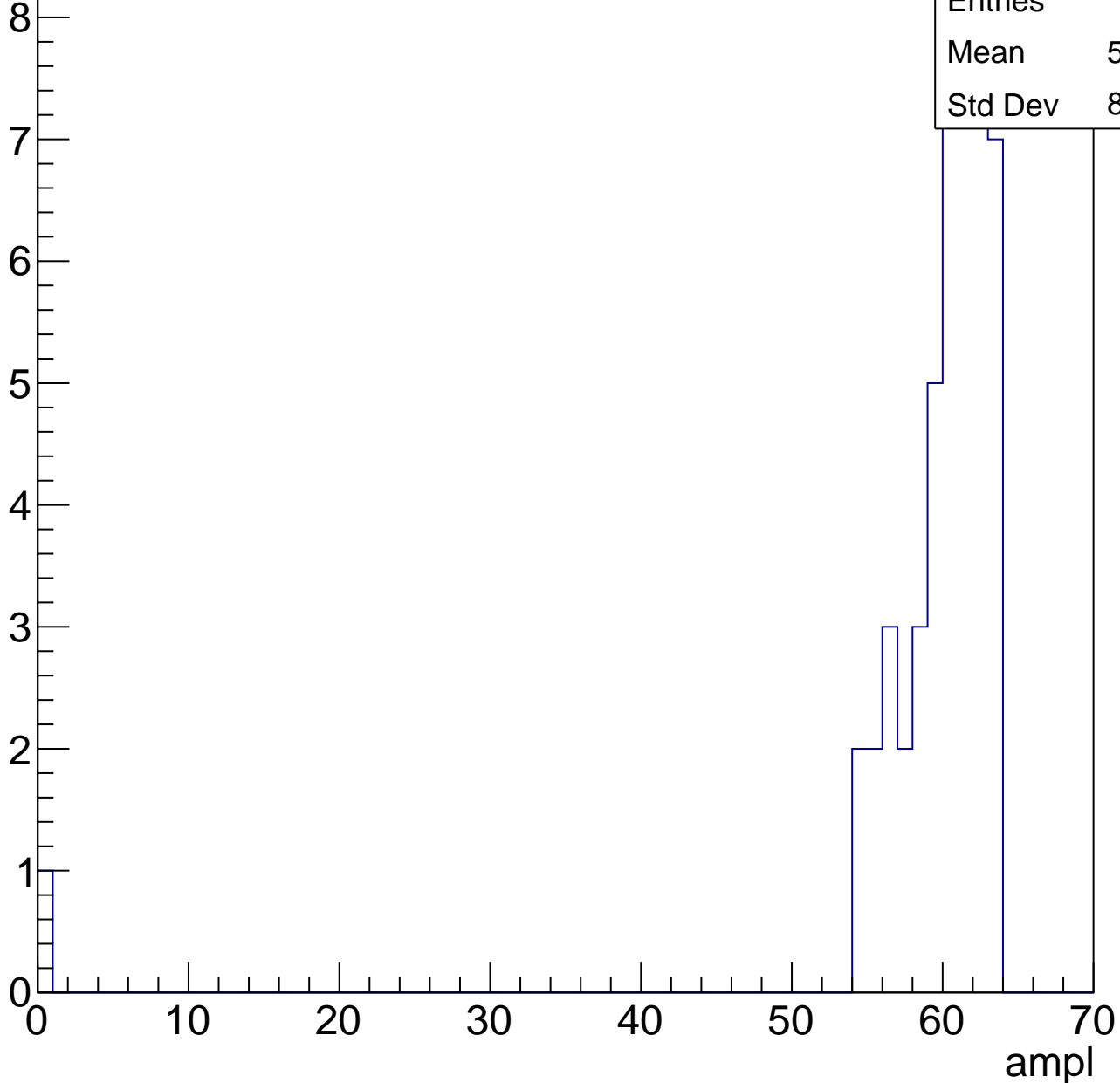


B1L103S, U10-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.65
Std Dev	8.828



B1L103S, U10-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

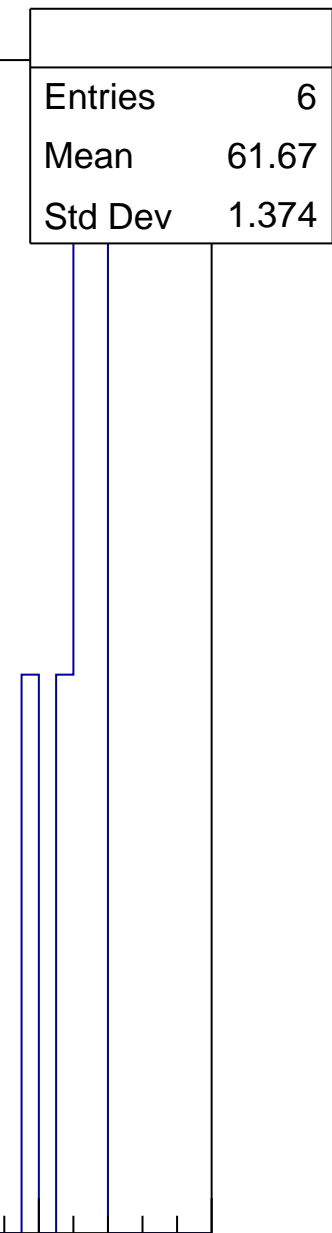
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.374

0 10 20 30 40 50 60 70

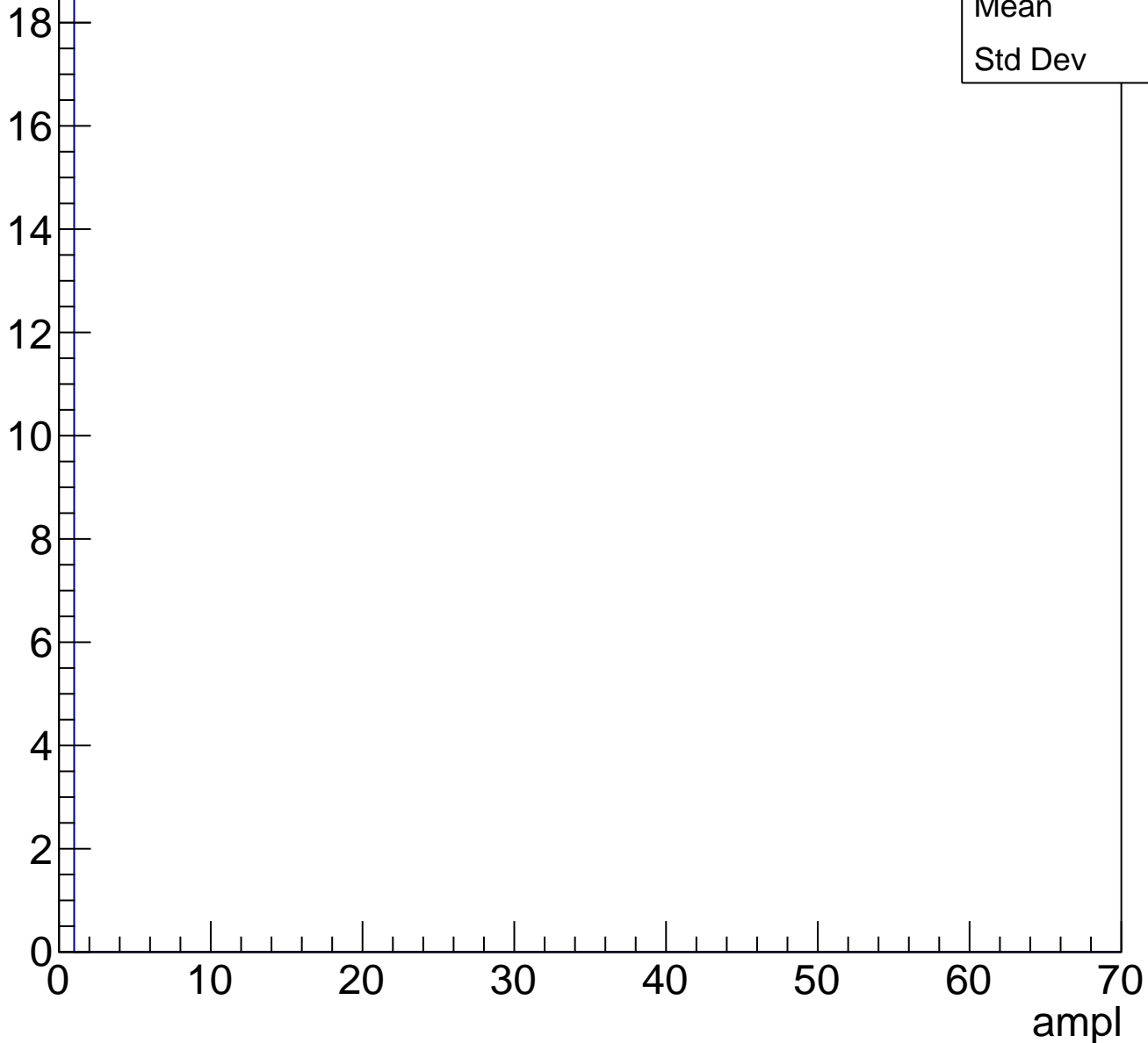
ampl



B1L103S, U10-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch80, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.8
Std Dev	12.02

Gaus mean : 30.2813

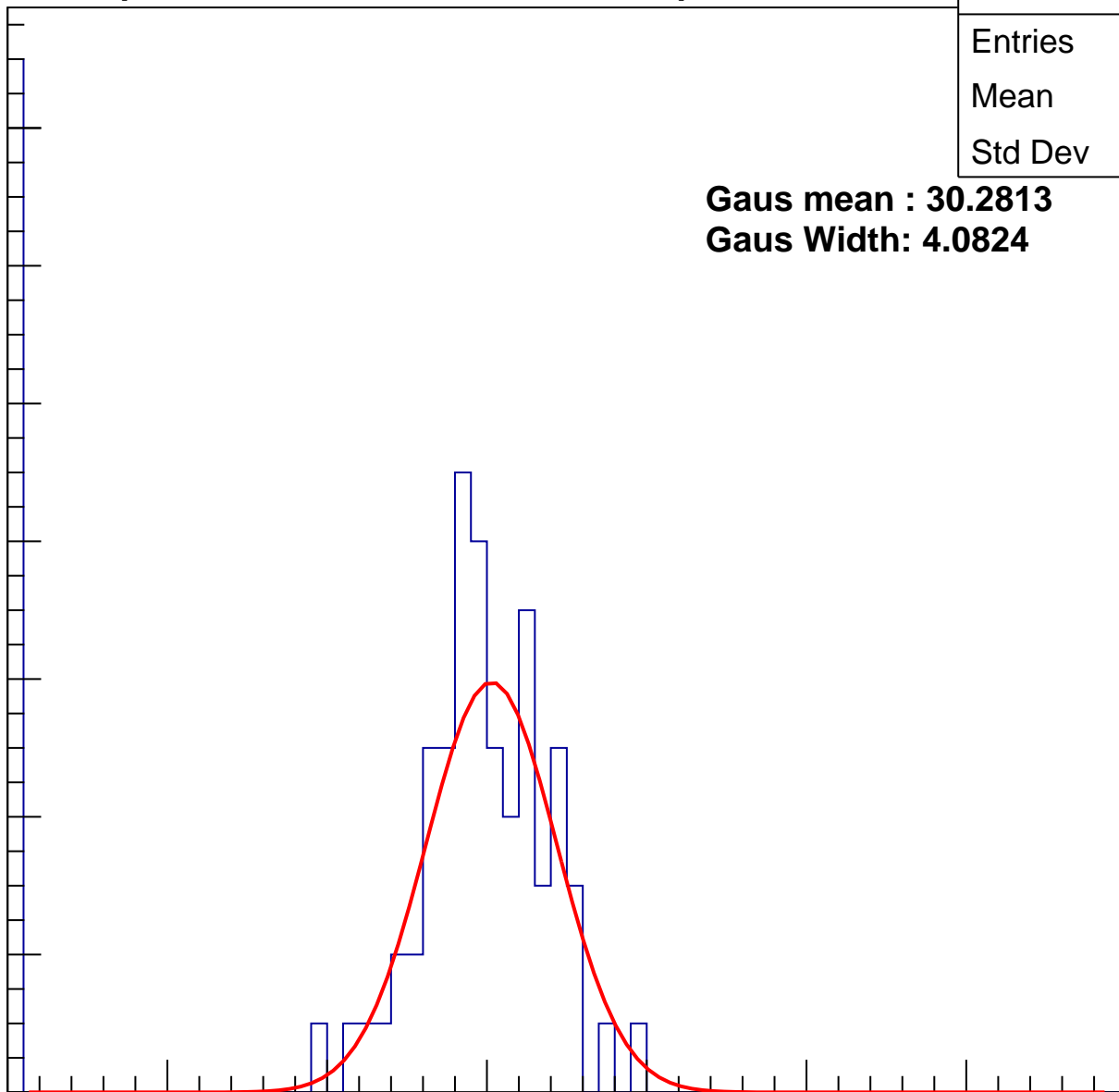
Gaus Width: 4.0824

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch80, adc1

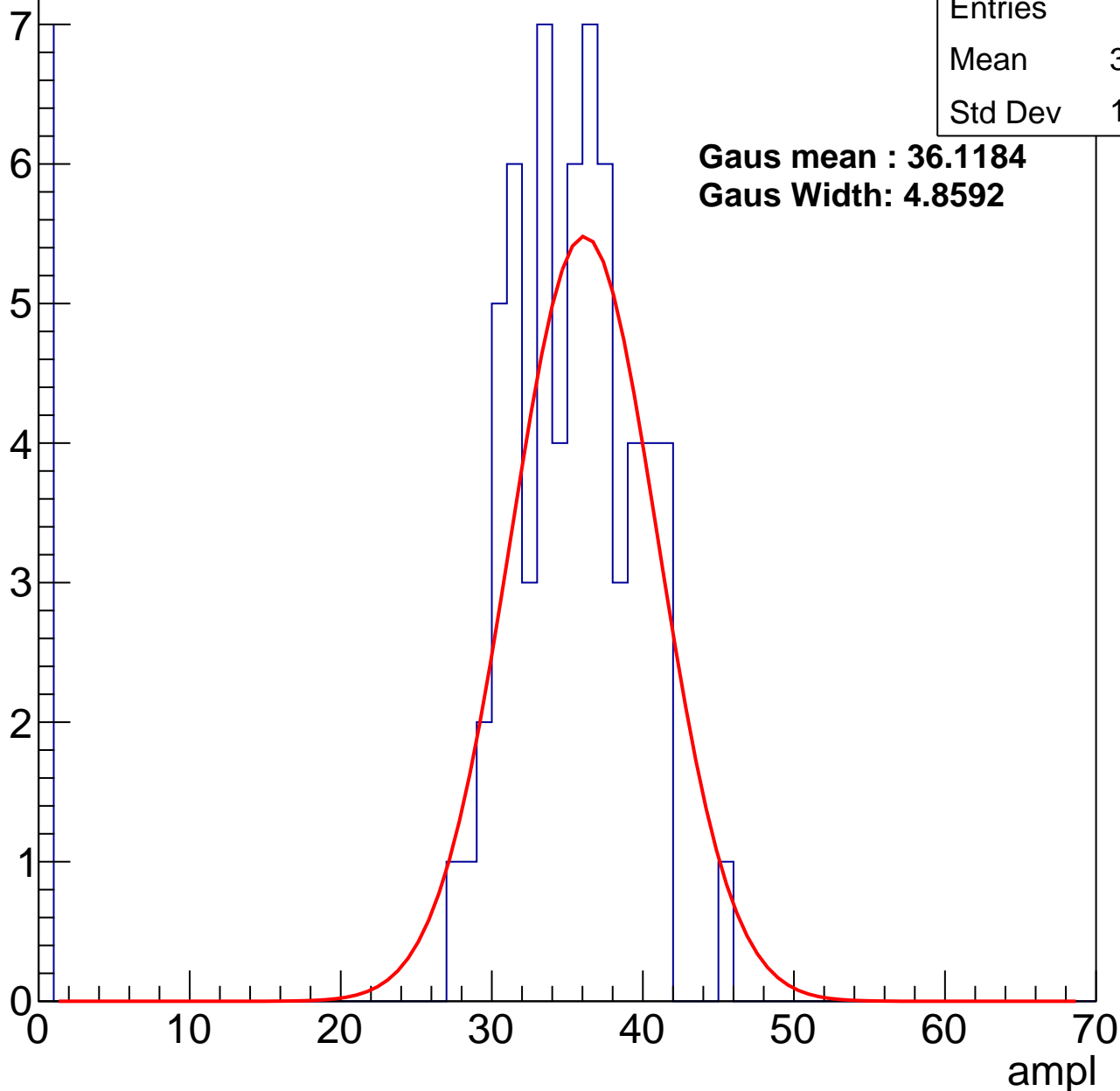
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.48
Std Dev	11.02

Gaus mean : 36.1184

Gaus Width: 4.8592



B1L103S, U10-ch80, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	39.3
Std Dev	10.69

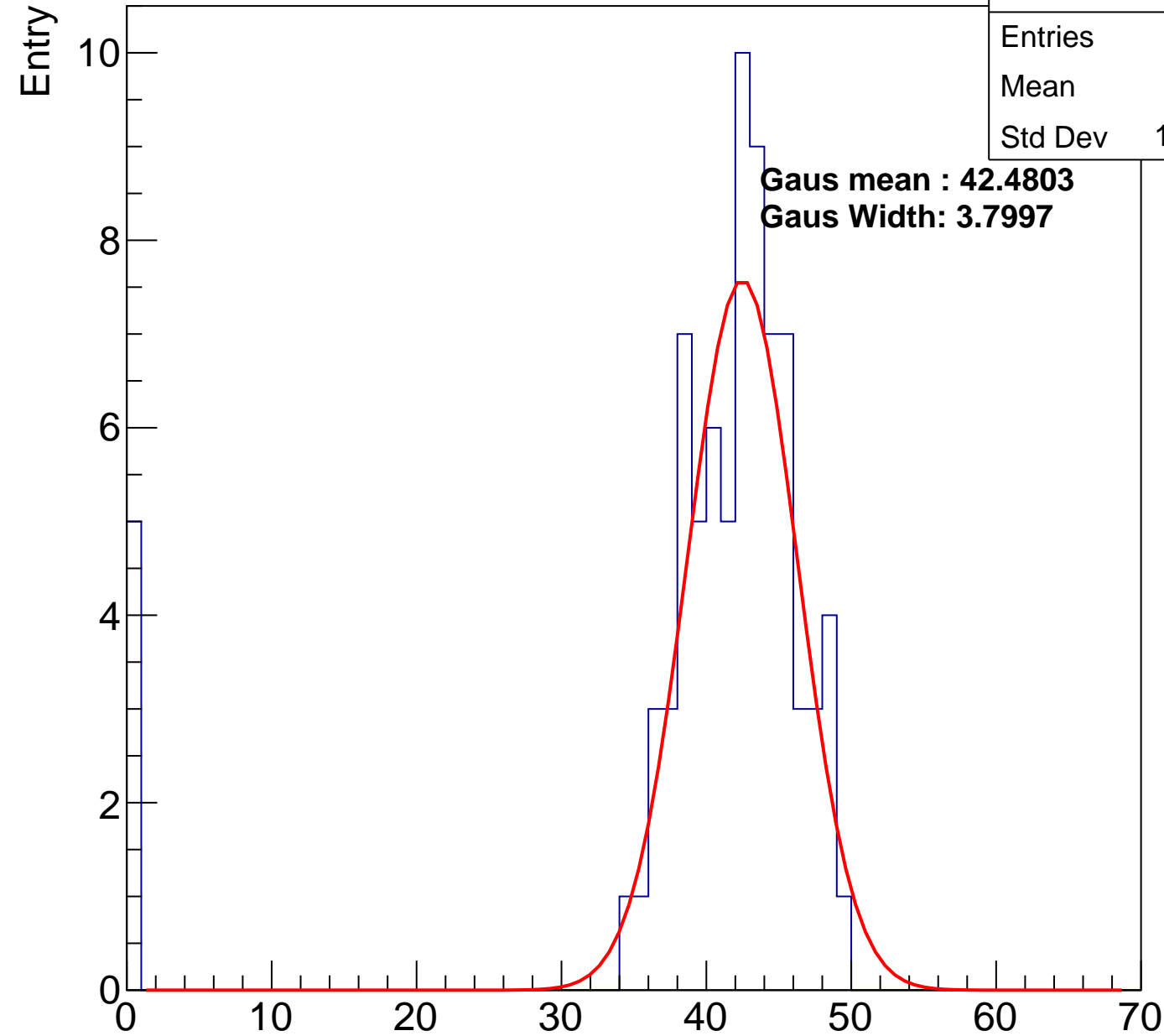
Gaus mean : 42.4803

Gaus Width: 3.7997

Entry

10
8
6
4
2
0

ampl

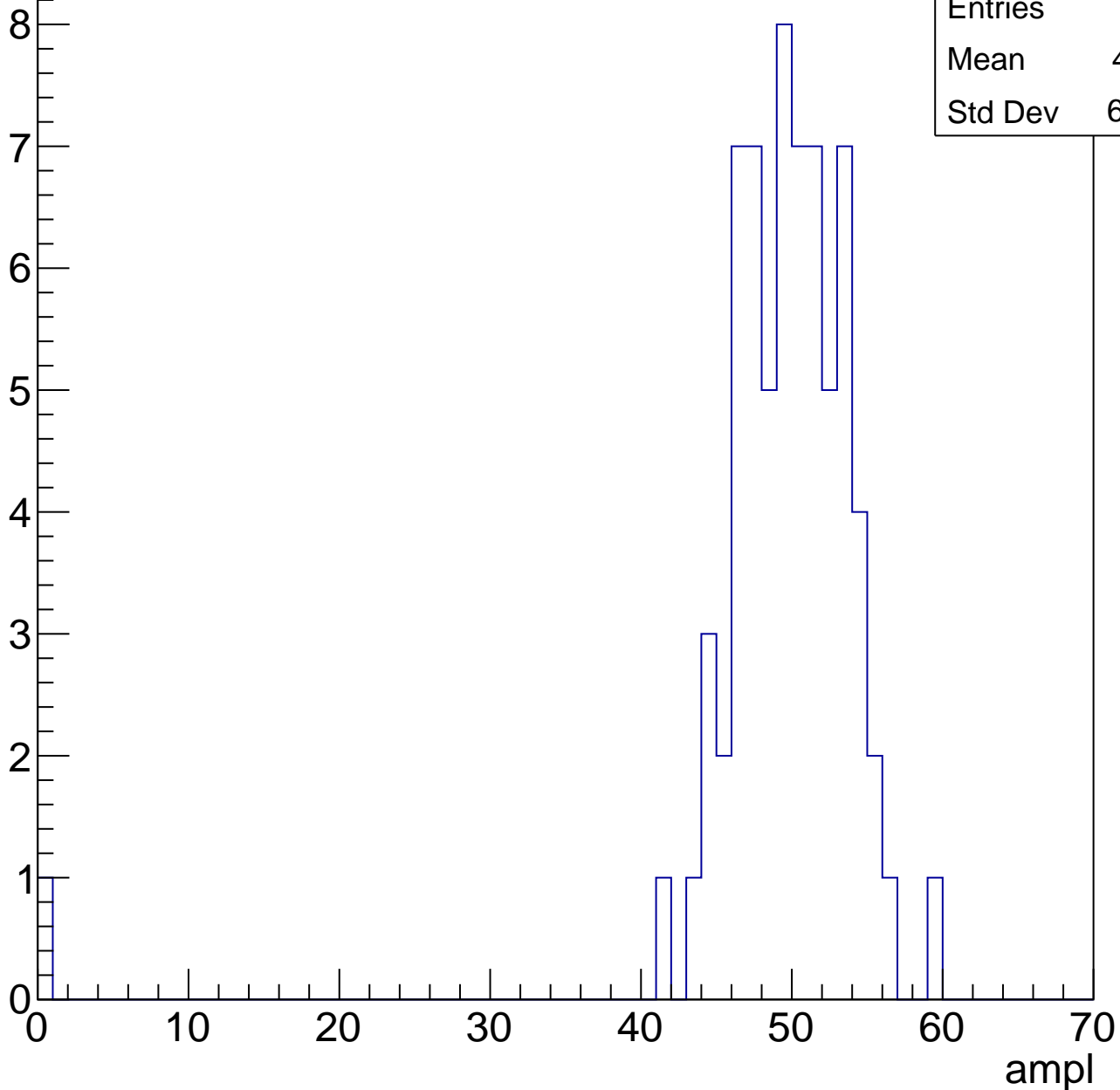


B1L103S, U10-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.81
Std Dev	6.826

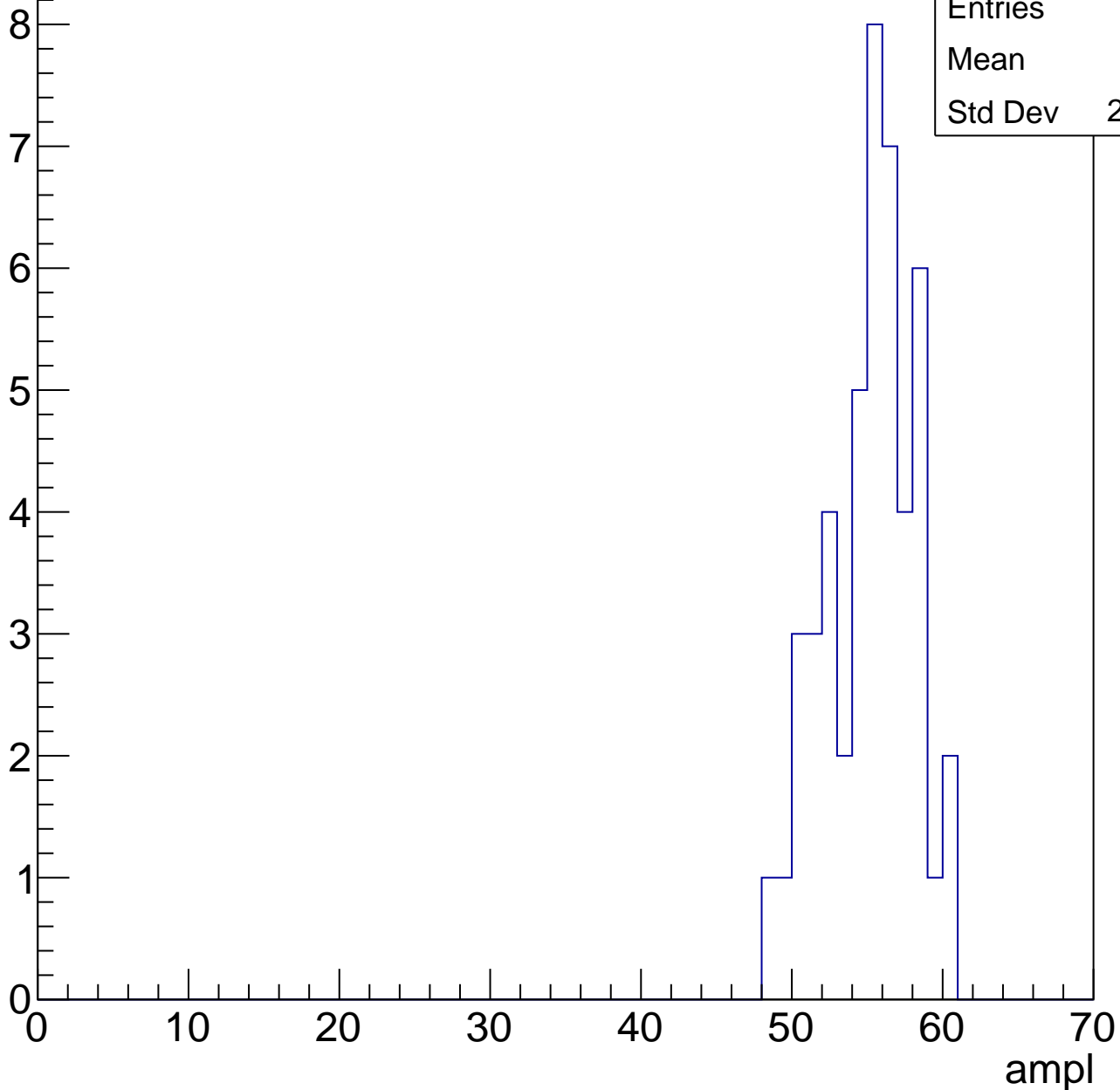


B1L103S, U10-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	54.7
Std Dev	2.902

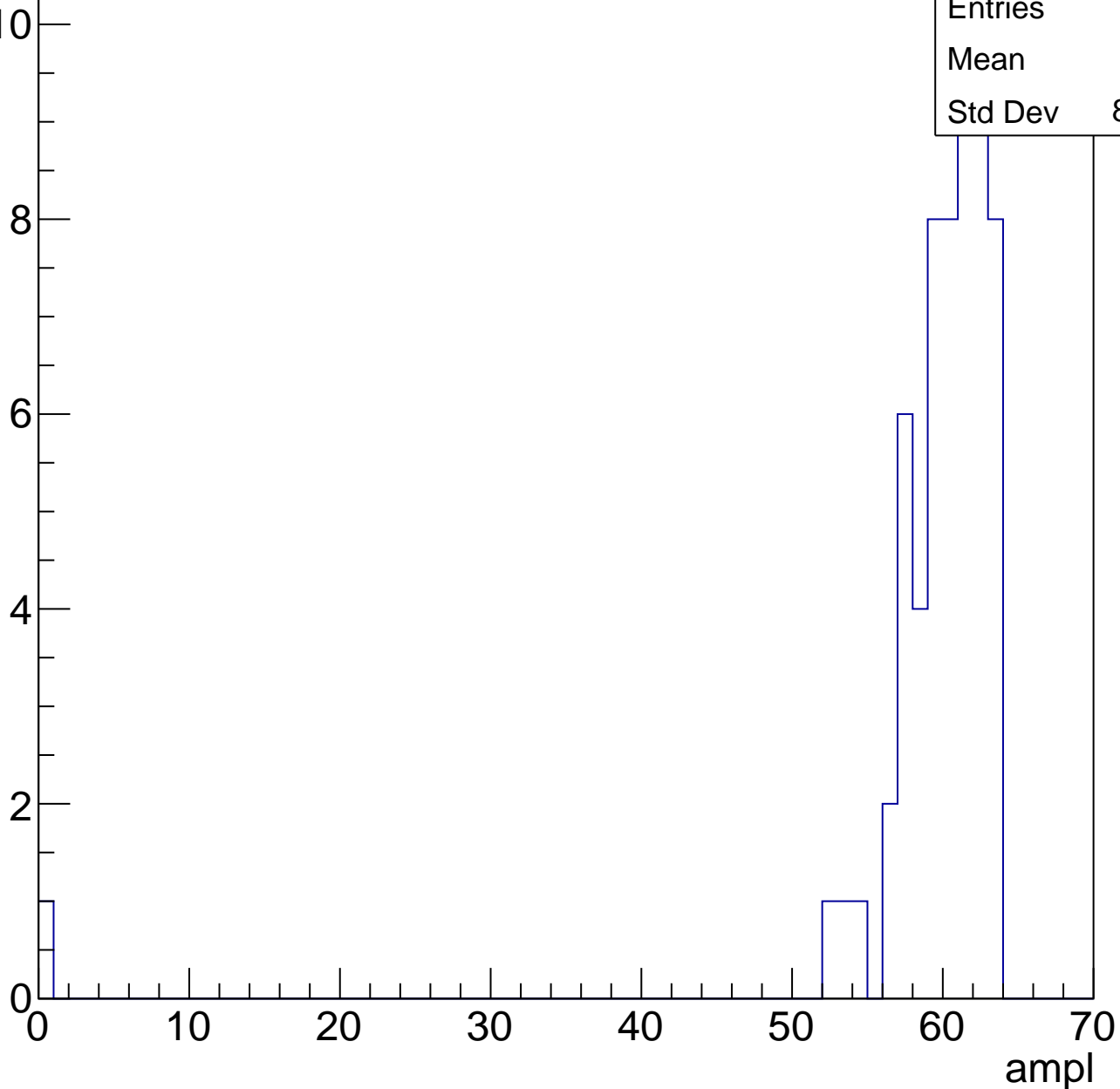


B1L103S, U10-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	58.8
Std Dev	8.121



B1L103S, U10-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch81, adc0

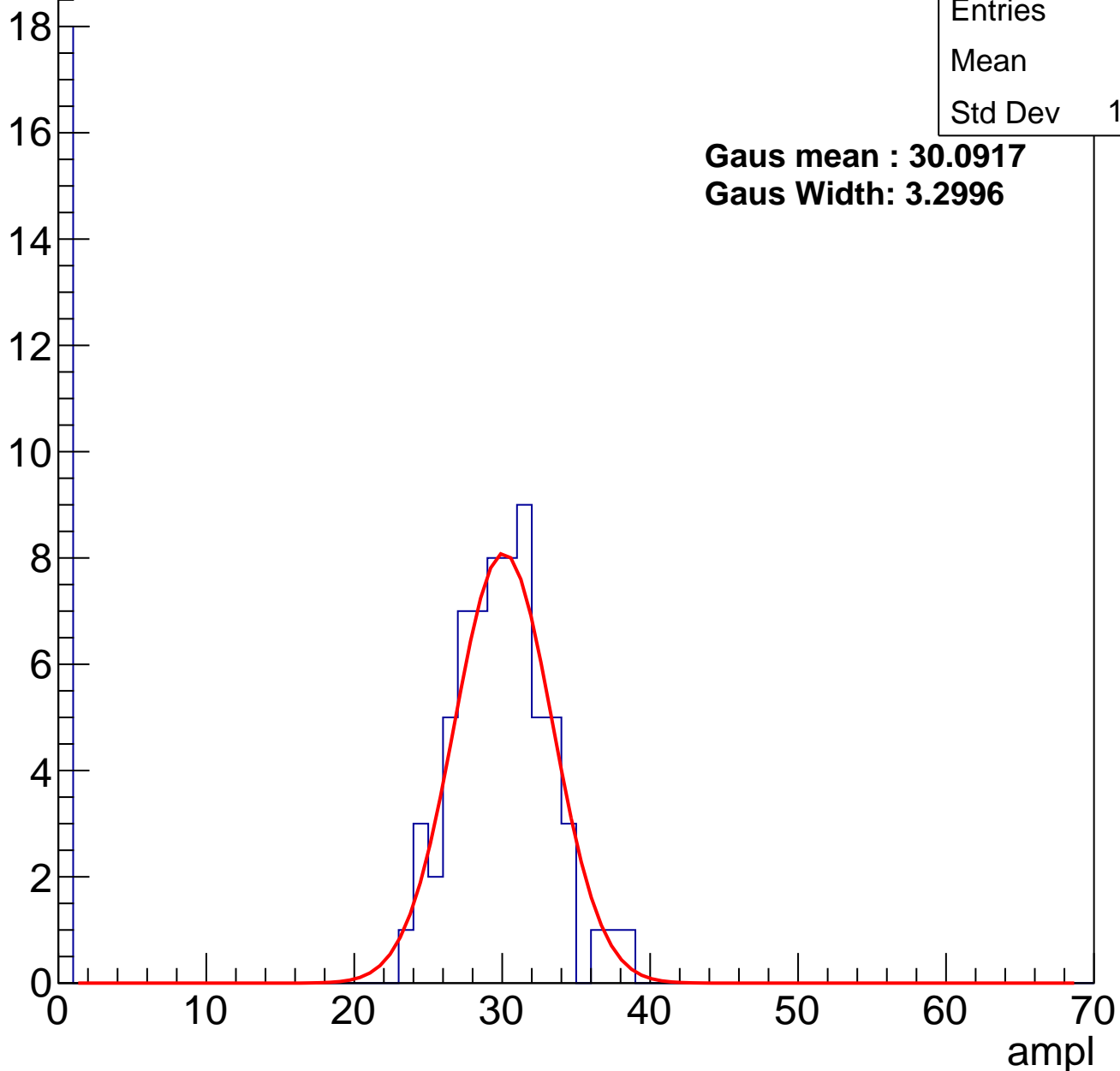
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.2
Std Dev	12.43

Gaus mean : 30.0917

Gaus Width: 3.2996

Entry



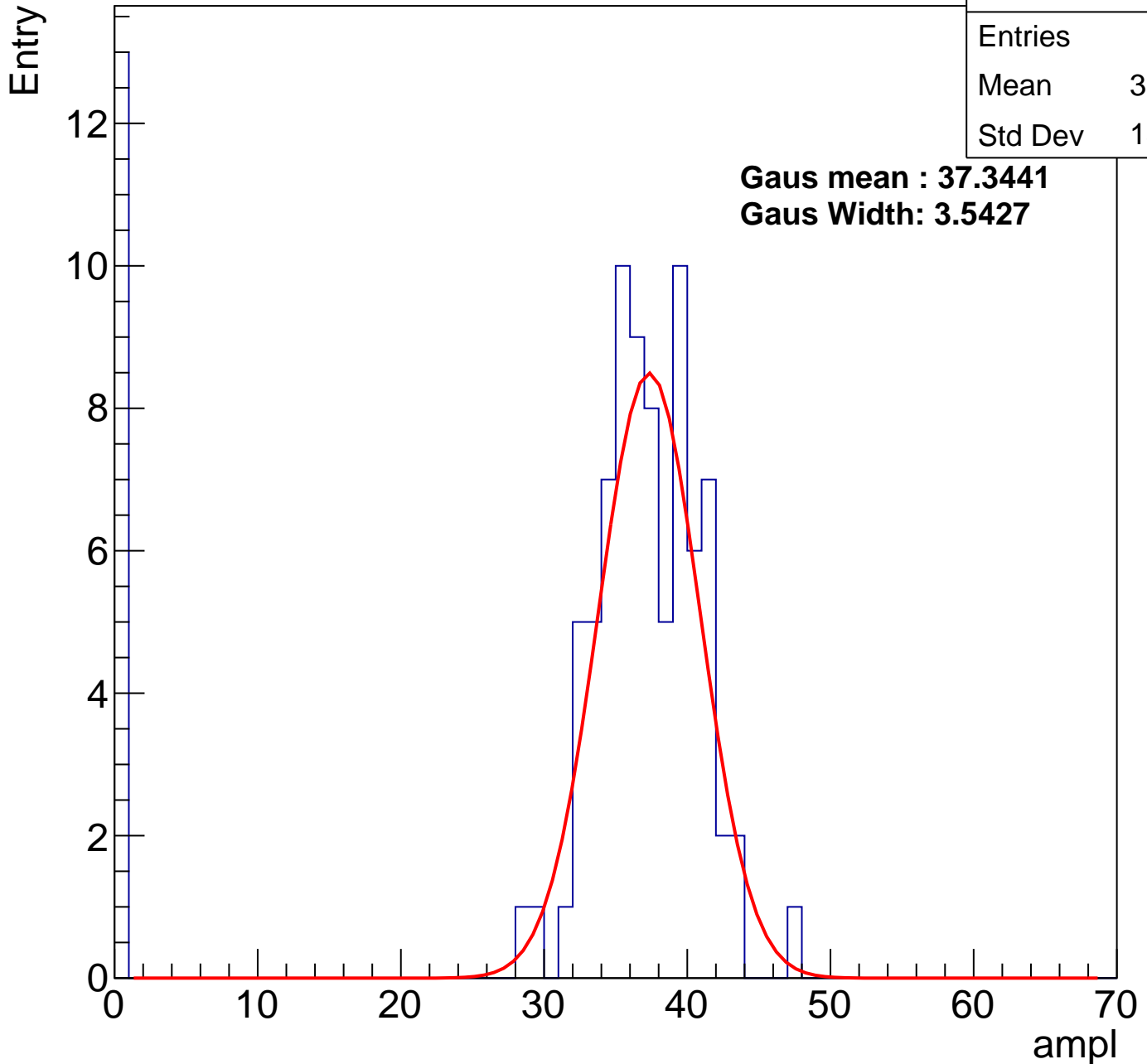
B1L103S, U10-ch81, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	31.67
Std Dev	13.15

Gaus mean : 37.3441

Gaus Width: 3.5427



B1L103S, U10-ch81, adc2

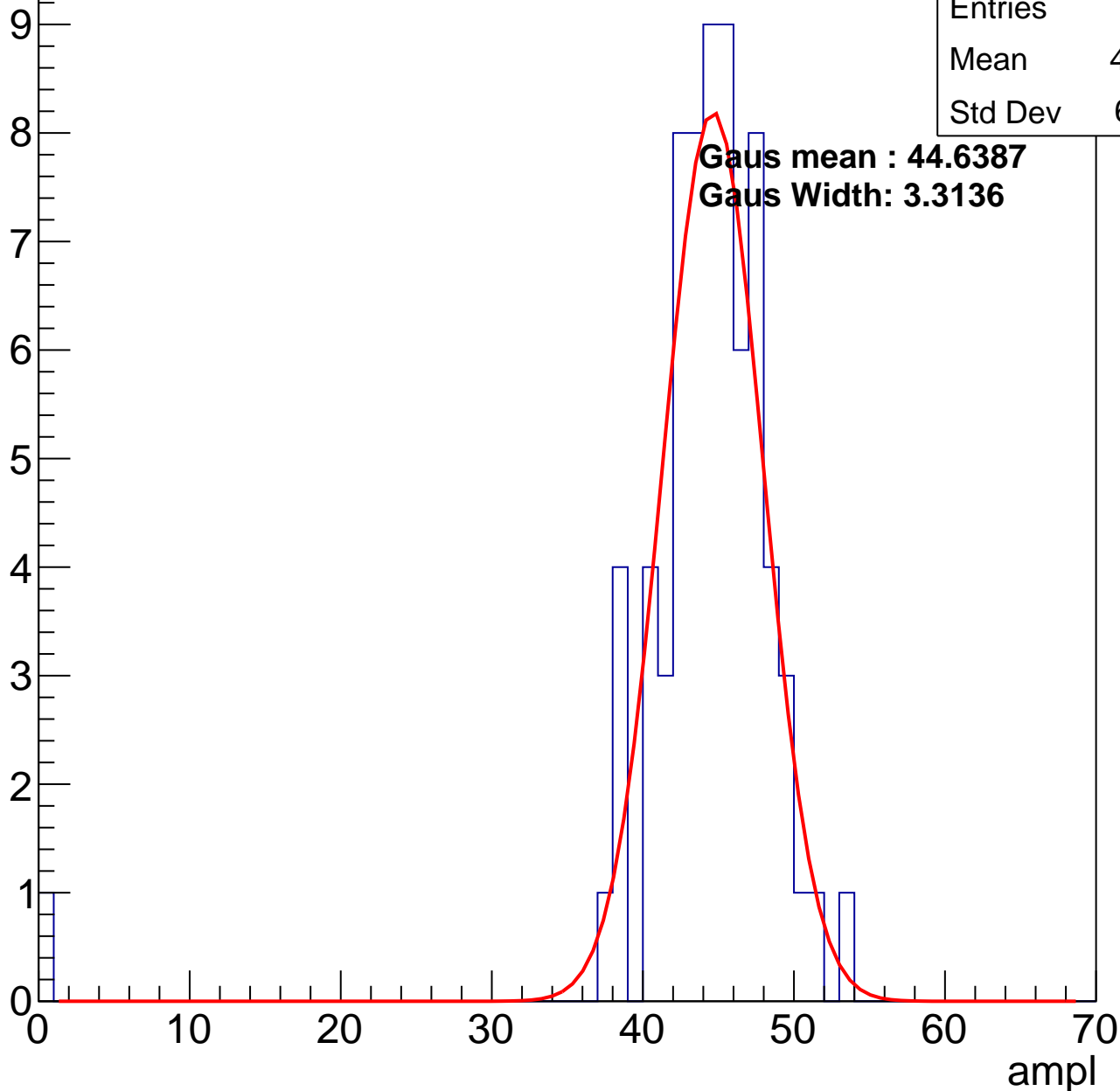
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.63
Std Dev	6.121

Gaus mean : 44.6387

Gaus Width: 3.3136

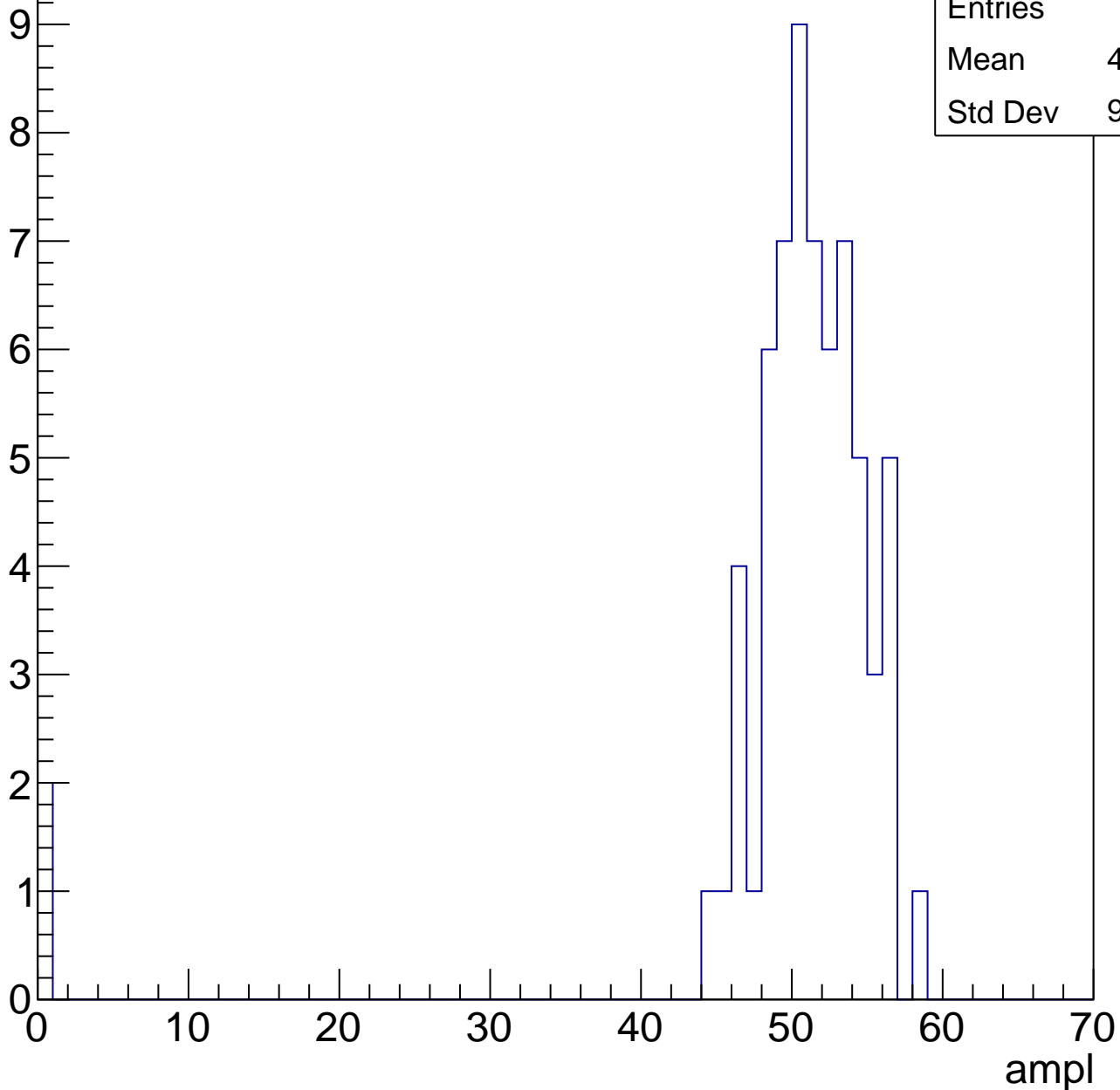


B1L103S, U10-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	49.45
Std Dev	9.313

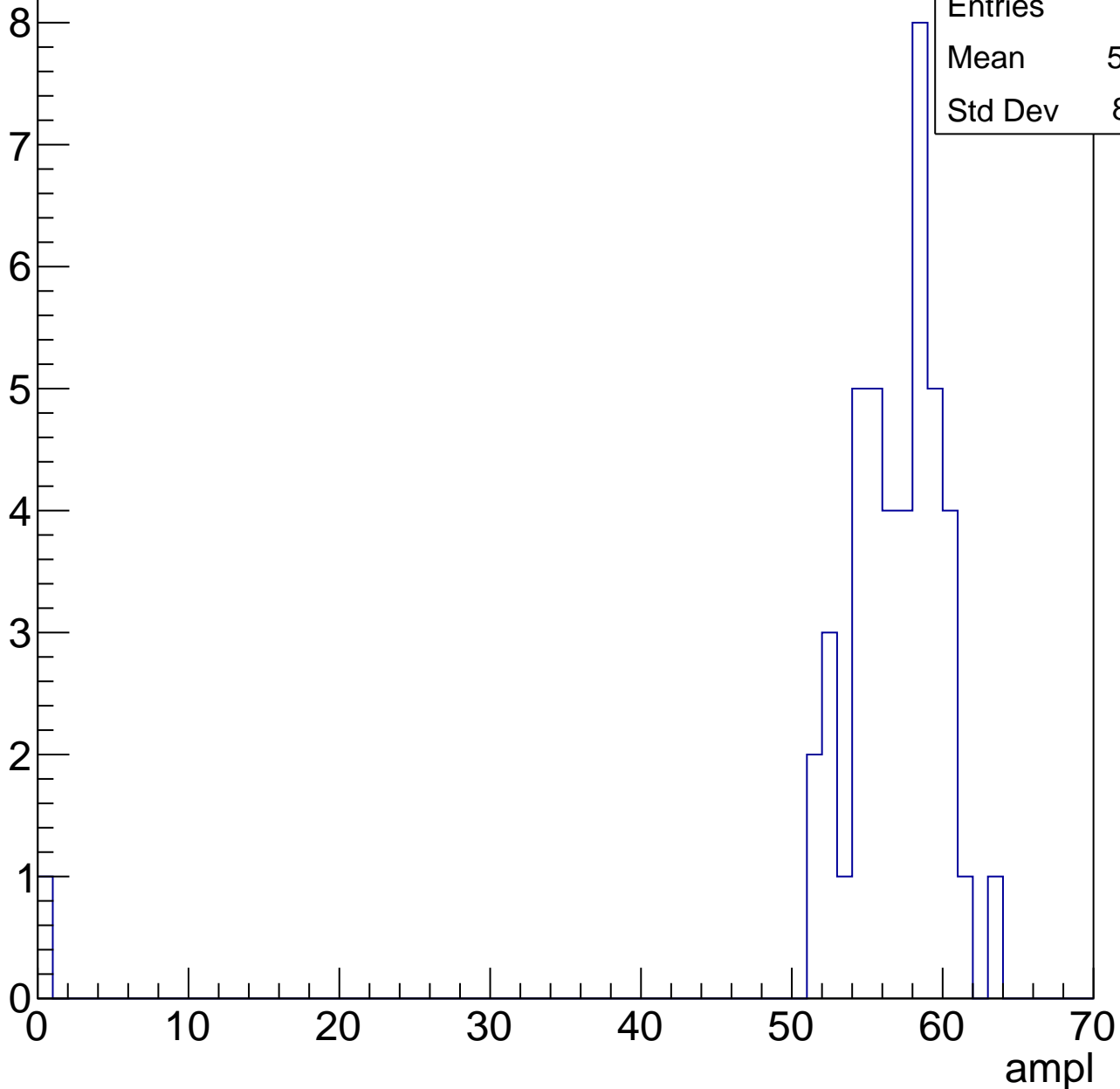


B1L103S, U10-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

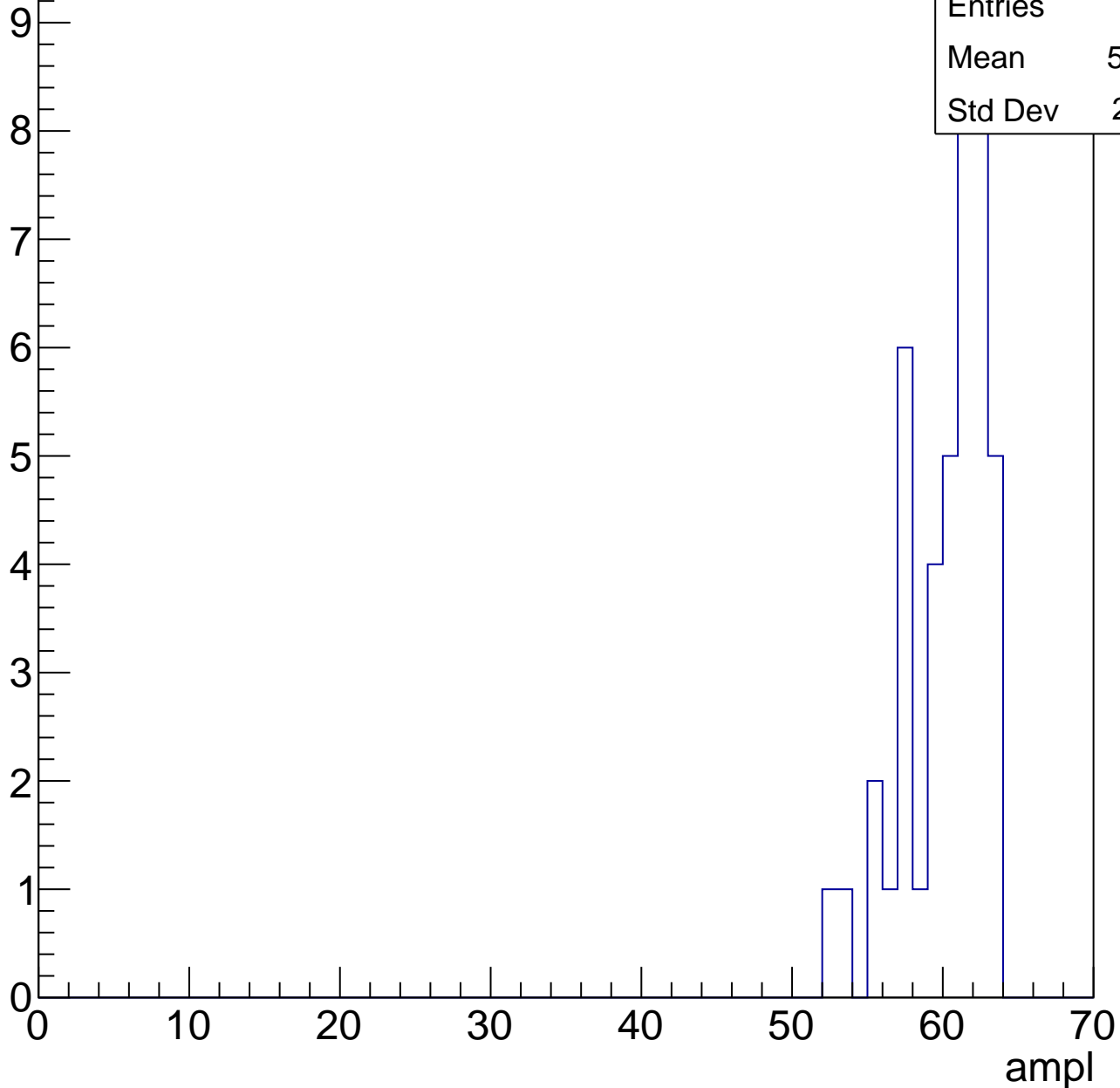
Entries	44
Mean	55.25
Std Dev	8.871



B1L103S, U10-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

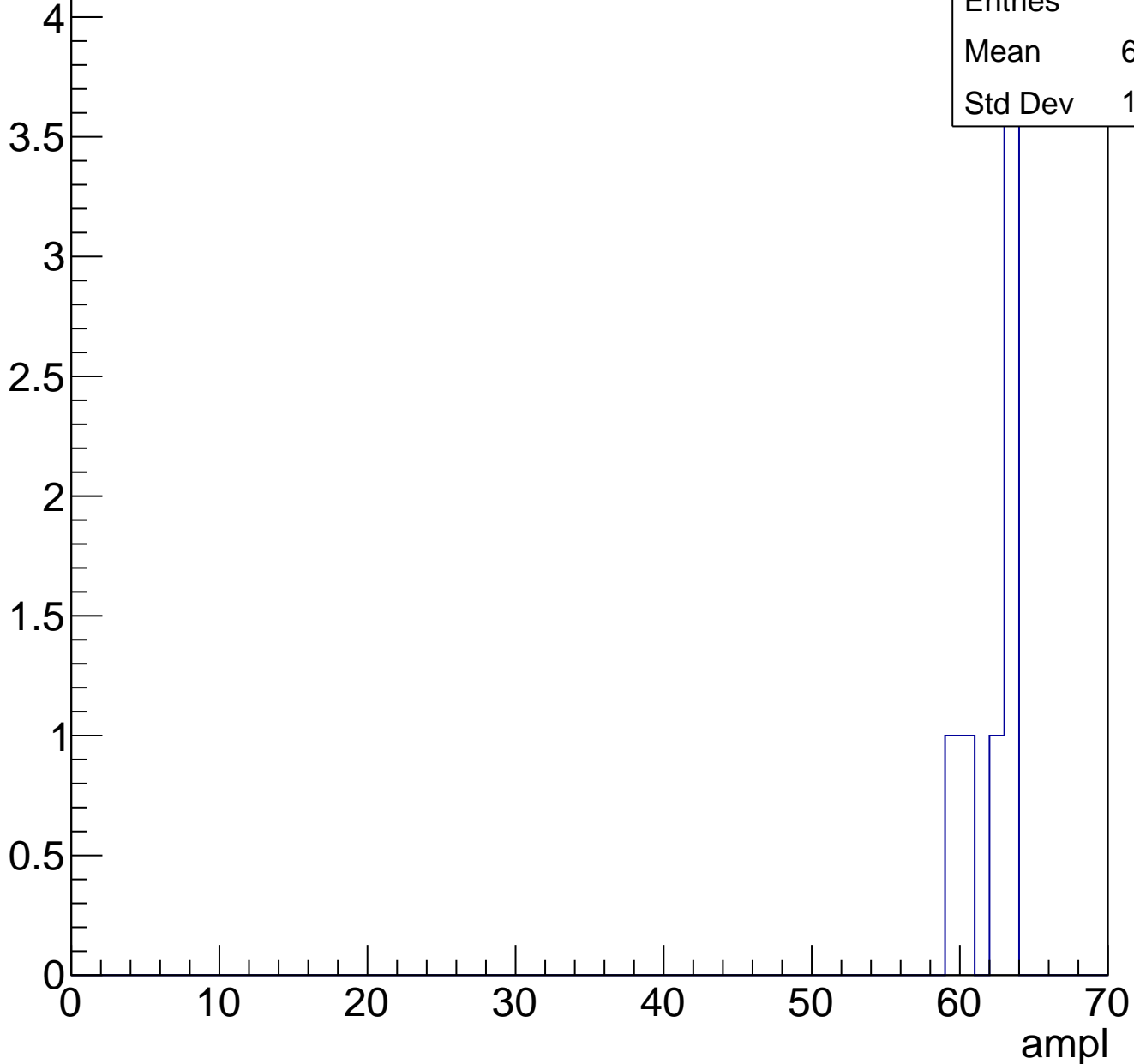
Entry



B1L103S, U10-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	155
Mean	14.82
Std Dev	13.2

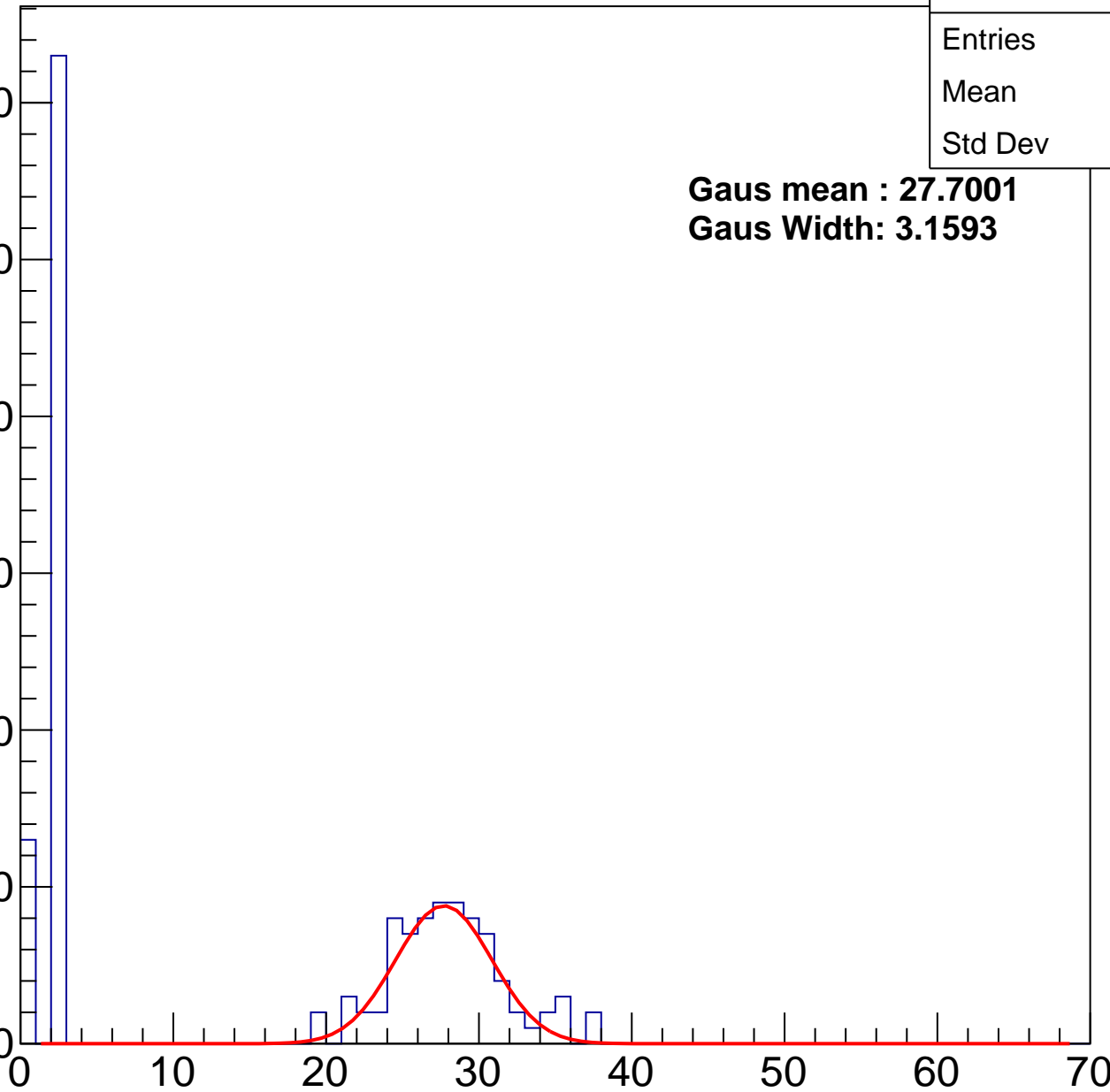
Gaus mean : 27.7001

Gaus Width: 3.1593

Entry

60
50
40
30
20
10
0

ampl



B1L103S, U10-ch82, adc1

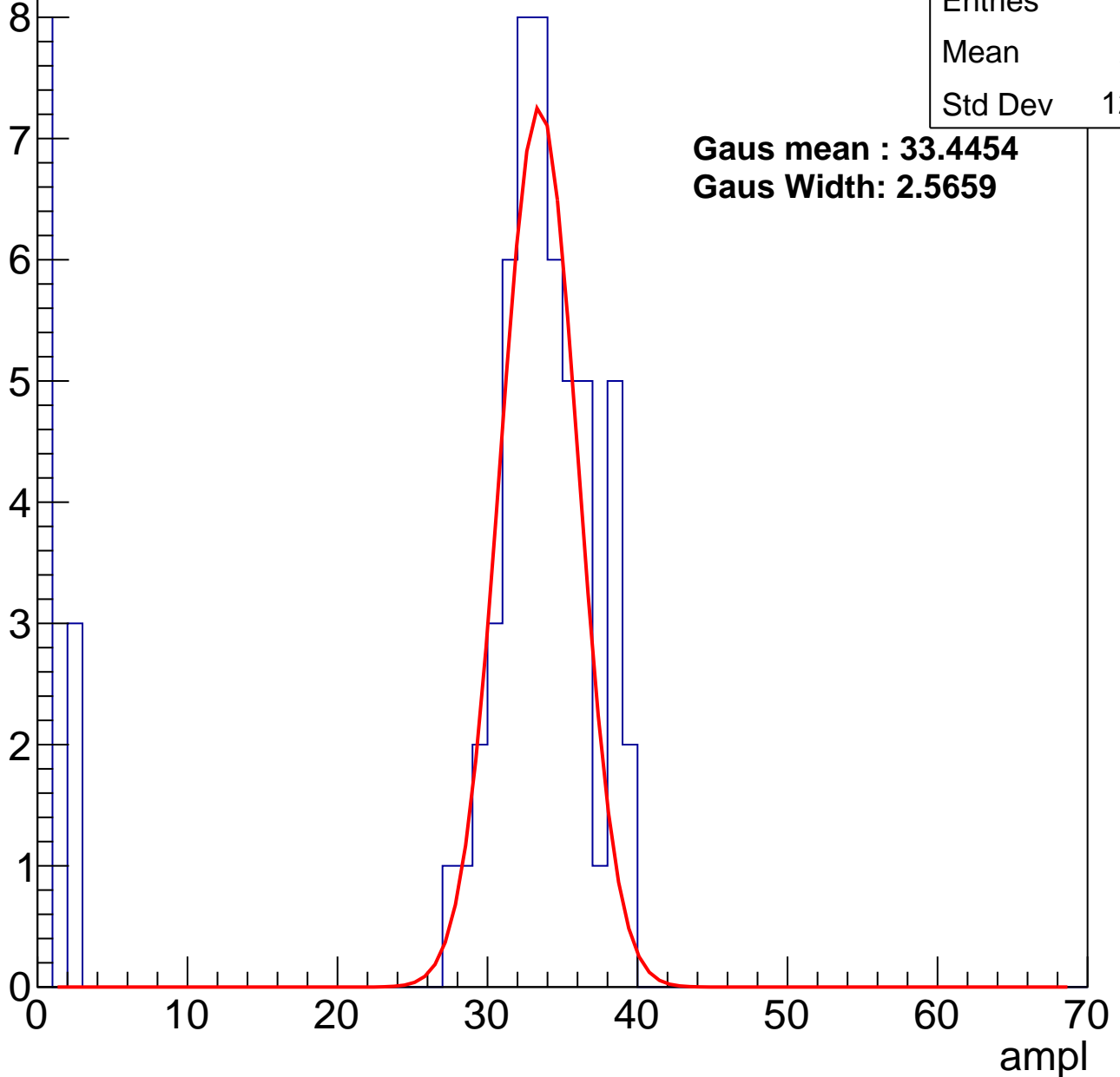
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	27.8
Std Dev	12.69

Gaus mean : 33.4454

Gaus Width: 2.5659



B1L103S, U10-ch82, adc2

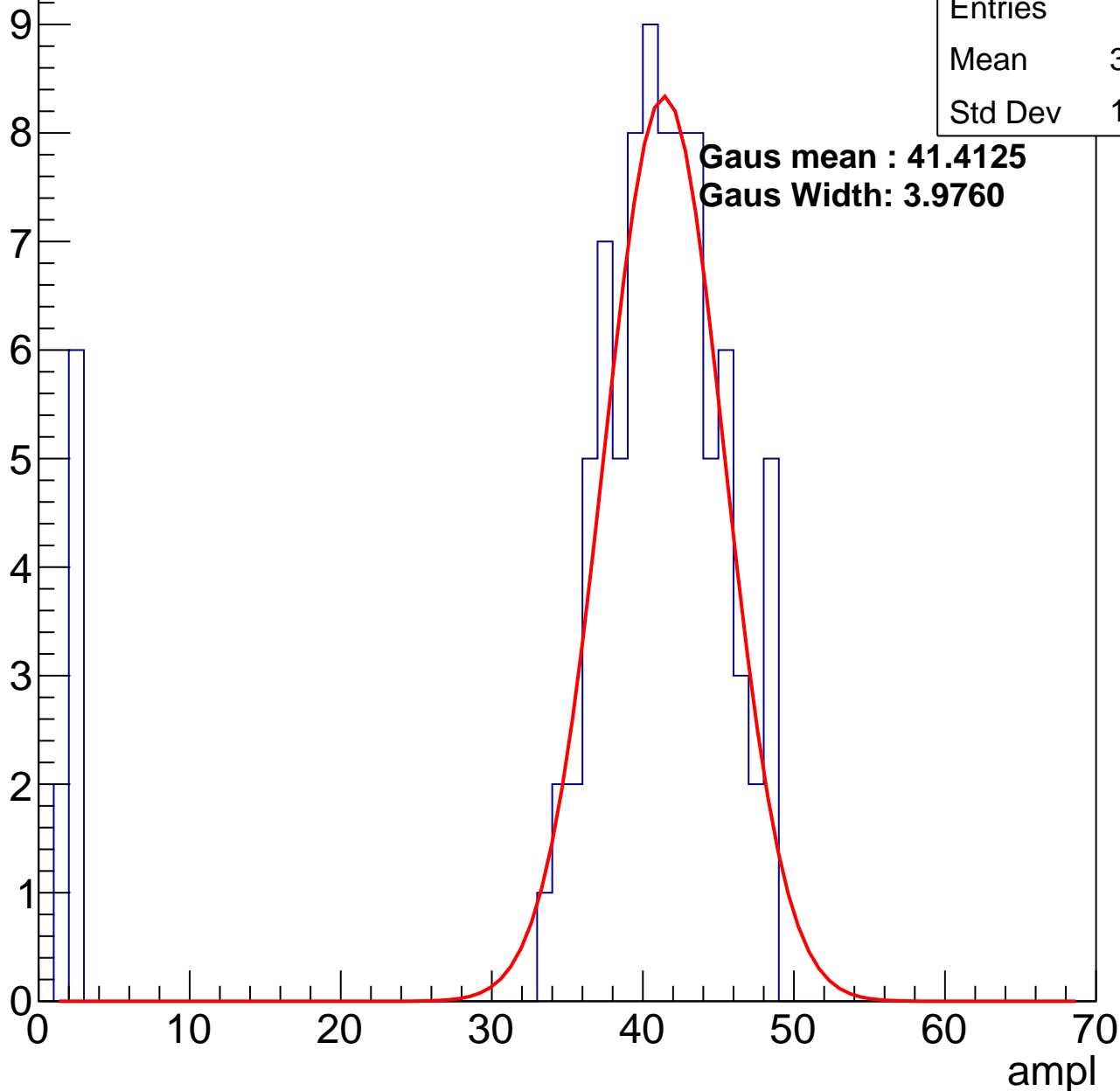
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	92
Mean	37.54
Std Dev	11.67

Gaus mean : 41.4125

Gaus Width: 3.9760

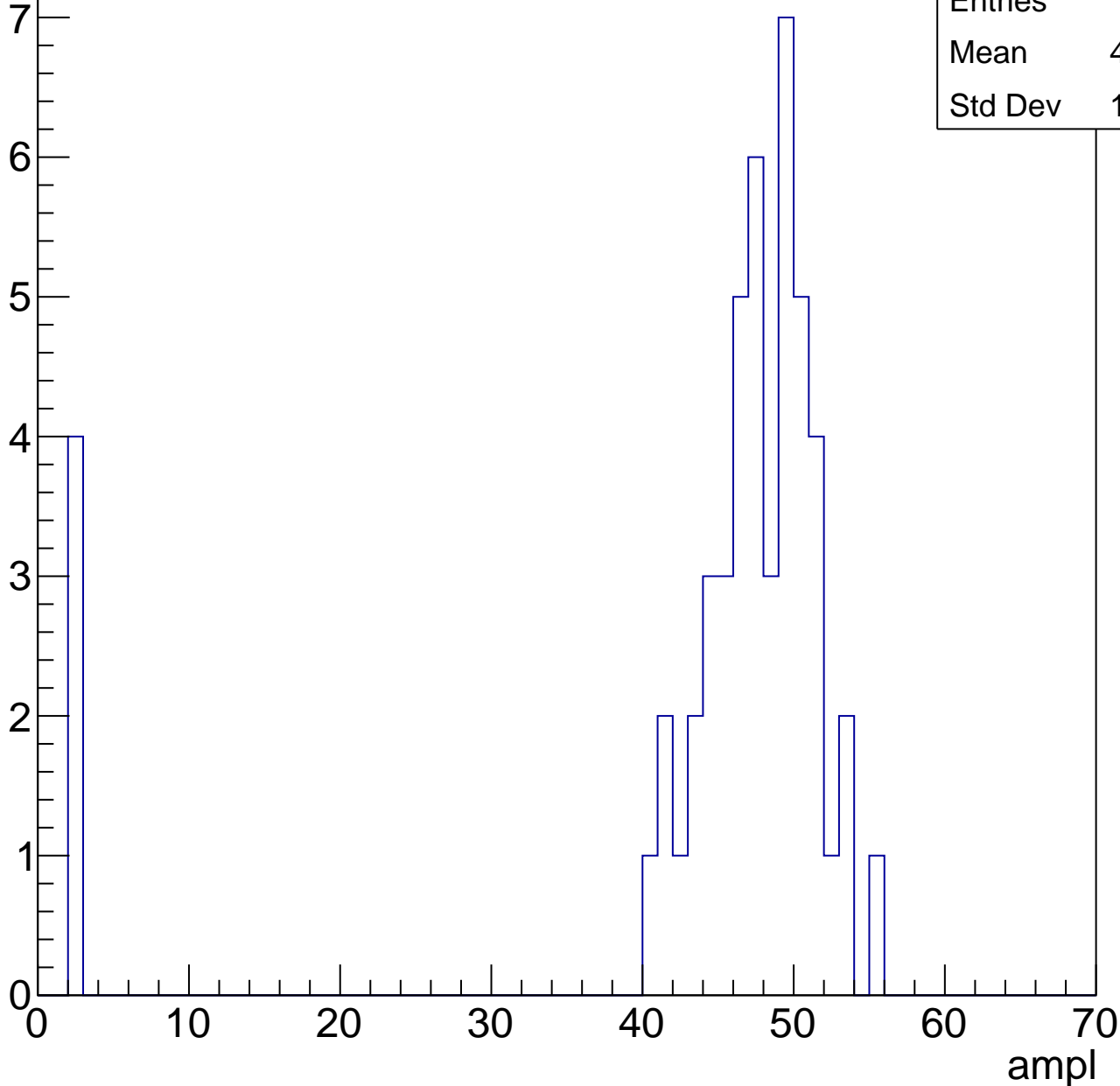


B1L103S, U10-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	43.82
Std Dev	12.74

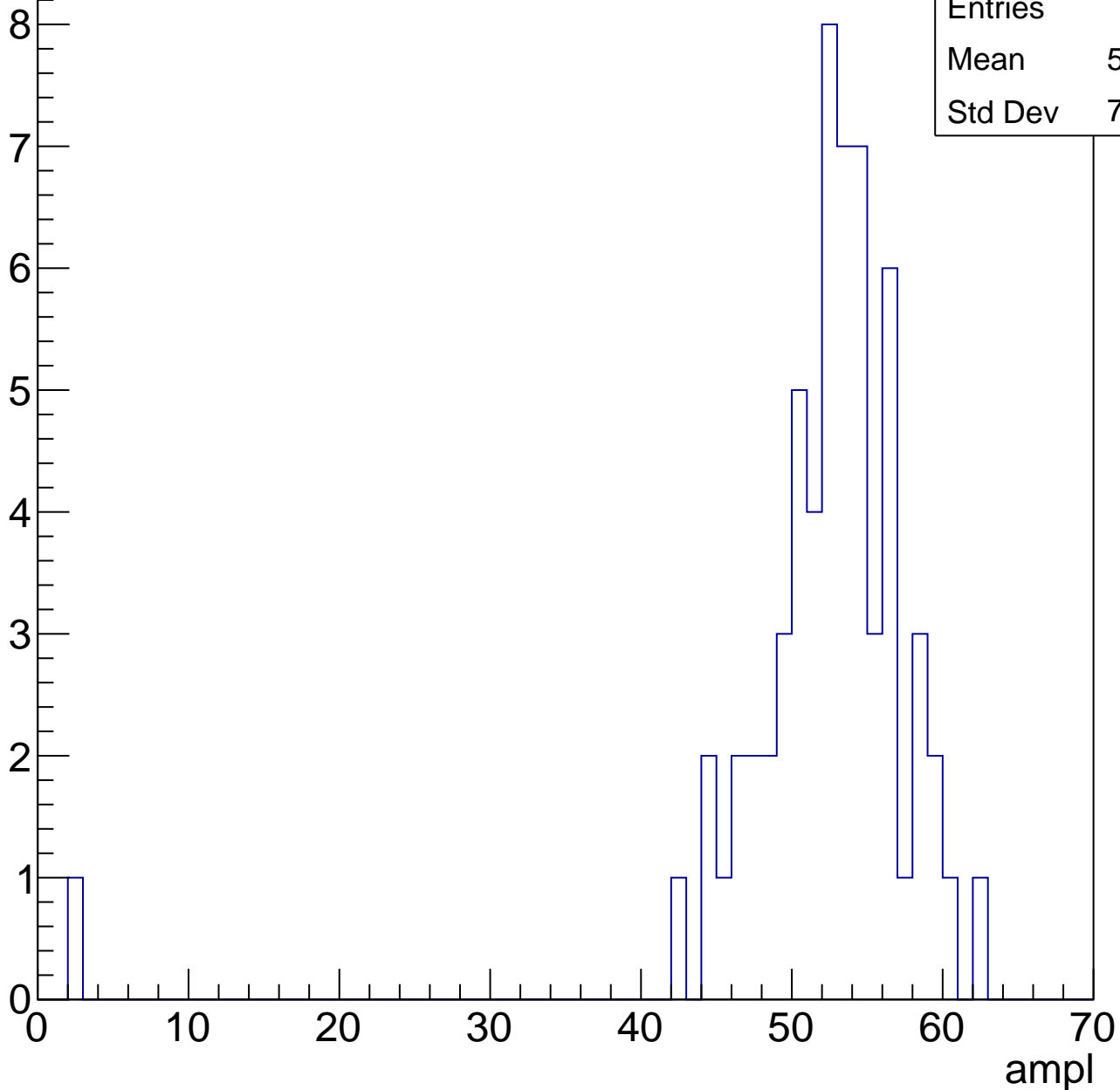


B1L103S, U10-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

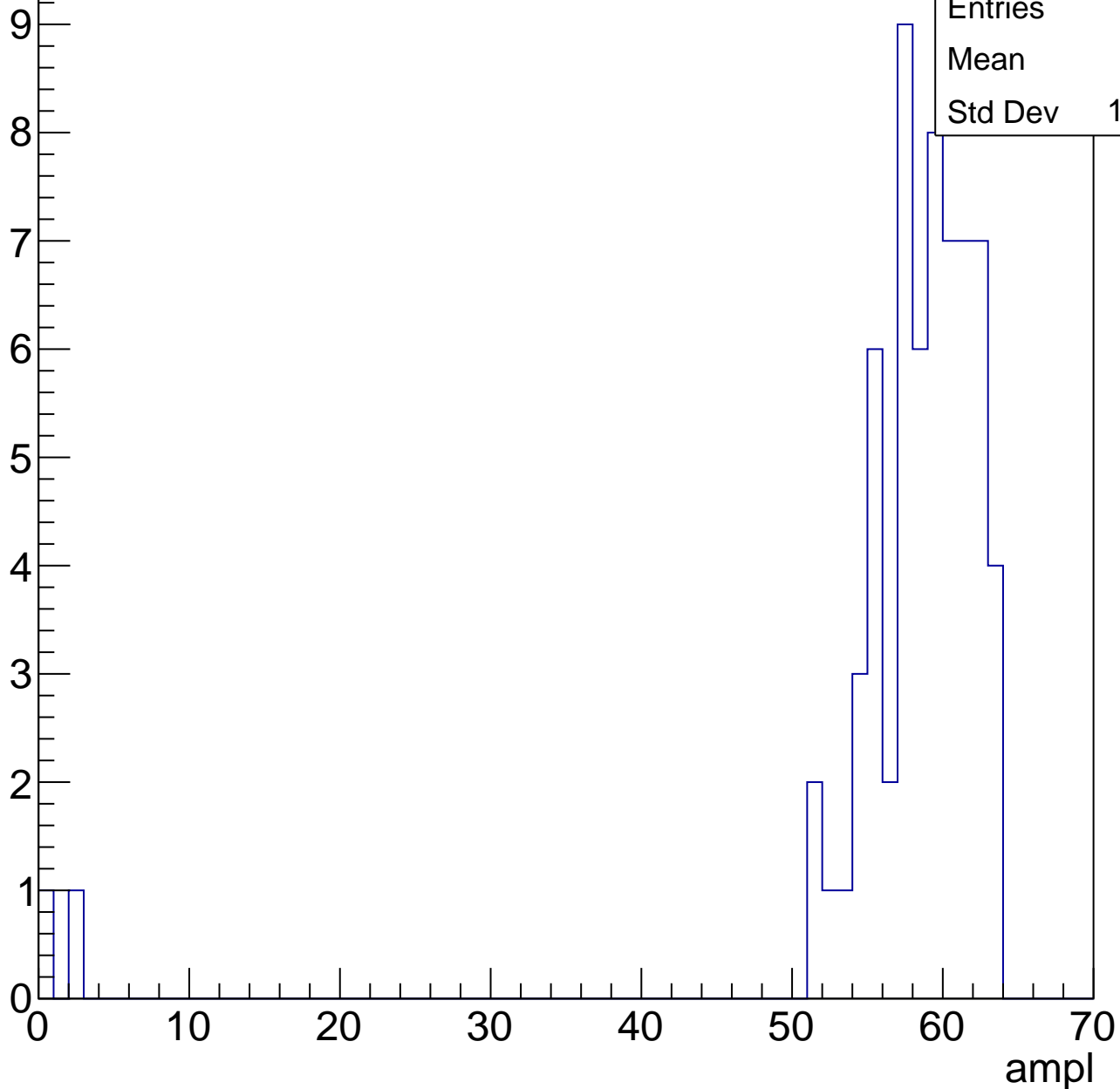
Entries	62
Mean	51.56
Std Dev	7.538



B1L103S, U10-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

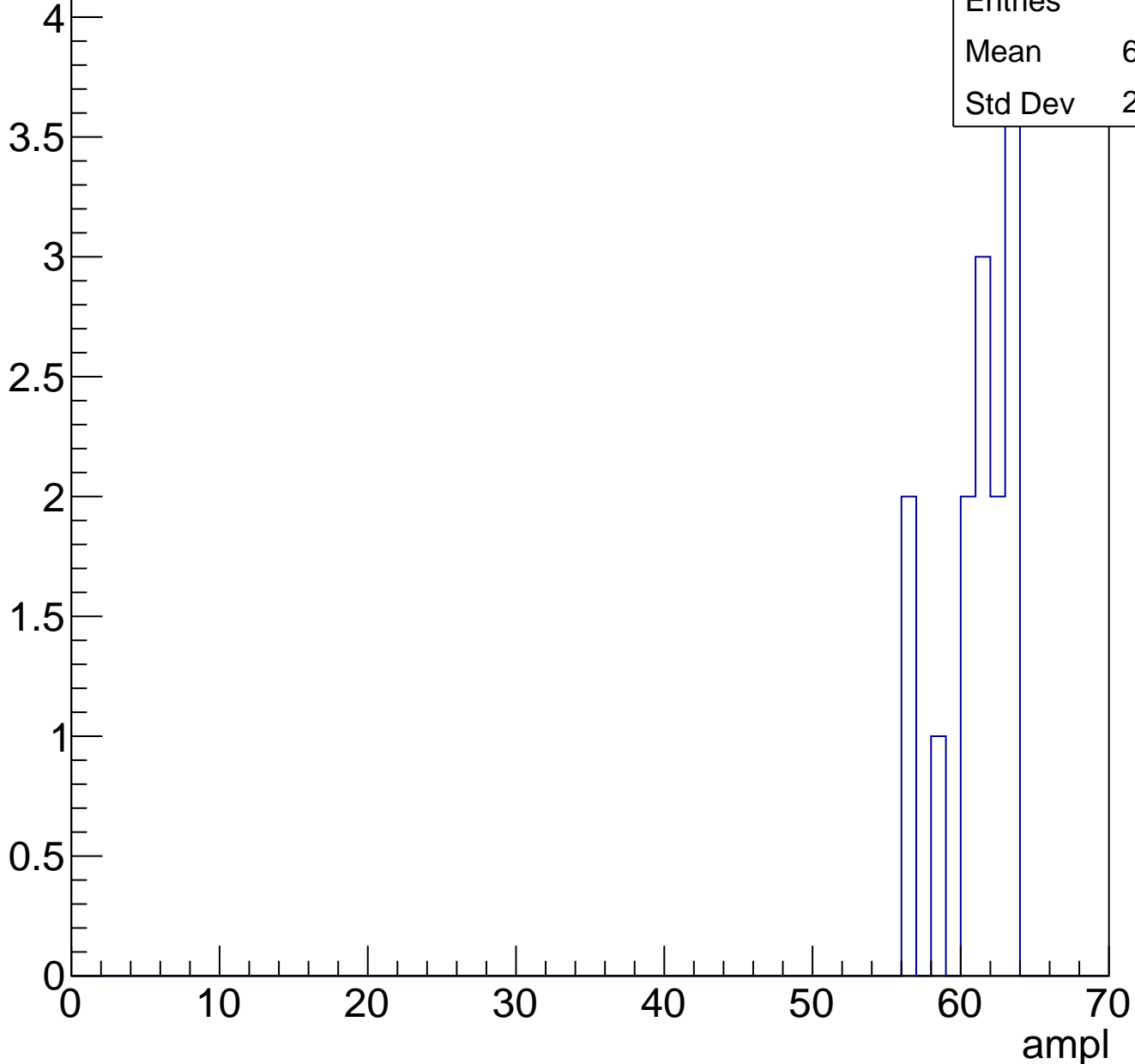
Entry



B1L103S, U10-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

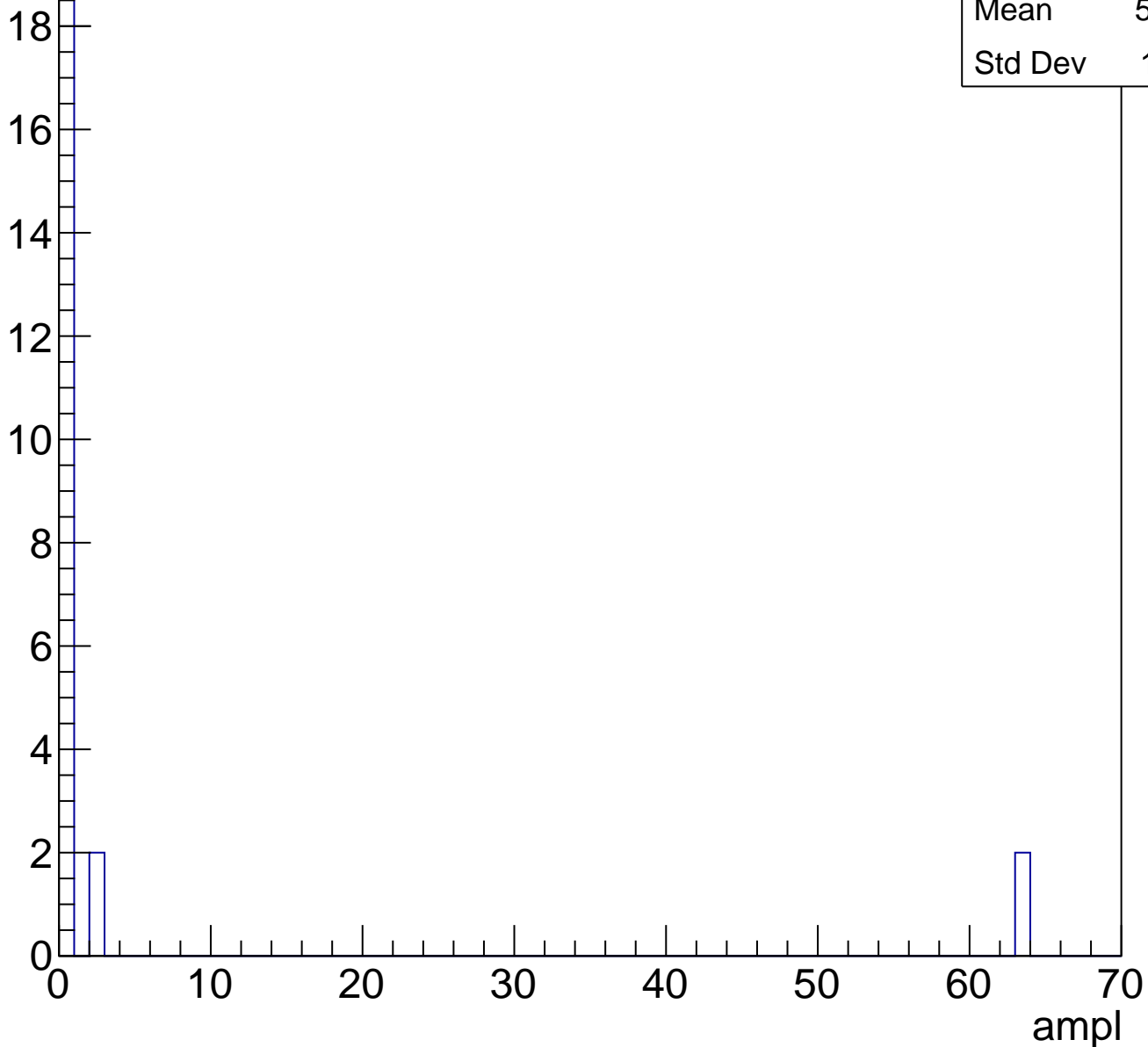


B1L103S, U10-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.652
Std Dev	17.71

Entry



B1L103S, U10-ch83, adc0

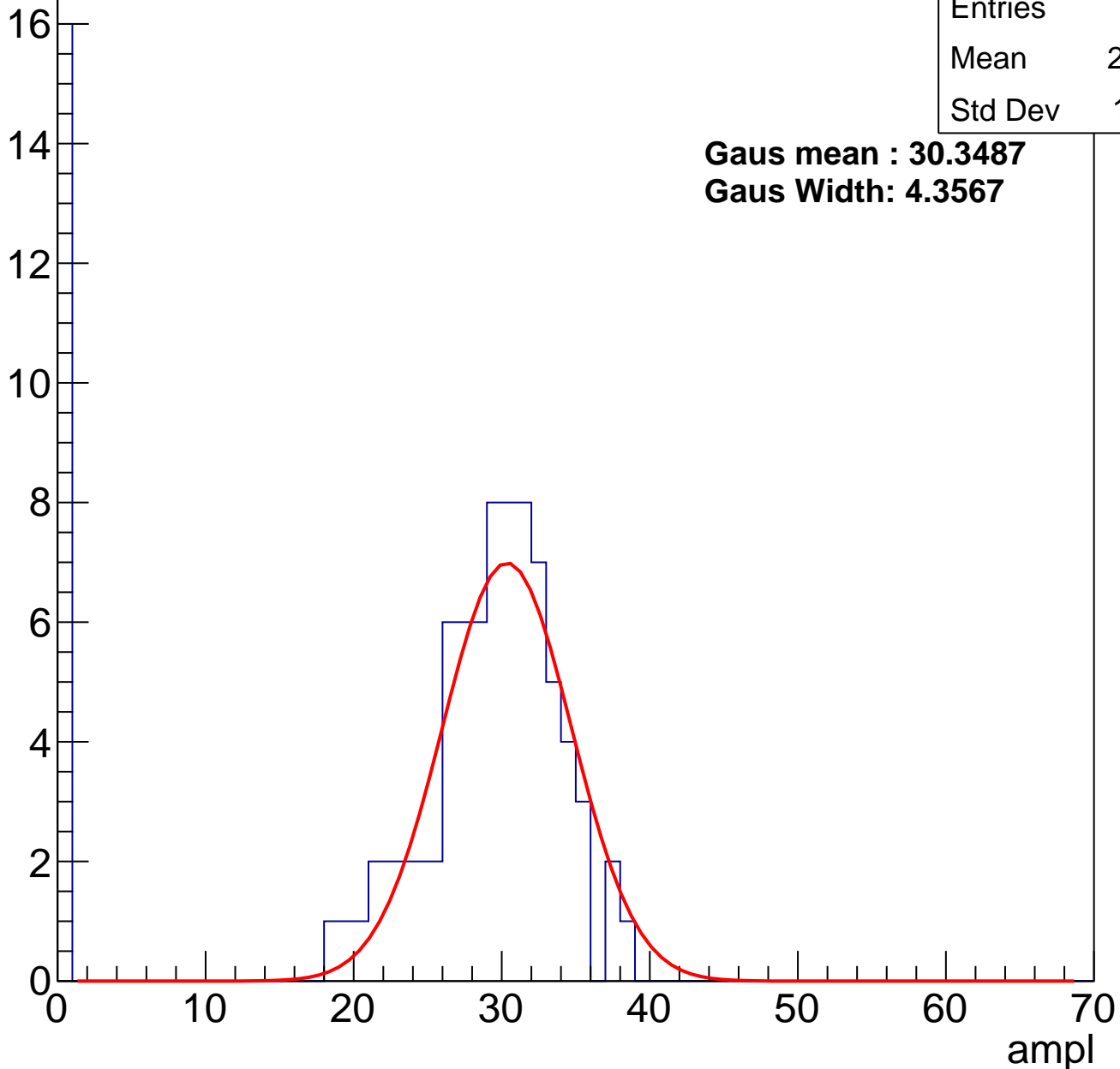
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	24.03
Std Dev	11.61

Gaus mean : 30.3487

Gaus Width: 4.3567

Entry



B1L103S, U10-ch83, adc1

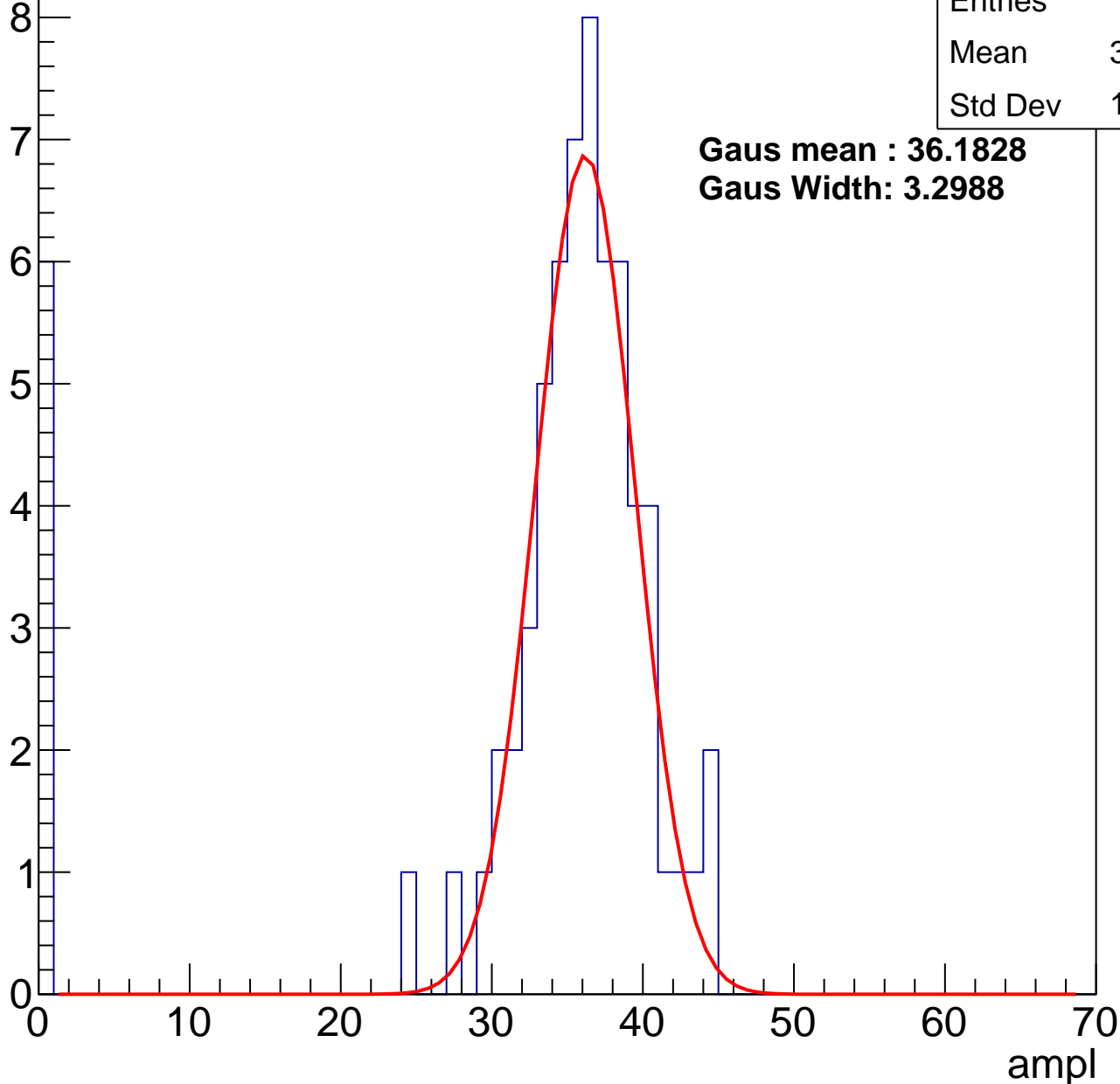
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.54
Std Dev	10.83

Gaus mean : 36.1828

Gaus Width: 3.2988



B1L103S, U10-ch83, adc2

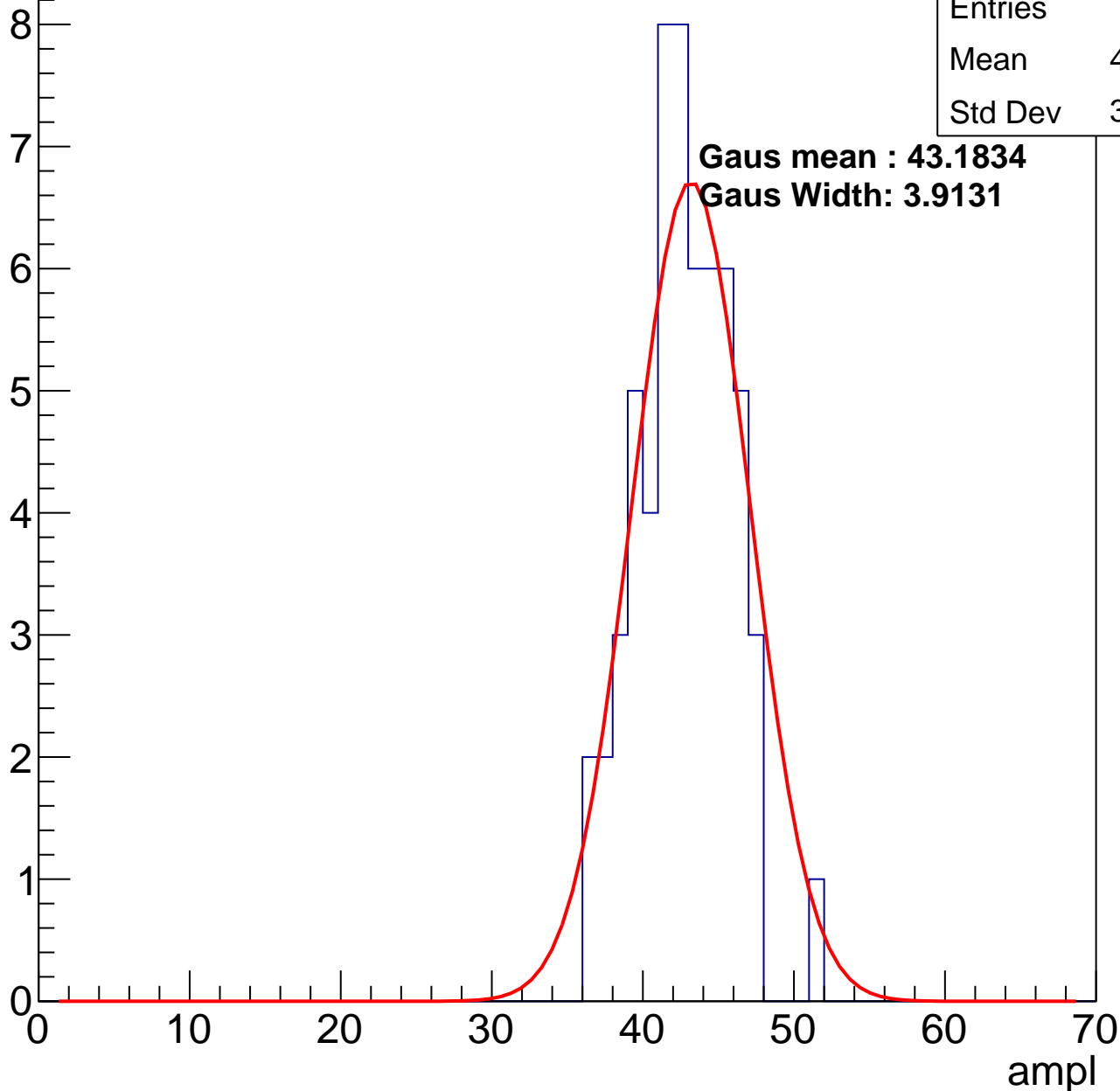
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.25
Std Dev	3.068

Gaus mean : 43.1834

Gaus Width: 3.9131

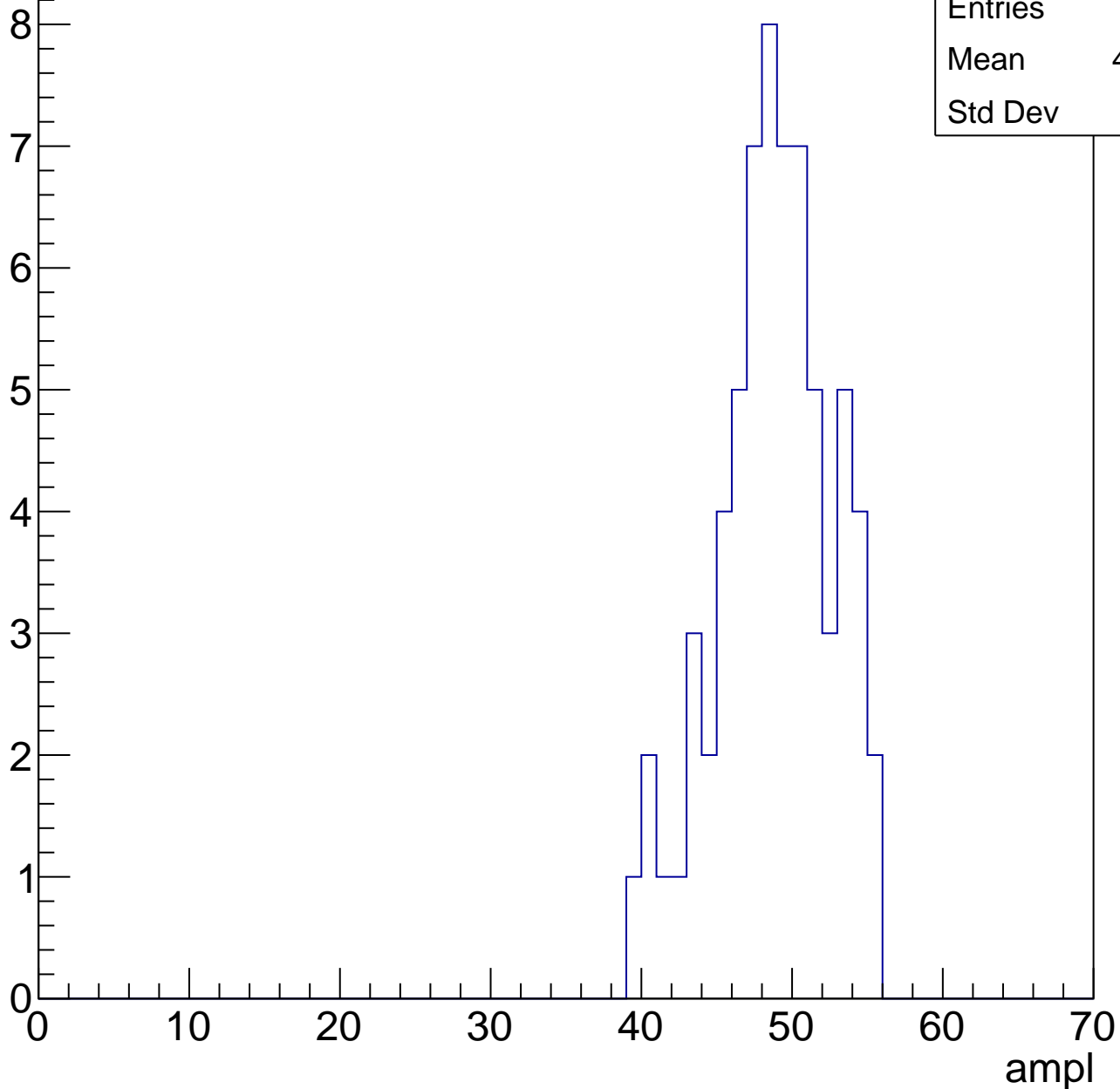


B1L103S, U10-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	48.31
Std Dev	3.77

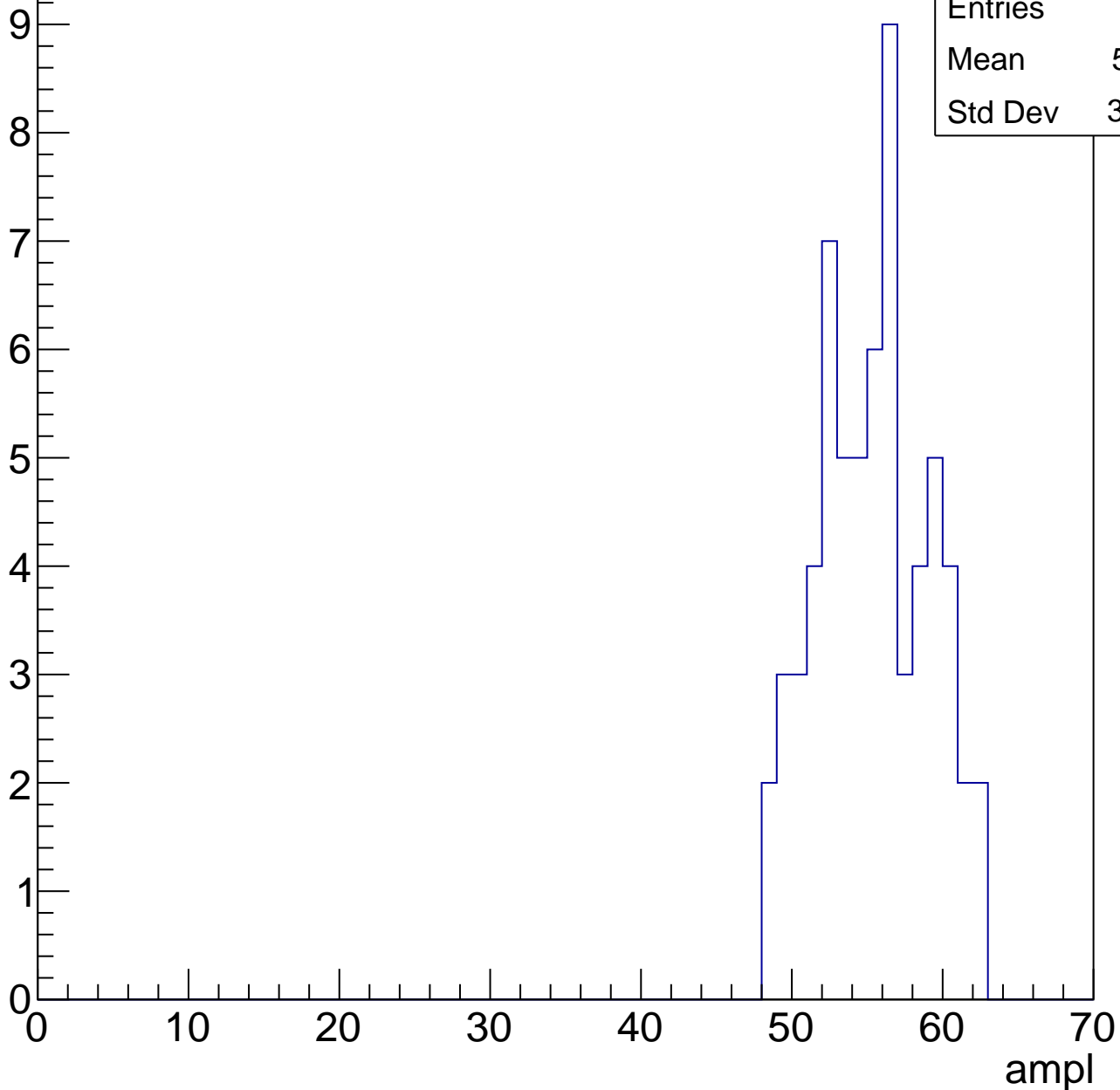


B1L103S, U10-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.91
Std Dev	3.622

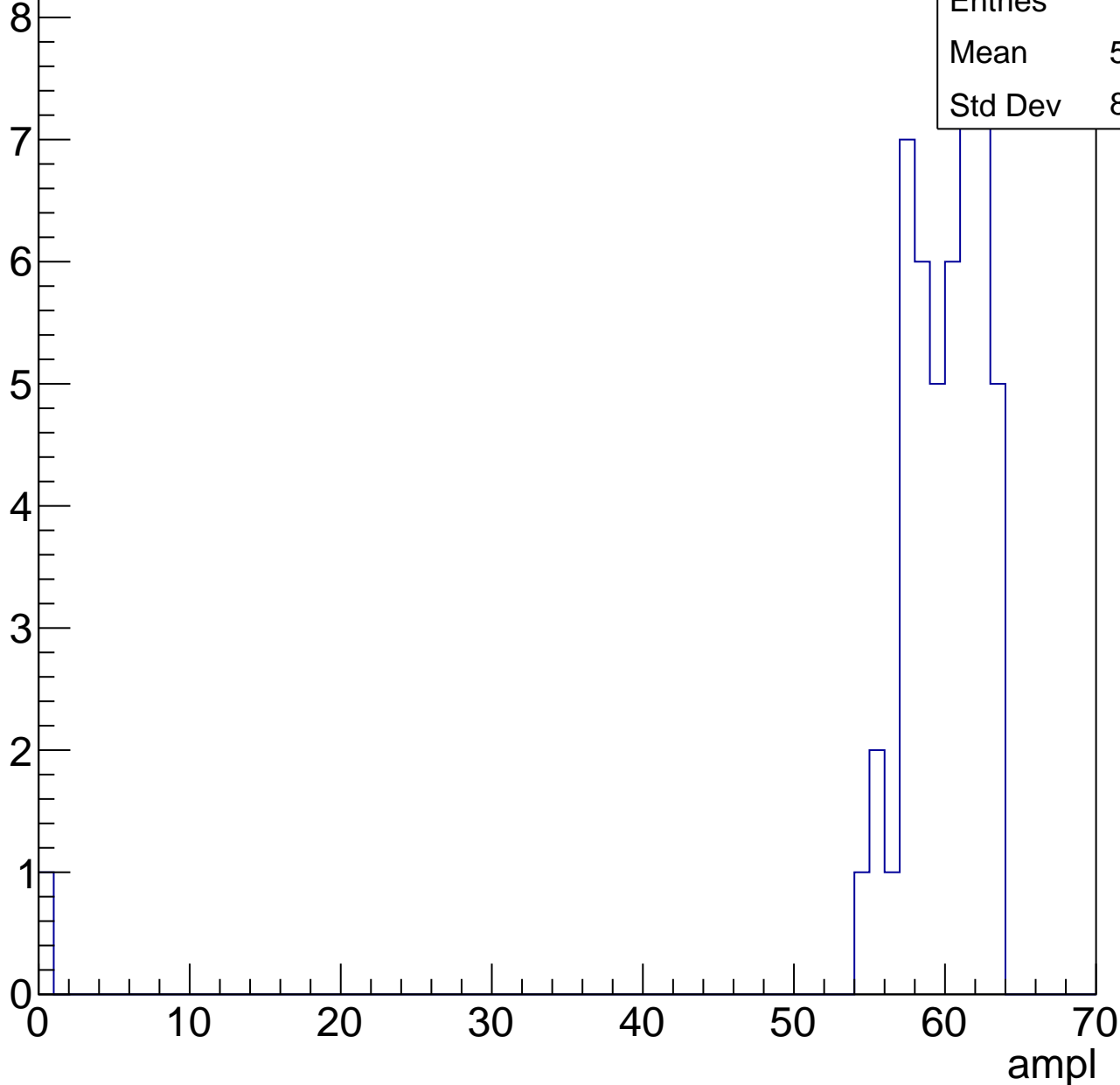


B1L103S, U10-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.42
Std Dev	8.665



B1L103S, U10-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch84, adc0

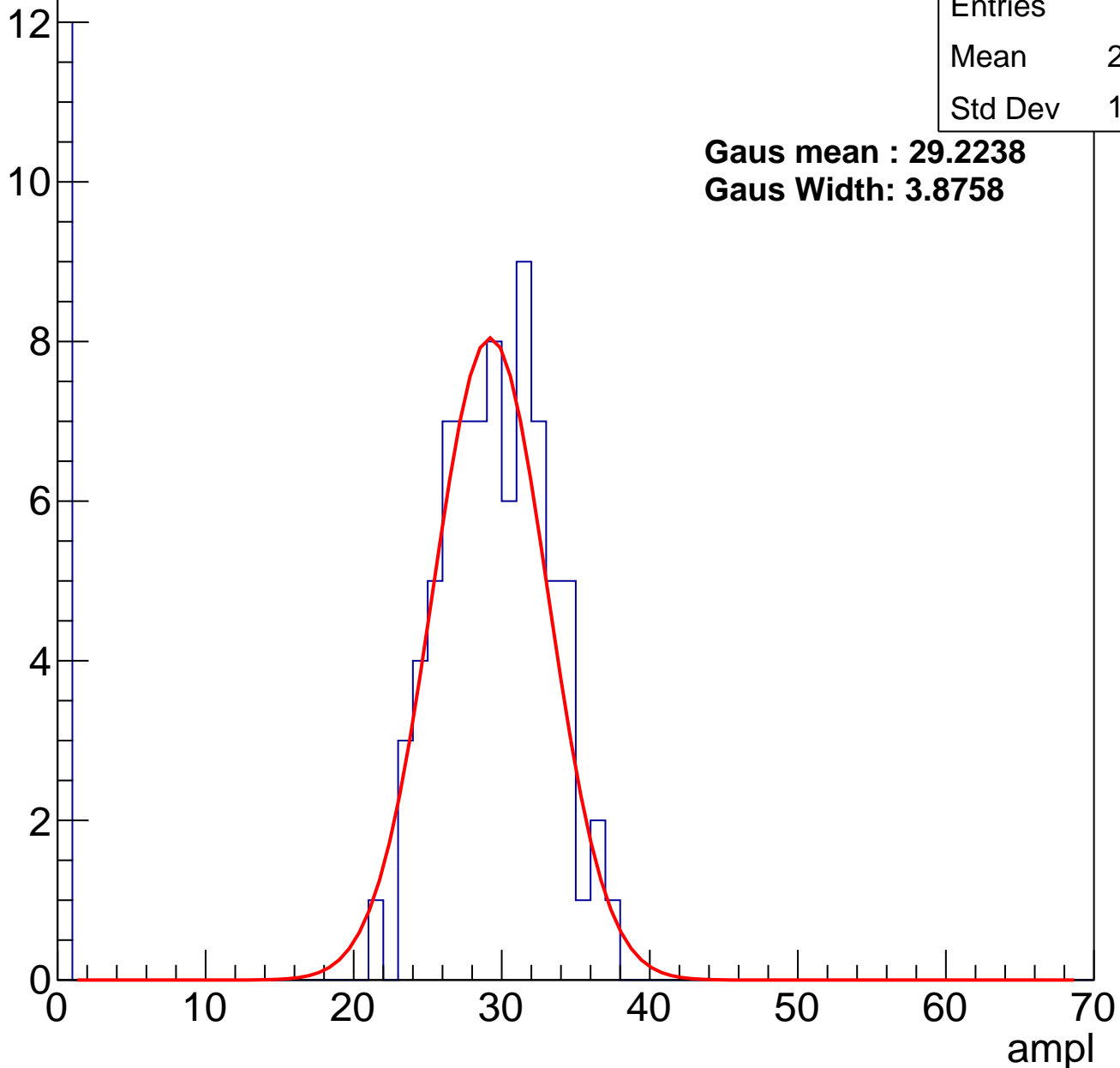
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.24
Std Dev	10.42

Gaus mean : 29.2238

Gaus Width: 3.8758

Entry



B1L103S, U10-ch84, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	31.93
Std Dev	13.06

Gaus mean : 37.5976

Gaus Width: 3.6376

Entry

10

8

6

4

2

0

0

10

20

30

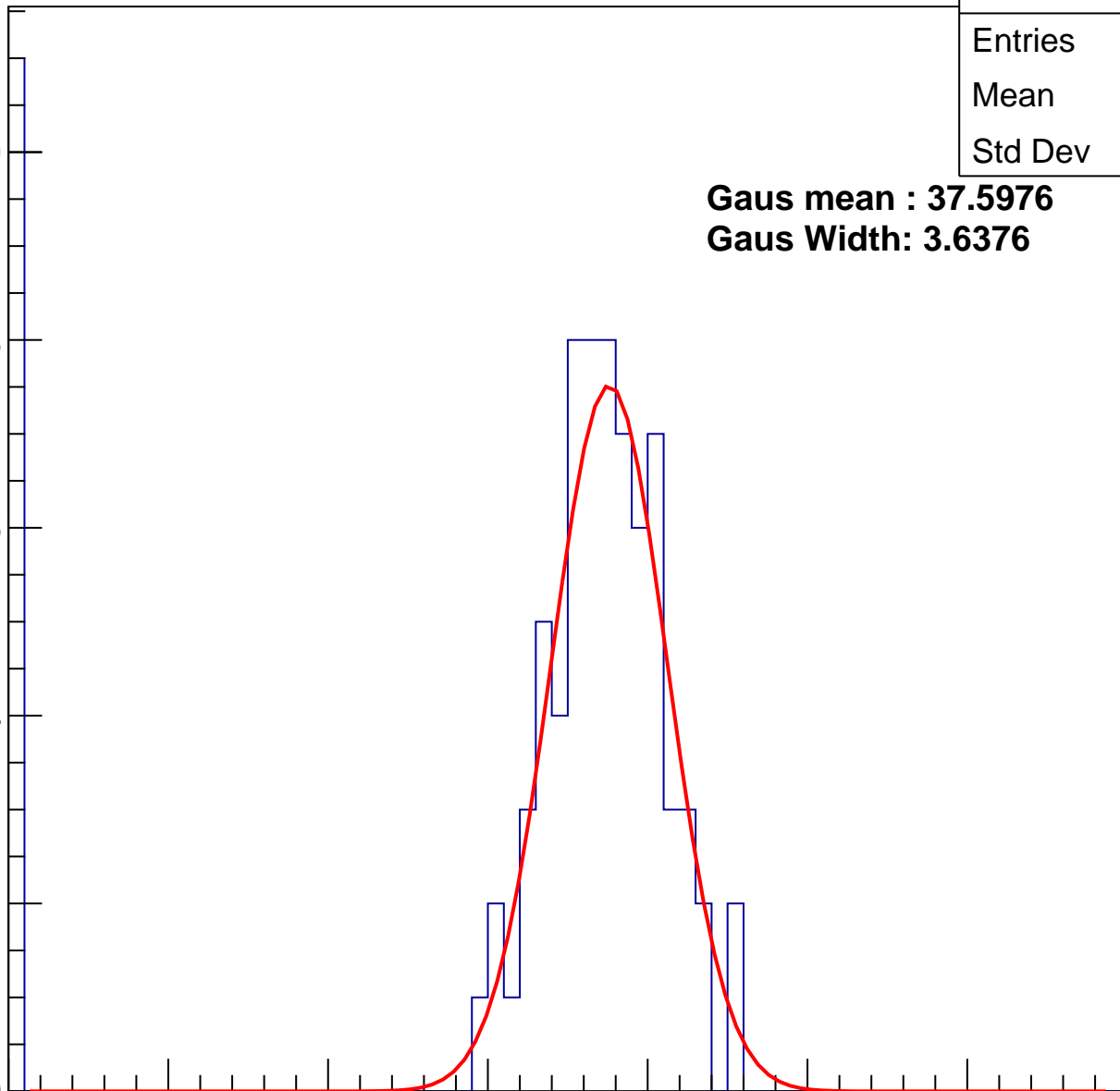
40

50

60

70

ampl



B1L103S, U10-ch84, adc2

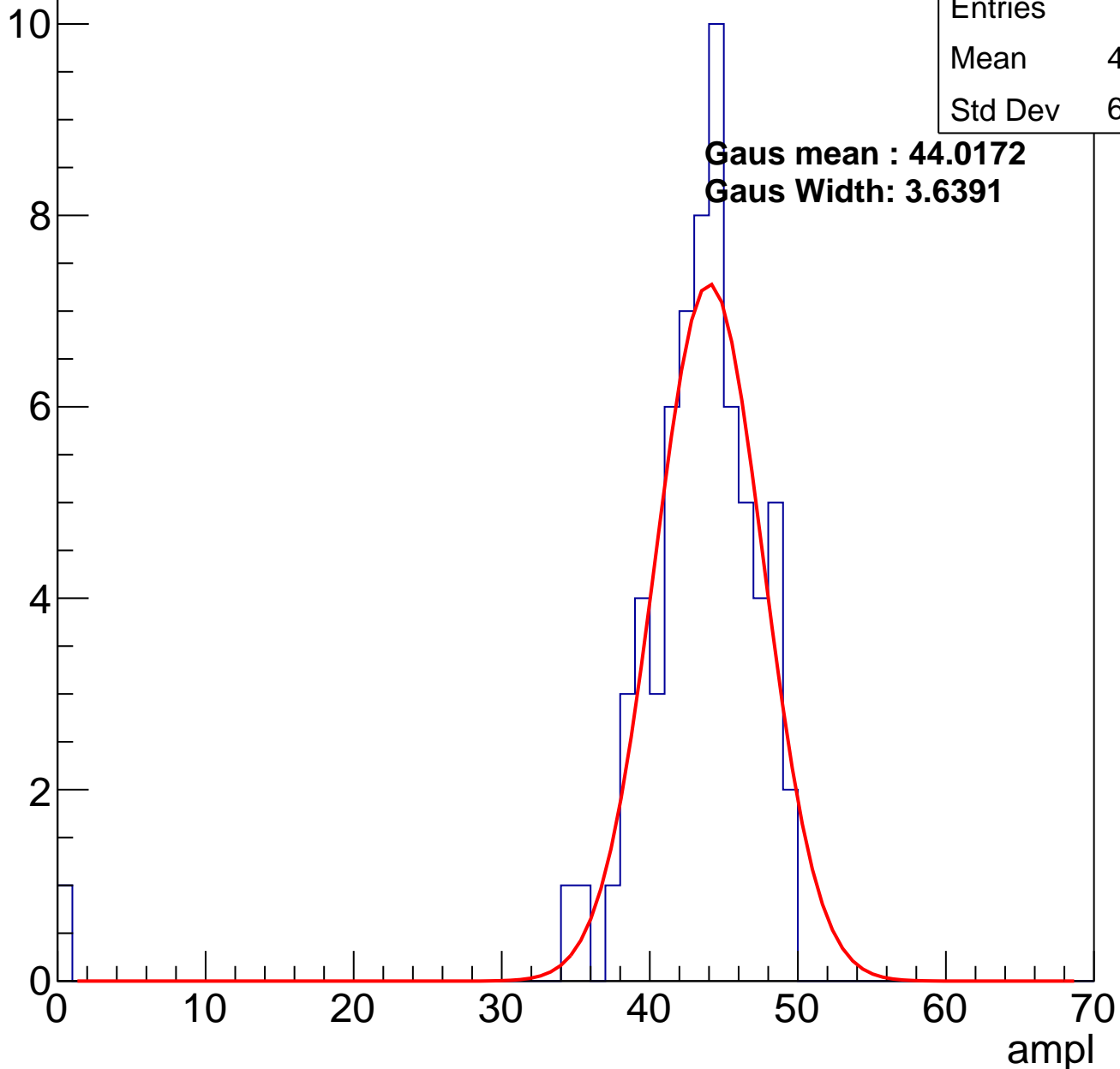
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	42.48
Std Dev	6.168

Gaus mean : 44.0172

Gaus Width: 3.6391

Entry

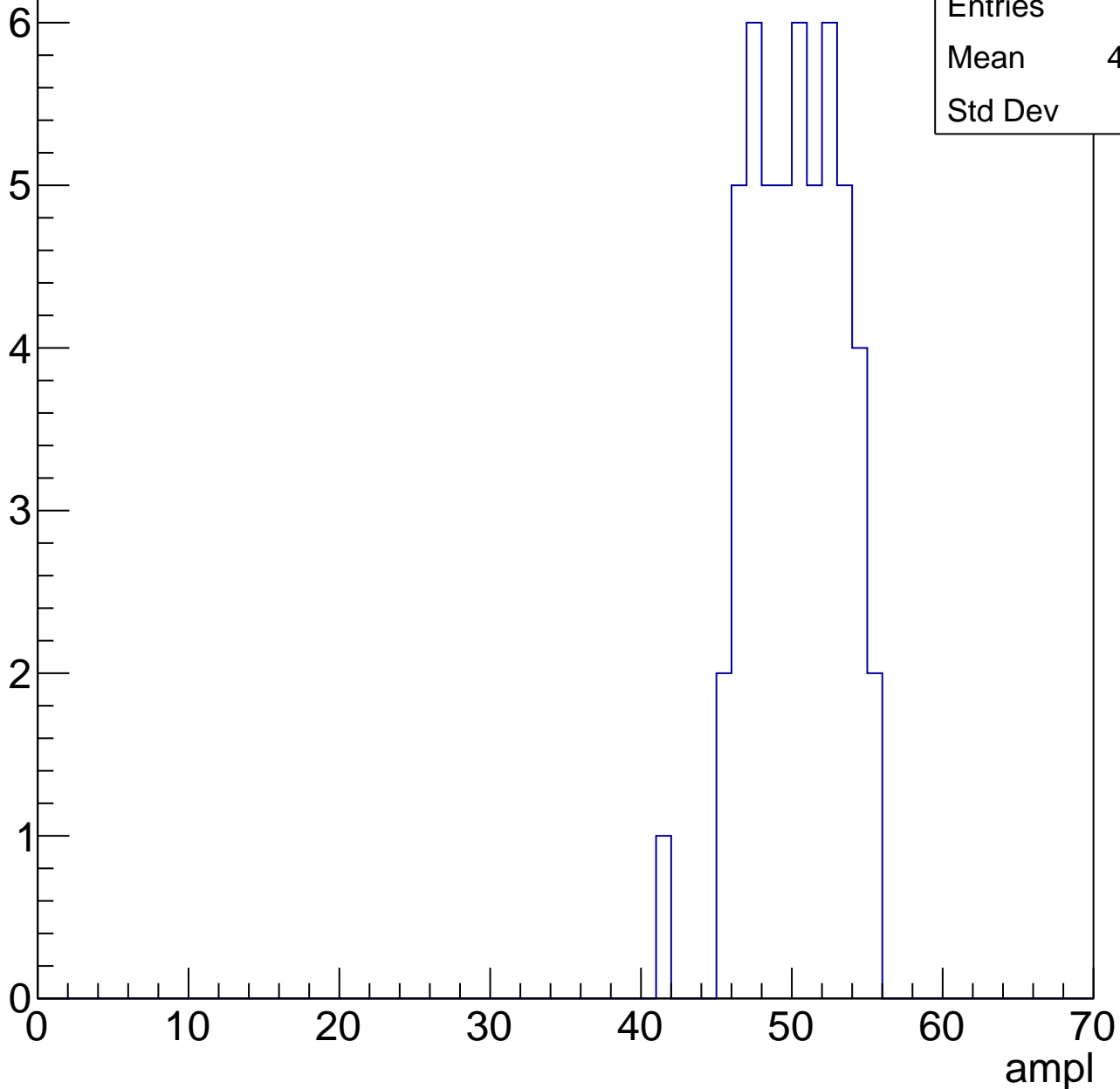


B1L103S, U10-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	49.73
Std Dev	3.02

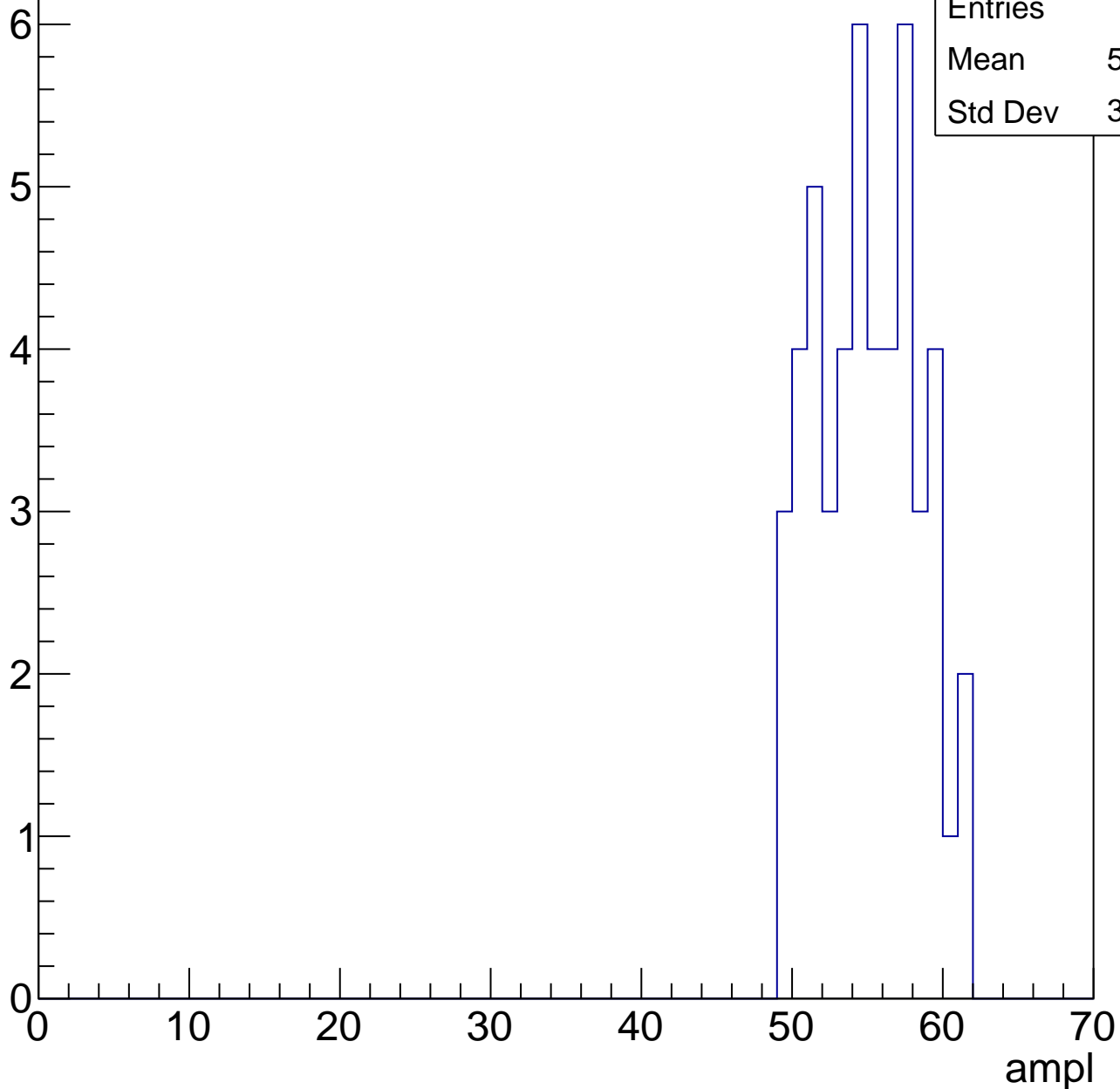


B1L103S, U10-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

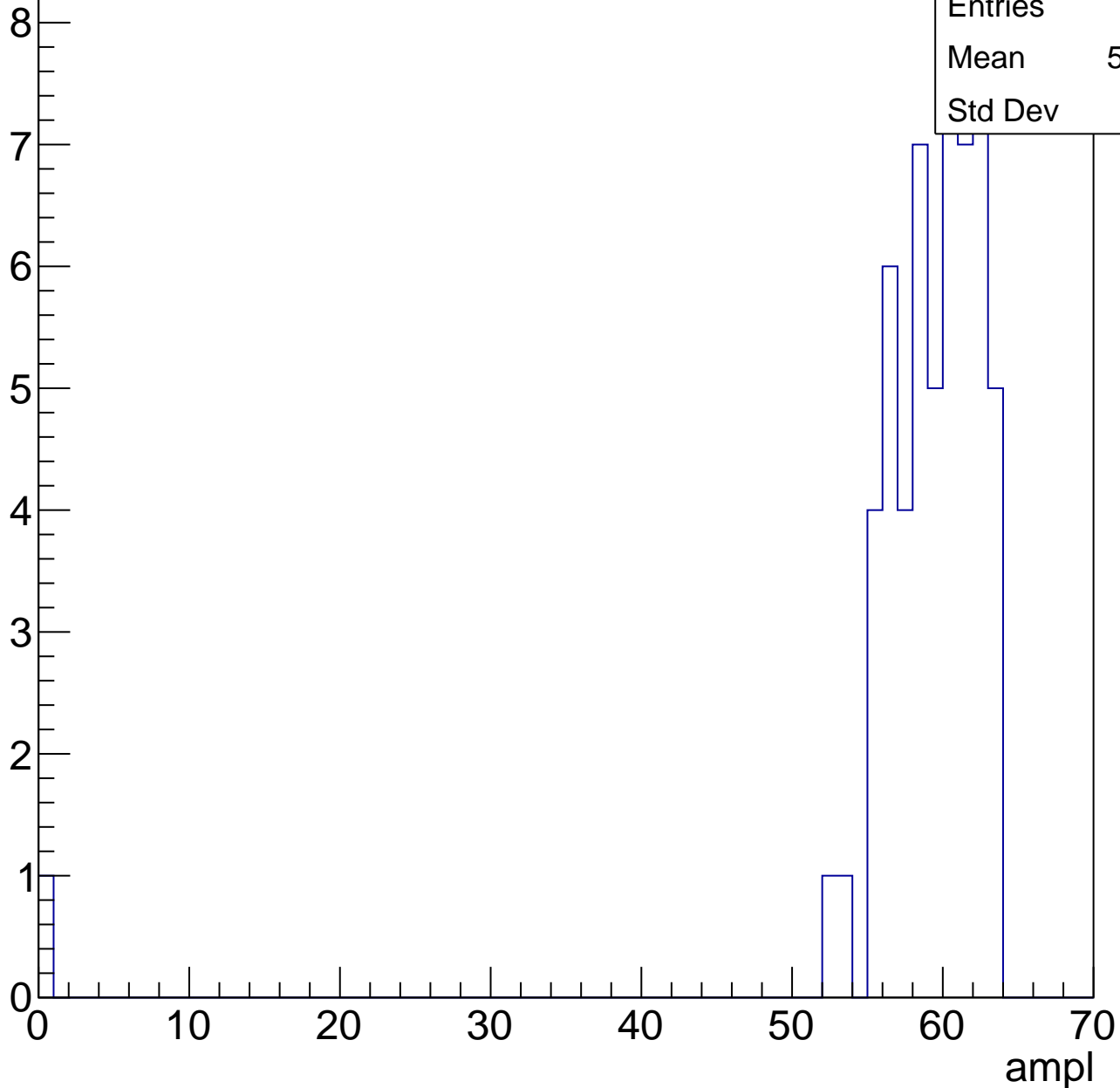
Entries	49
Mean	54.53
Std Dev	3.326



B1L103S, U10-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

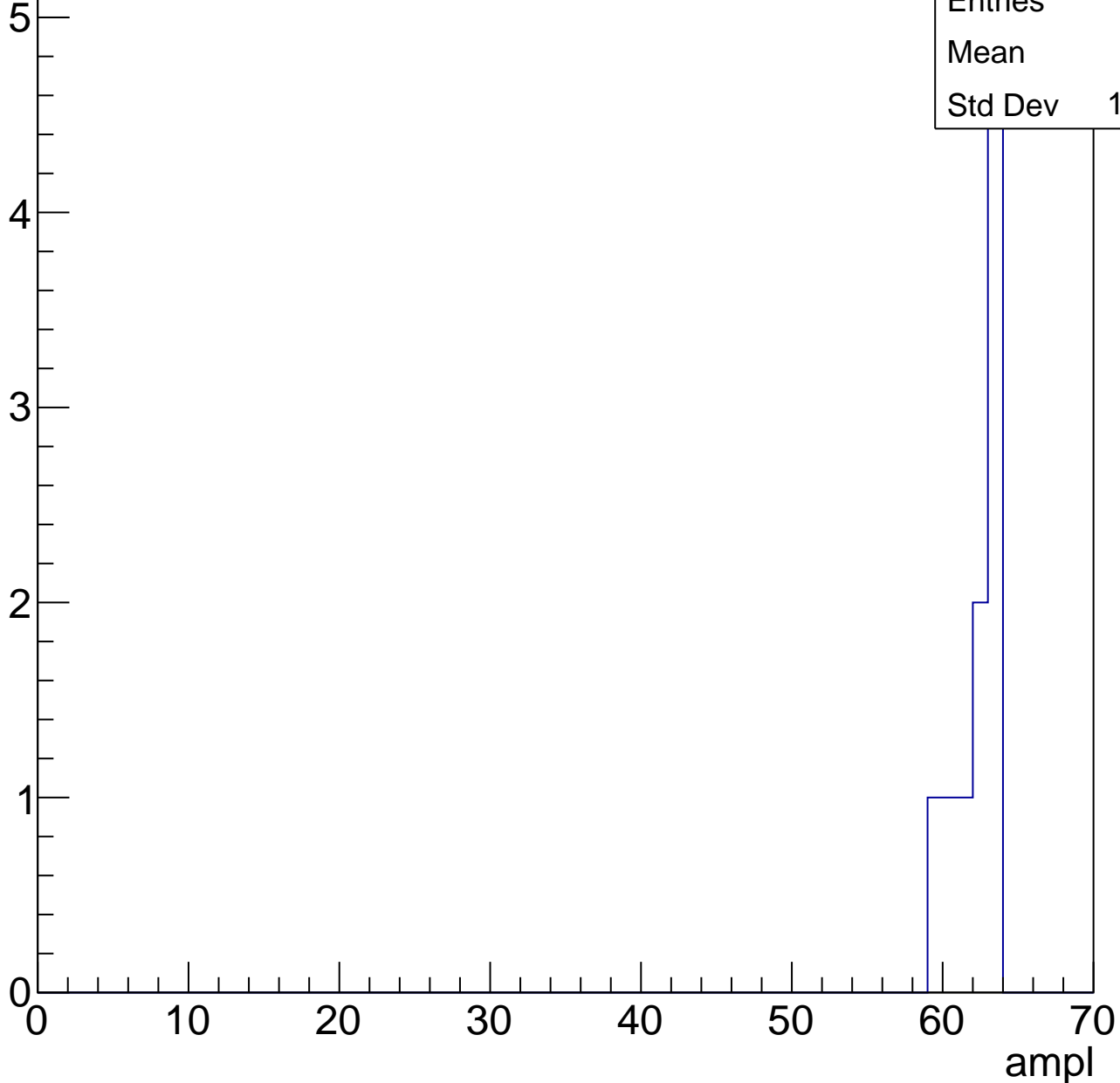


B1L103S, U10-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

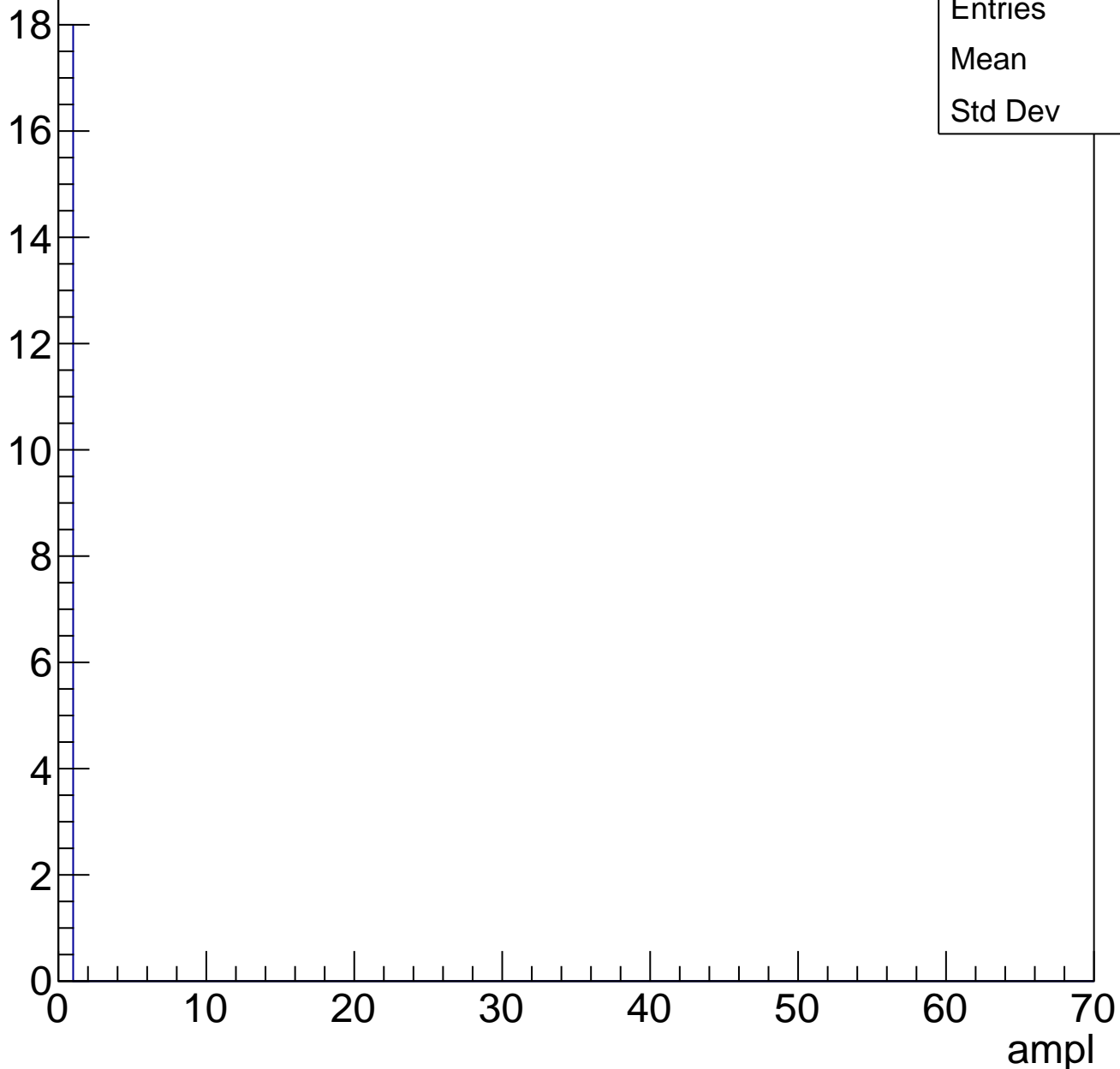
Entries	10
Mean	61.9
Std Dev	1.375



B1L103S, U10-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch85, adc0

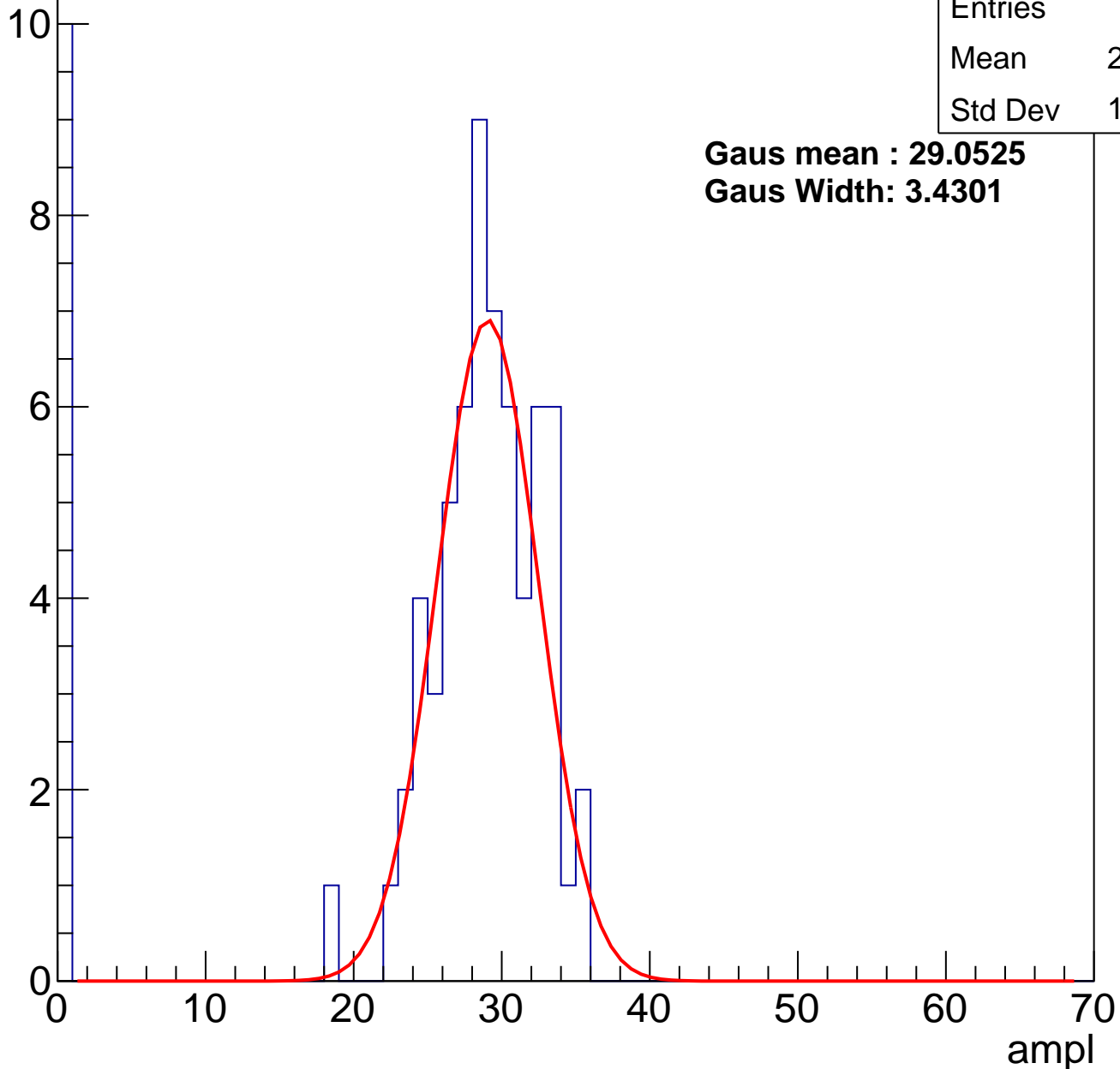
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.68
Std Dev	10.33

Gaus mean : 29.0525

Gaus Width: 3.4301

Entry



B1L103S, U10-ch85, adc1

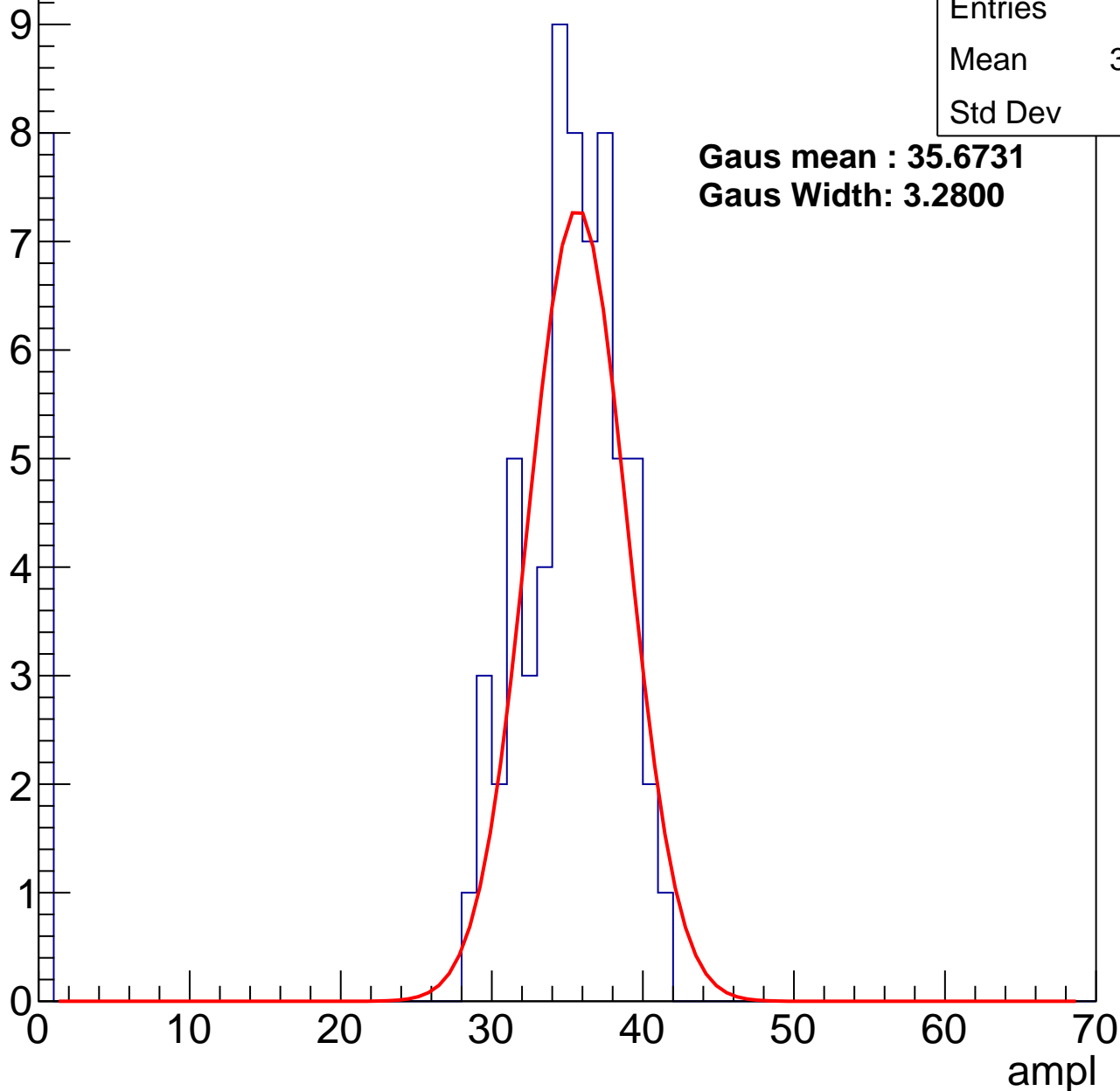
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.96
Std Dev	11.4

Gaus mean : 35.6731

Gaus Width: 3.2800



B1L103S, U10-ch85, adc2

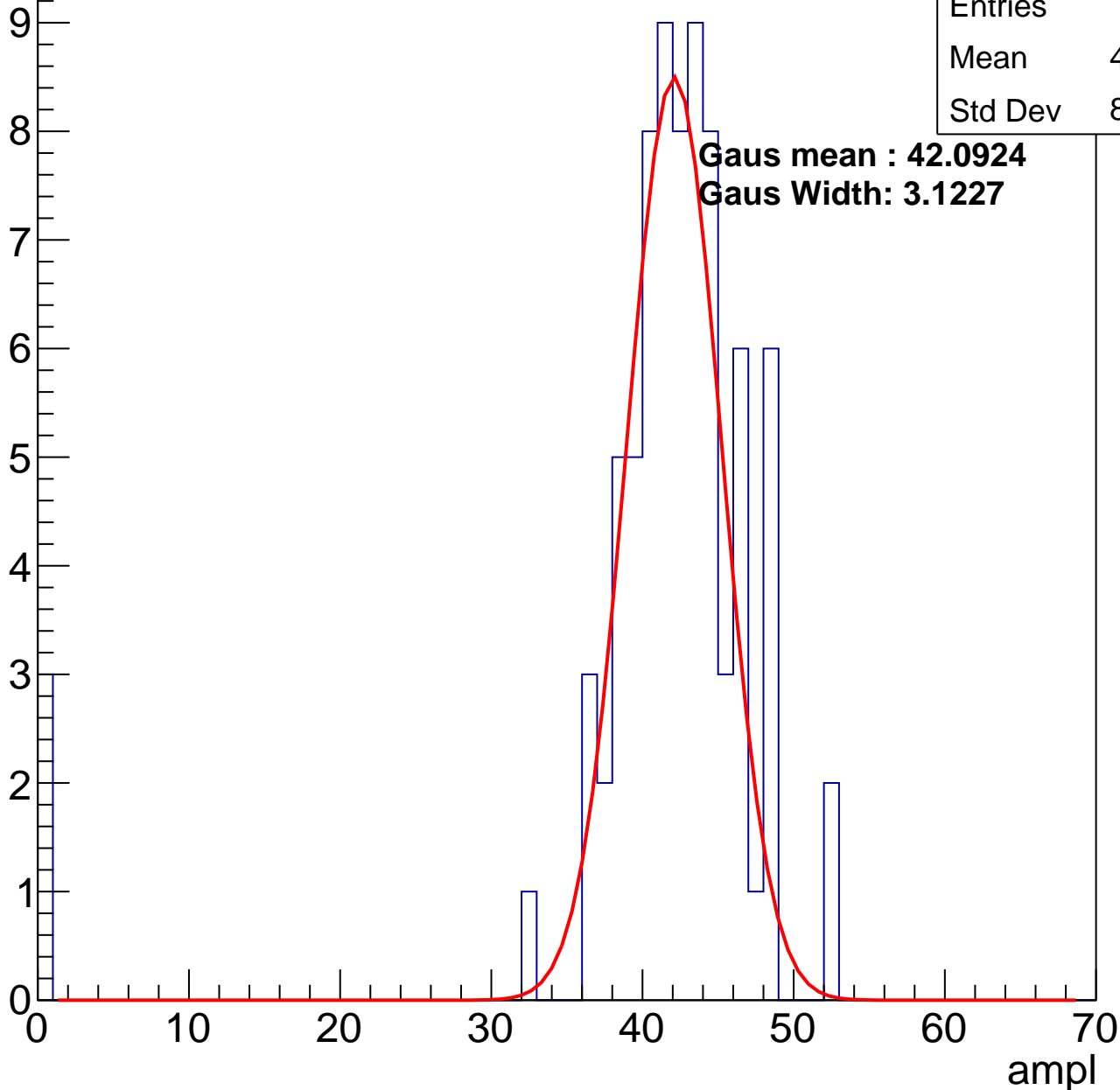
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	40.67
Std Dev	8.846

Gaus mean : 42.0924

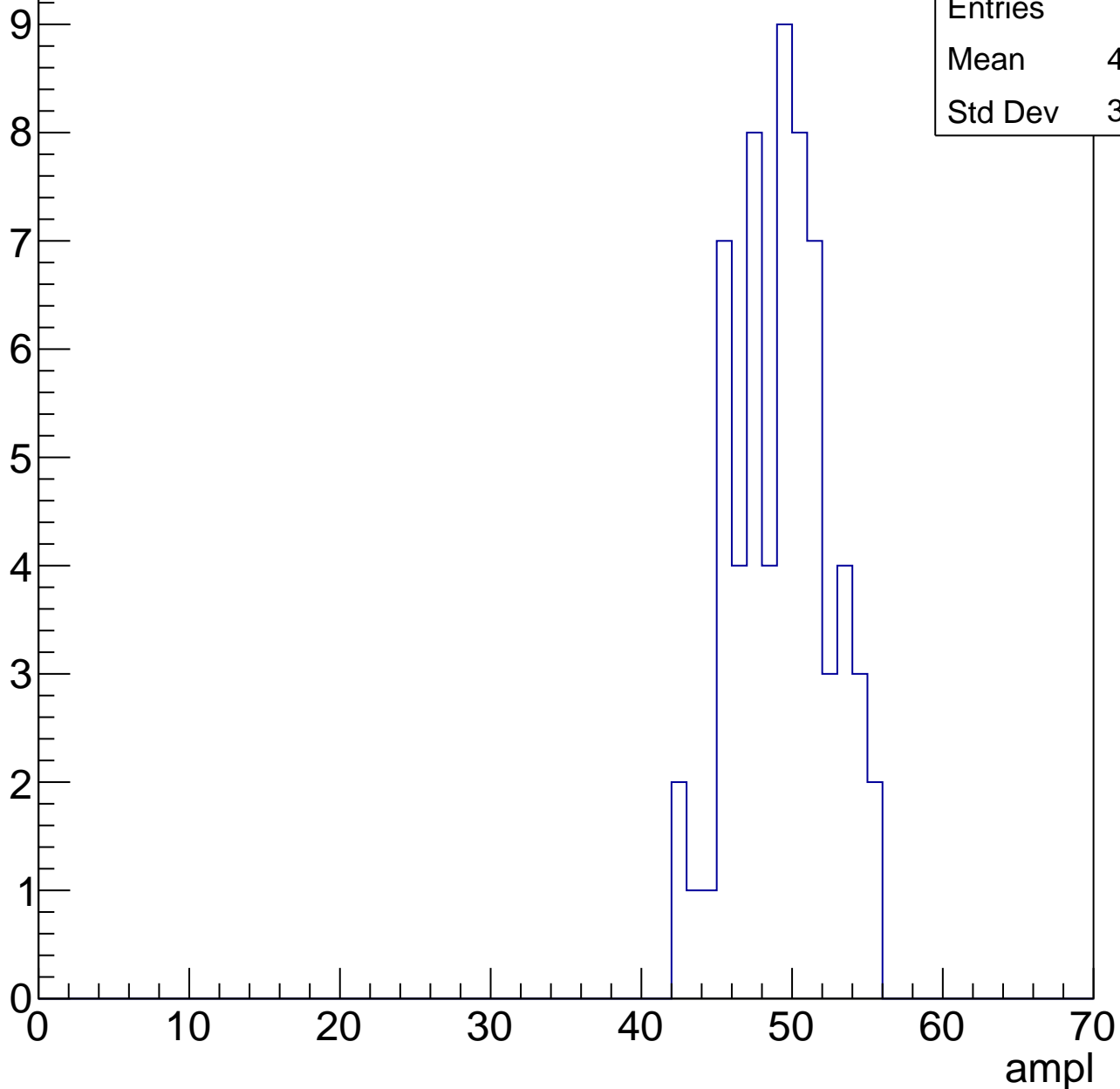
Gaus Width: 3.1227



B1L103S, U10-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



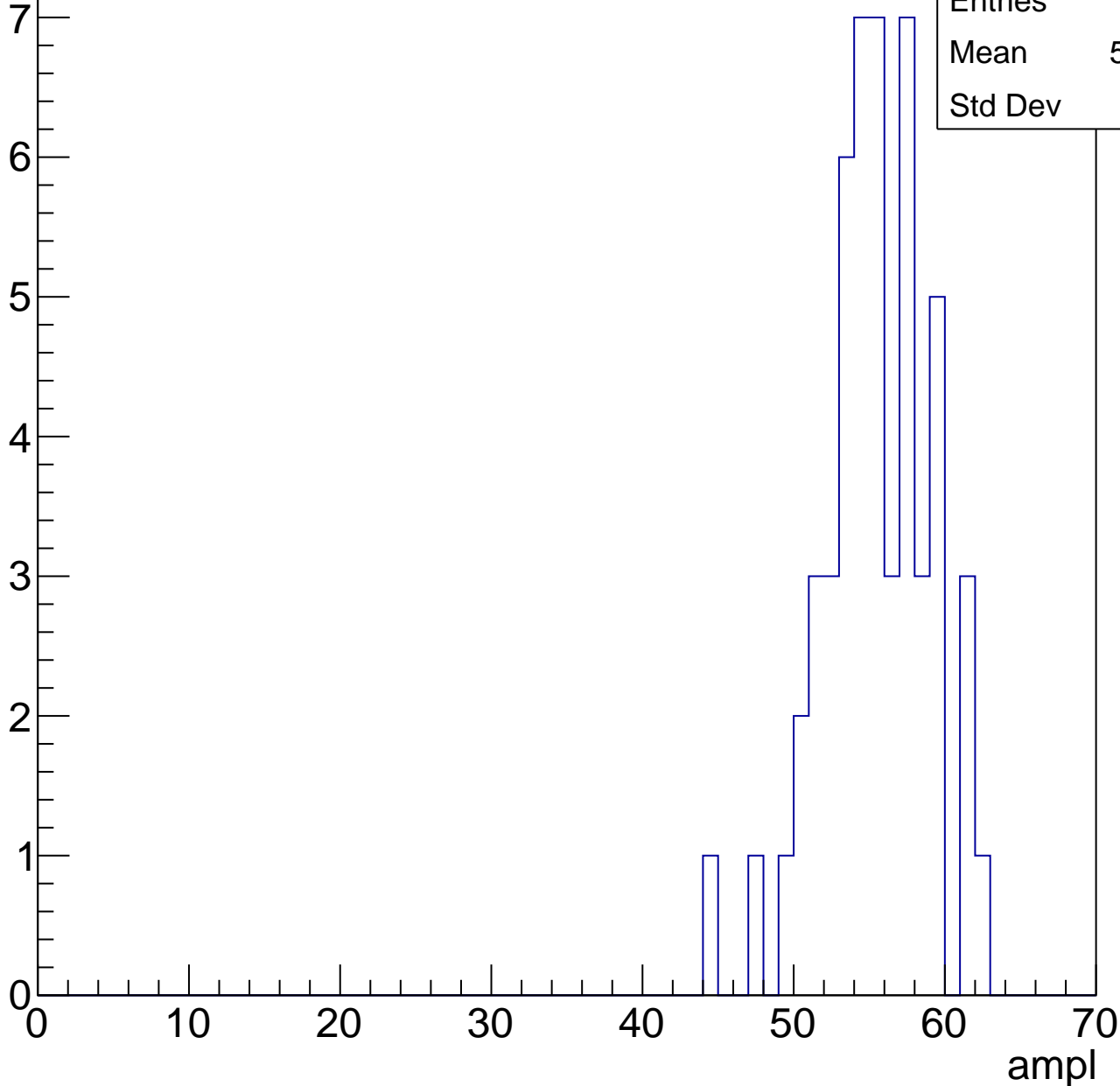
Entries	63
Mean	48.83
Std Dev	3.125

B1L103S, U10-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	54.92
Std Dev	3.56

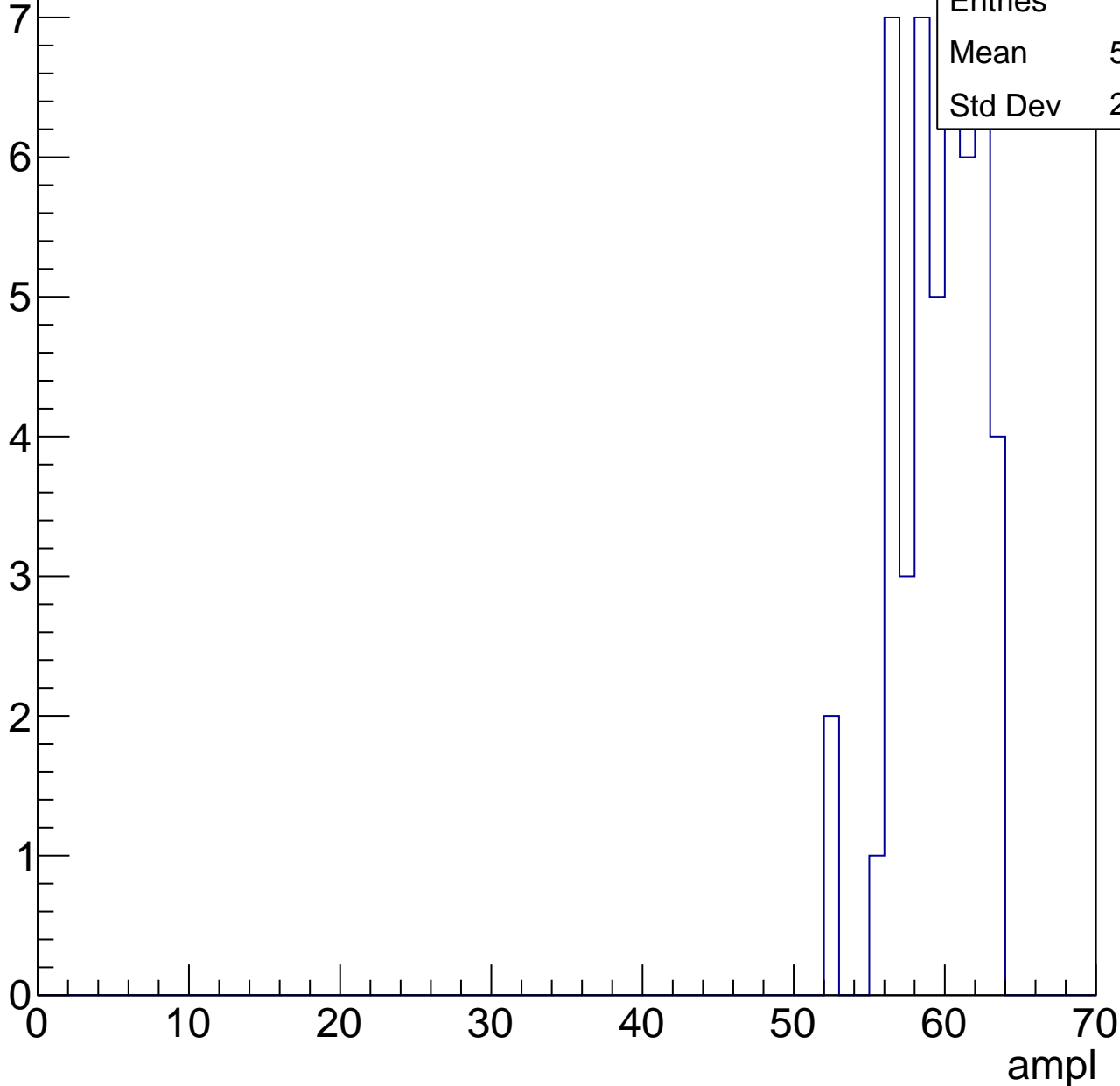


B1L103S, U10-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	59.08
Std Dev	2.687

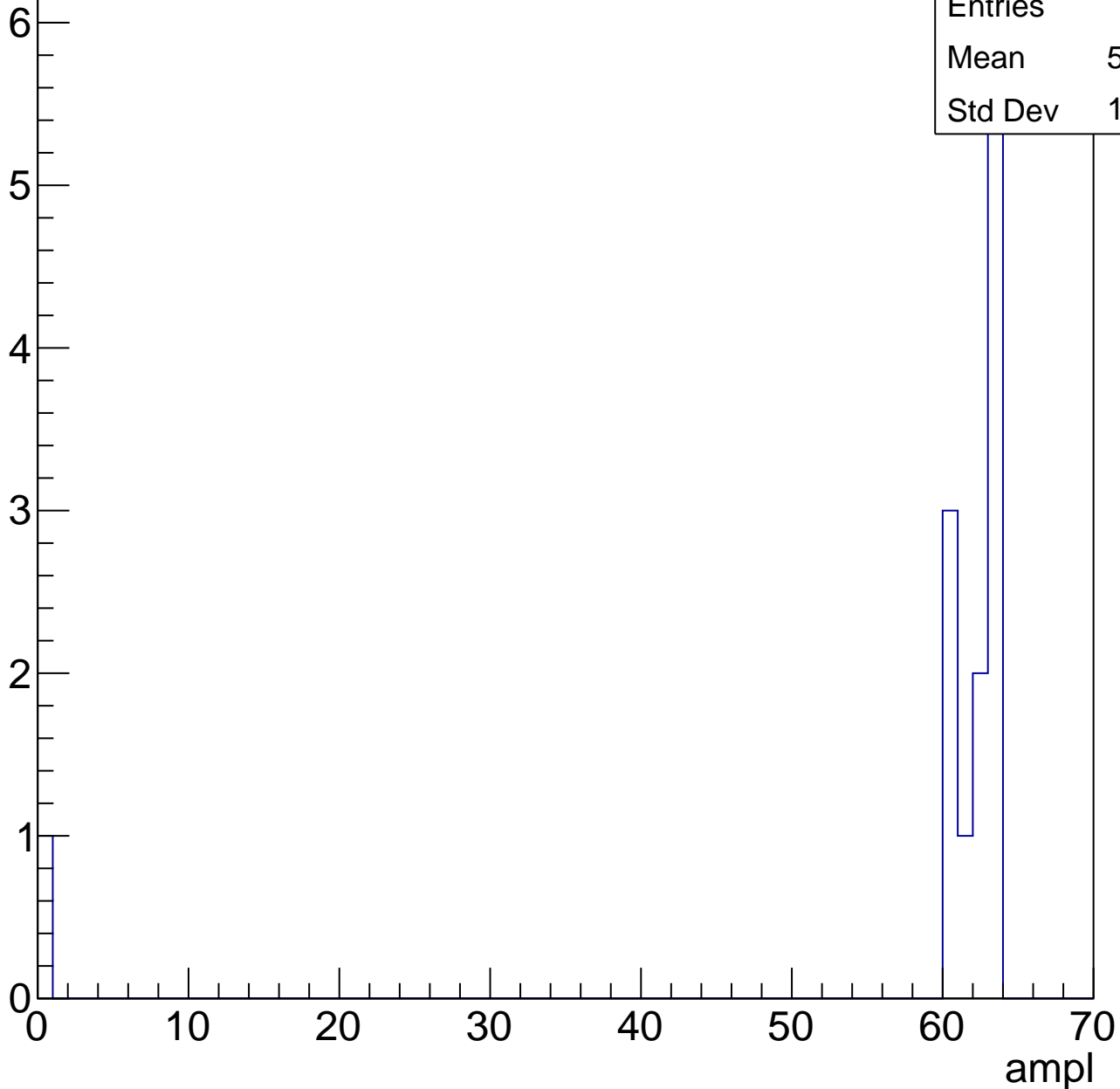


B1L103S, U10-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.15
Std Dev	16.54



B1L103S, U10-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch86, adc0

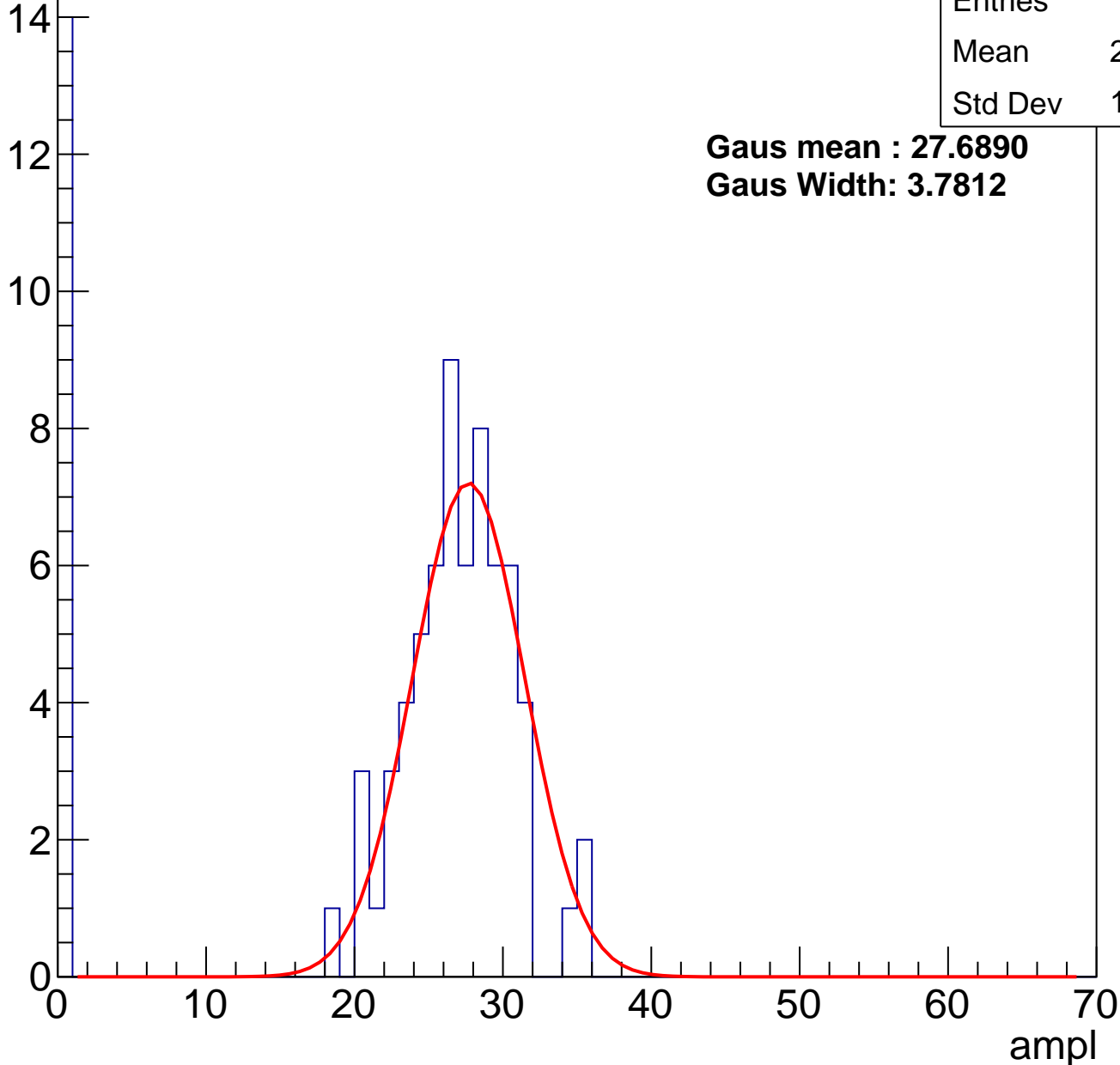
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	21.89
Std Dev	10.64

Gaus mean : 27.6890

Gaus Width: 3.7812

Entry



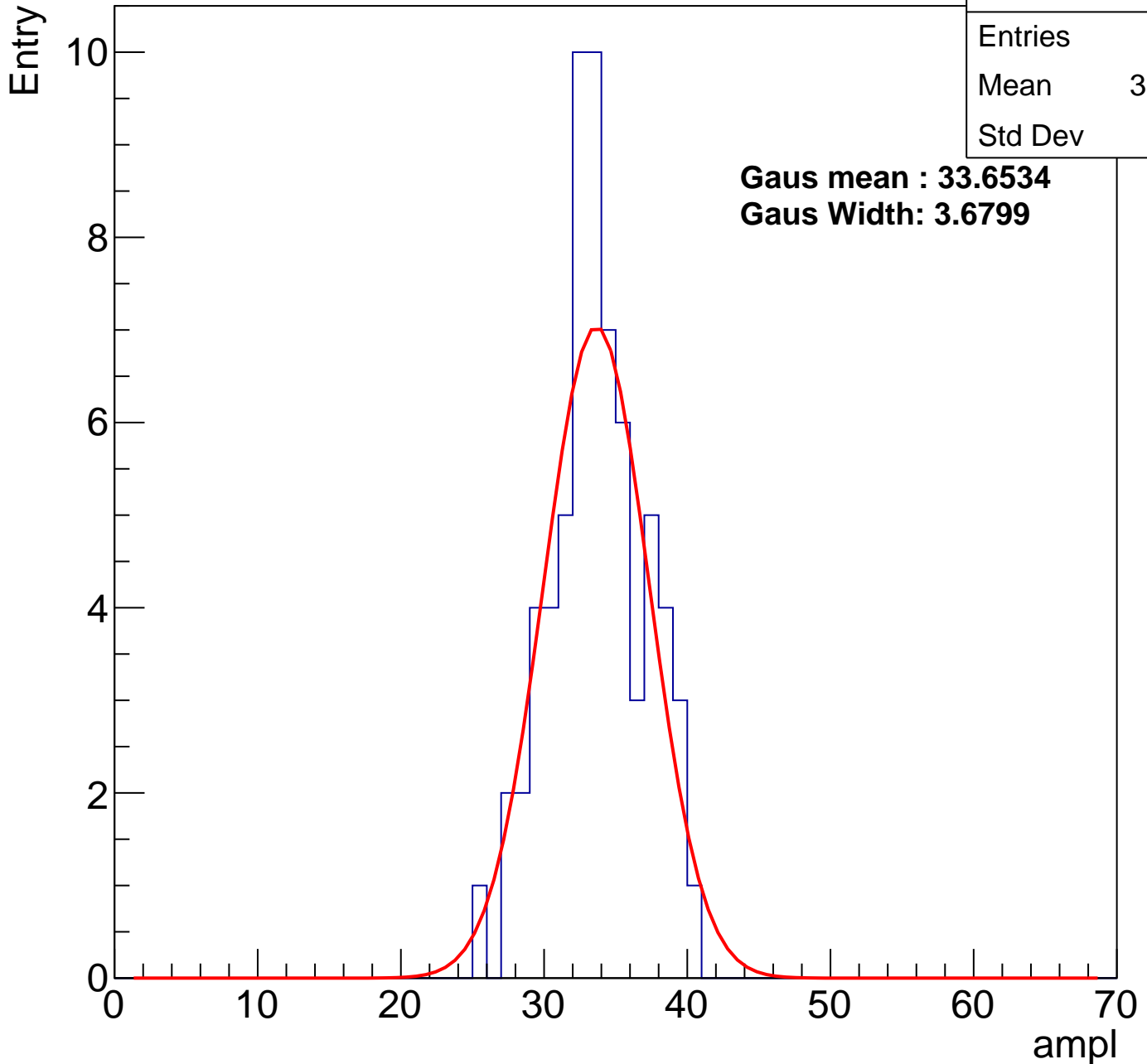
B1L103S, U10-ch86, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	33.22
Std Dev	3.25

Gaus mean : 33.6534

Gaus Width: 3.6799



B1L103S, U10-ch86, adc2

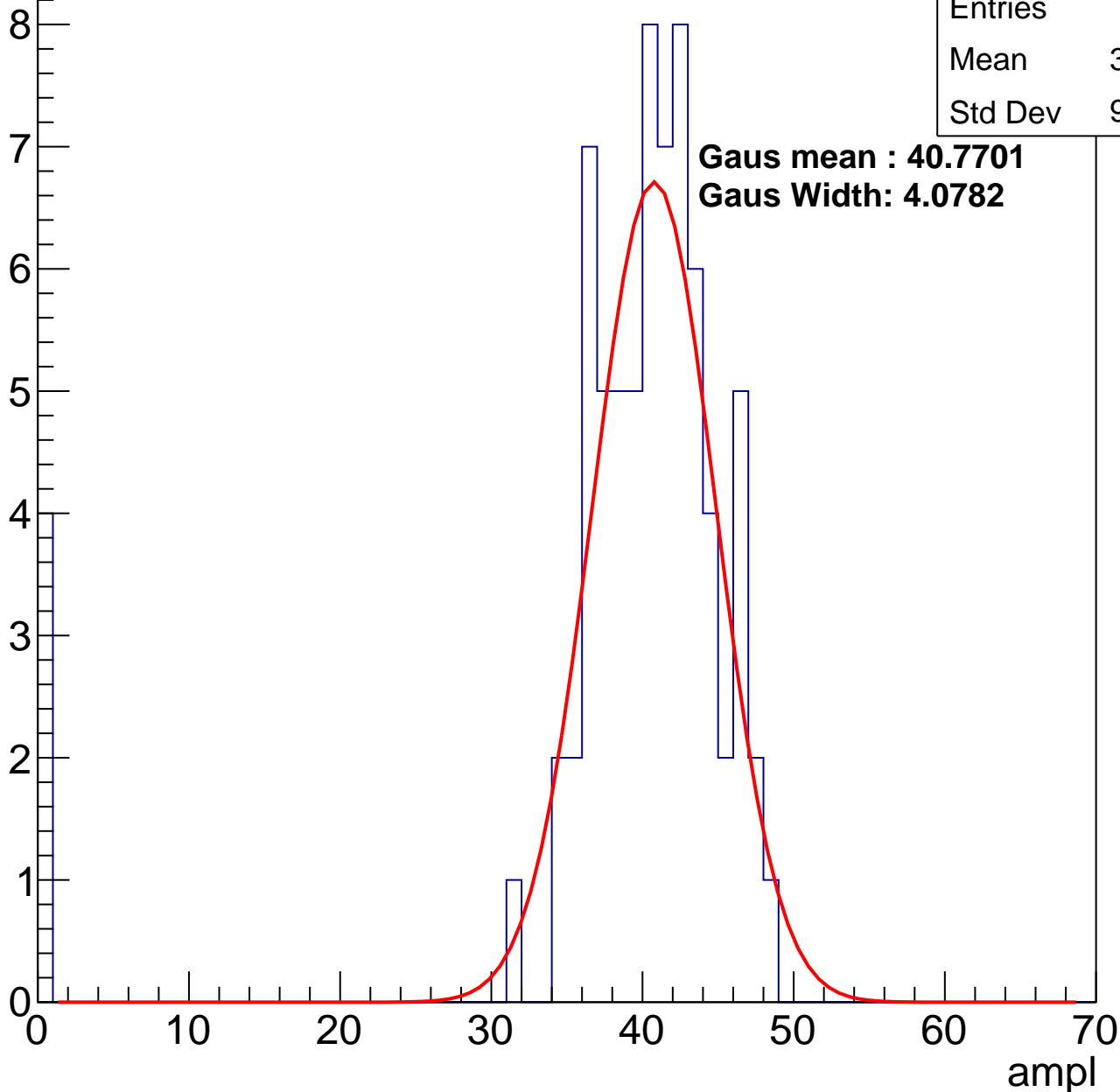
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	38.24
Std Dev	9.799

Gaus mean : 40.7701

Gaus Width: 4.0782

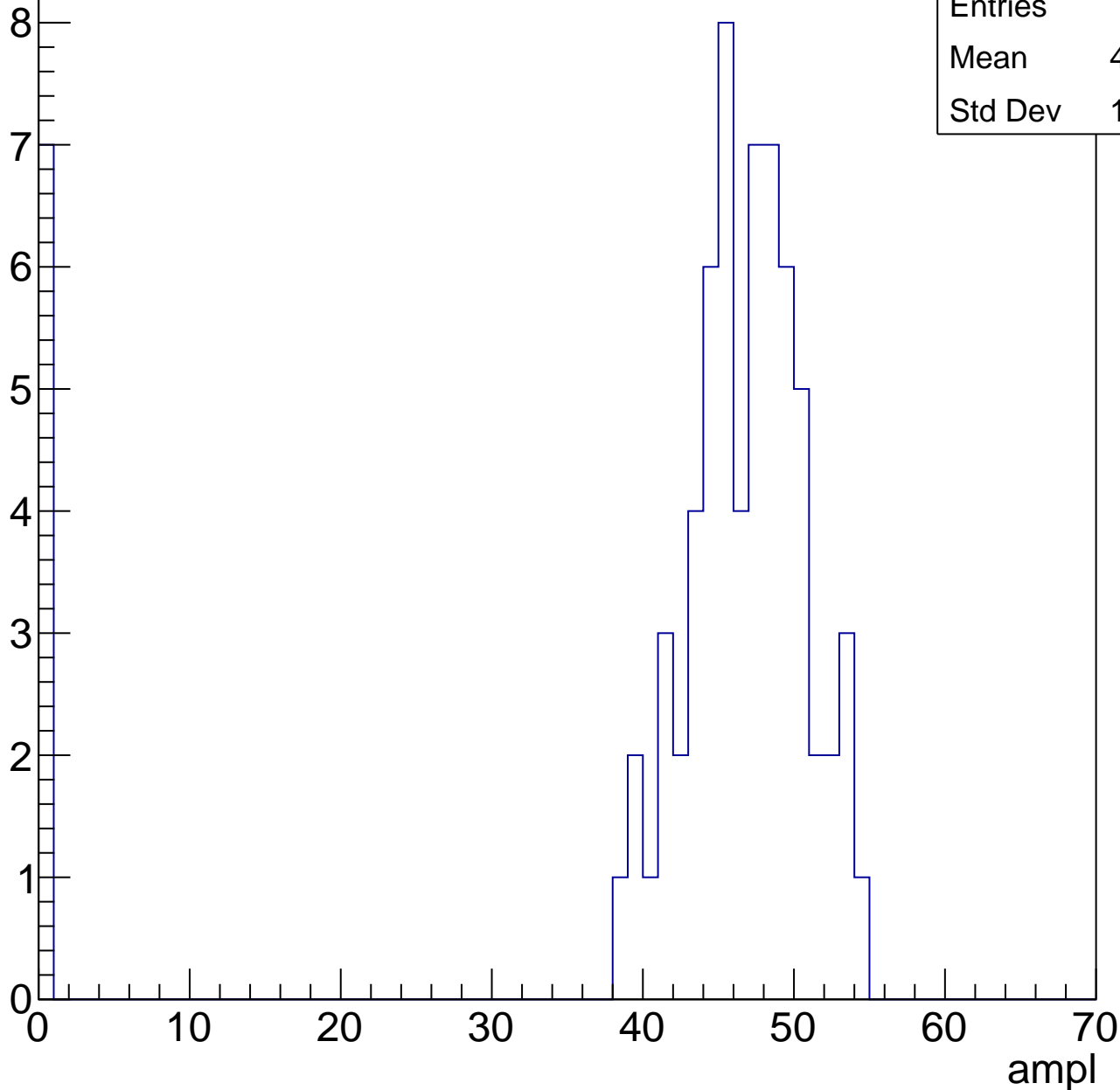


B1L103S, U10-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	41.85
Std Dev	14.27

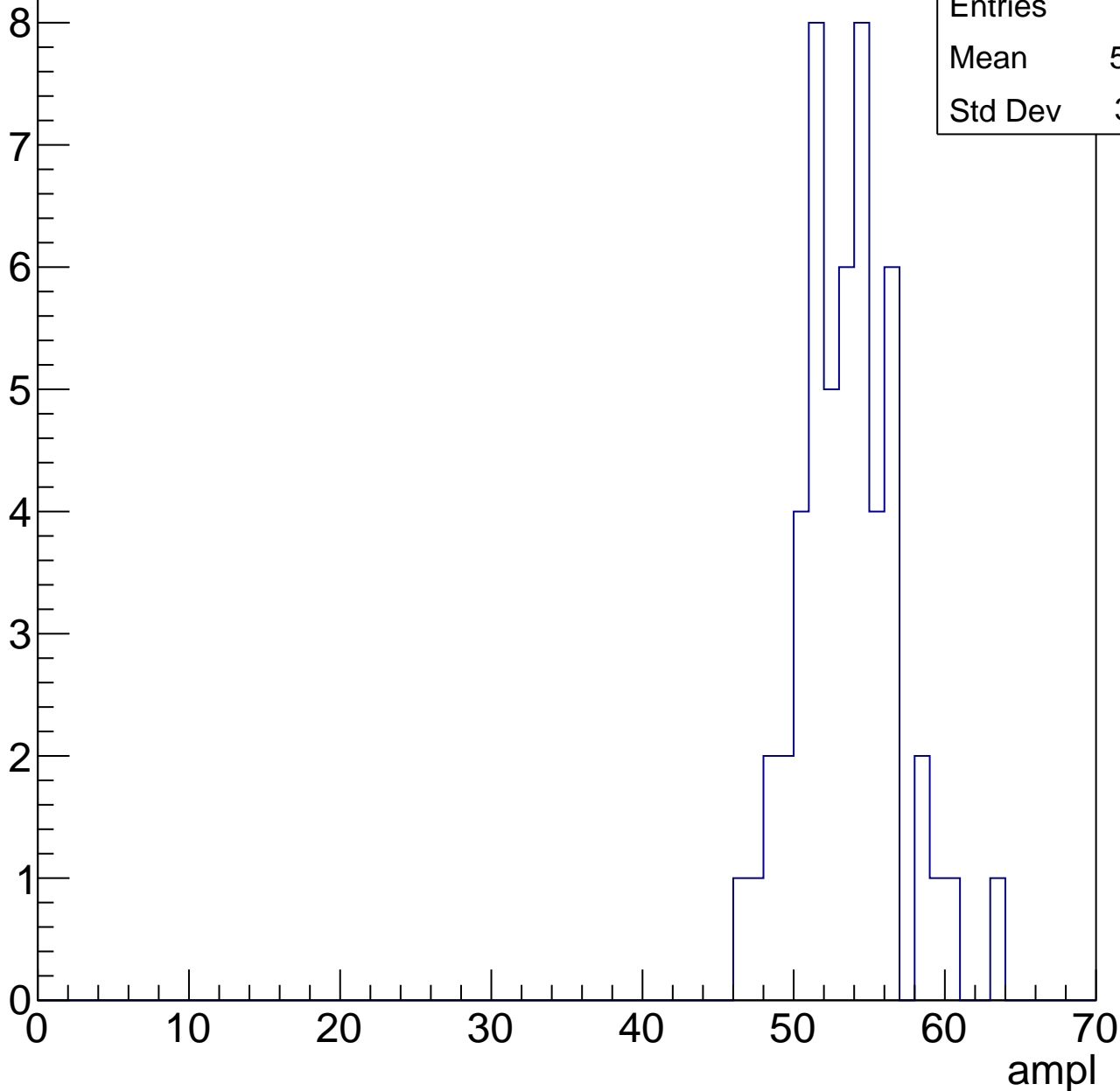


B1L103S, U10-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.06
Std Dev	3.261



B1L103S, U10-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	56.64
Std Dev	8.006

Entry

10

8

6

4

2

0

0

10

20

30

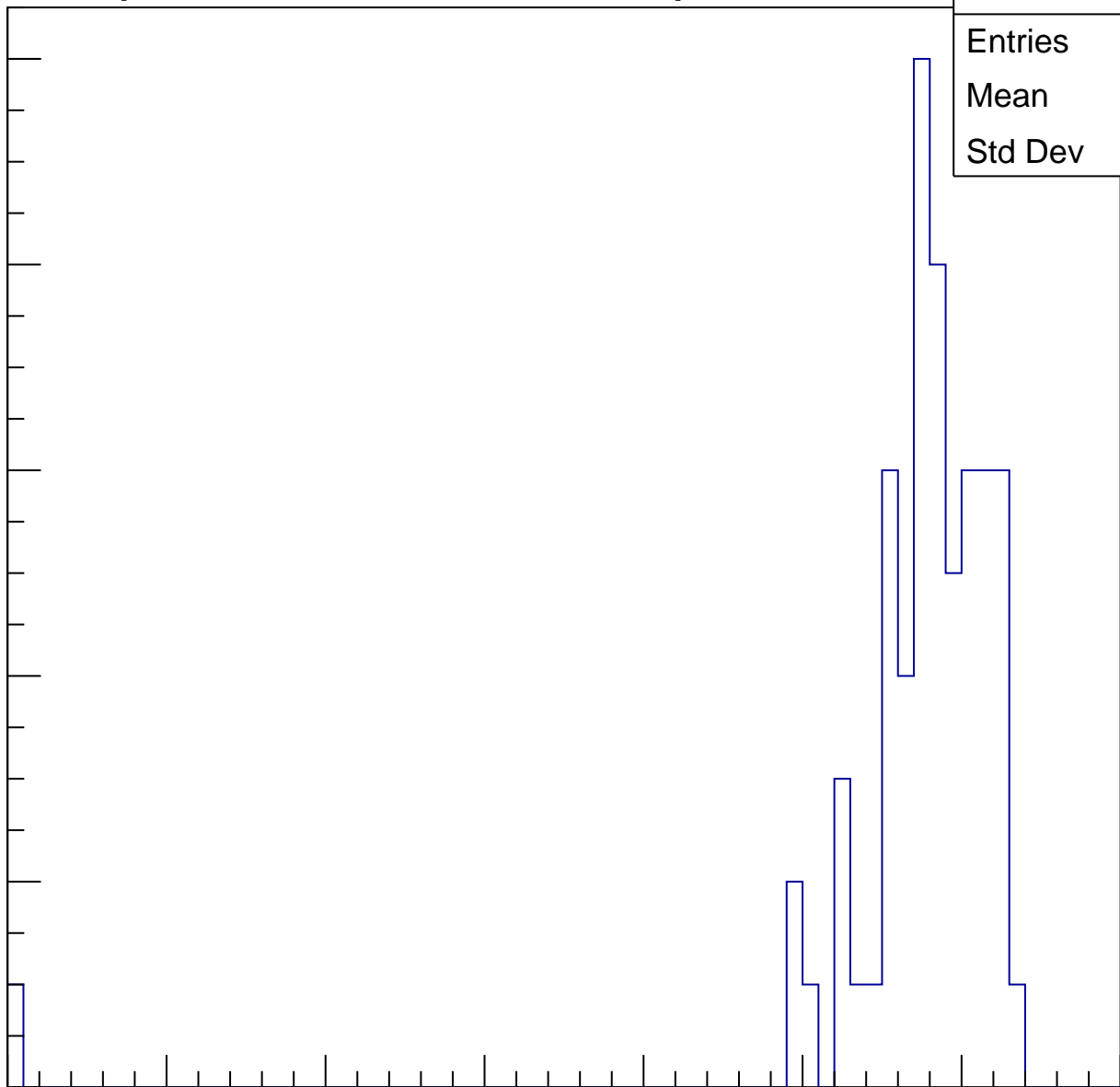
40

50

60

70

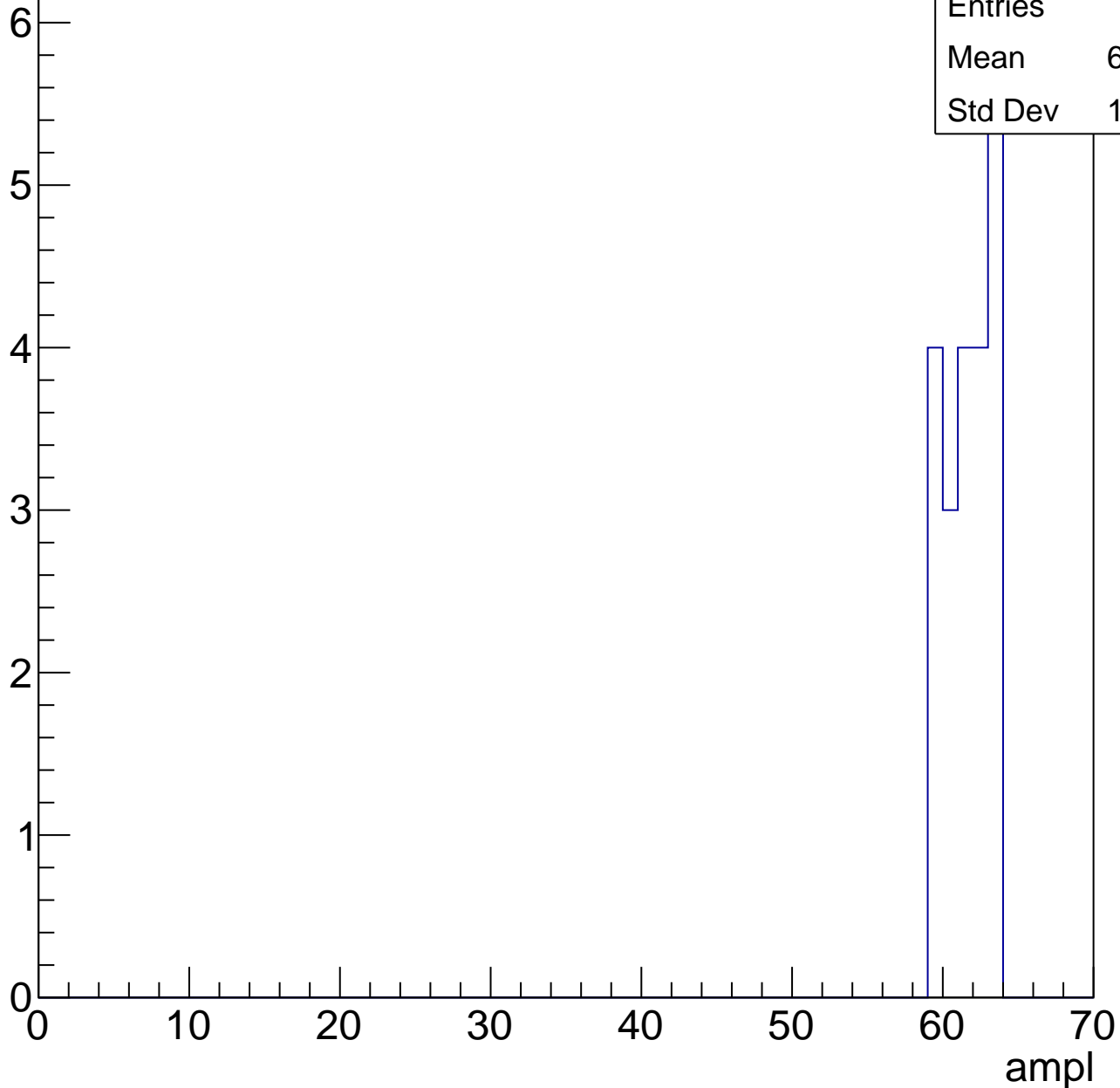
ampl



B1L103S, U10-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U10-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	26.72
Std Dev	9.187

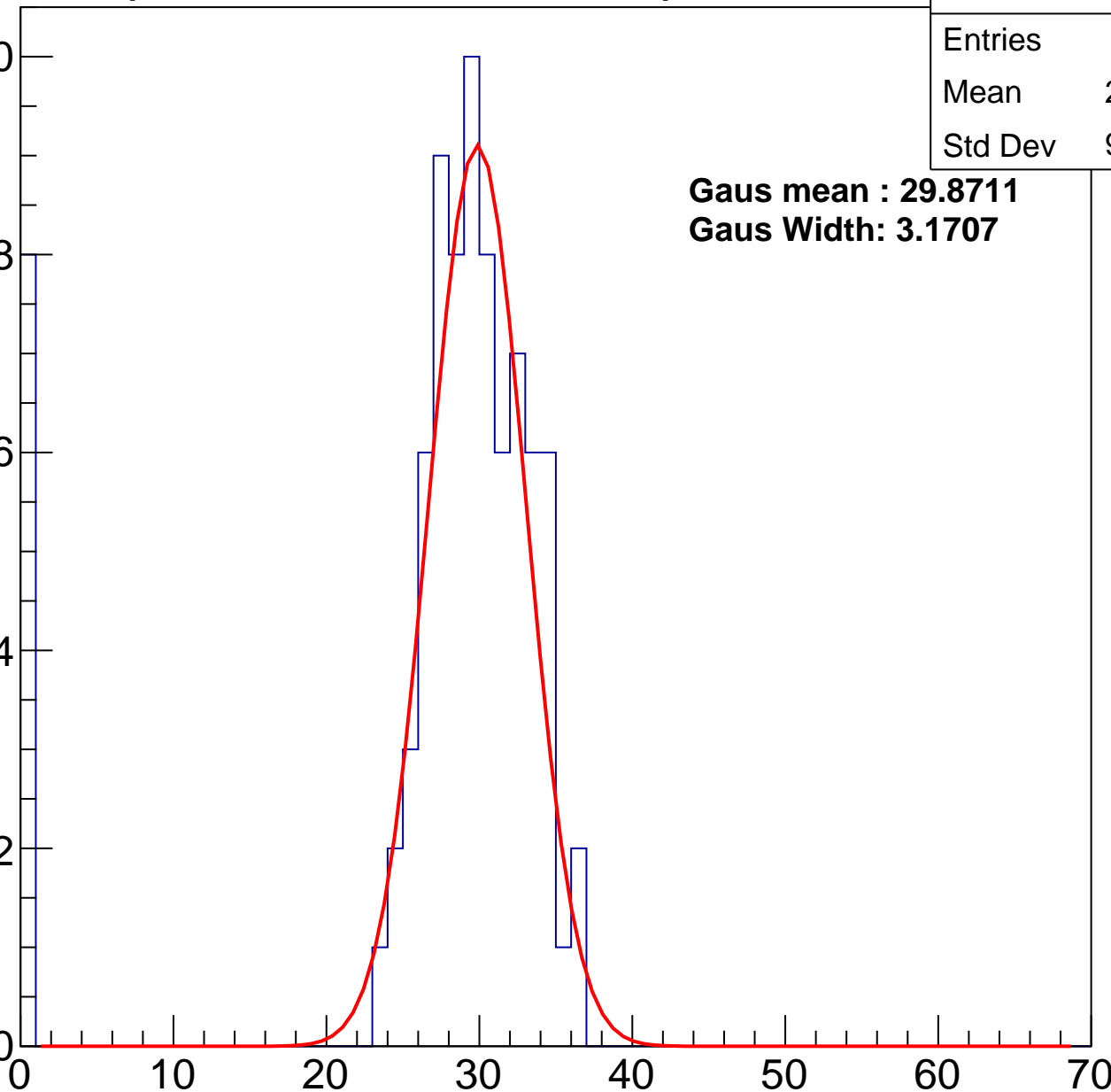
Gaus mean : 29.8711

Gaus Width: 3.1707

Entry

10
8
6
4
2
0

ampl



B1L103S, U10-ch87, adc1

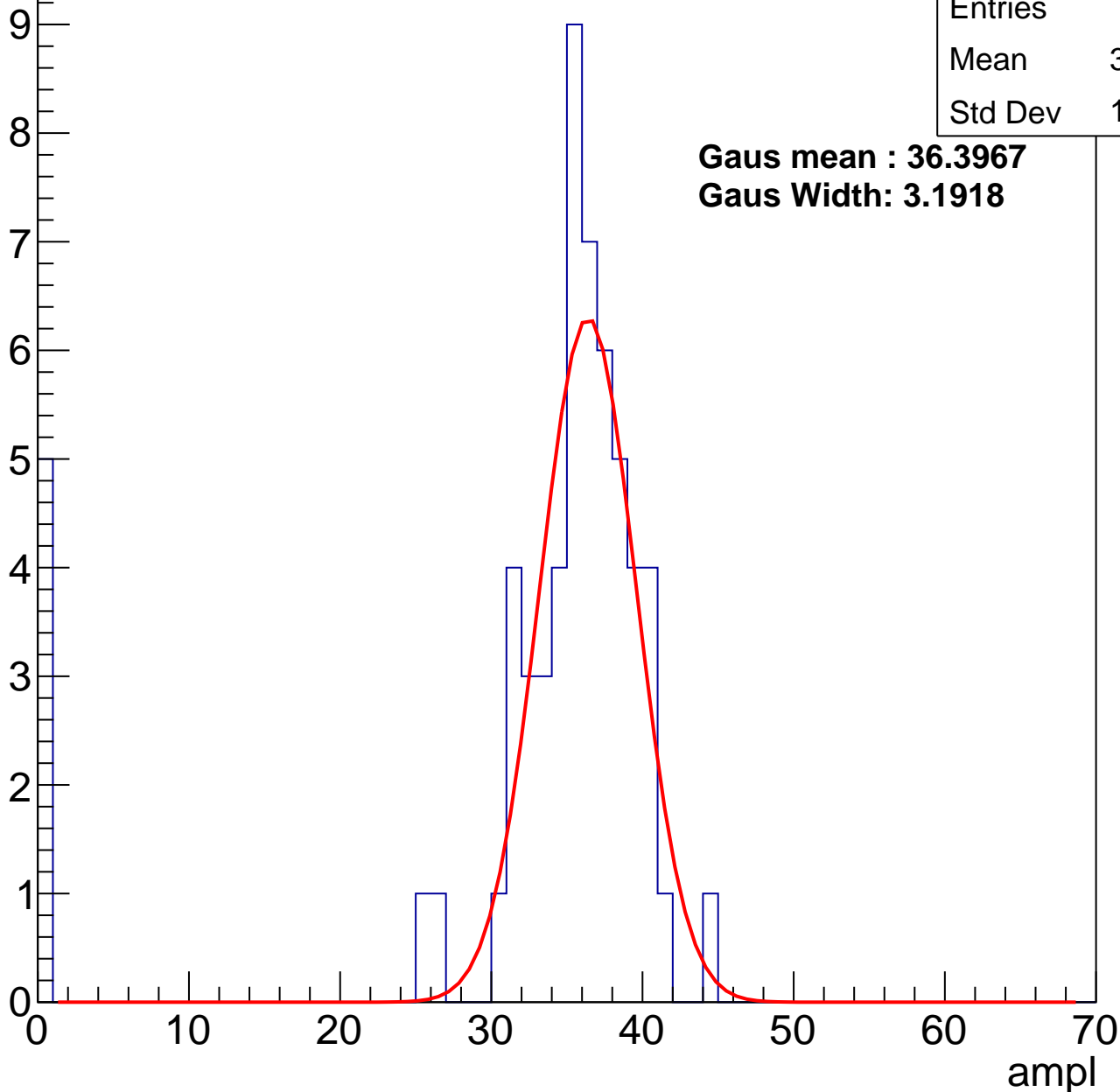
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	32.47
Std Dev	10.43

Gaus mean : 36.3967

Gaus Width: 3.1918



B1L103S, U10-ch87, adc2

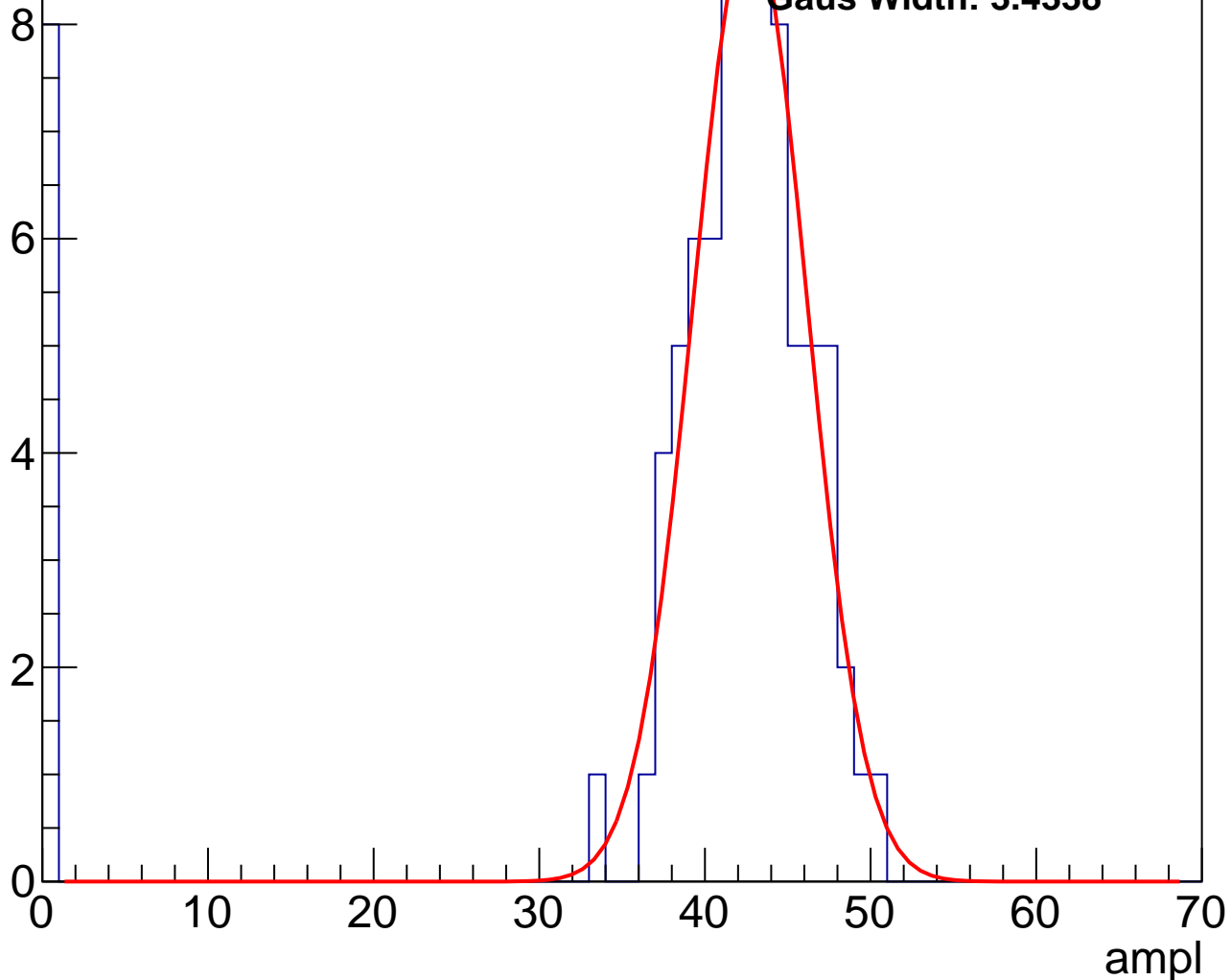
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	38.31
Std Dev	12.67

Gaus mean : 42.7340

Gaus Width: 3.4338

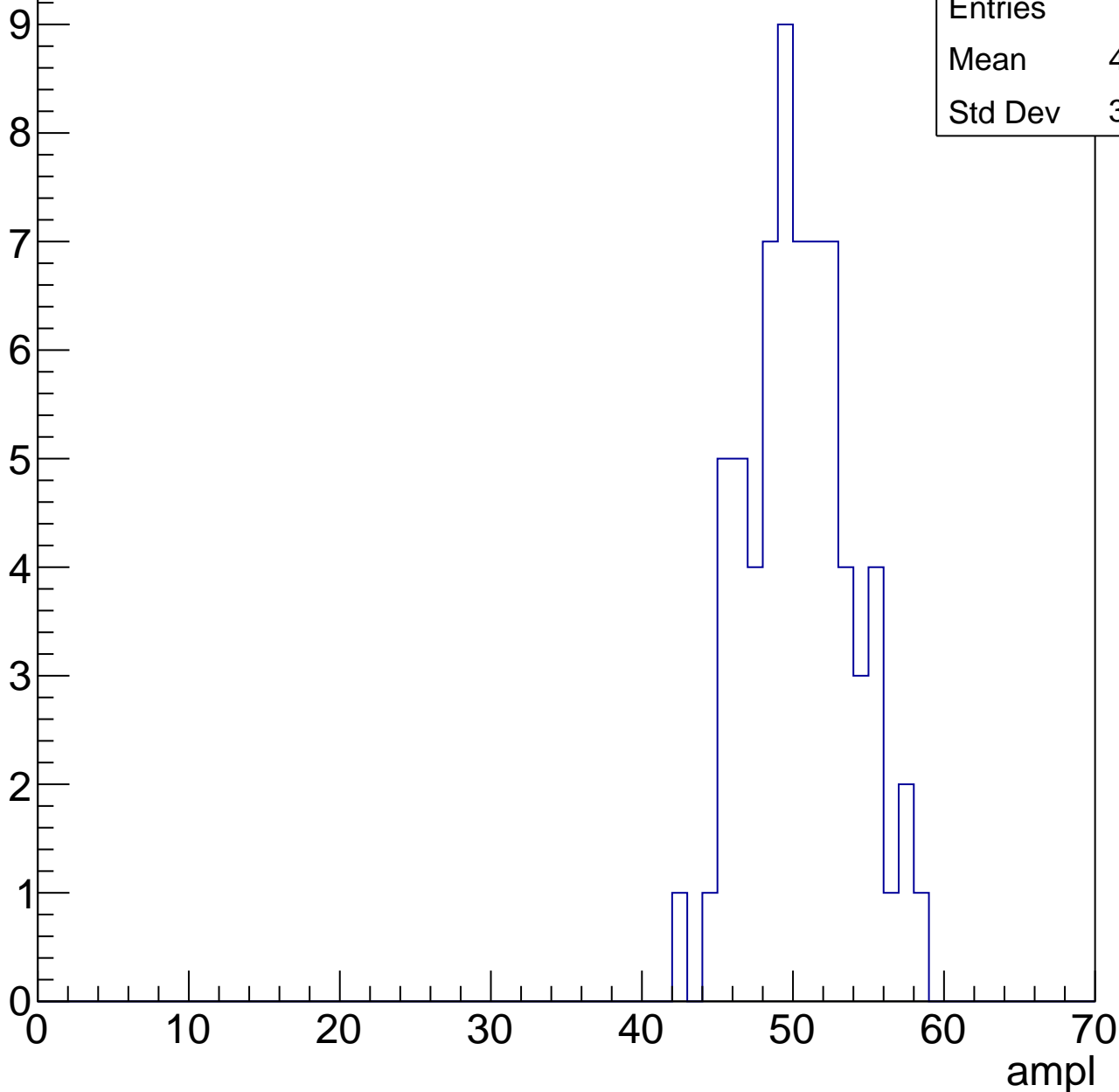


B1L103S, U10-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	49.99
Std Dev	3.419

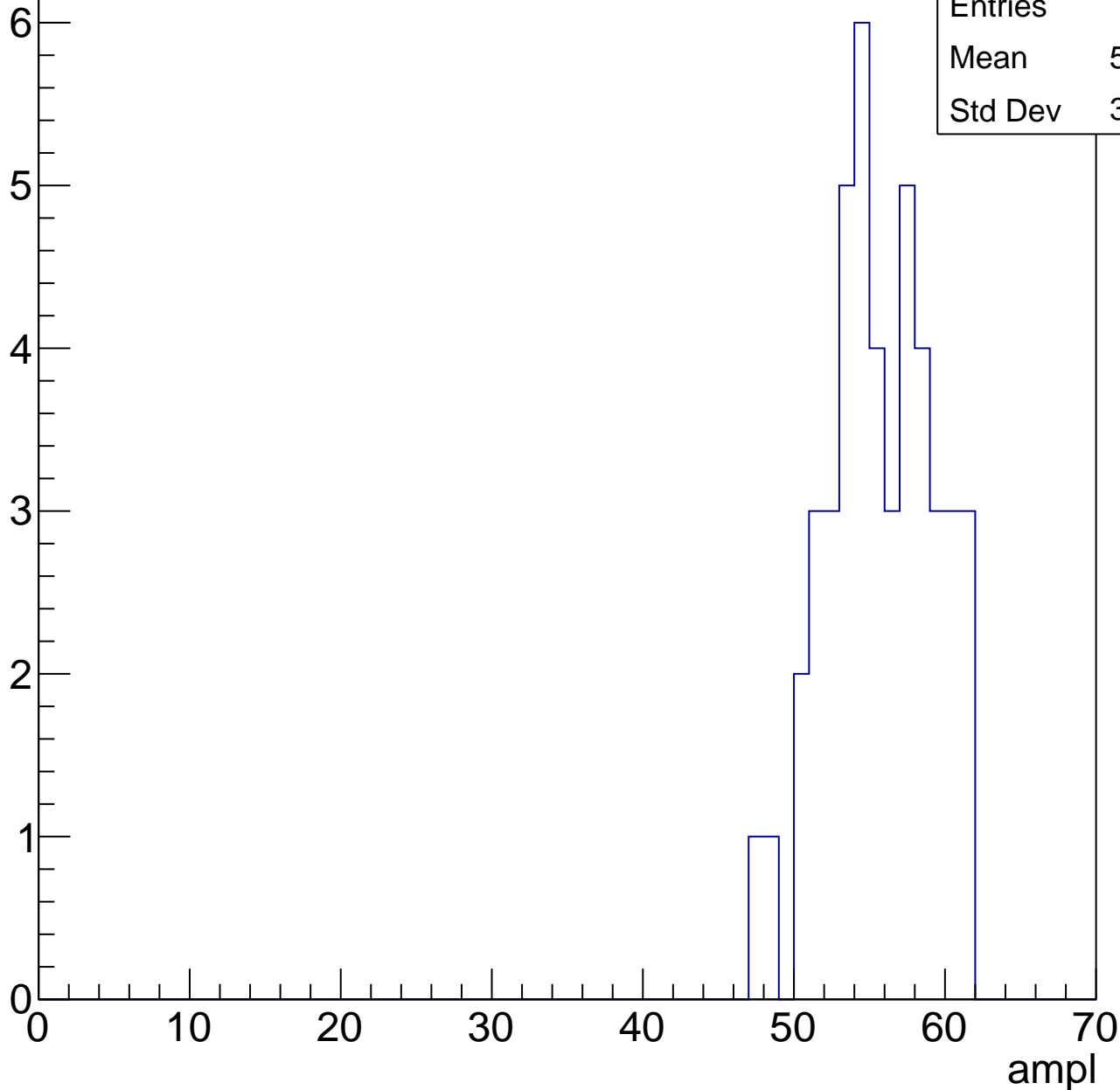


B1L103S, U10-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	55.17
Std Dev	3.466

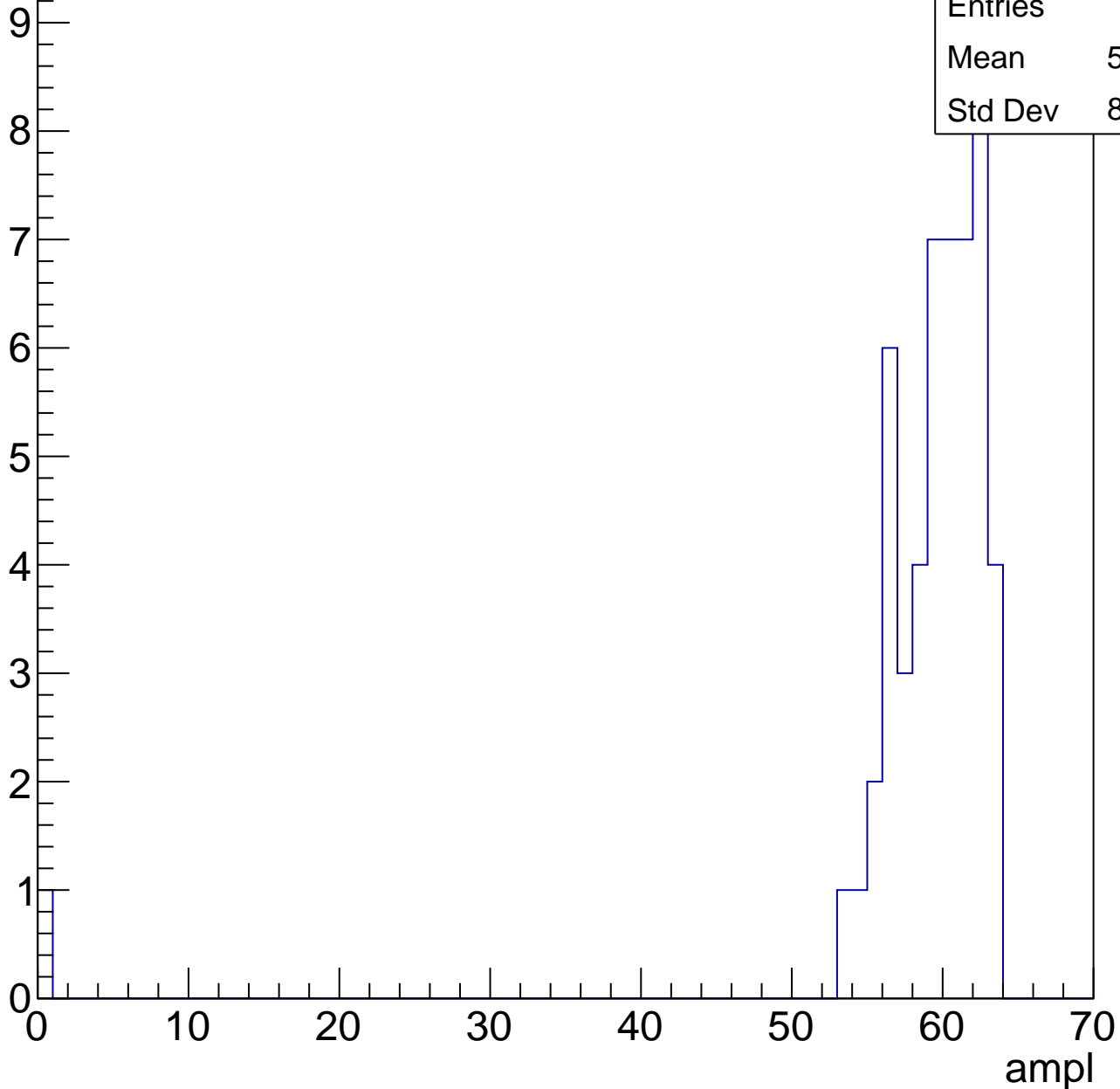


B1L103S, U10-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.19
Std Dev	8.537

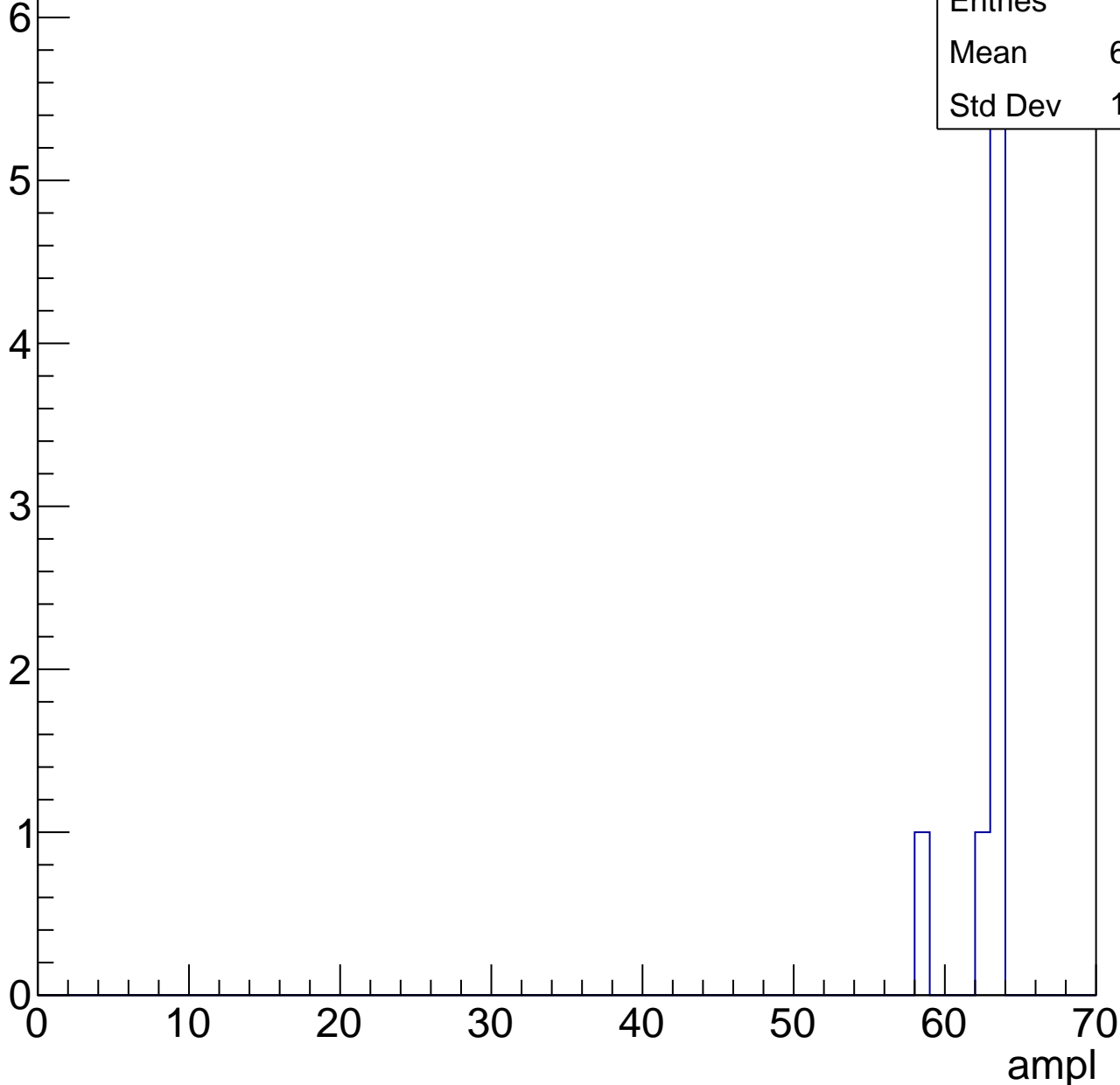


B1L103S, U10-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	8
Mean	62.25
Std Dev	1.639



B1L103S, U10-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch88, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.44
Std Dev	7.126

Gaus mean : 26.6635

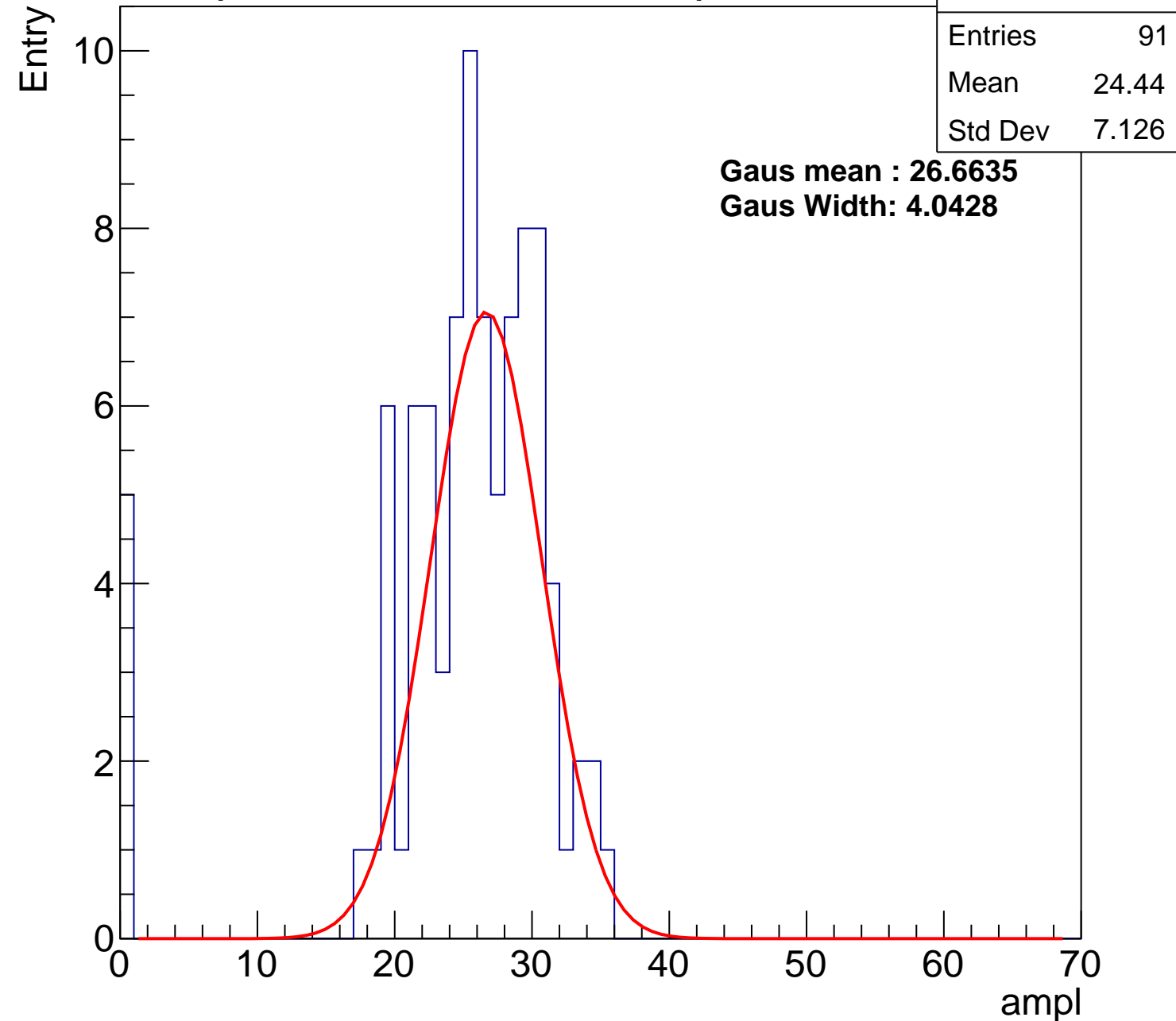
Gaus Width: 4.0428

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch88, adc1

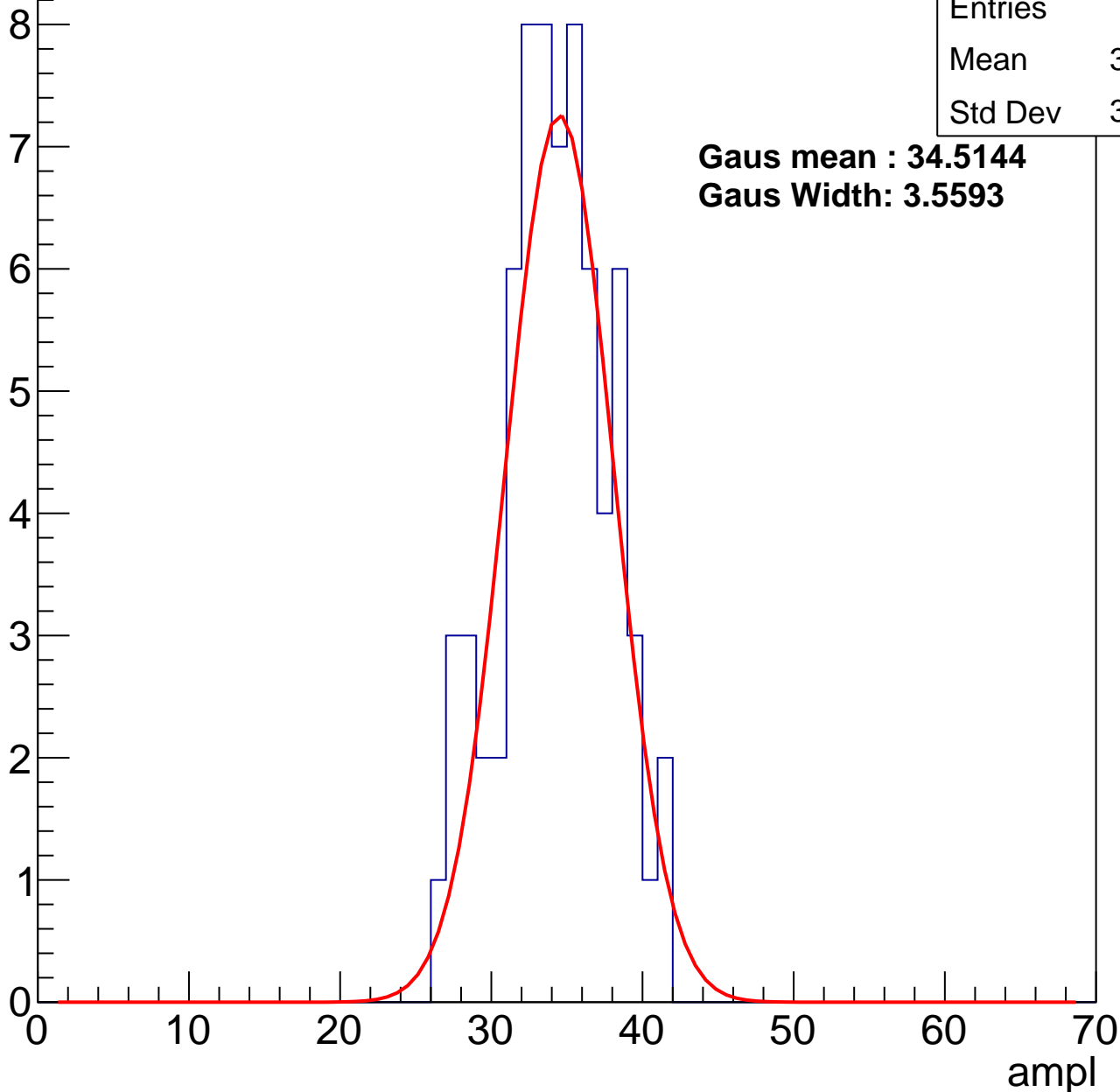
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.77
Std Dev	3.514

Gaus mean : 34.5144

Gaus Width: 3.5593



B1L103S, U10-ch88, adc2

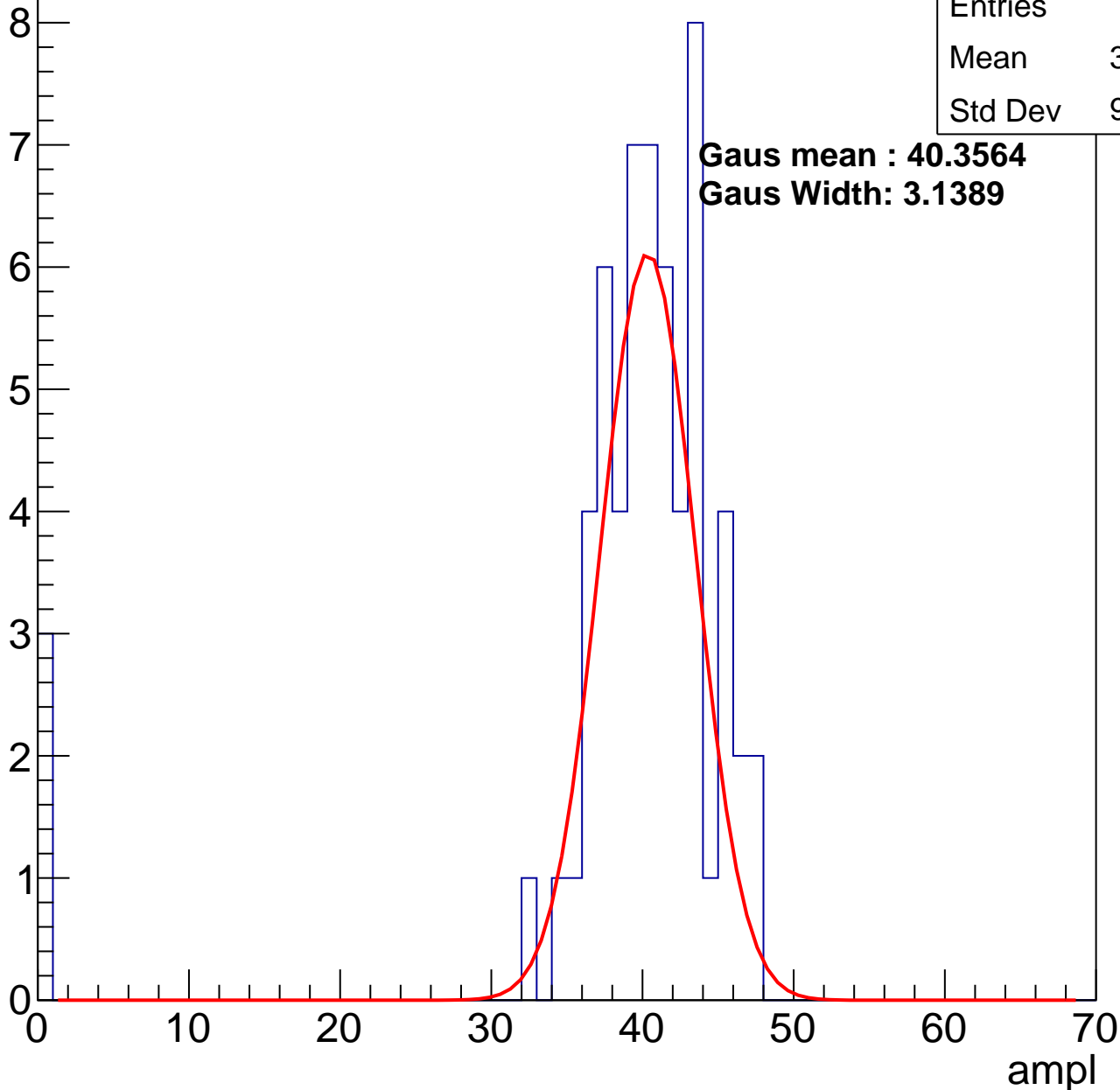
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.36
Std Dev	9.305

Gaus mean : 40.3564

Gaus Width: 3.1389

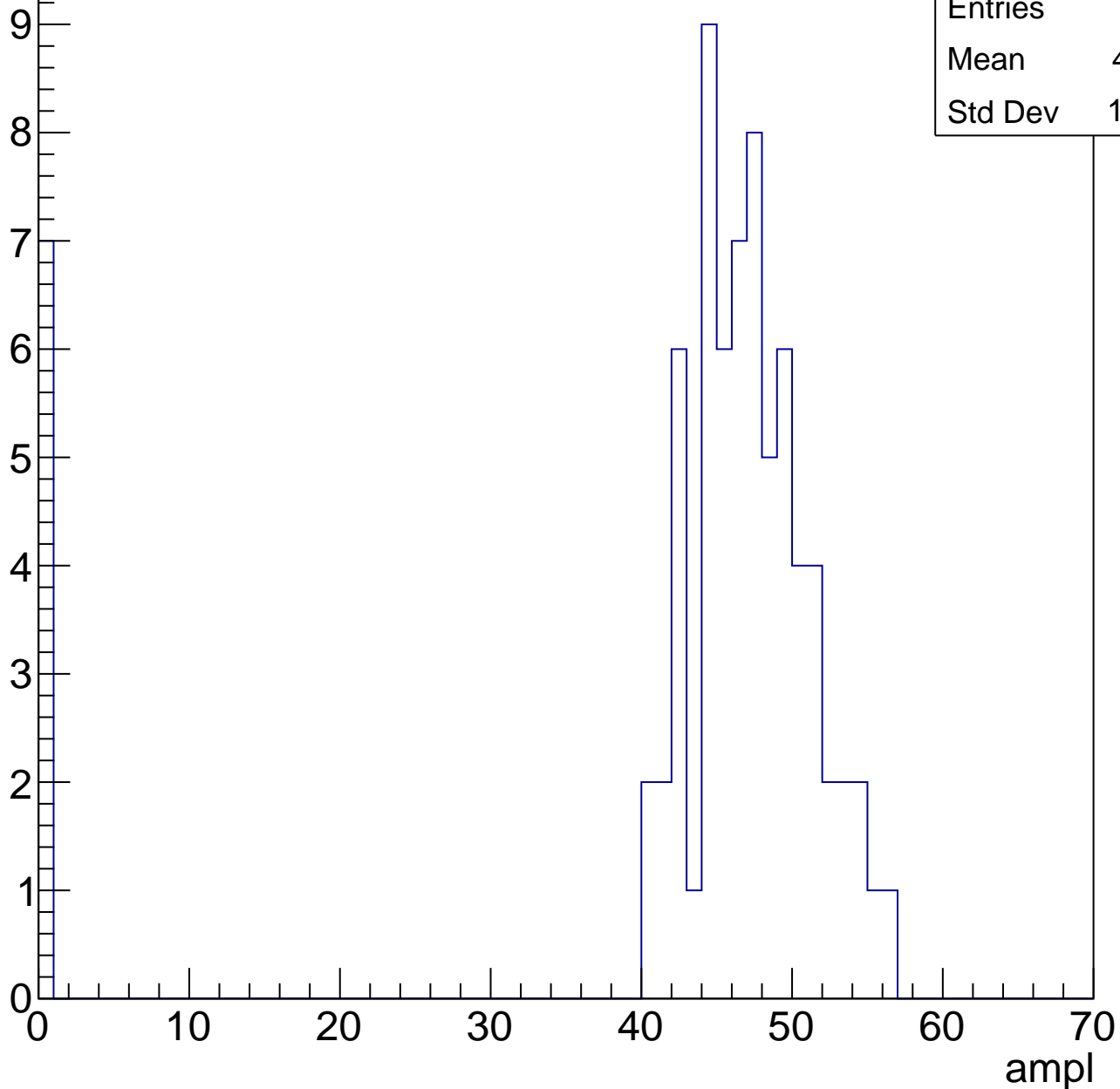


B1L103S, U10-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	42.51
Std Dev	14.09

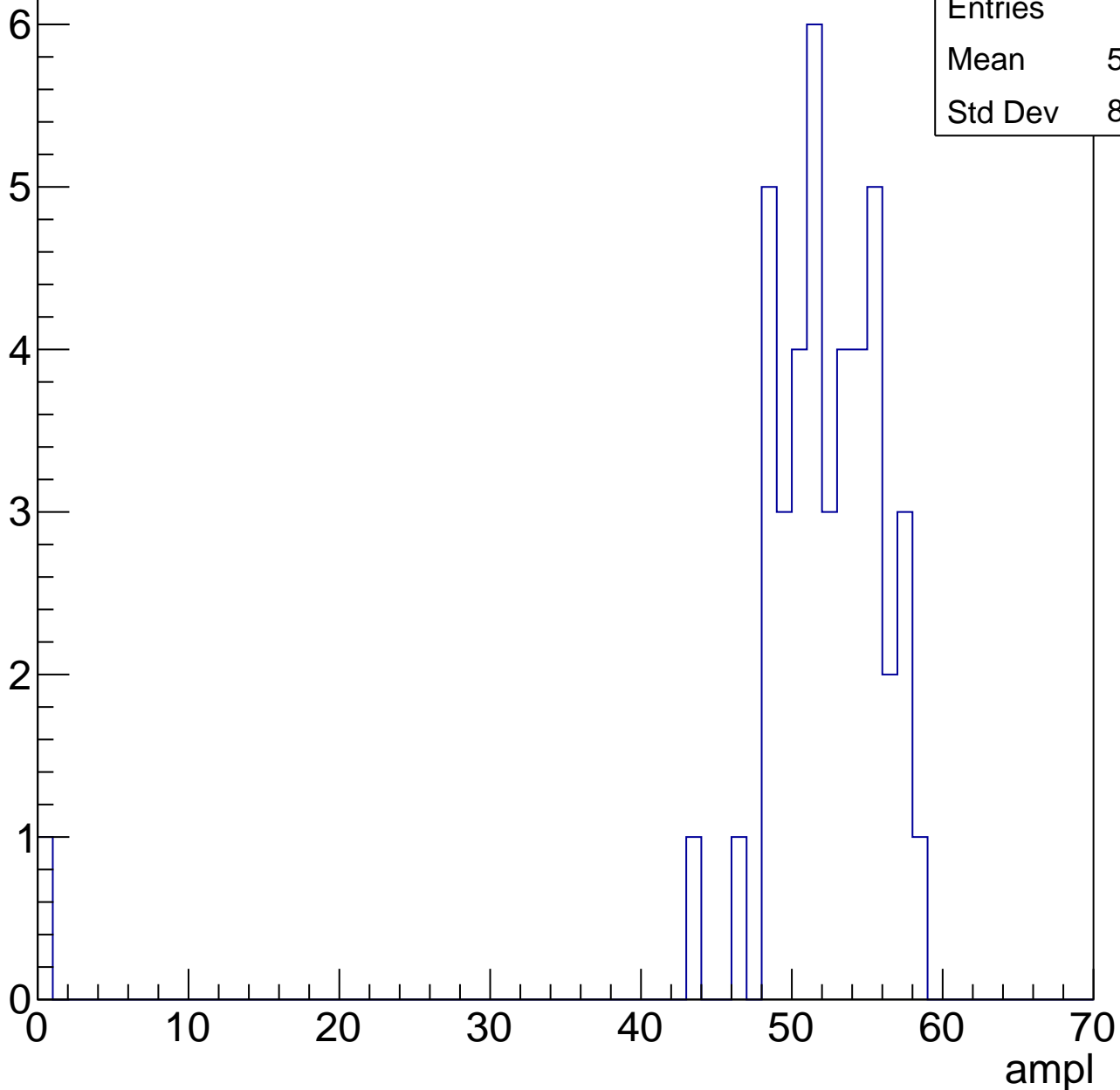


B1L103S, U10-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	50.74
Std Dev	8.477

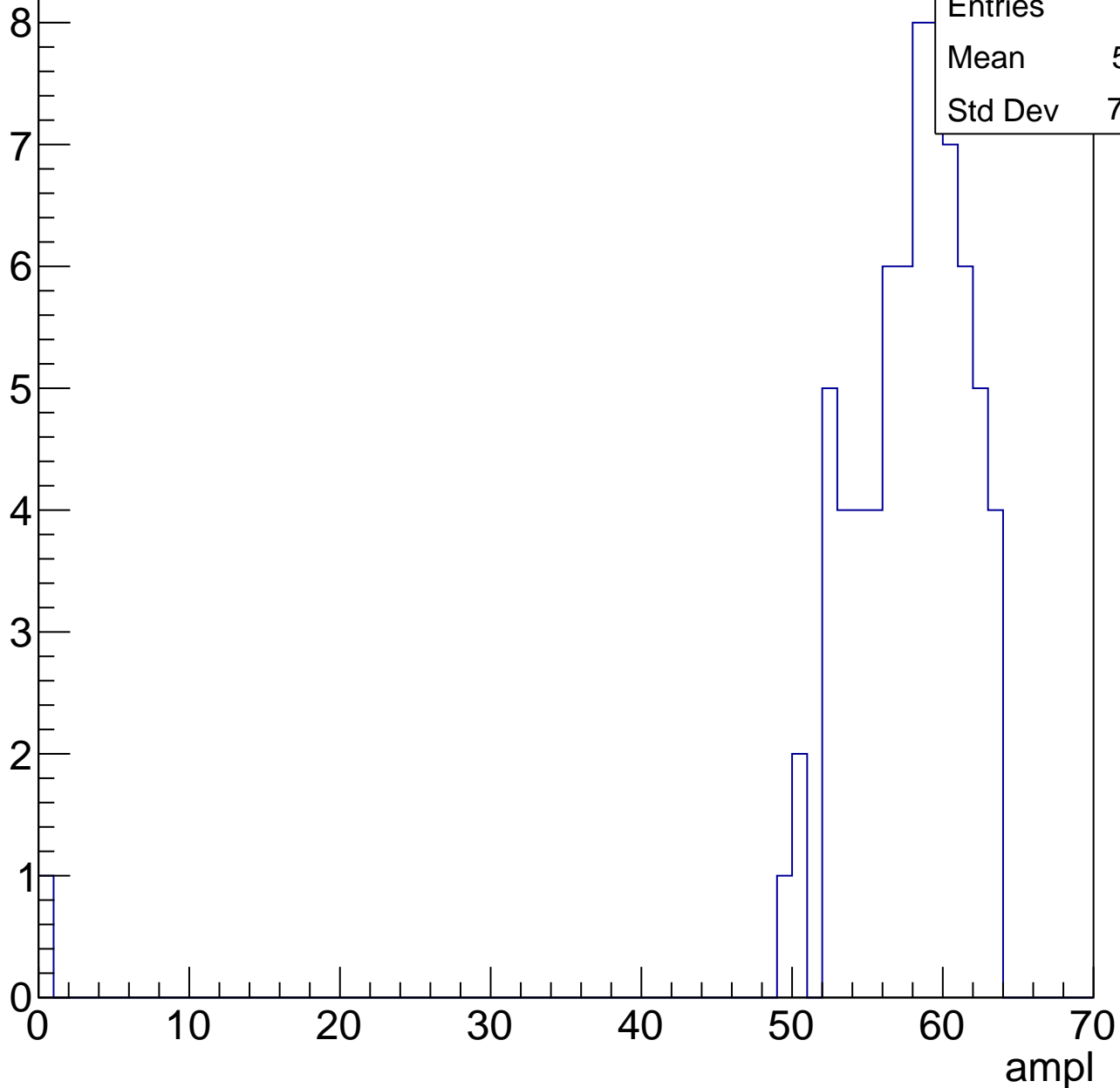


B1L103S, U10-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	56.61
Std Dev	7.613

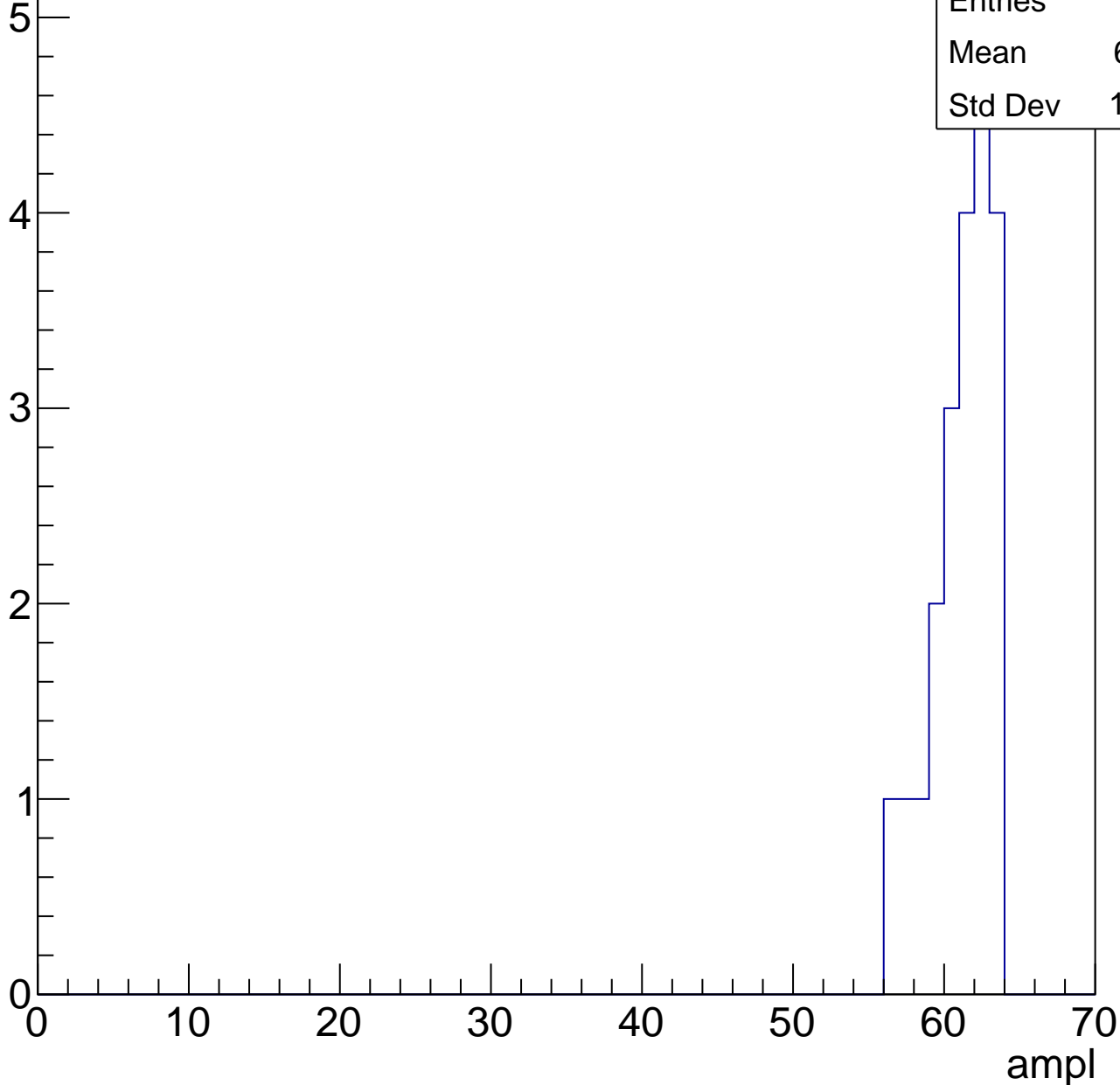


B1L103S, U10-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	60.71
Std Dev	1.955

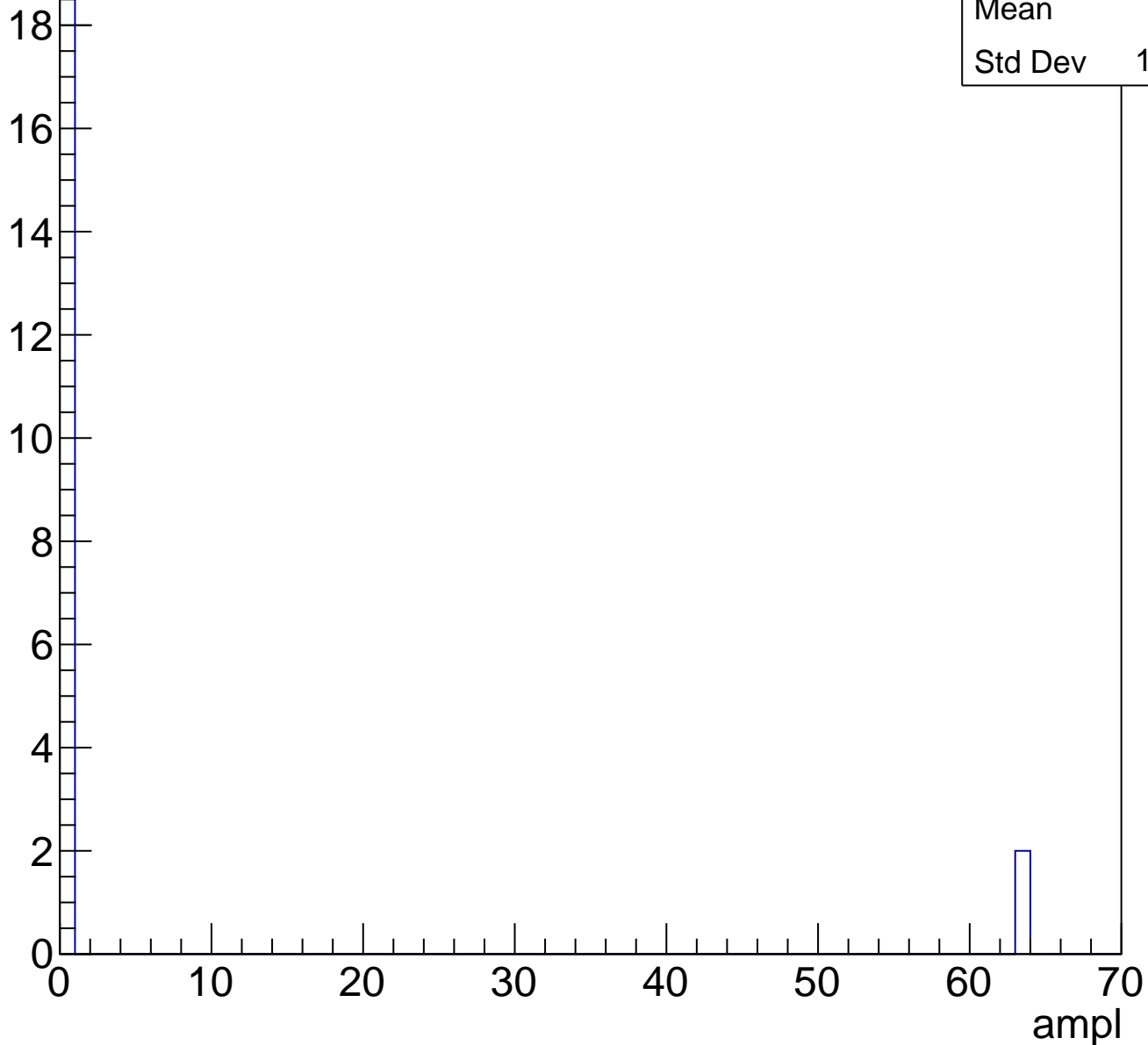


B1L103S, U10-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U10-ch89, adc0

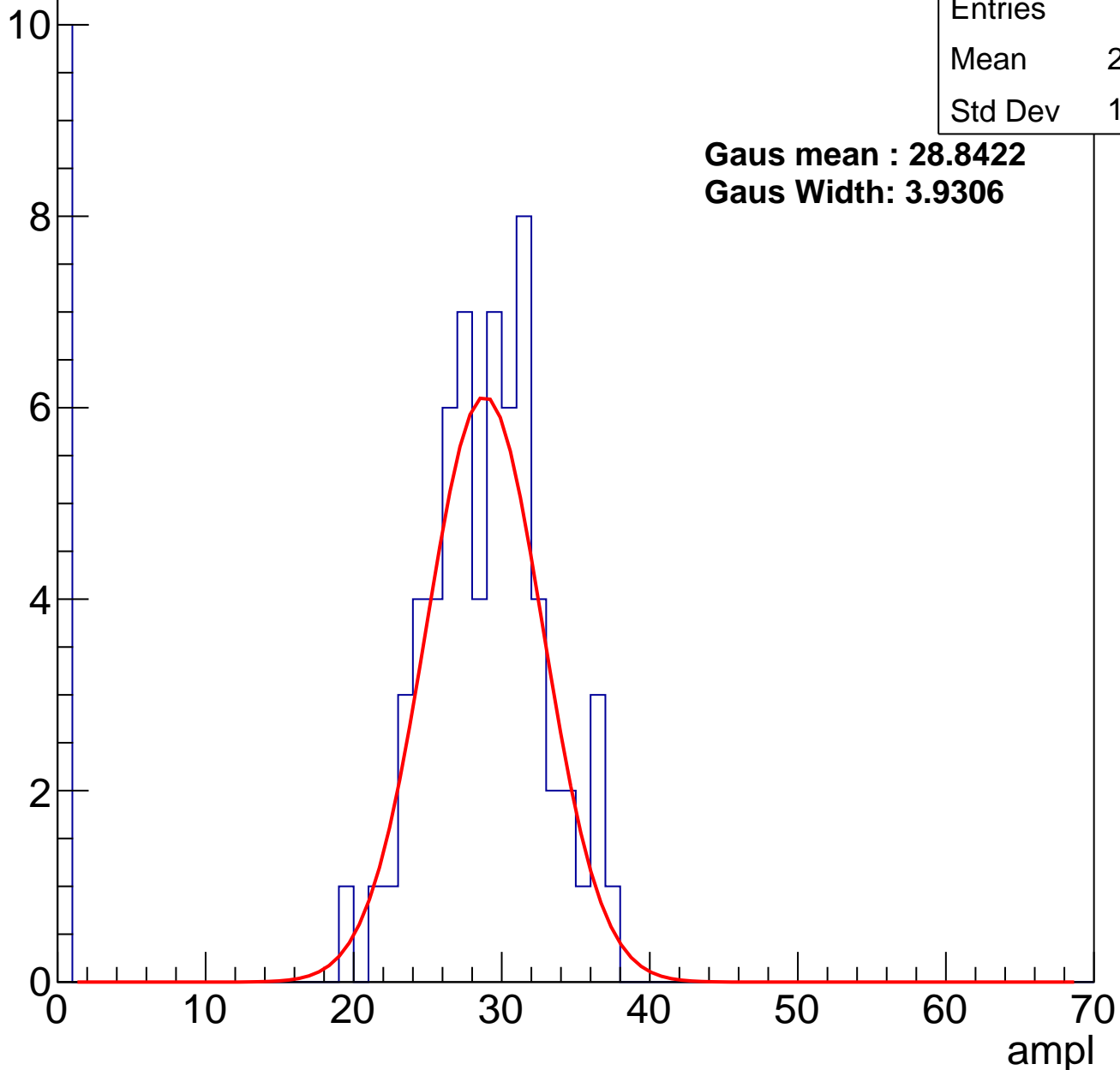
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.76
Std Dev	10.35

Gaus mean : 28.8422

Gaus Width: 3.9306

Entry



B1L103S, U10-ch89, adc1

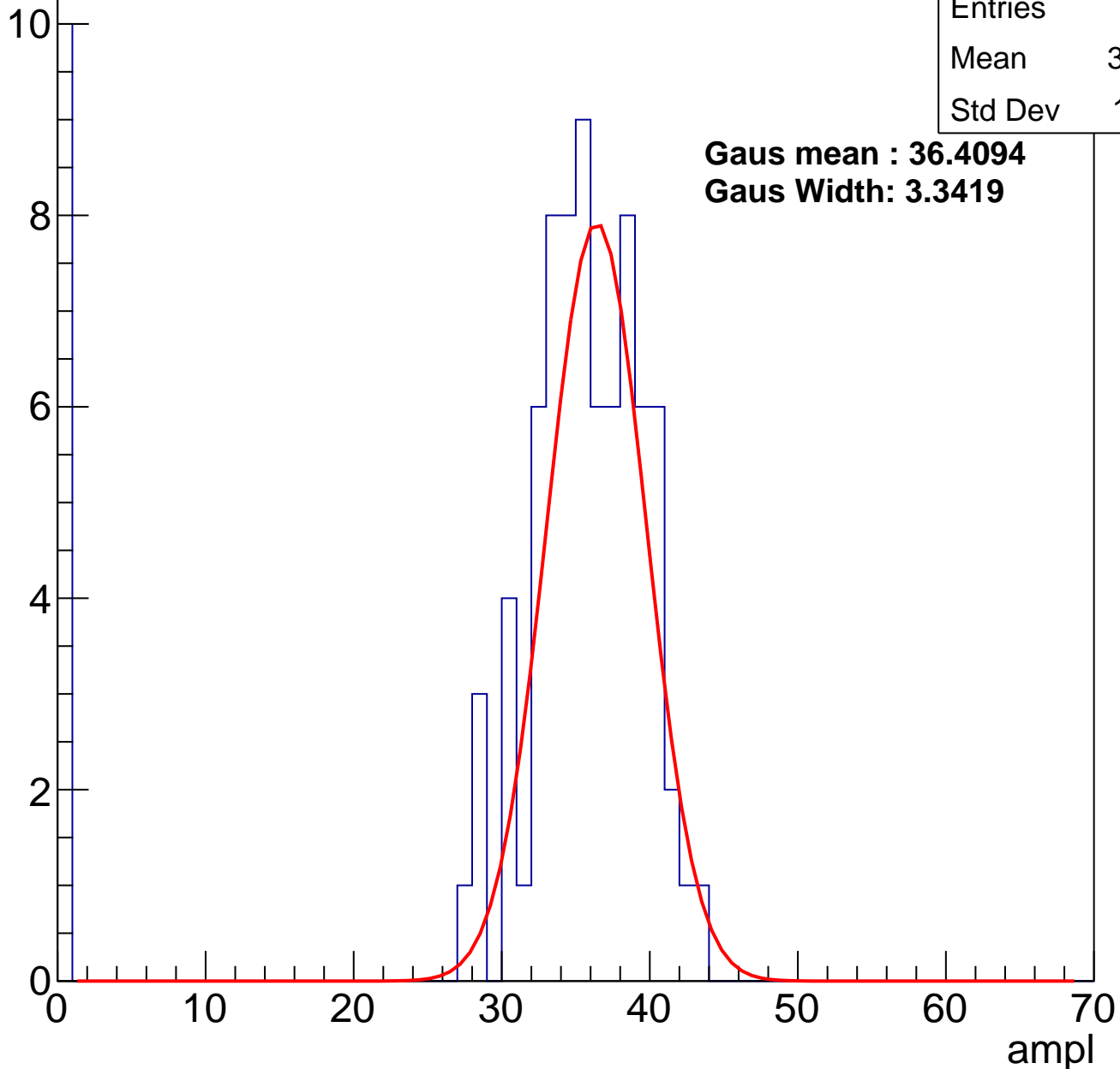
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	31.26
Std Dev	11.81

Gaus mean : 36.4094

Gaus Width: 3.3419

Entry



B1L103S, U10-ch89, adc2

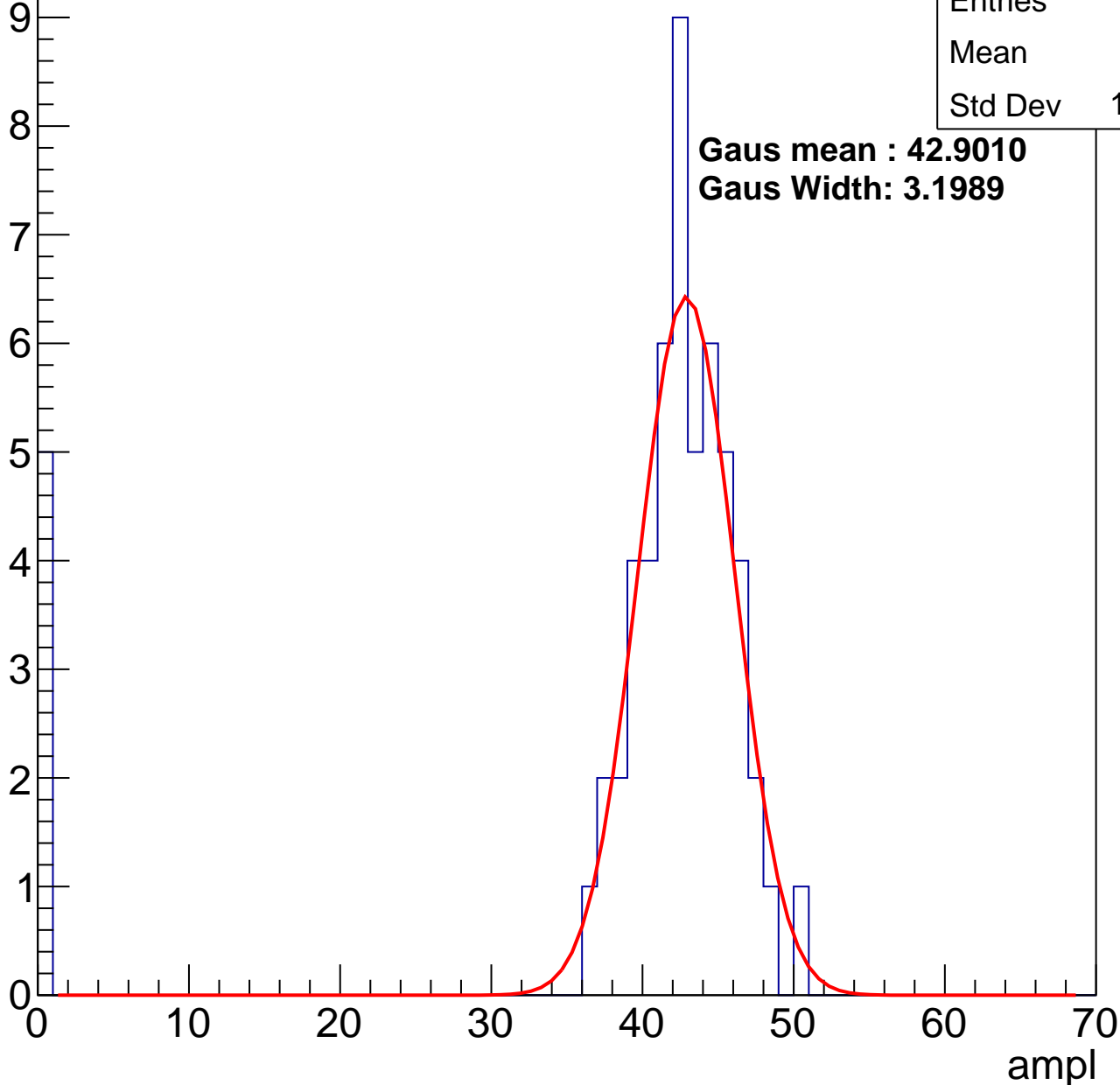
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	38.7
Std Dev	12.33

Gaus mean : 42.9010

Gaus Width: 3.1989

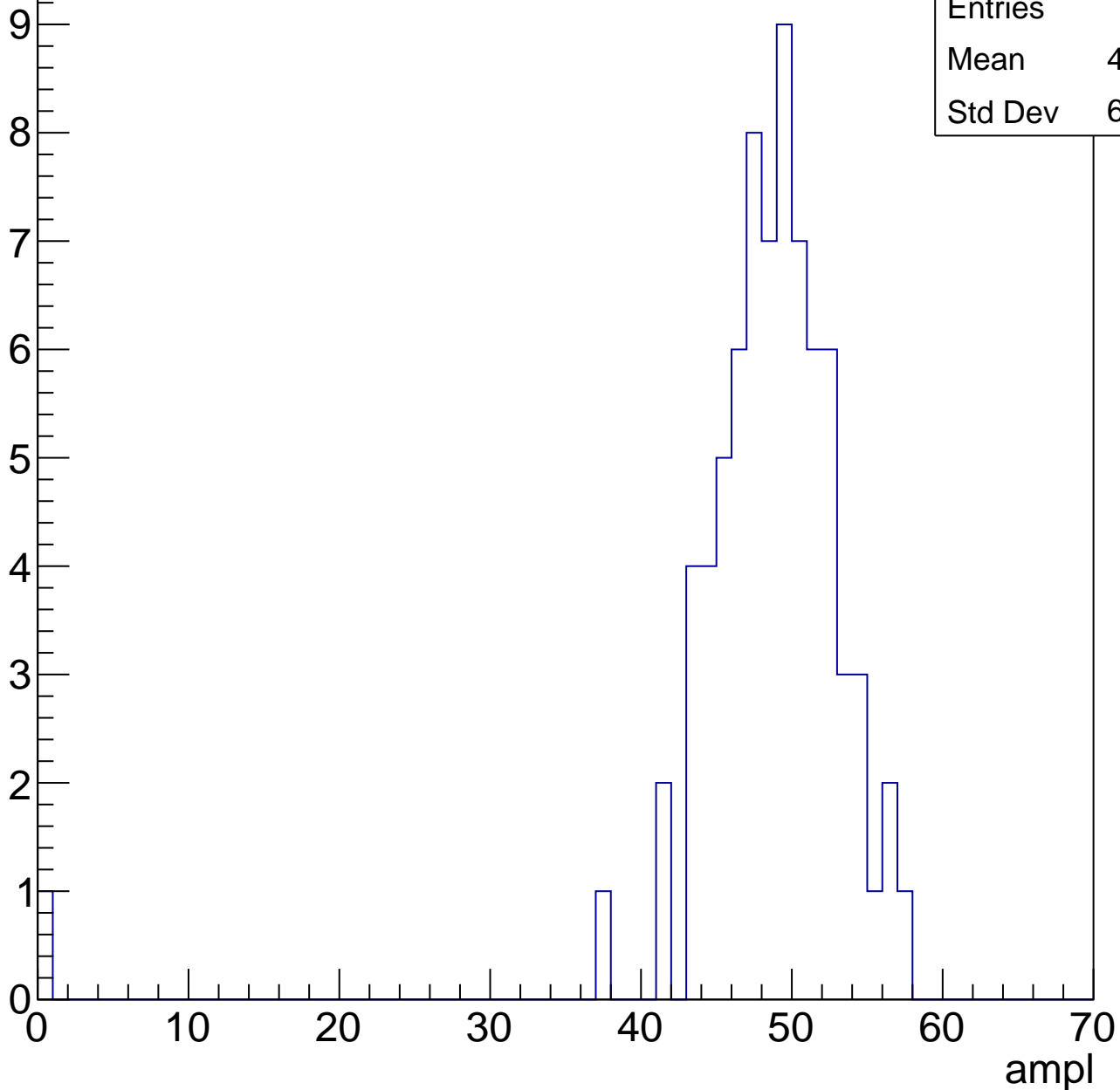


B1L103S, U10-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

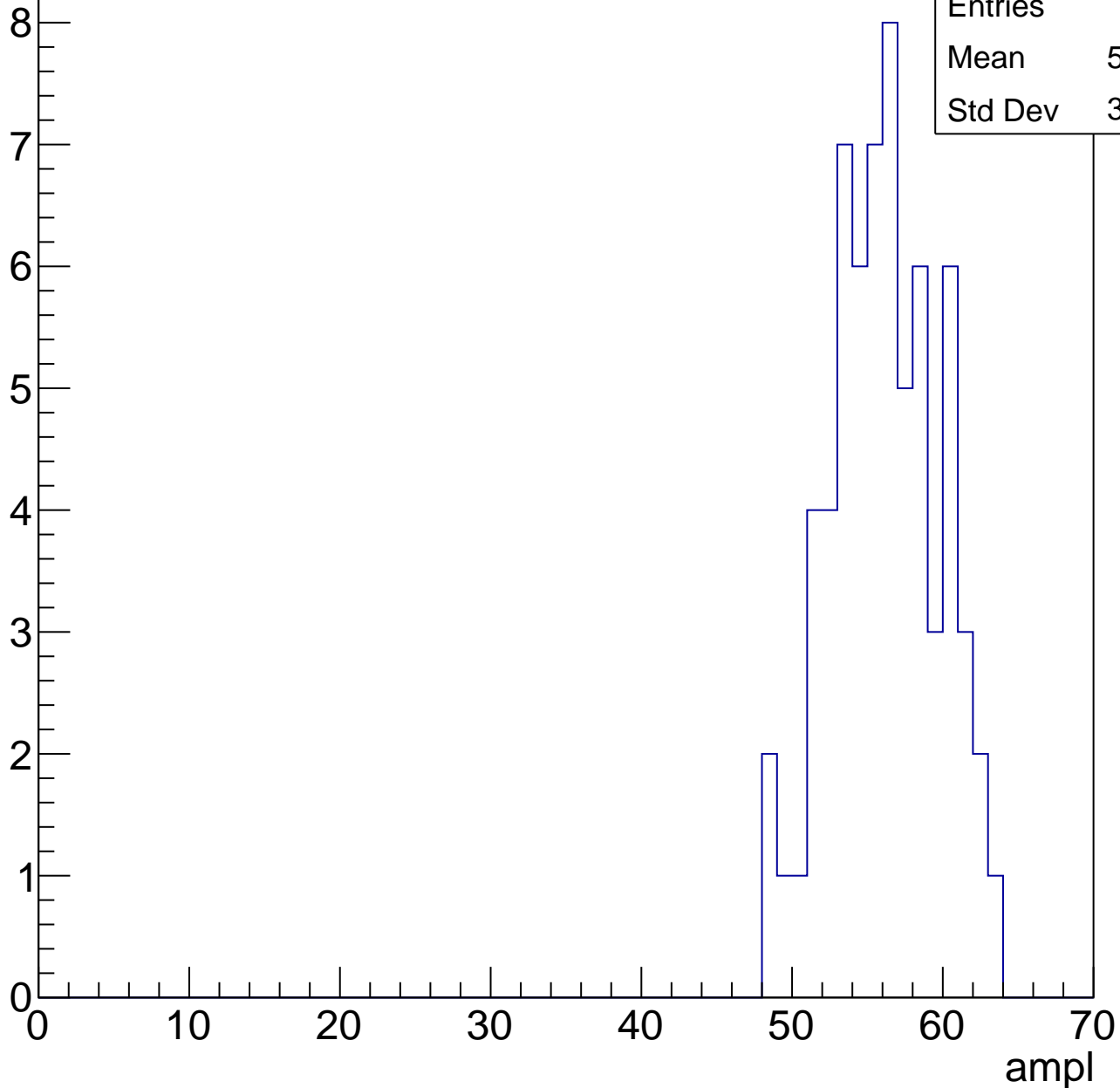
Entries	76
Mean	47.82
Std Dev	6.678



B1L103S, U10-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	66
Mean	55.68
Std Dev	3.508

B1L103S, U10-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 41

Mean 58.1

Std Dev 9.599

ampl

0

10

20

30

40

50

60

70

B1L103S, U10-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch90, adc0

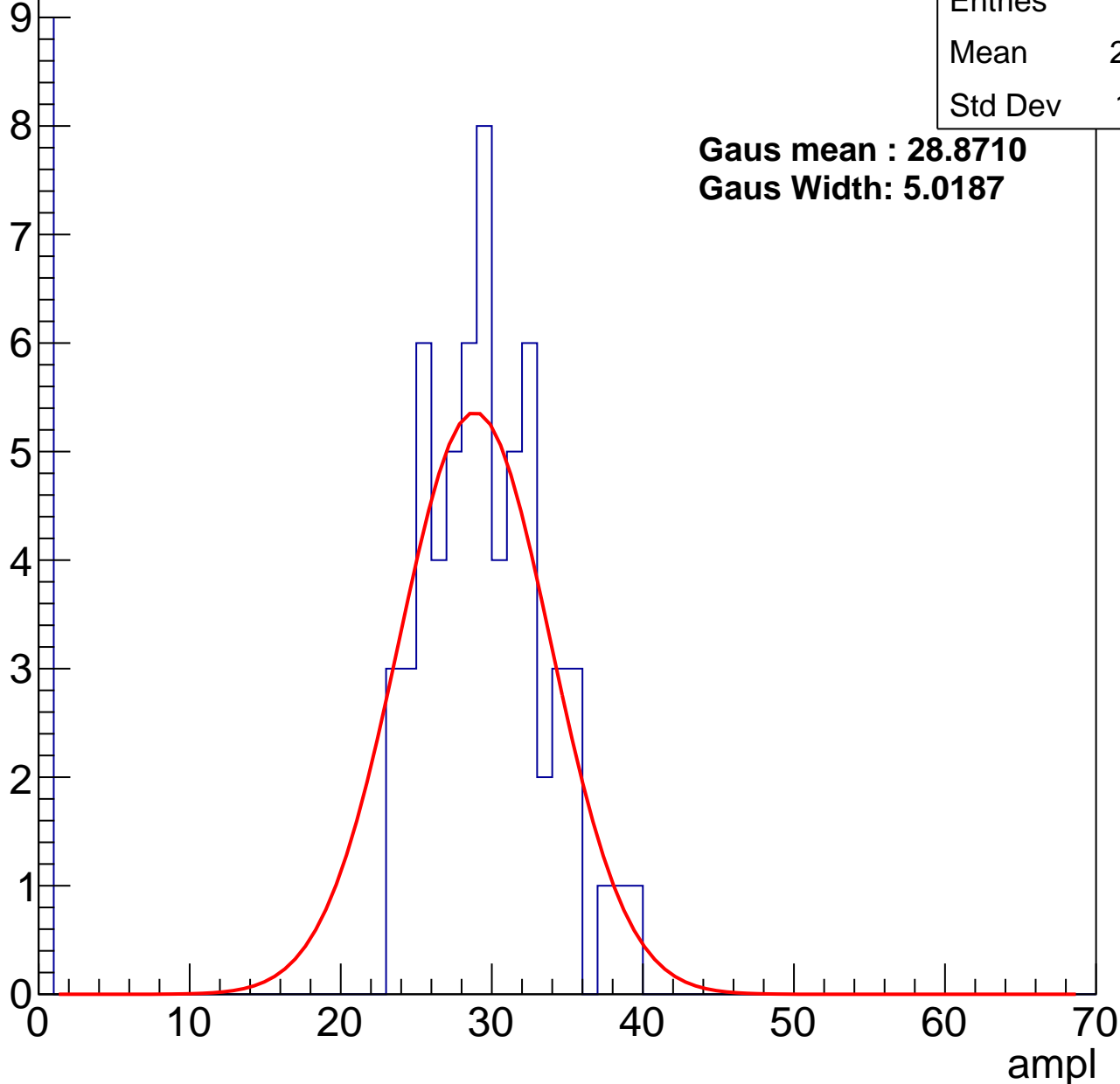
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	25.49
Std Dev	10.41

Gaus mean : 28.8710

Gaus Width: 5.0187



B1L103S, U10-ch90, adc1

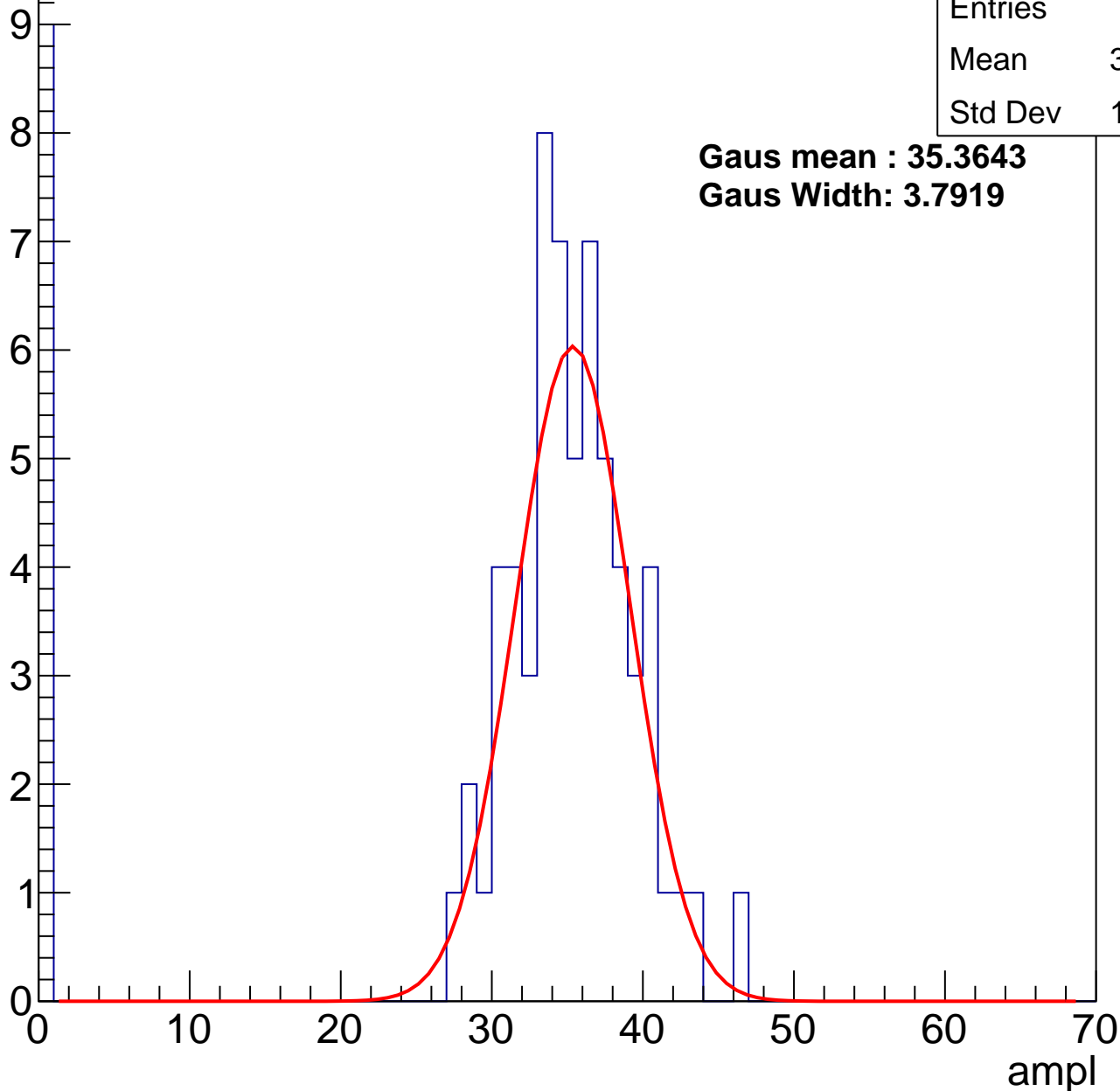
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.52
Std Dev	12.17

Gaus mean : 35.3643

Gaus Width: 3.7919



B1L103S, U10-ch90, adc2

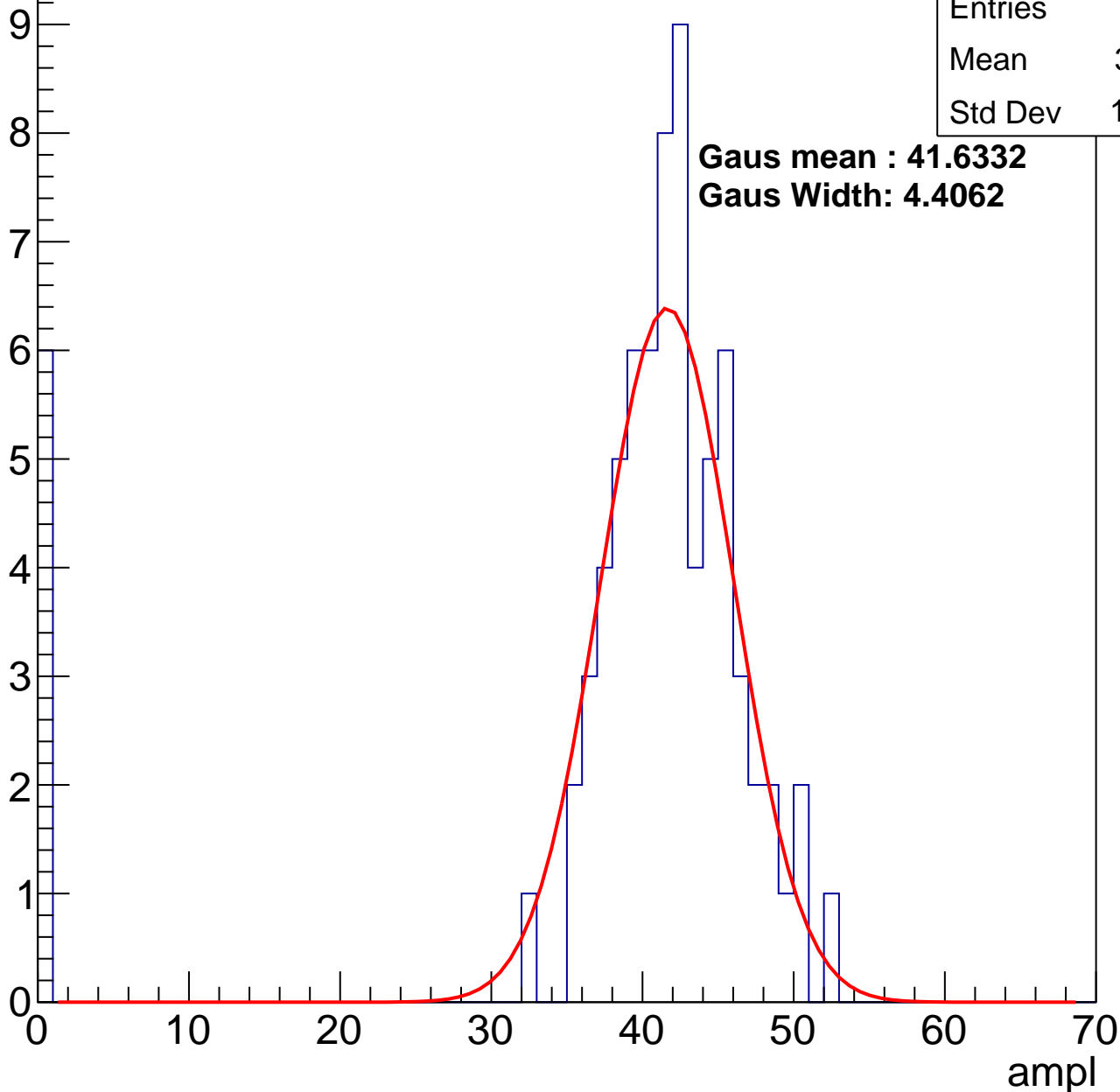
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	38.41
Std Dev	11.87

Gaus mean : 41.6332

Gaus Width: 4.4062

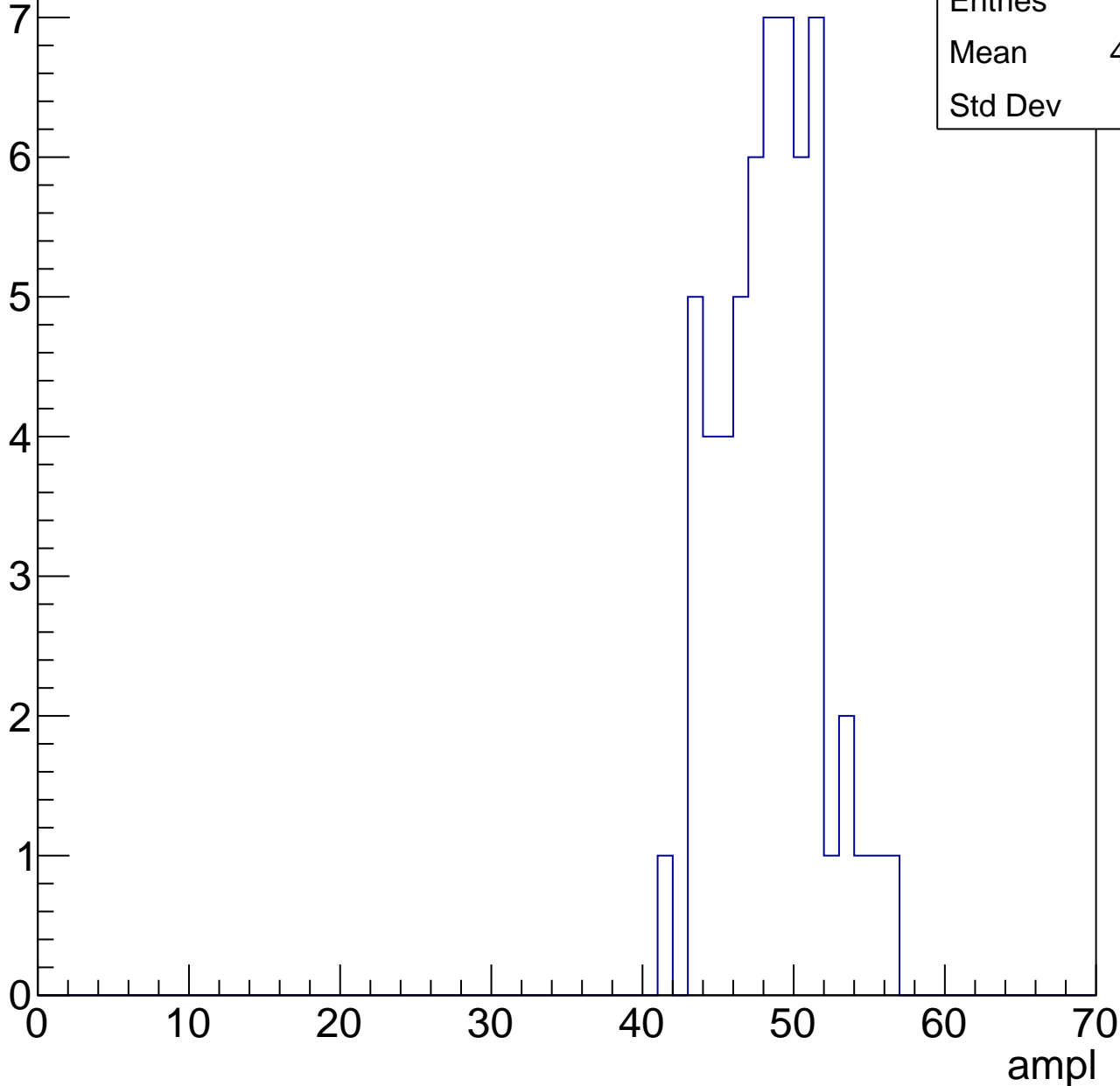


B1L103S, U10-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.98
Std Dev	3.24

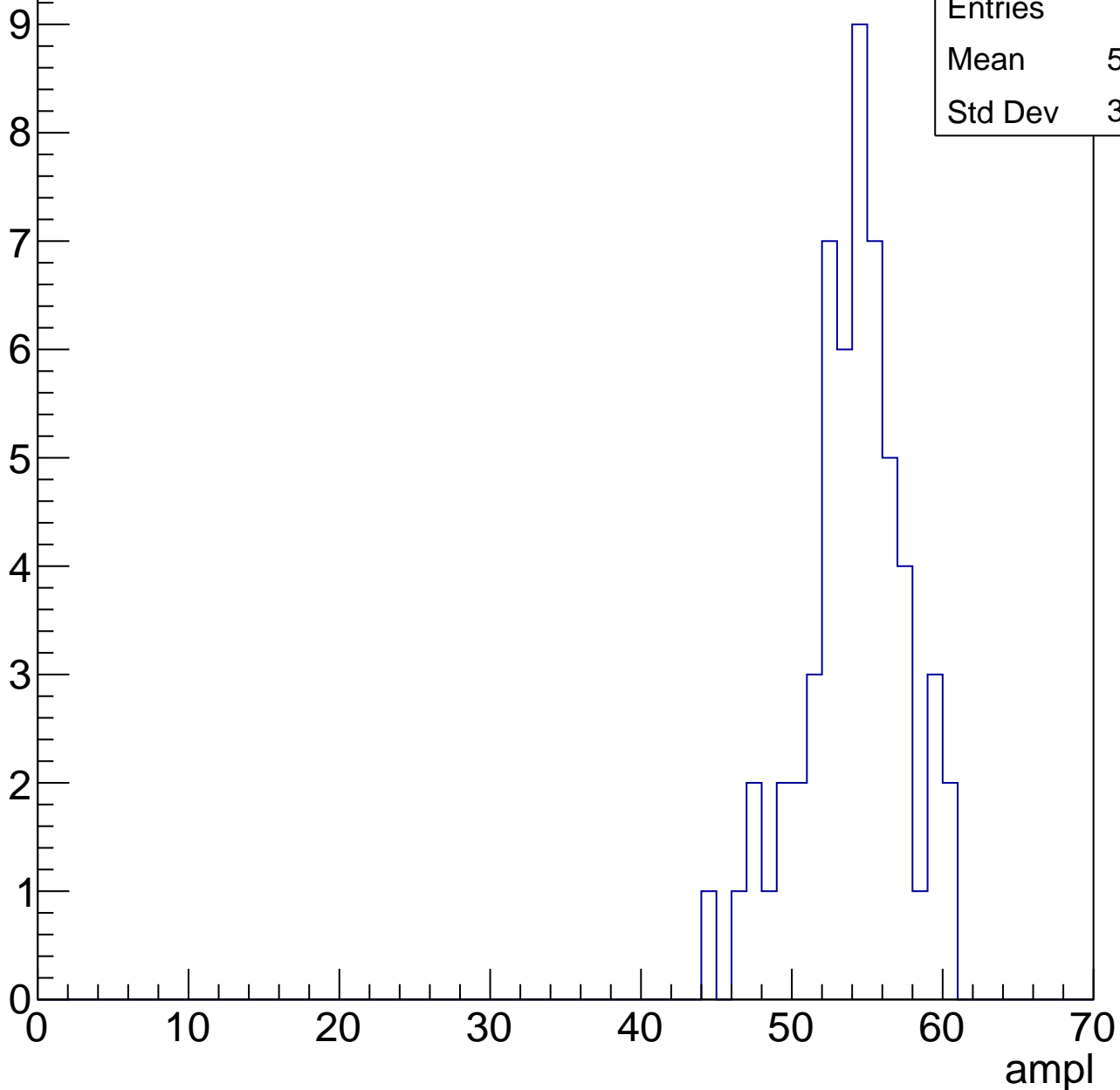


B1L103S, U10-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.55
Std Dev	3.422

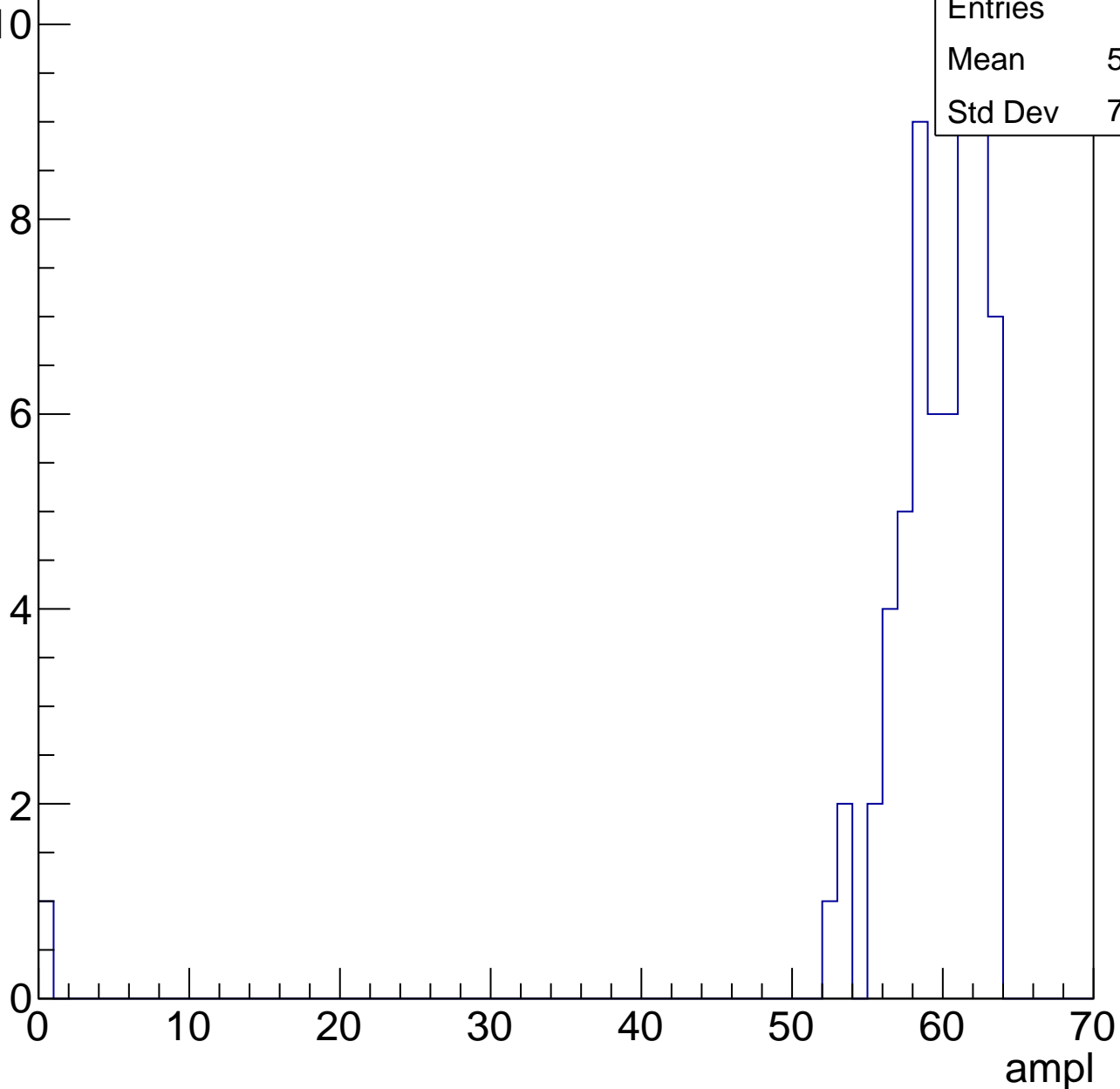


B1L103S, U10-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	58.44
Std Dev	7.955



B1L103S, U10-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

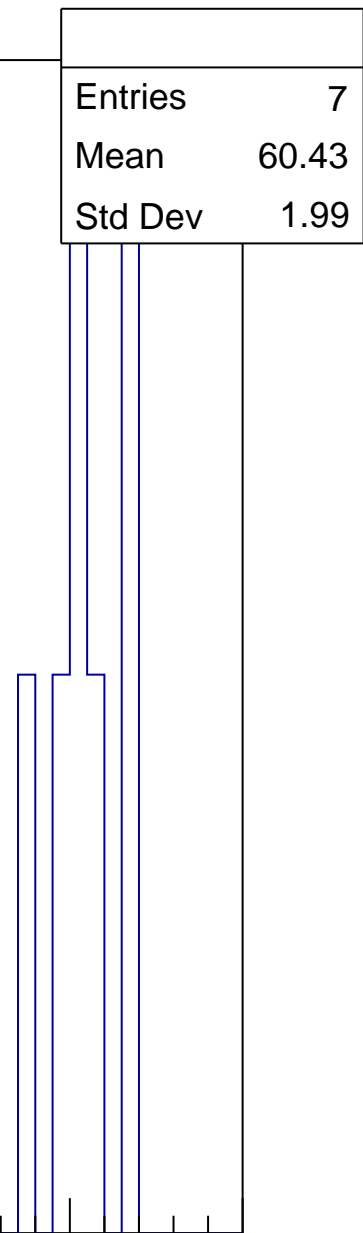
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.43
Std Dev	1.99

ampl

0 10 20 30 40 50 60 70

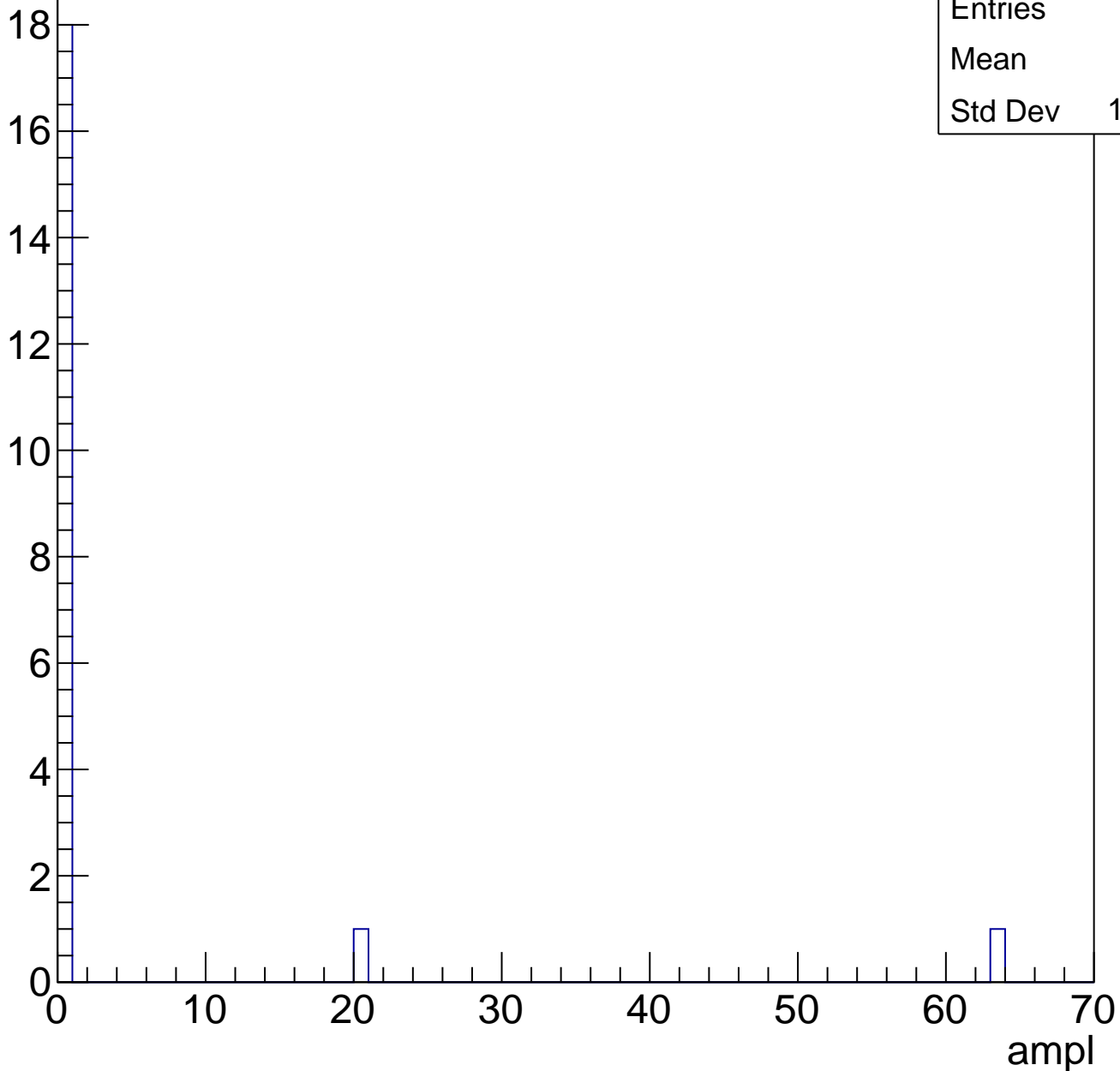


B1L103S, U10-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.19

Entry



B1L103S, U10-ch91, adc0

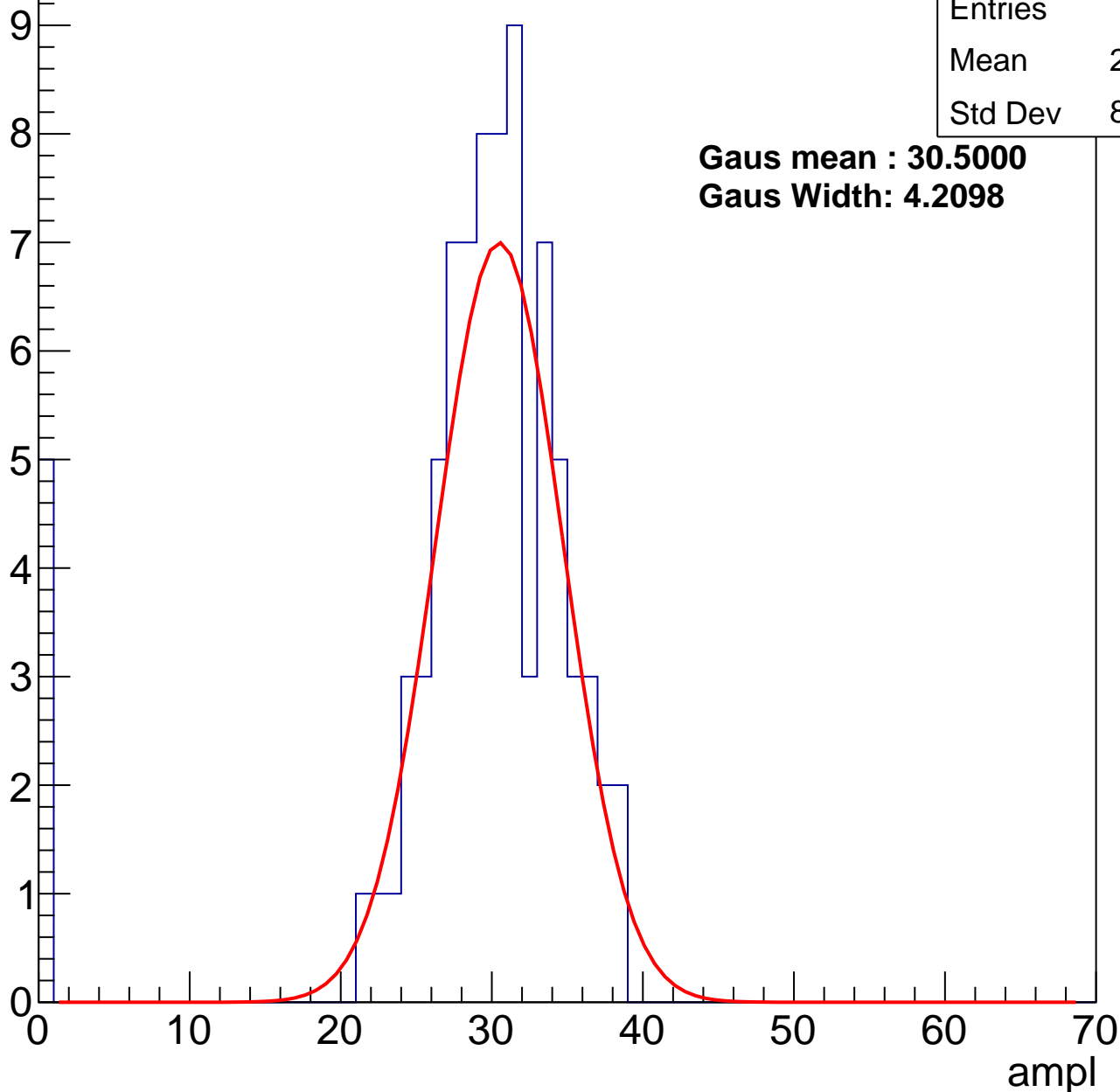
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	28.18
Std Dev	8.032

Gaus mean : 30.5000

Gaus Width: 4.2098



B1L103S, U10-ch91, adc1

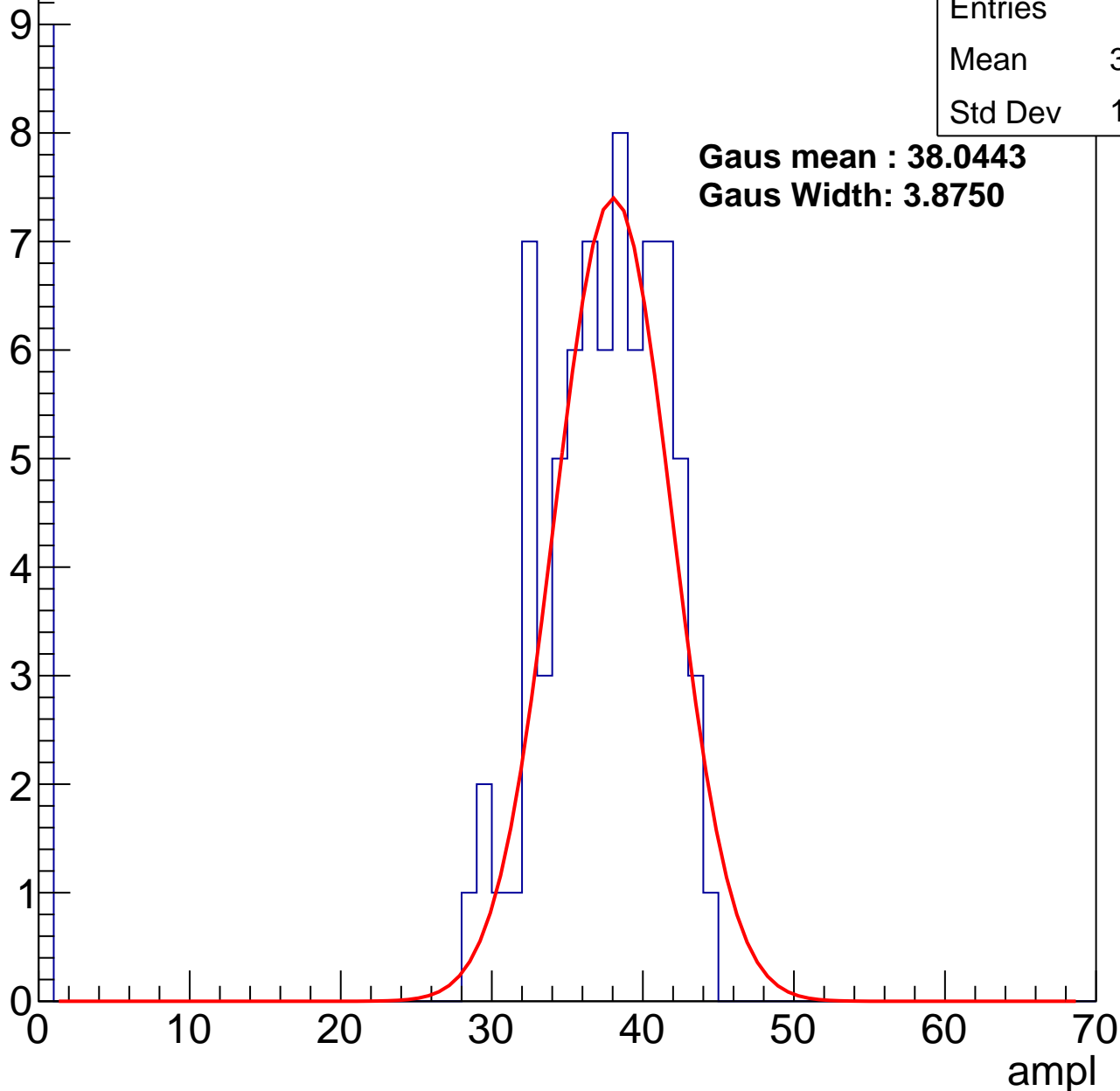
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	33.08
Std Dev	11.93

Gaus mean : 38.0443

Gaus Width: 3.8750



B1L103S, U10-ch91, adc2

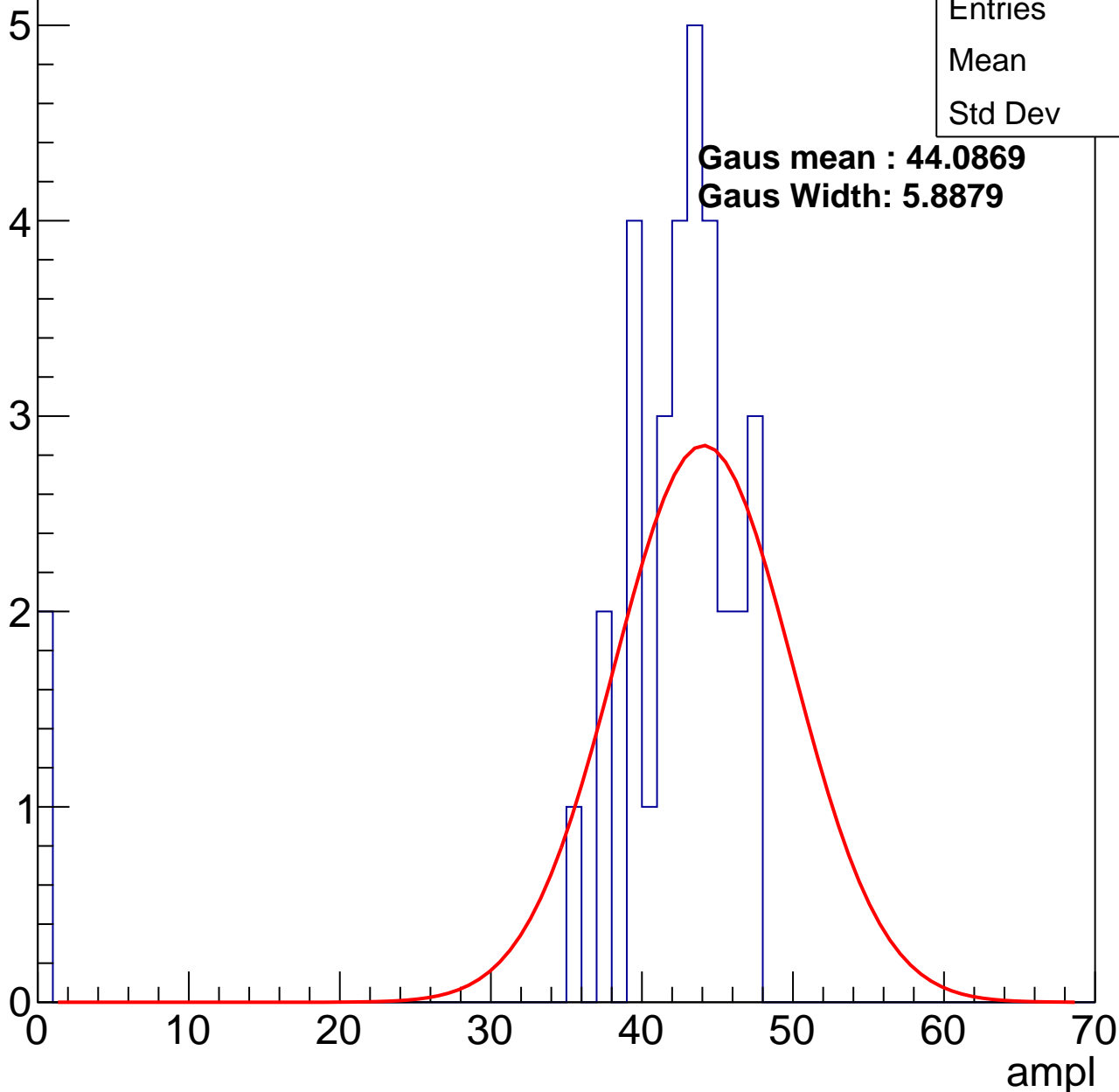
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	39.7
Std Dev	10.5

Gaus mean : 44.0869

Gaus Width: 5.8879



B1L103S, U10-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	48.57
Std Dev	3.707

Entry

10

8

6

4

2

0

0

10

20

30

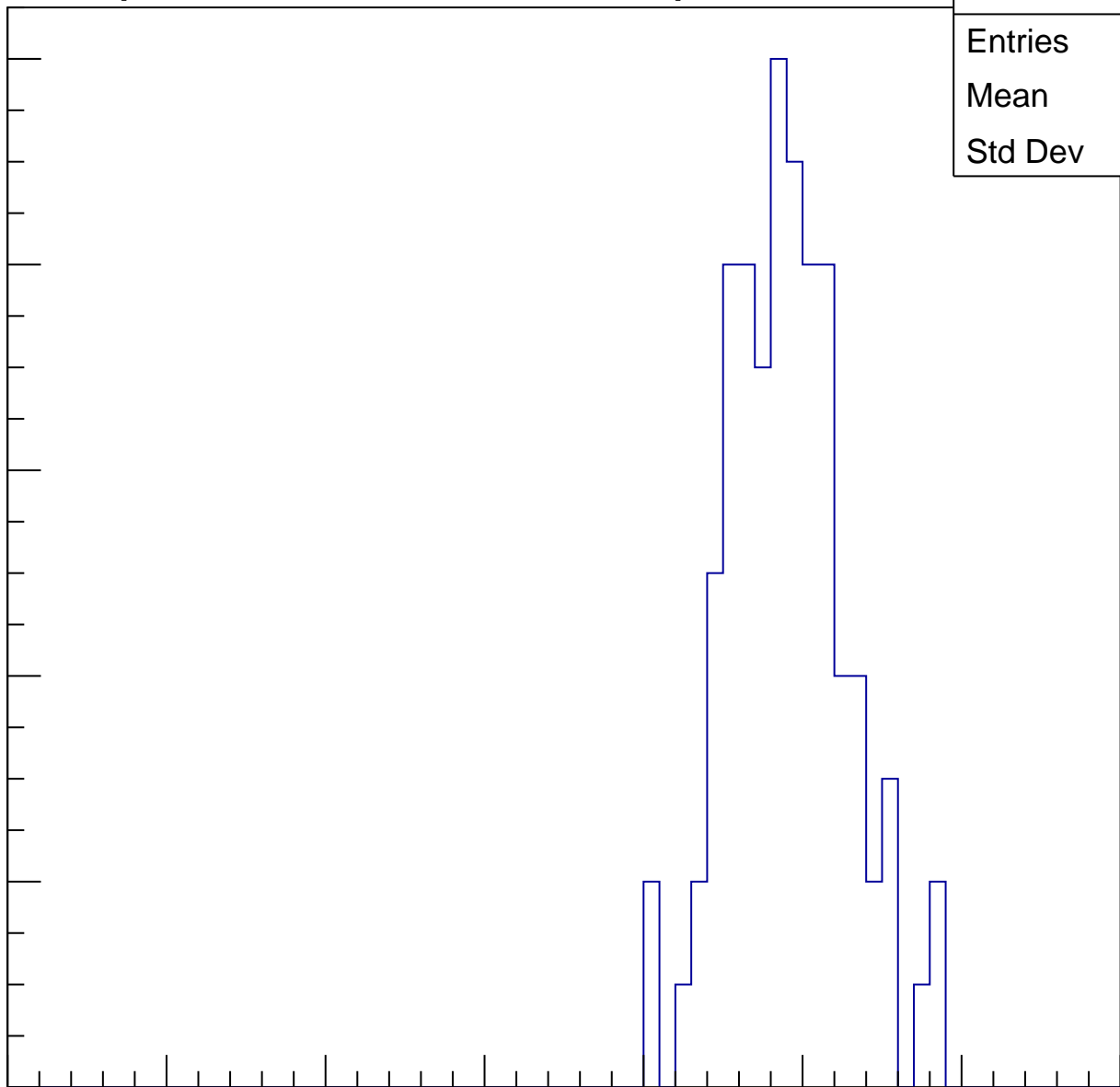
40

50

60

70

ampl

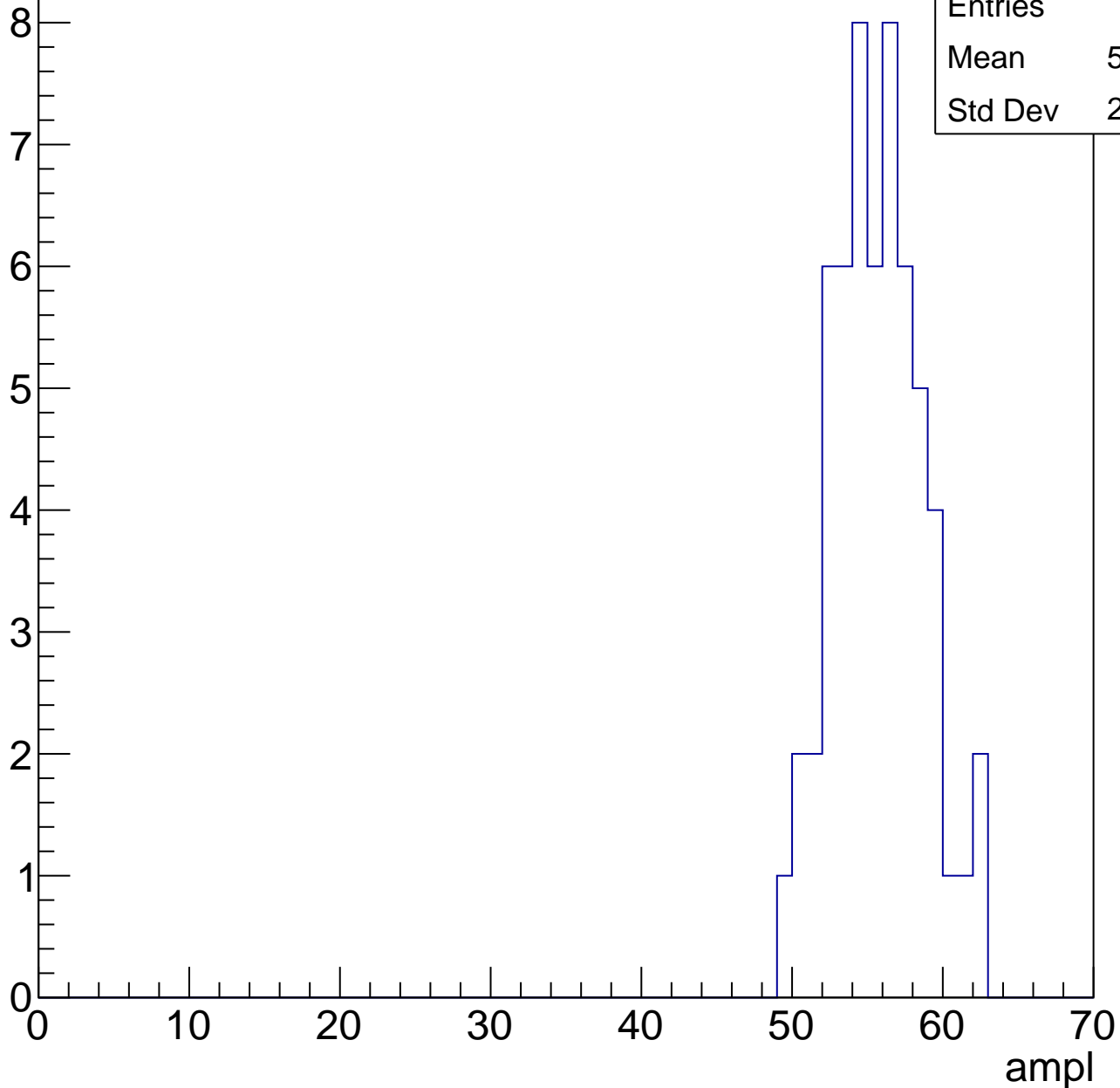


B1L103S, U10-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	55.24
Std Dev	2.938

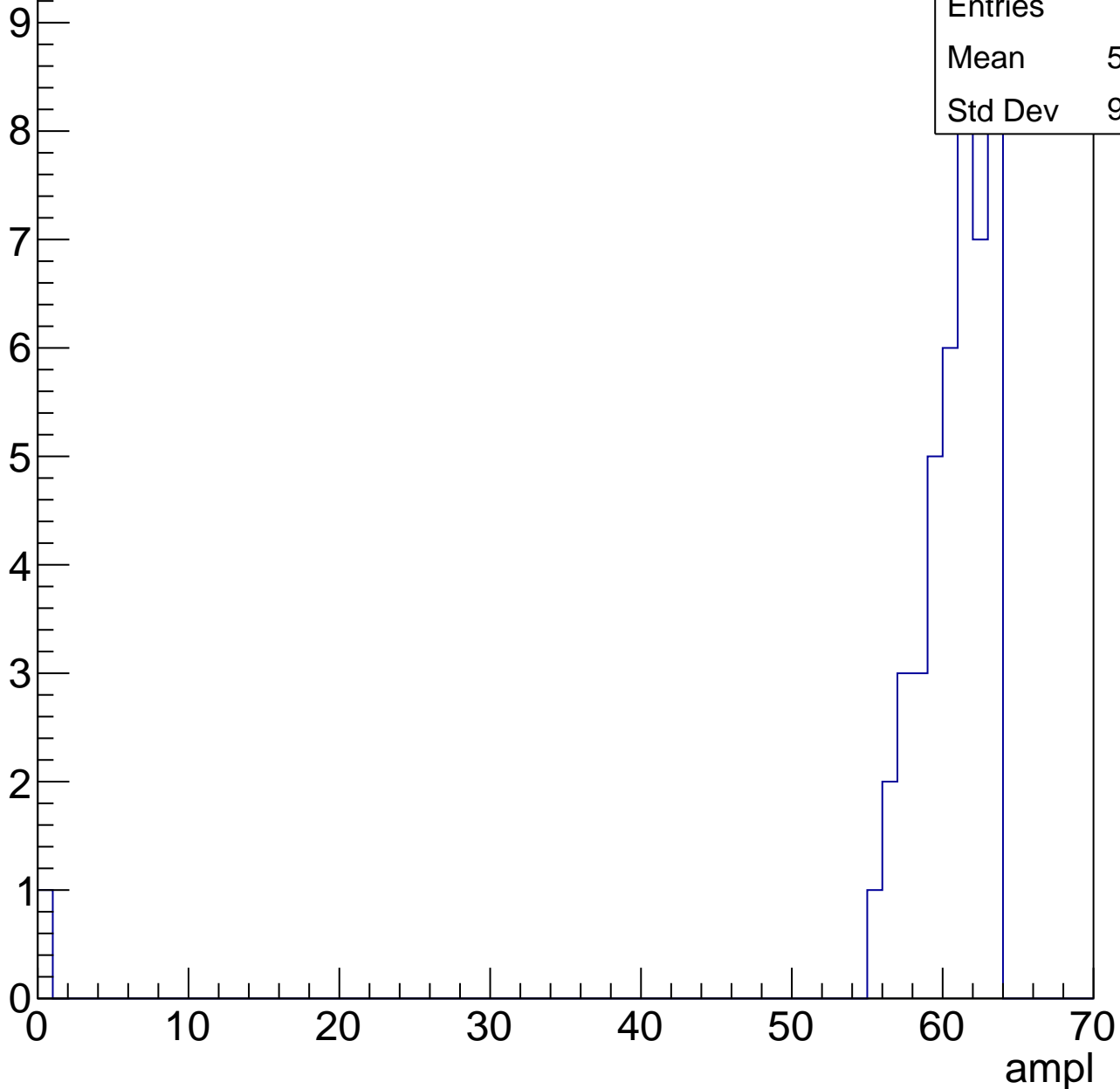


B1L103S, U10-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.98
Std Dev	9.144



B1L103S, U10-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

7

Mean

61

Std Dev

1.512

Entries	7
Mean	61
Std Dev	1.512

B1L103S, U10-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U10-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.56
Std Dev	9.433

Gaus mean : 26.4901

Gaus Width: 4.3553

Entry

10

8

6

4

2

0

0

10

20

30

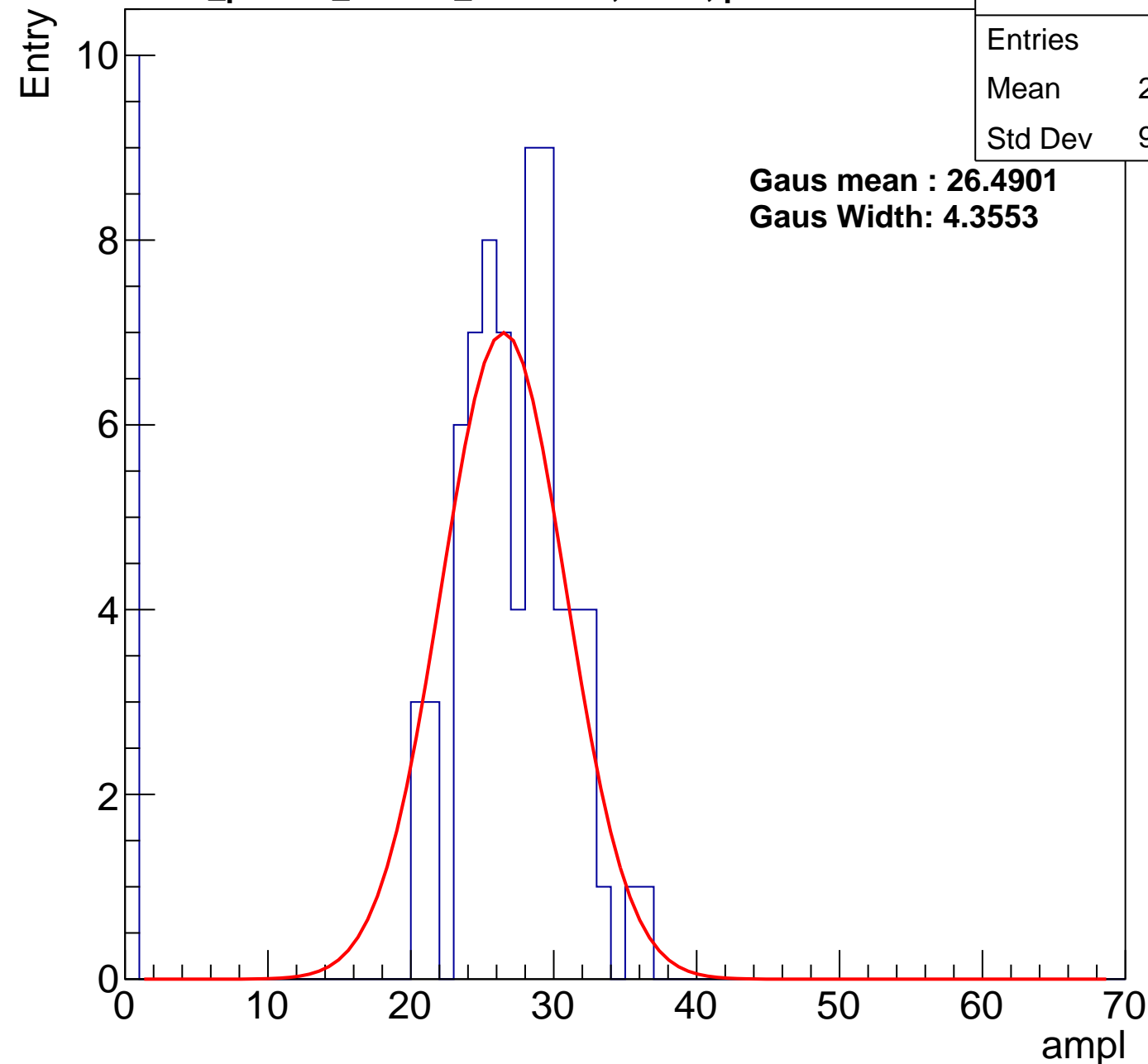
40

50

60

70

ampl



B1L103S, U10-ch92, adc1

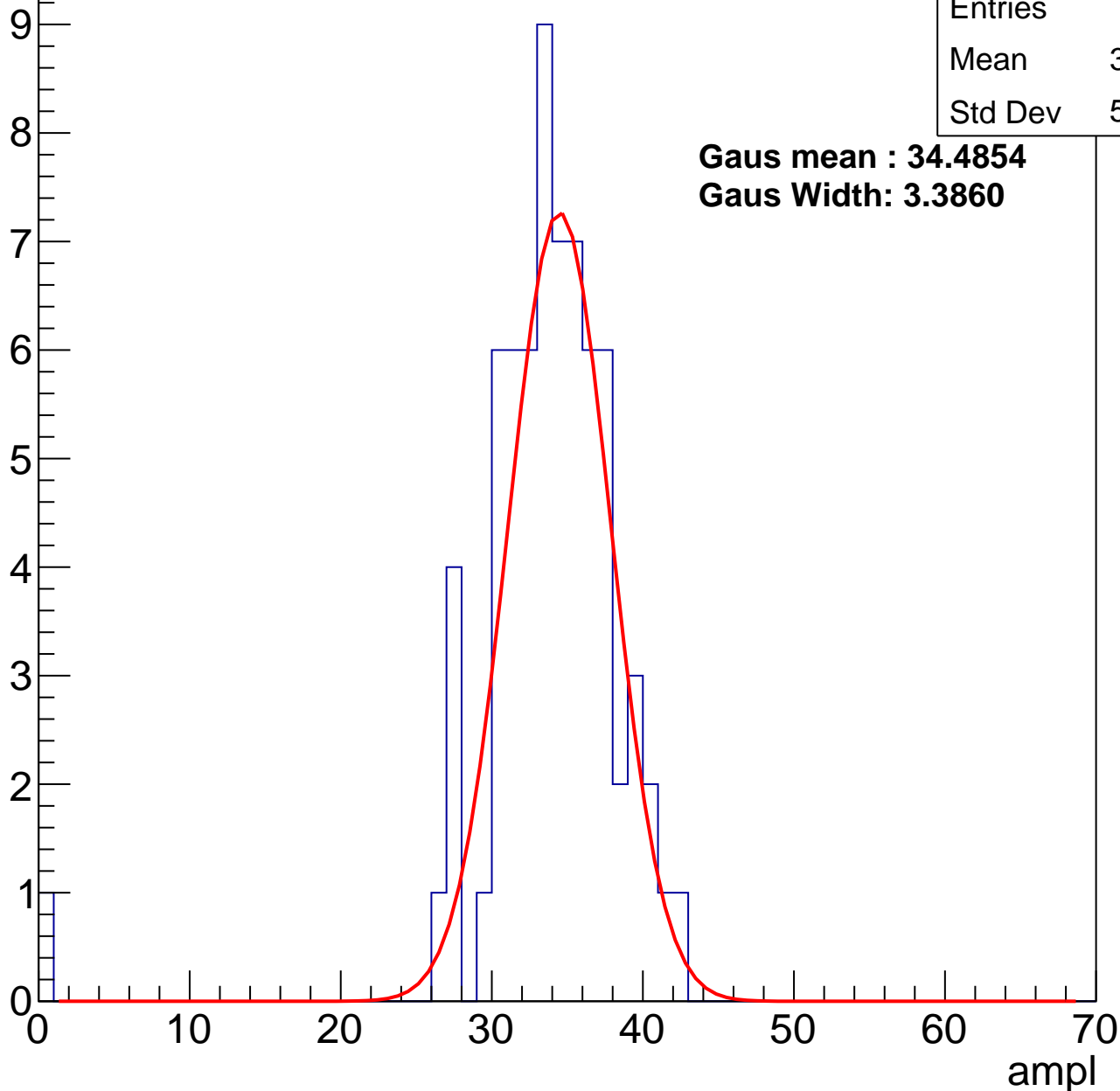
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.26
Std Dev	5.334

Gaus mean : 34.4854

Gaus Width: 3.3860



B1L103S, U10-ch92, adc2

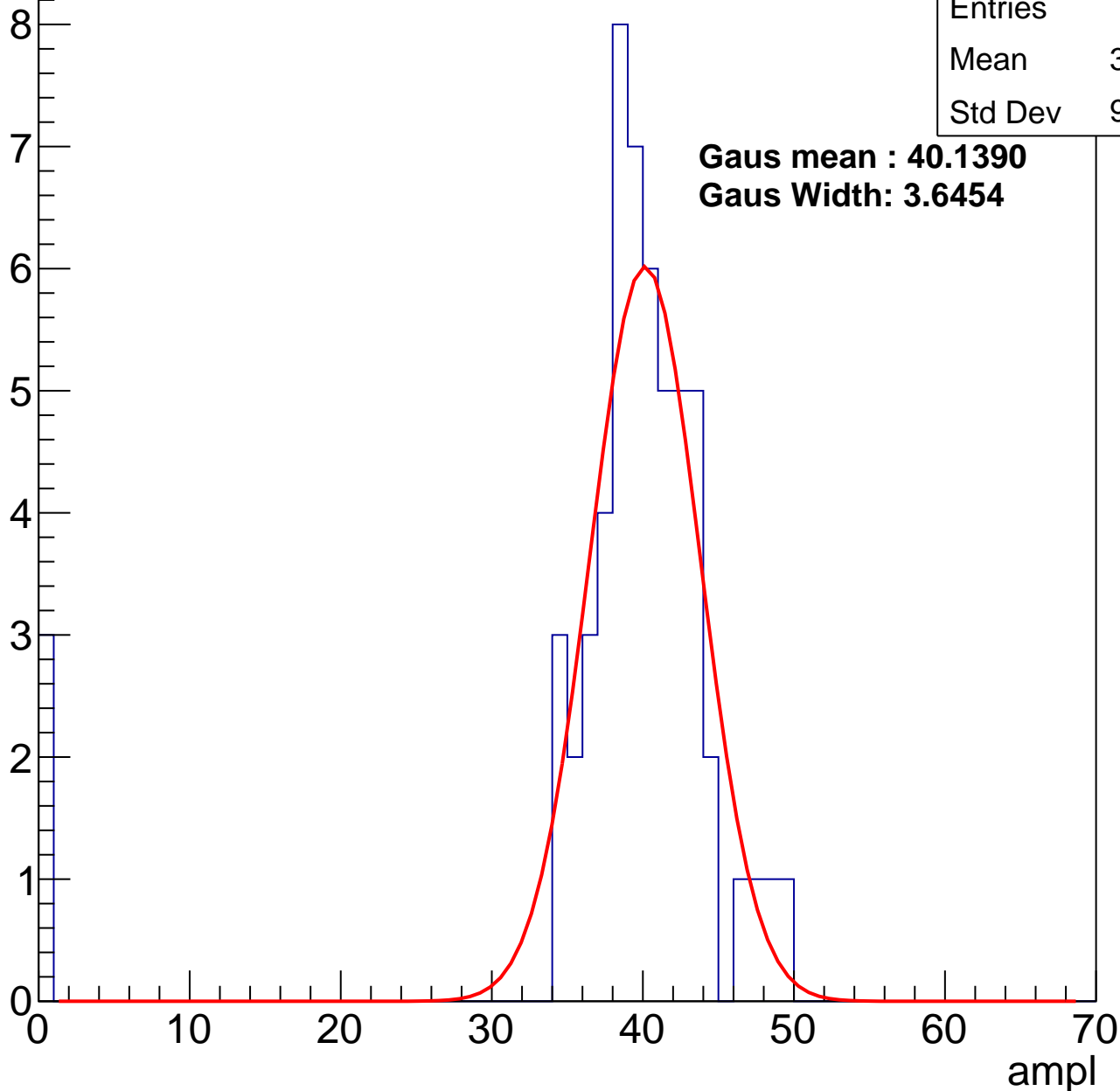
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	37.77
Std Dev	9.487

Gaus mean : 40.1390

Gaus Width: 3.6454



B1L103S, U10-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	39.93
Std Dev	15.99

Entry

10

8

6

4

2

0

0

10

20

30

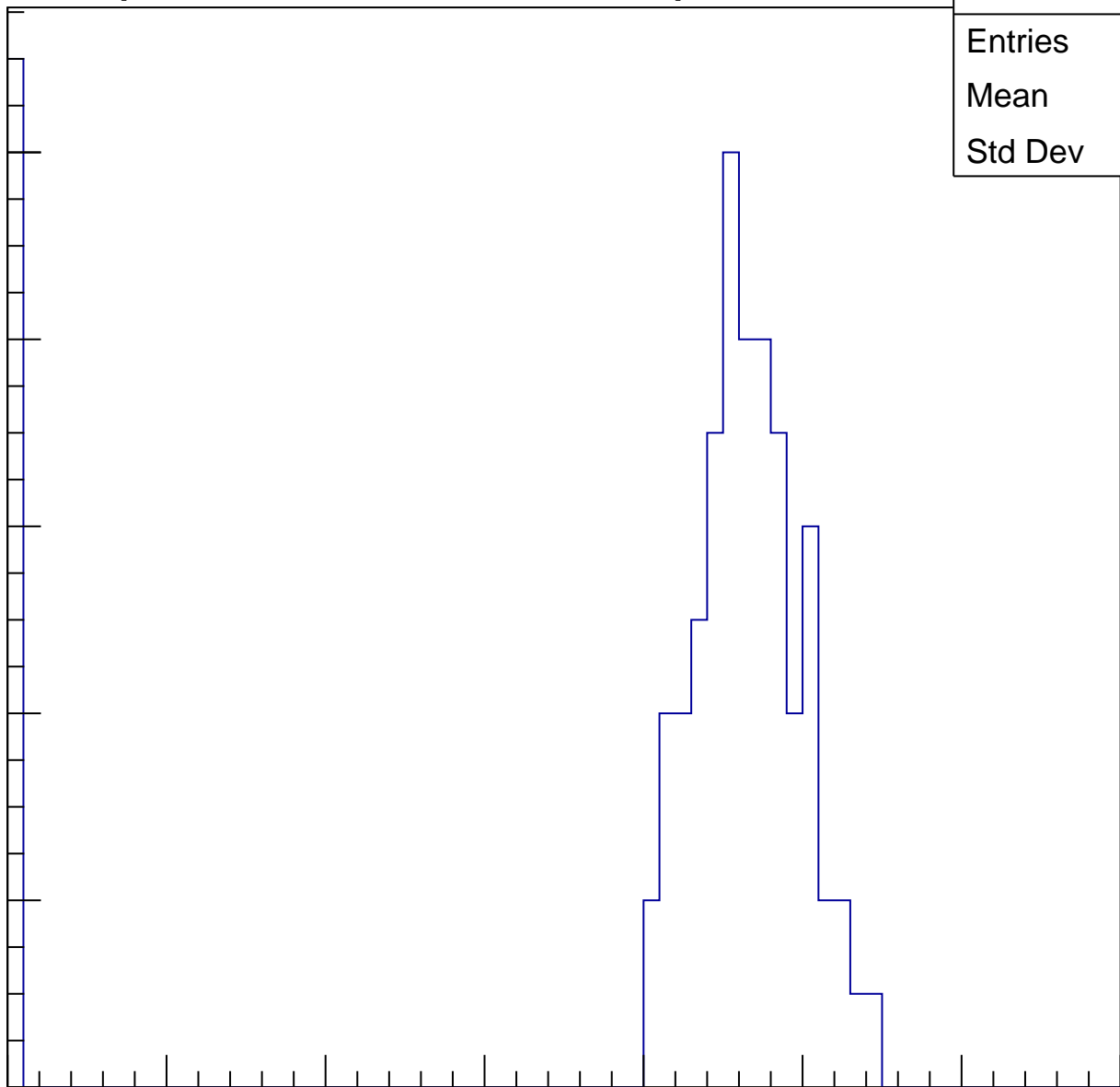
40

50

60

70

ampl

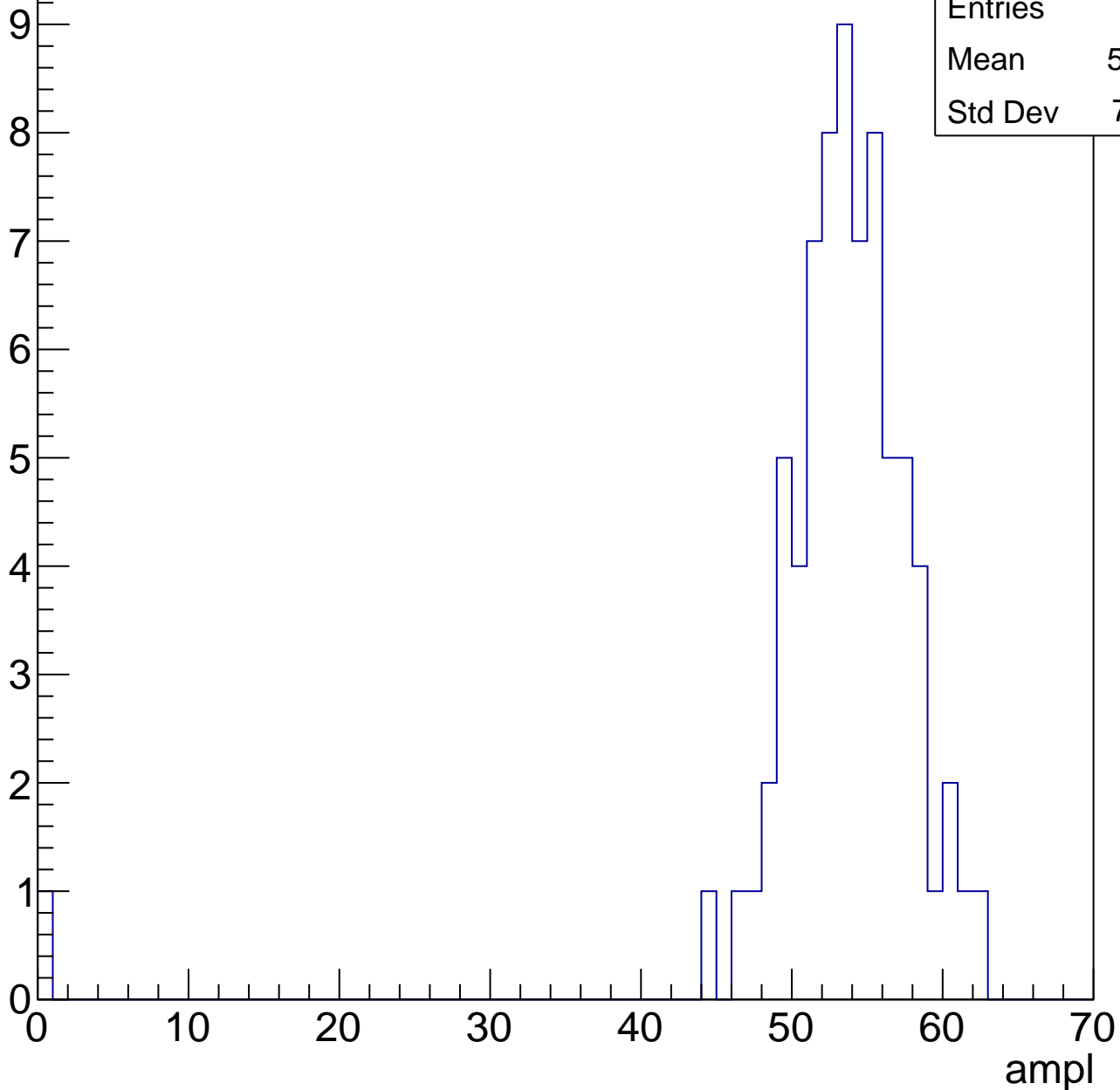


B1L103S, U10-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

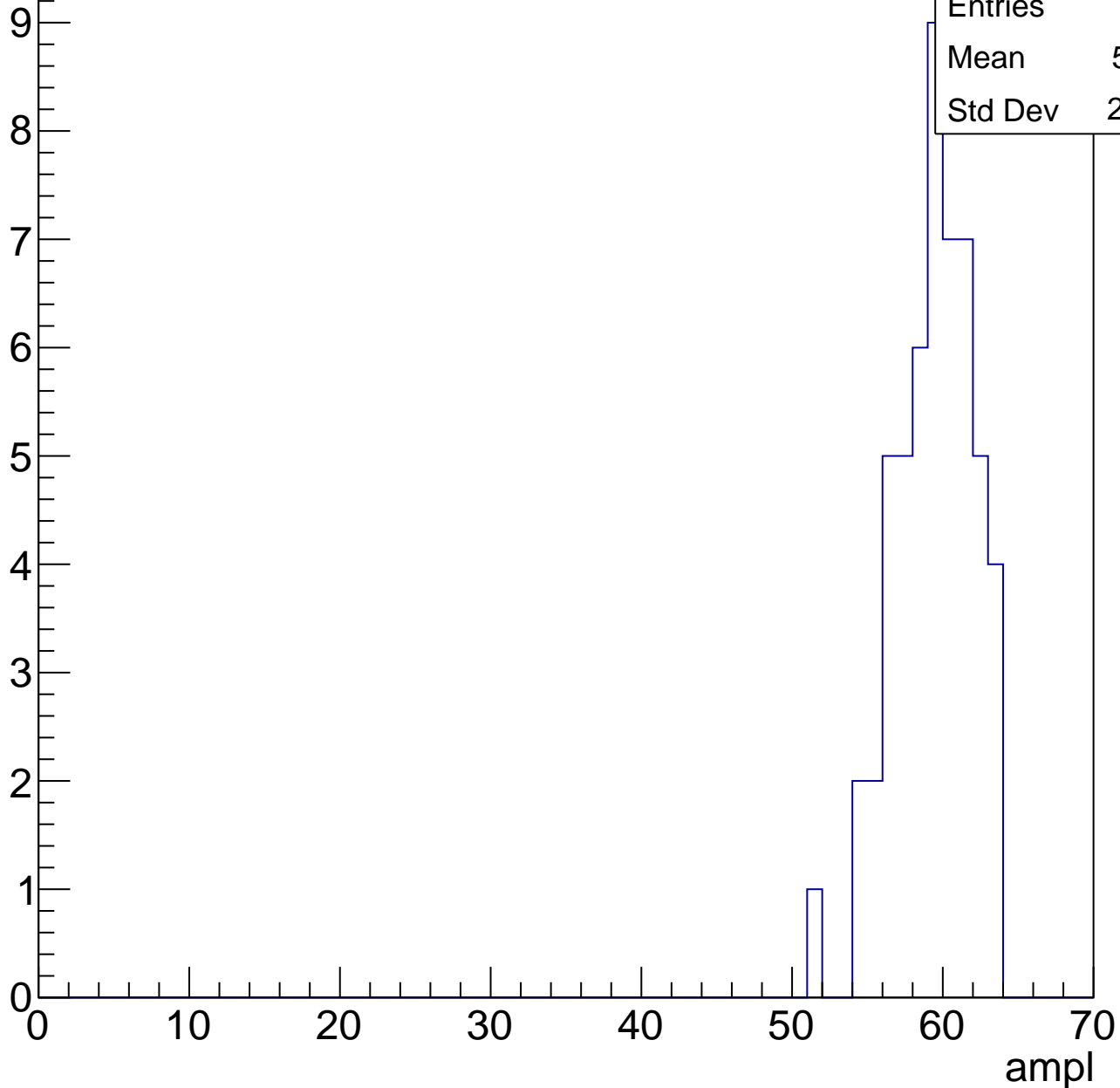
Entries	73
Mean	52.67
Std Dev	7.131



B1L103S, U10-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

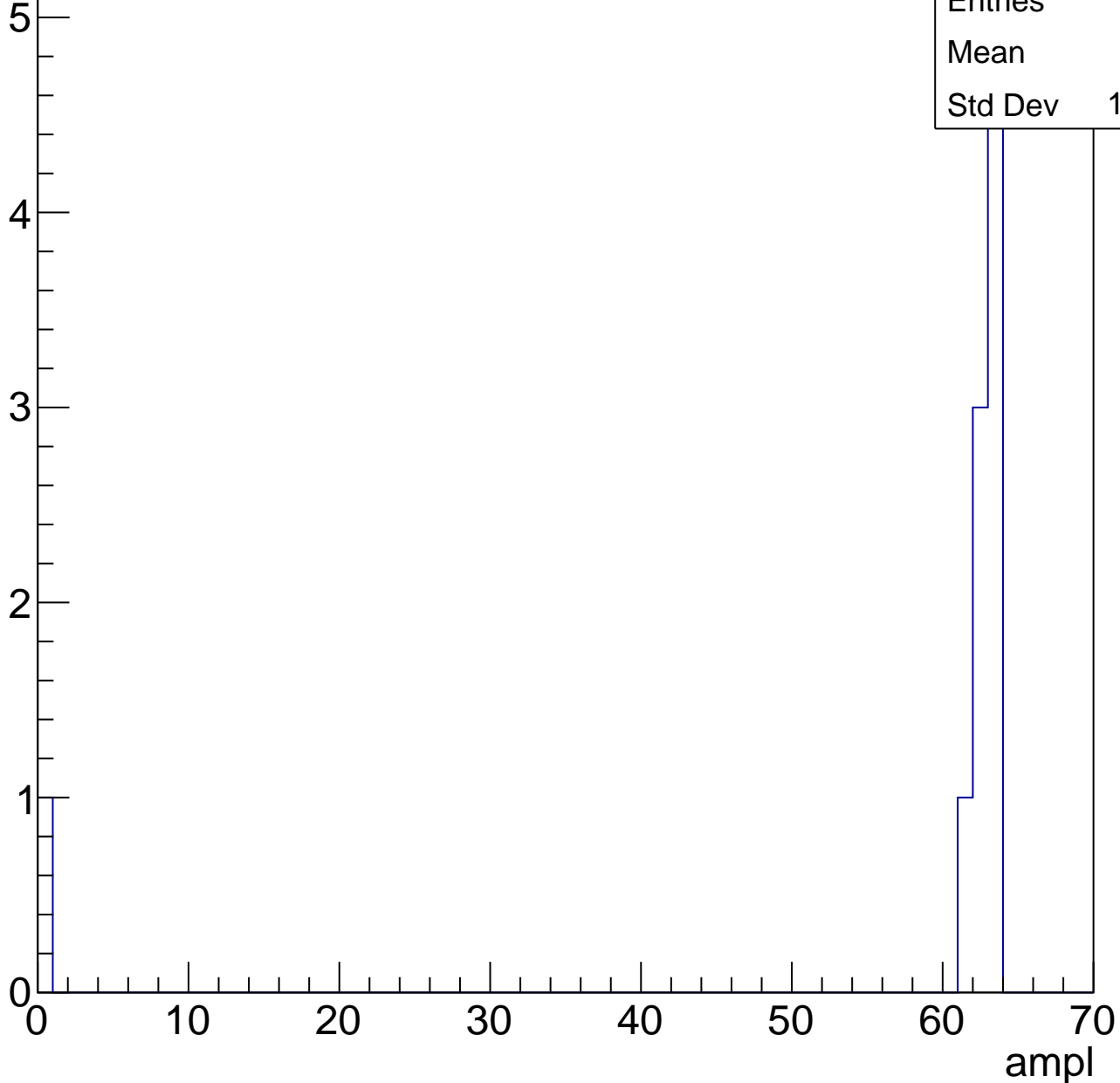


B1L103S, U10-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	56.2
Std Dev	18.74

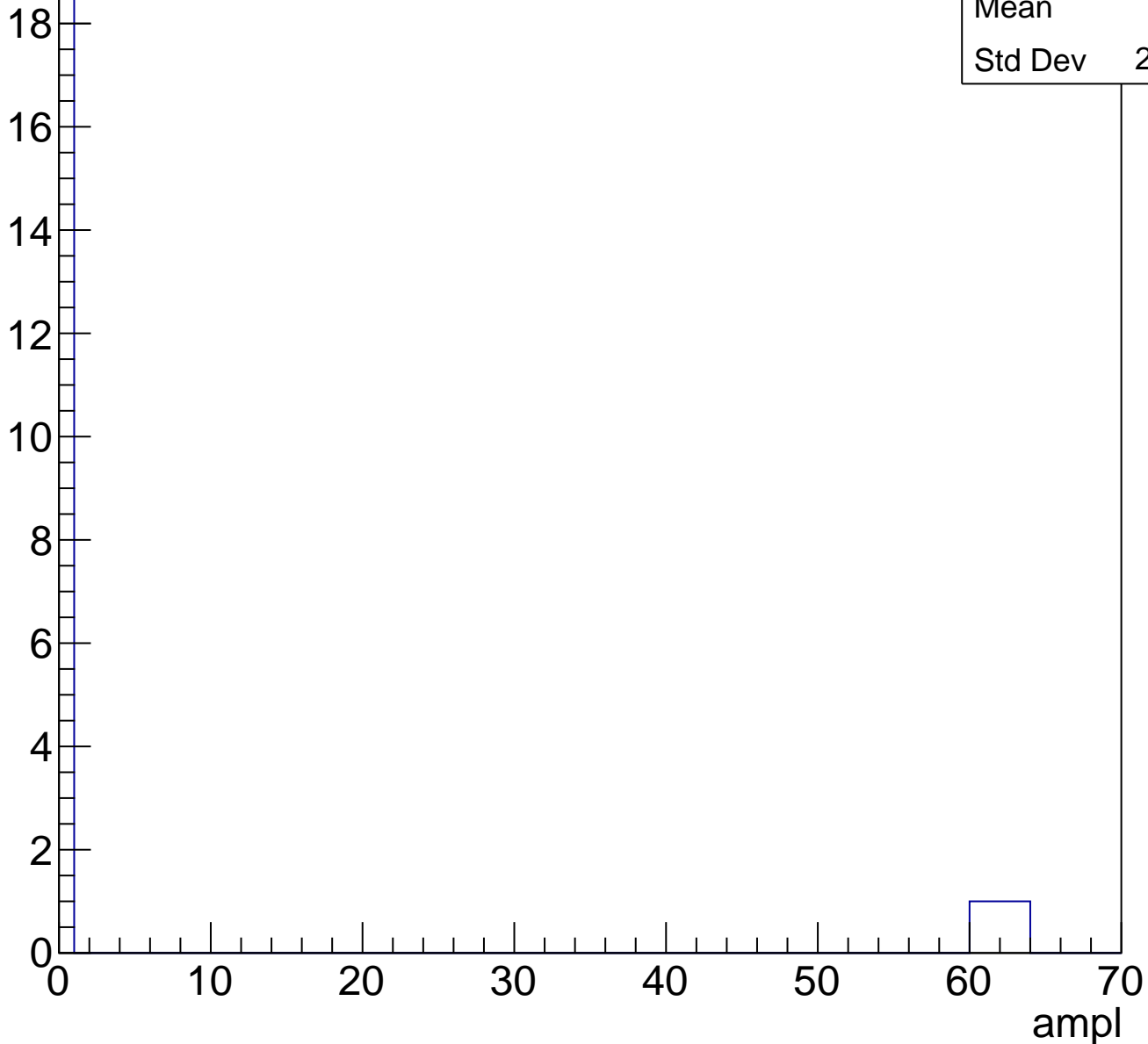


B1L103S, U10-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.7
Std Dev	23.32

Entry



B1L103S, U10-ch93, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	24.85
Std Dev	12.23

Gaus mean : 30.6996

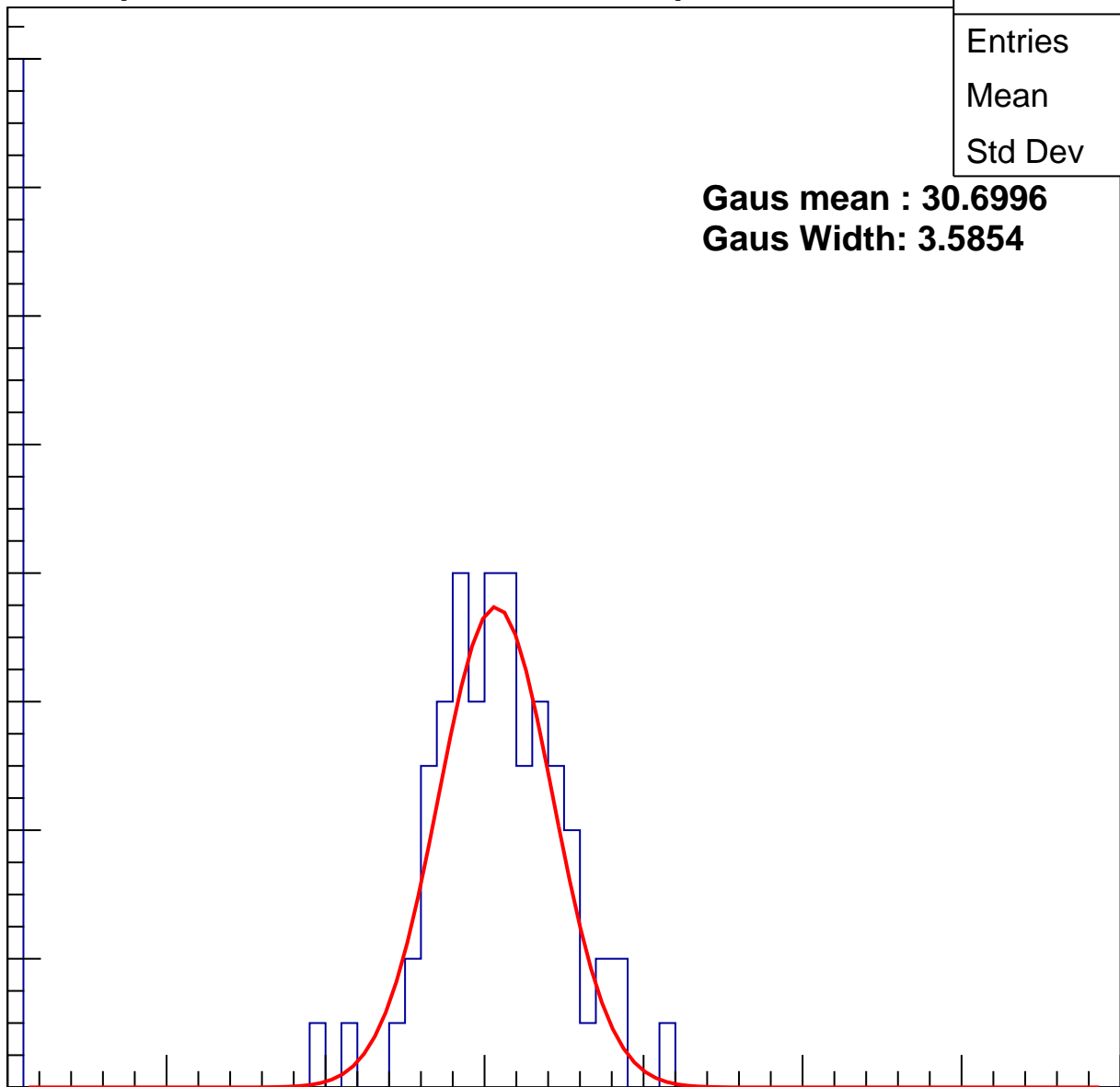
Gaus Width: 3.5854

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch93, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	32.78
Std Dev	12.95

Gaus mean : 38.0625

Gaus Width: 3.6749

Entry

10

8

6

4

2

0

0

10

20

30

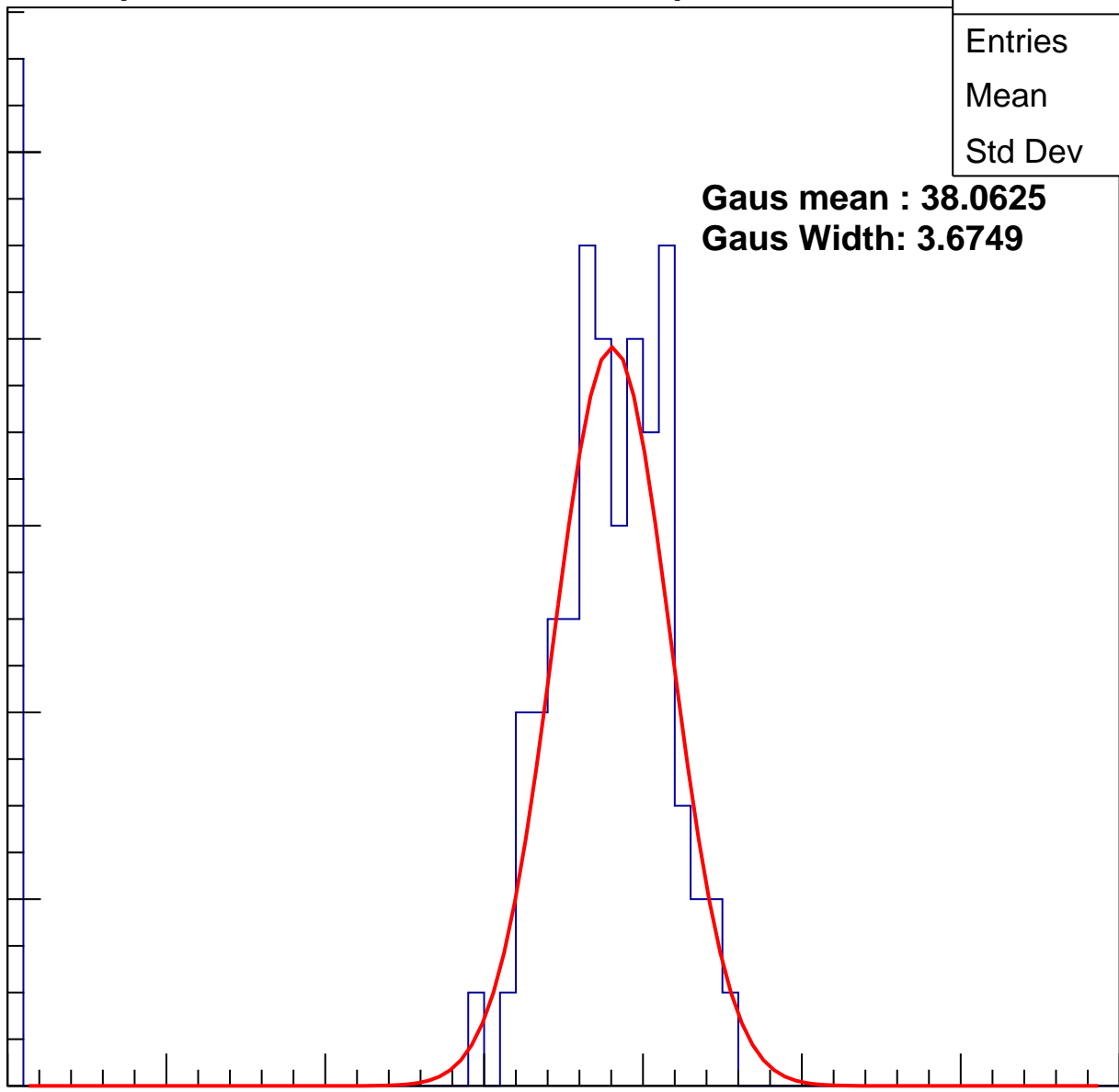
40

50

60

70

ampl



B1L103S, U10-ch93, adc2

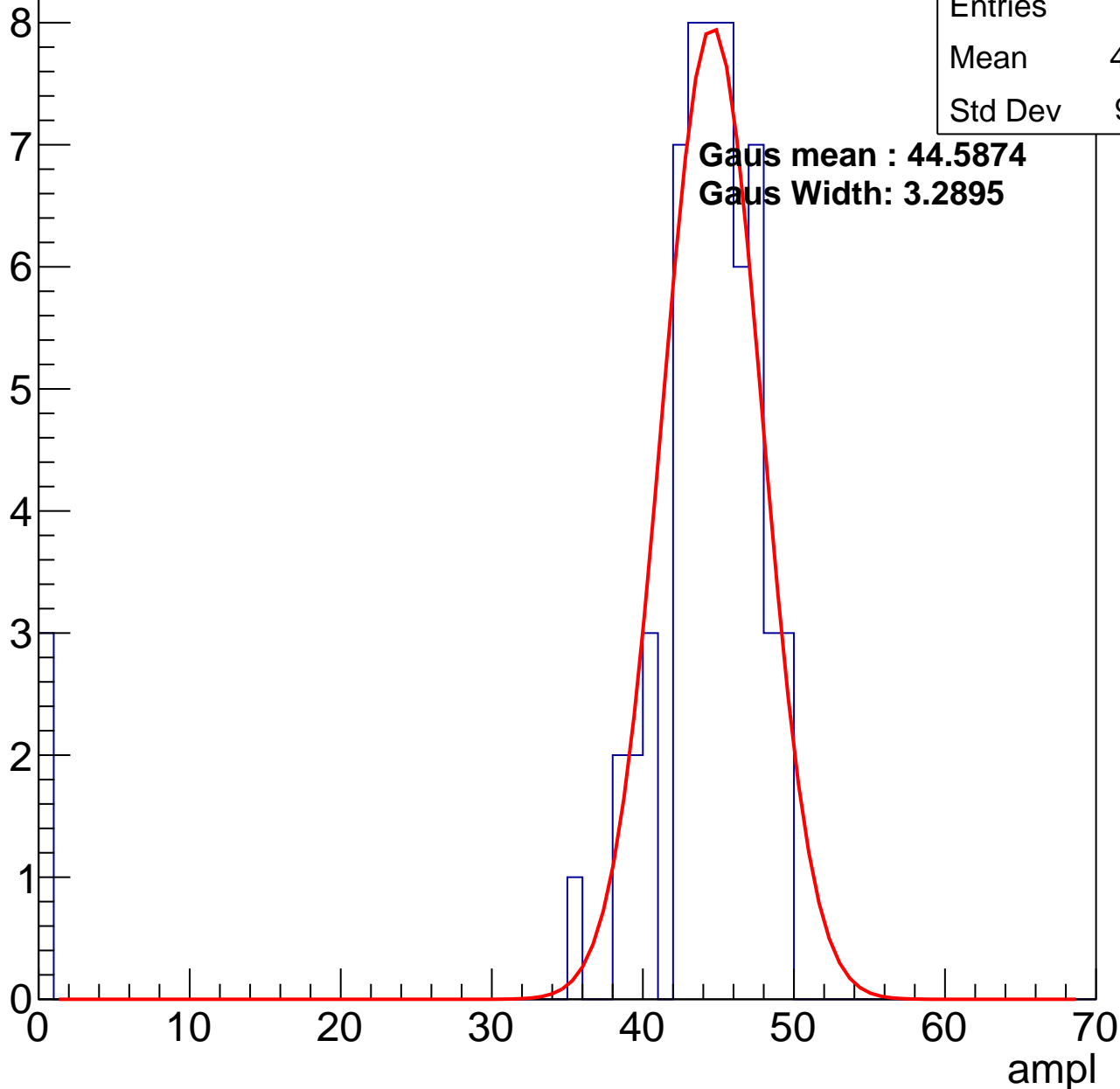
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	41.89
Std Dev	9.951

Gaus mean : 44.5874

Gaus Width: 3.2895



B1L103S, U10-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

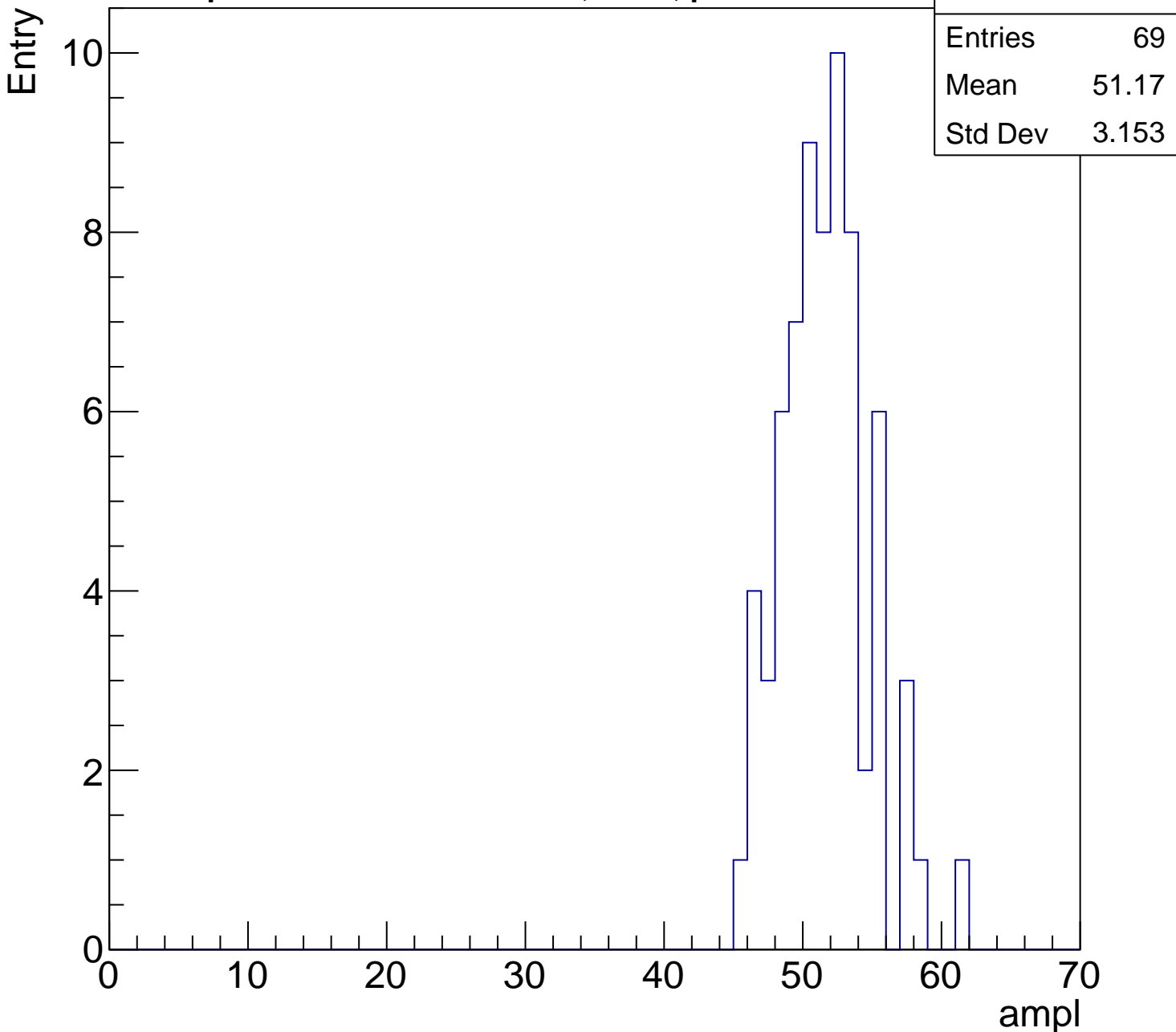
Entries	69
Mean	51.17
Std Dev	3.153

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

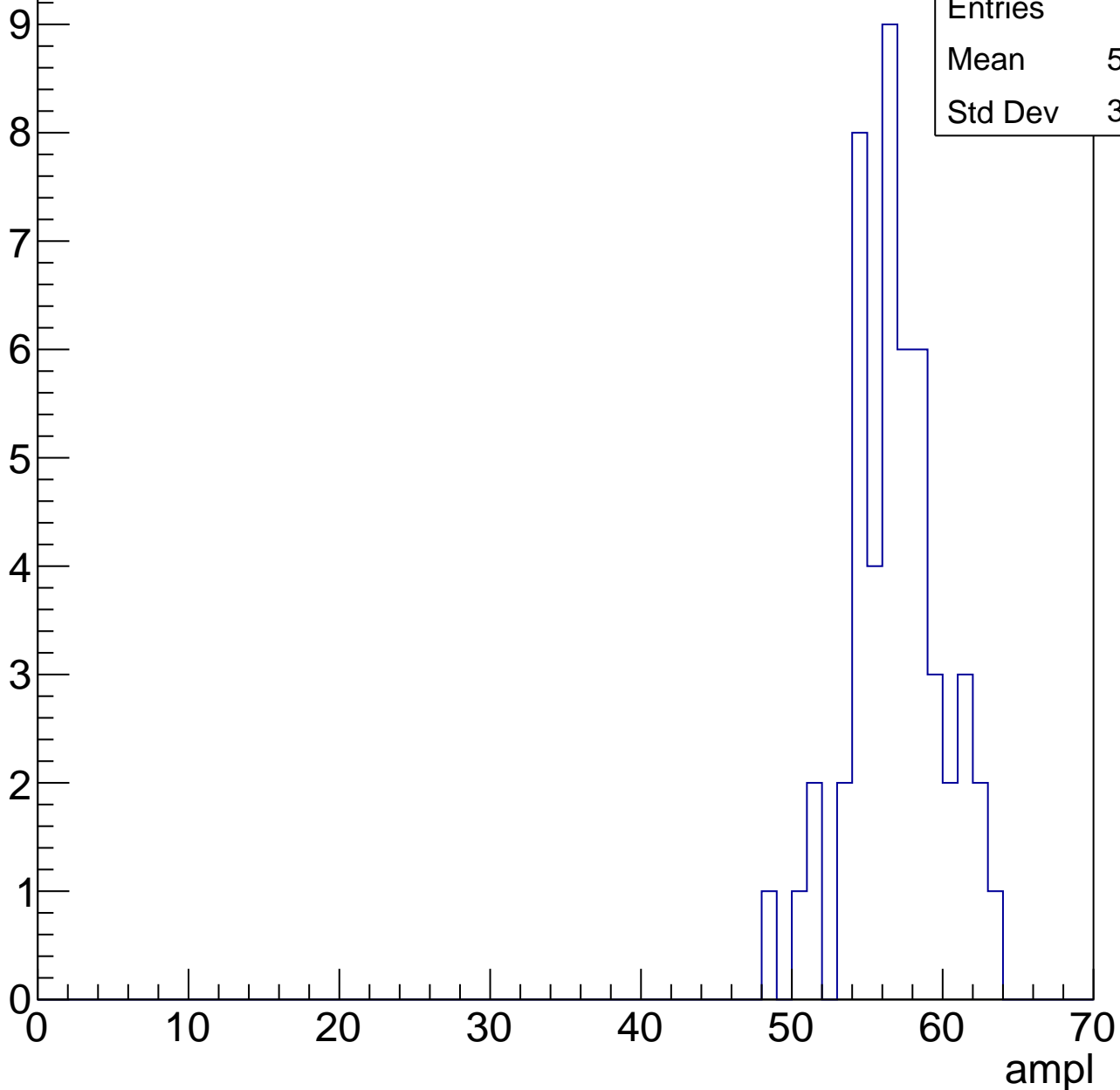


B1L103S, U10-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	56.38
Std Dev	3.104

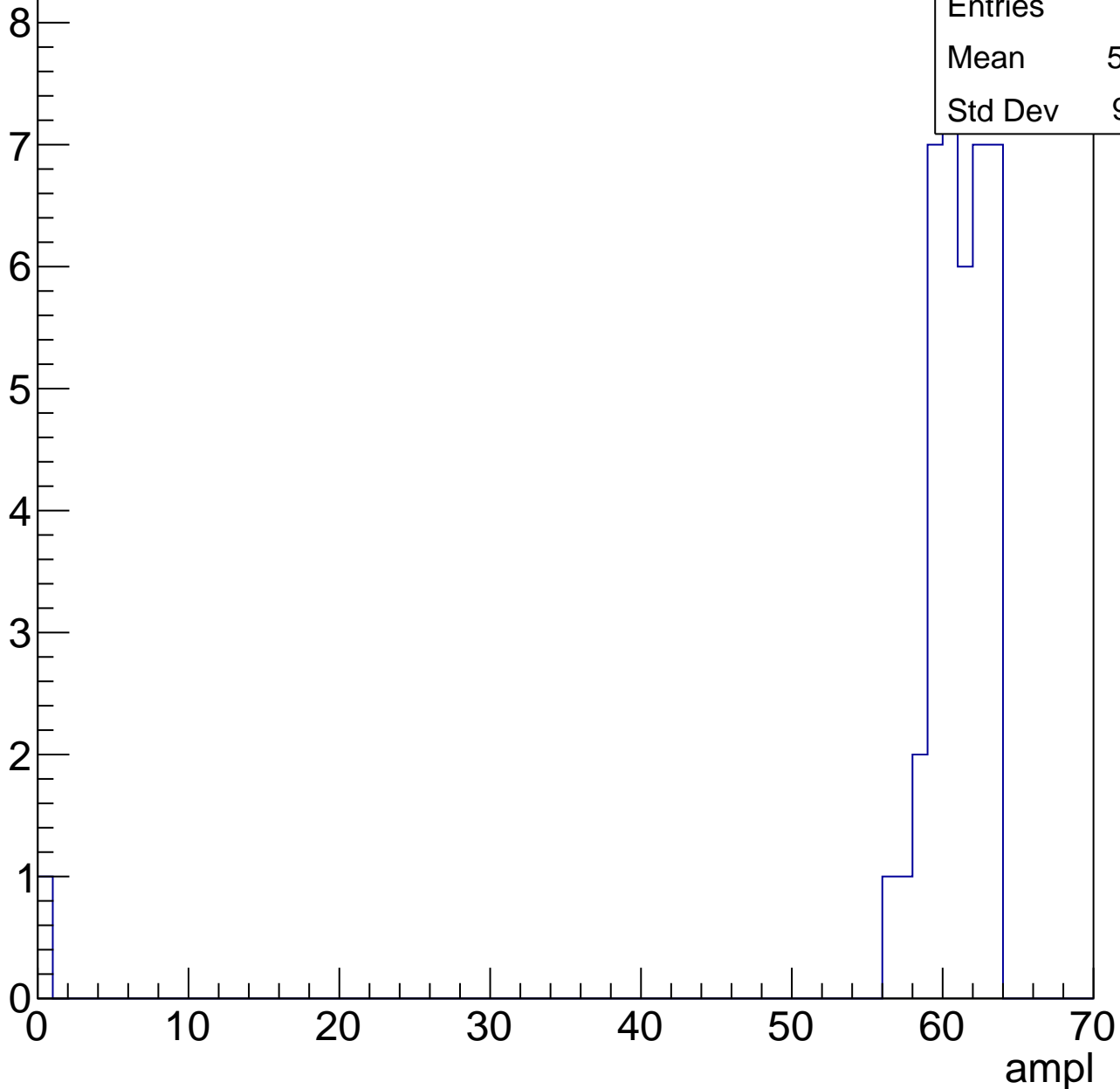


B1L103S, U10-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.08
Std Dev	9.621



B1L103S, U10-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch94, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.65
Std Dev	10.04

Gaus mean : 28.7952

Gaus Width: 3.0787

Entry

10

8

6

4

2

0

ampl

0

10

20

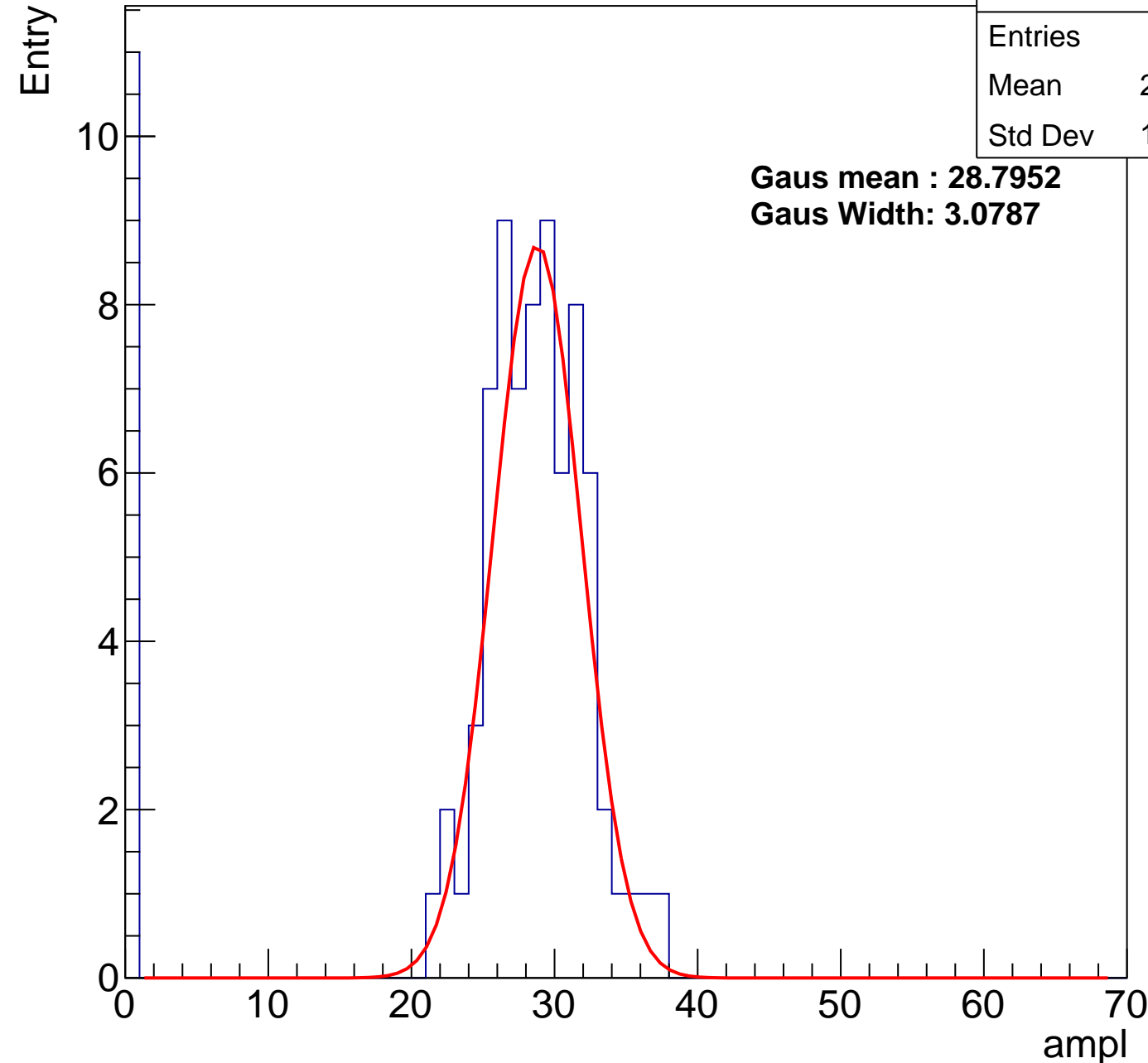
30

40

50

60

70



B1L103S, U10-ch94, adc1

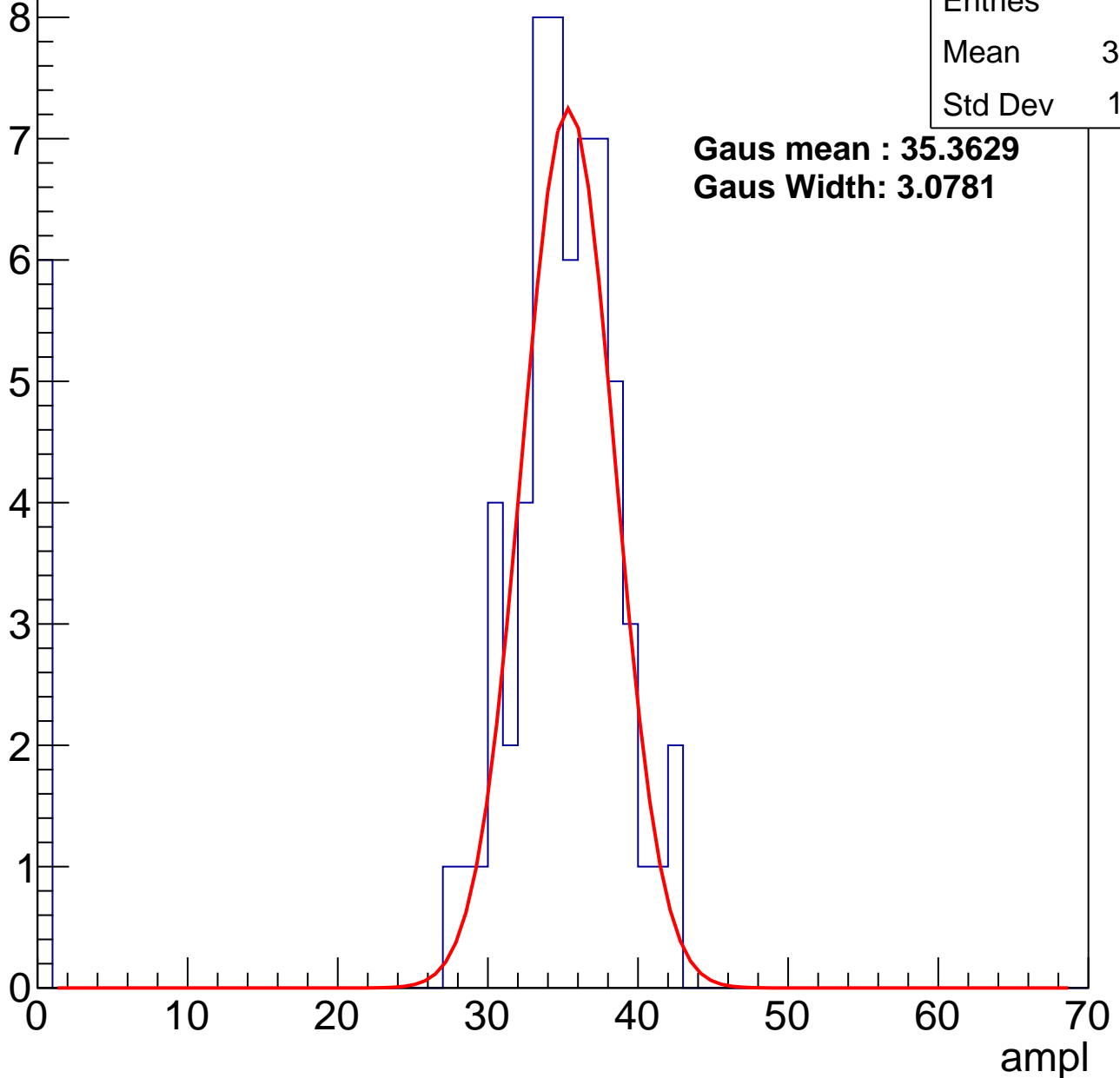
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	31.69
Std Dev	10.41

Gaus mean : 35.3629

Gaus Width: 3.0781



B1L103S, U10-ch94, adc2

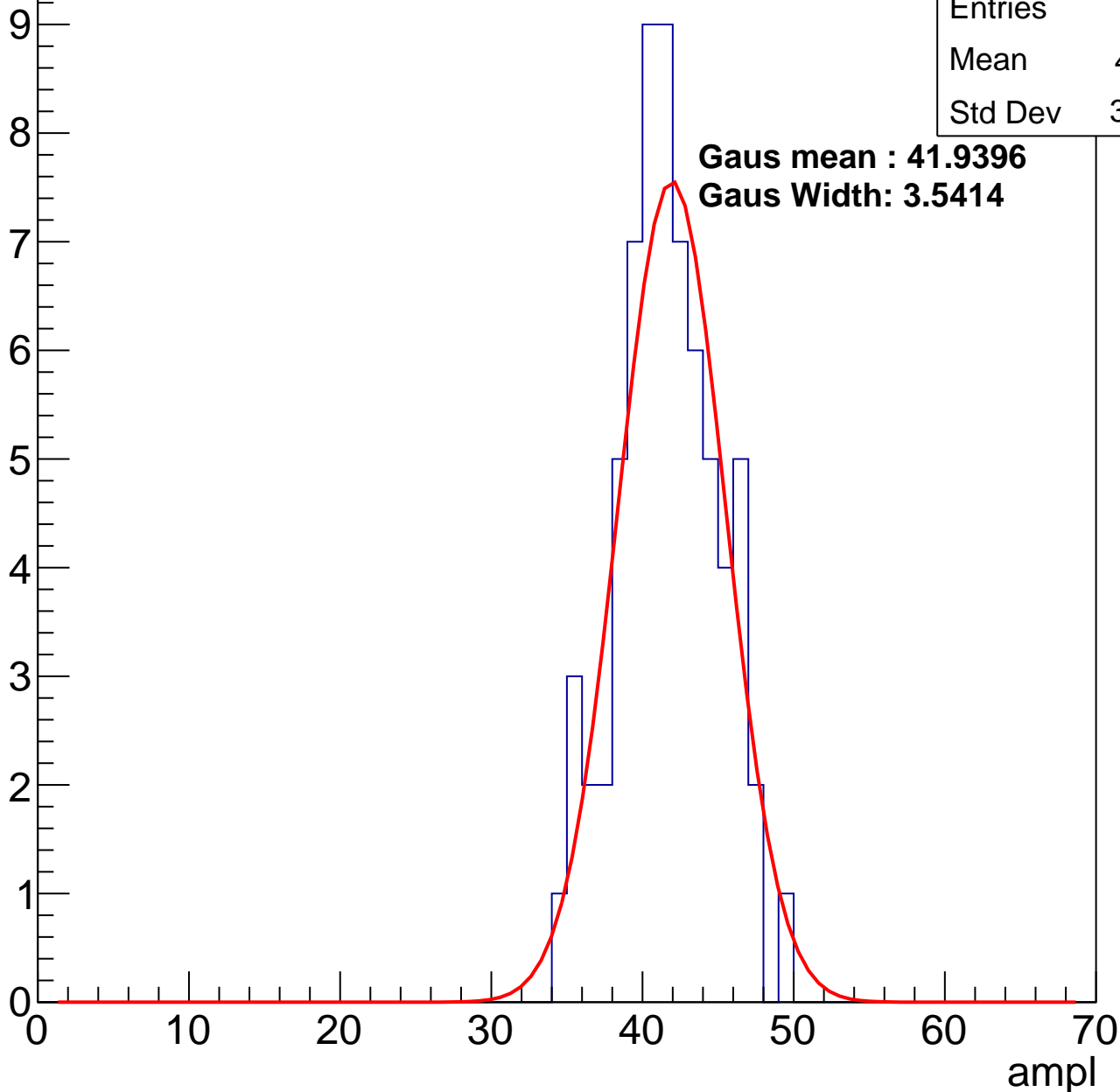
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.21
Std Dev	3.252

Gaus mean : 41.9396

Gaus Width: 3.5414

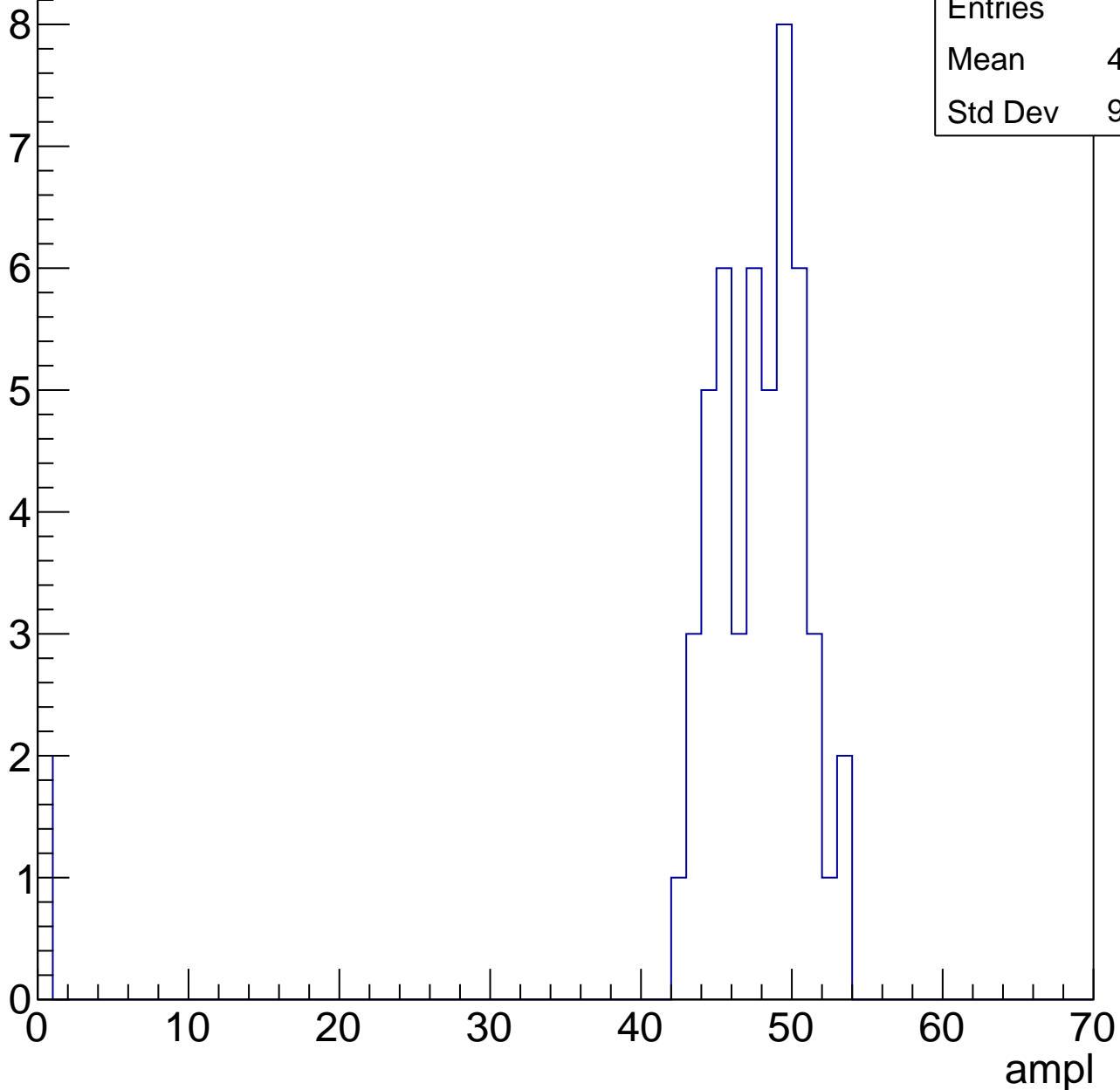


B1L103S, U10-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	45.57
Std Dev	9.594

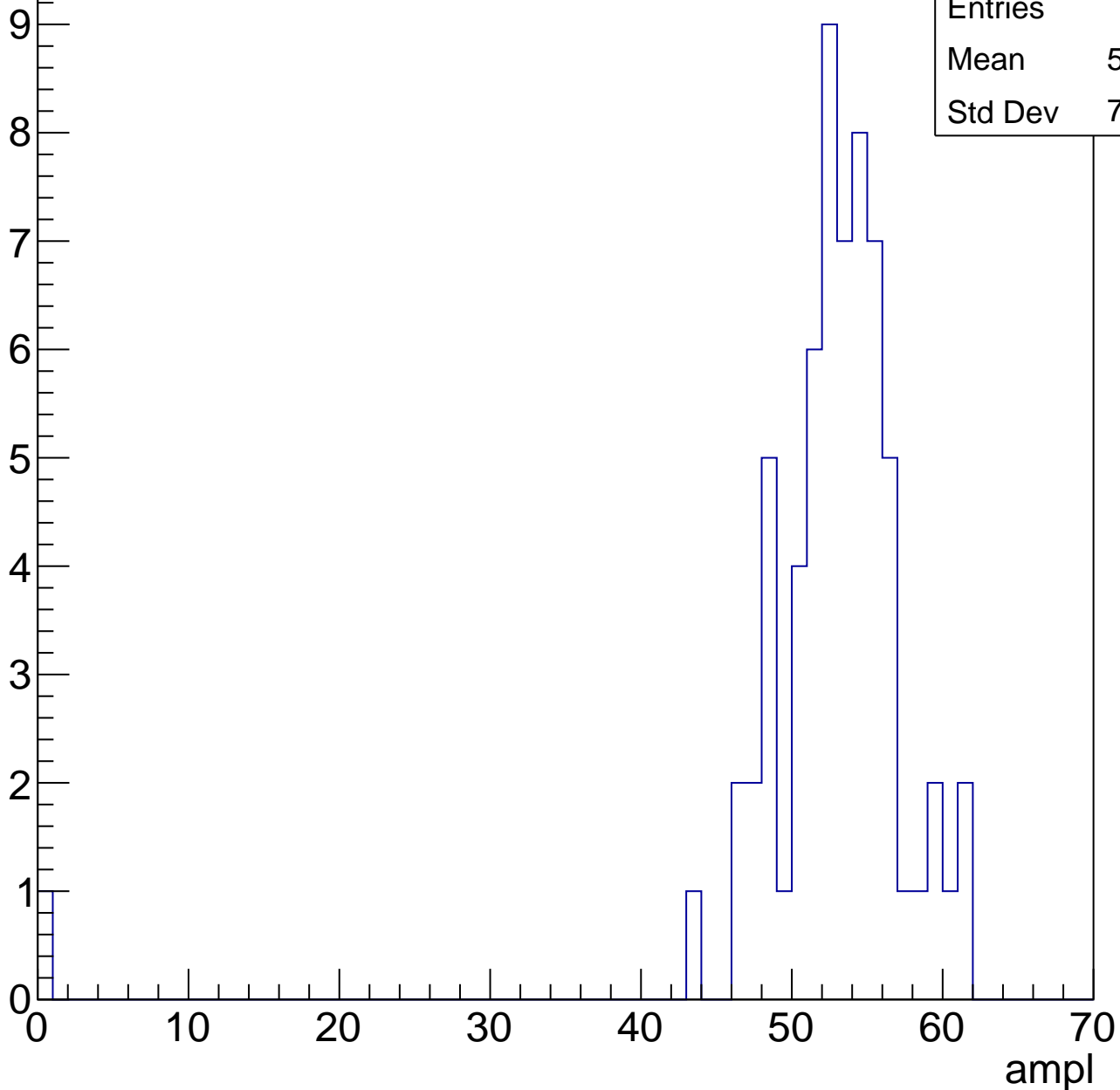


B1L103S, U10-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	51.92
Std Dev	7.426

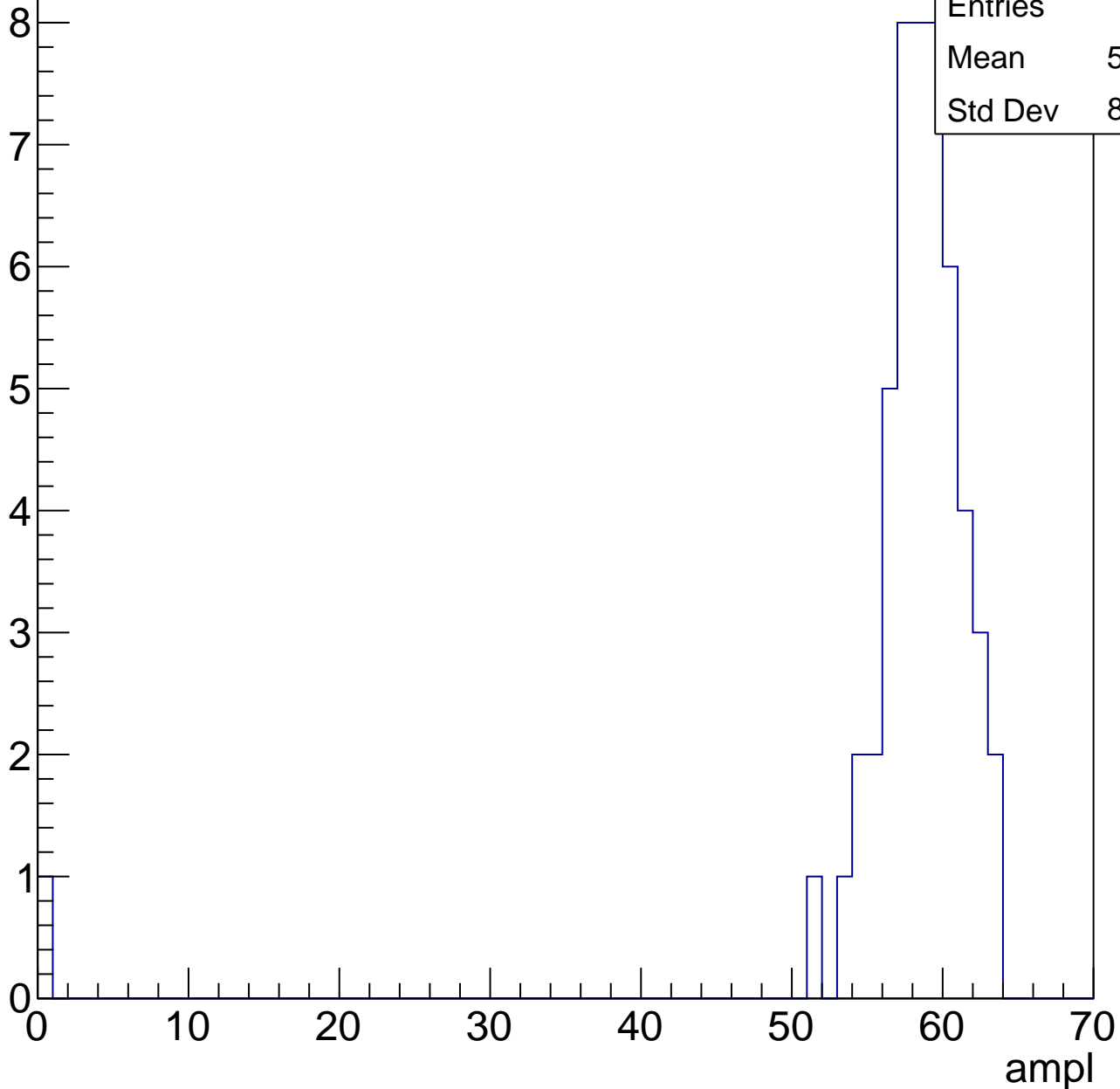


B1L103S, U10-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.06
Std Dev	8.445

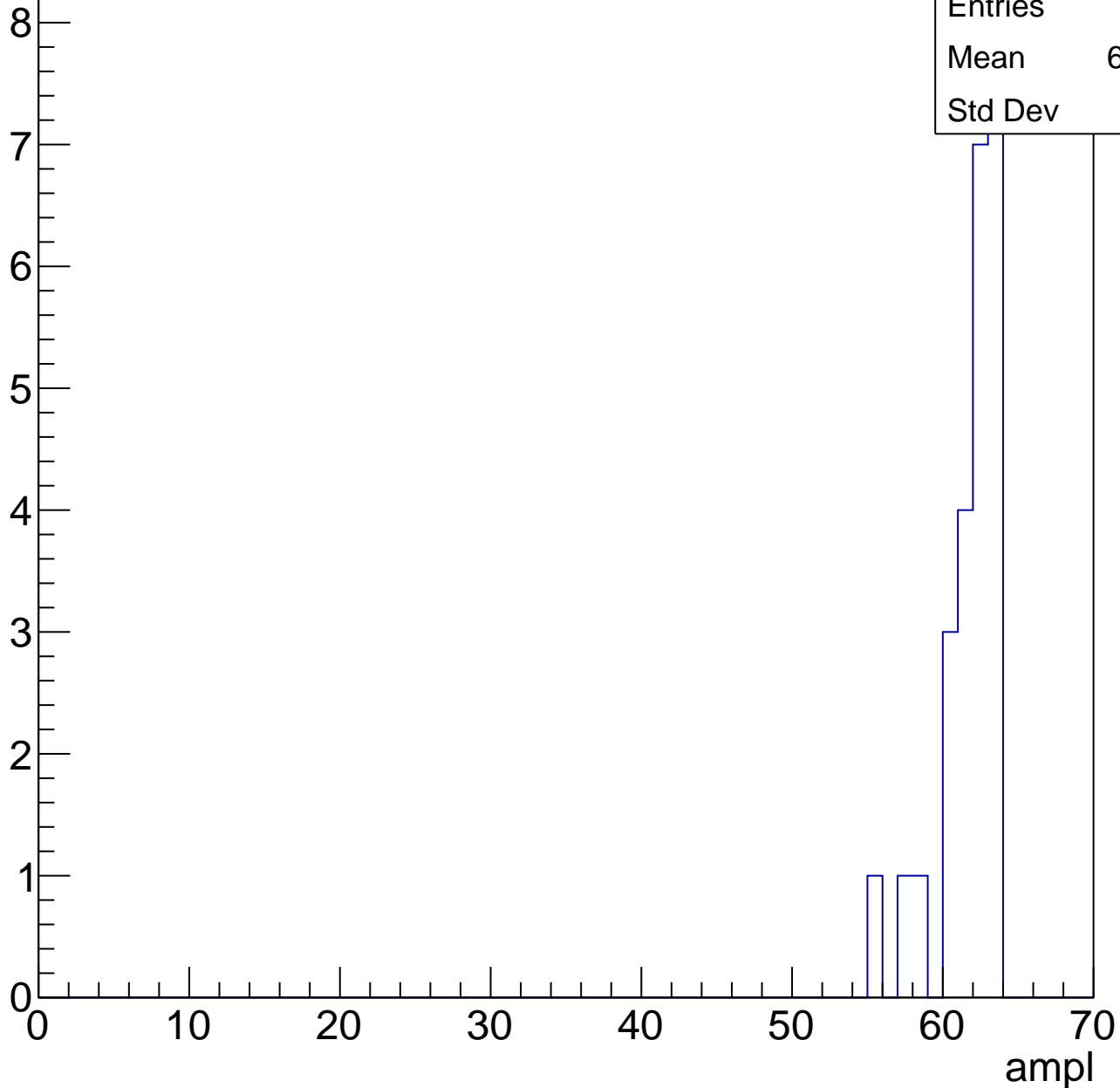


B1L103S, U10-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	61.28
Std Dev	2.01

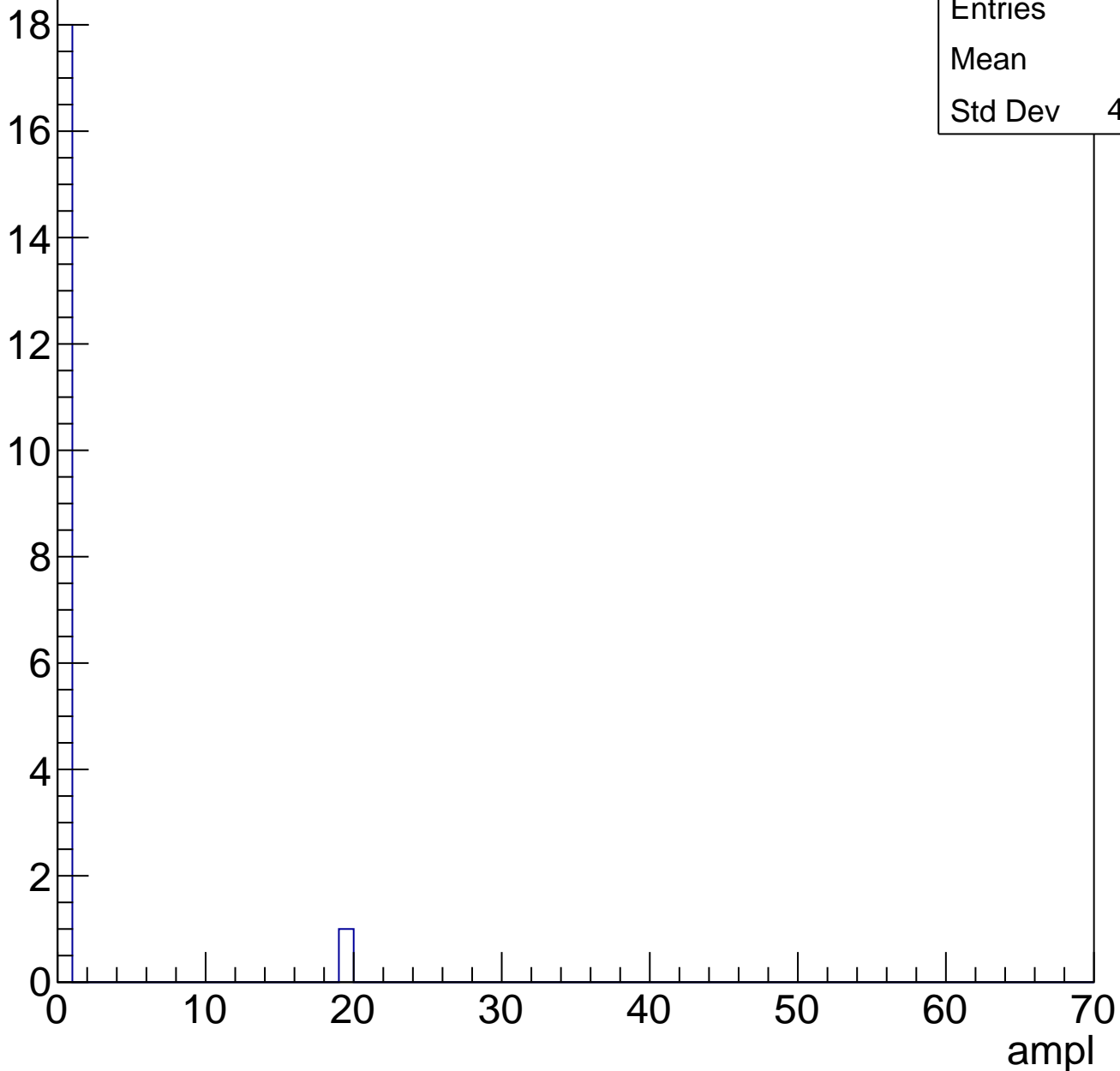


B1L103S, U10-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U10-ch95, adc0

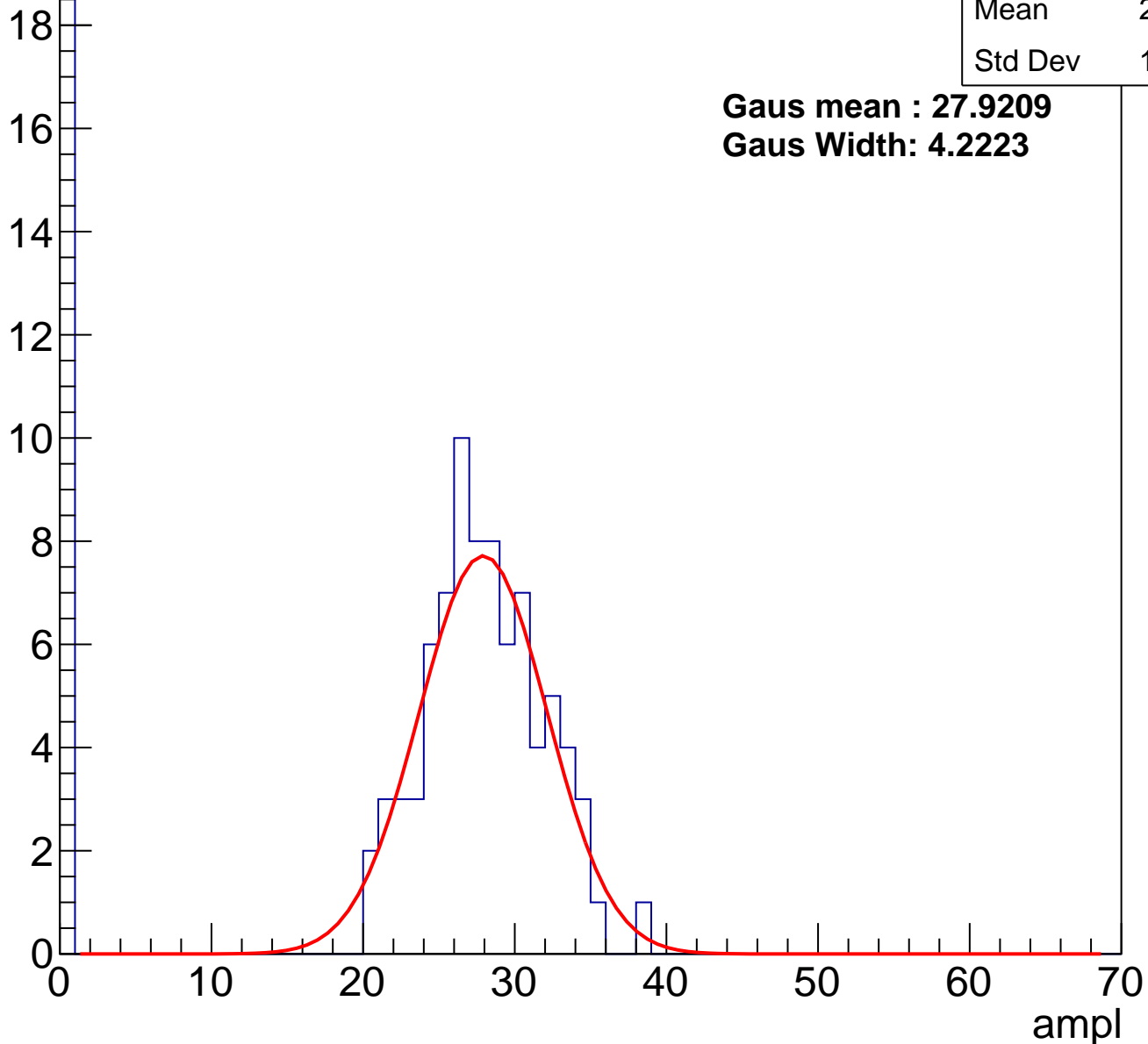
calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	22.32
Std Dev	11.33

Gaus mean : 27.9209

Gaus Width: 4.2223

Entry



B1L103S, U10-ch95, adc1

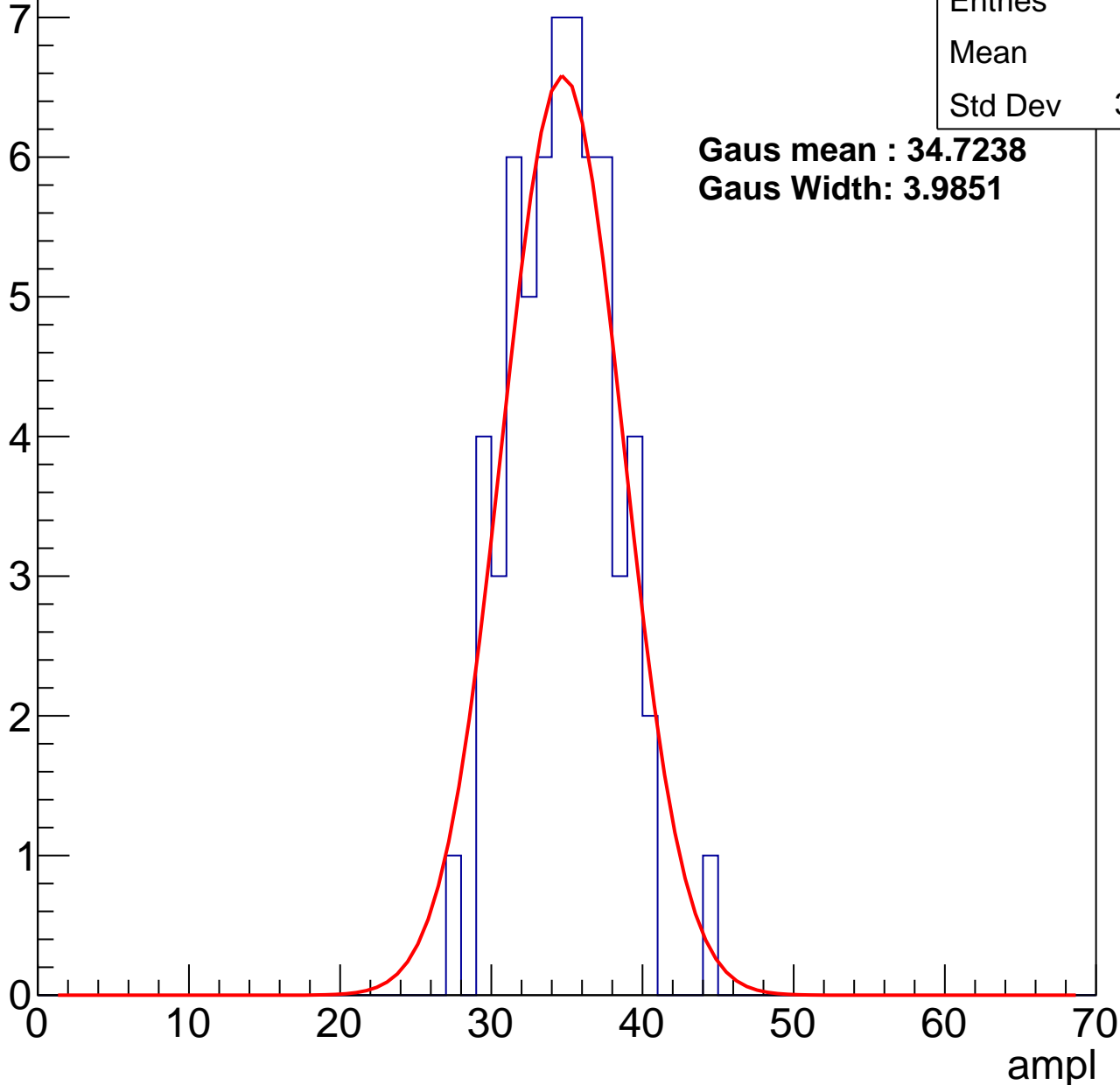
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.3
Std Dev	3.331

Gaus mean : 34.7238

Gaus Width: 3.9851



B1L103S, U10-ch95, adc2

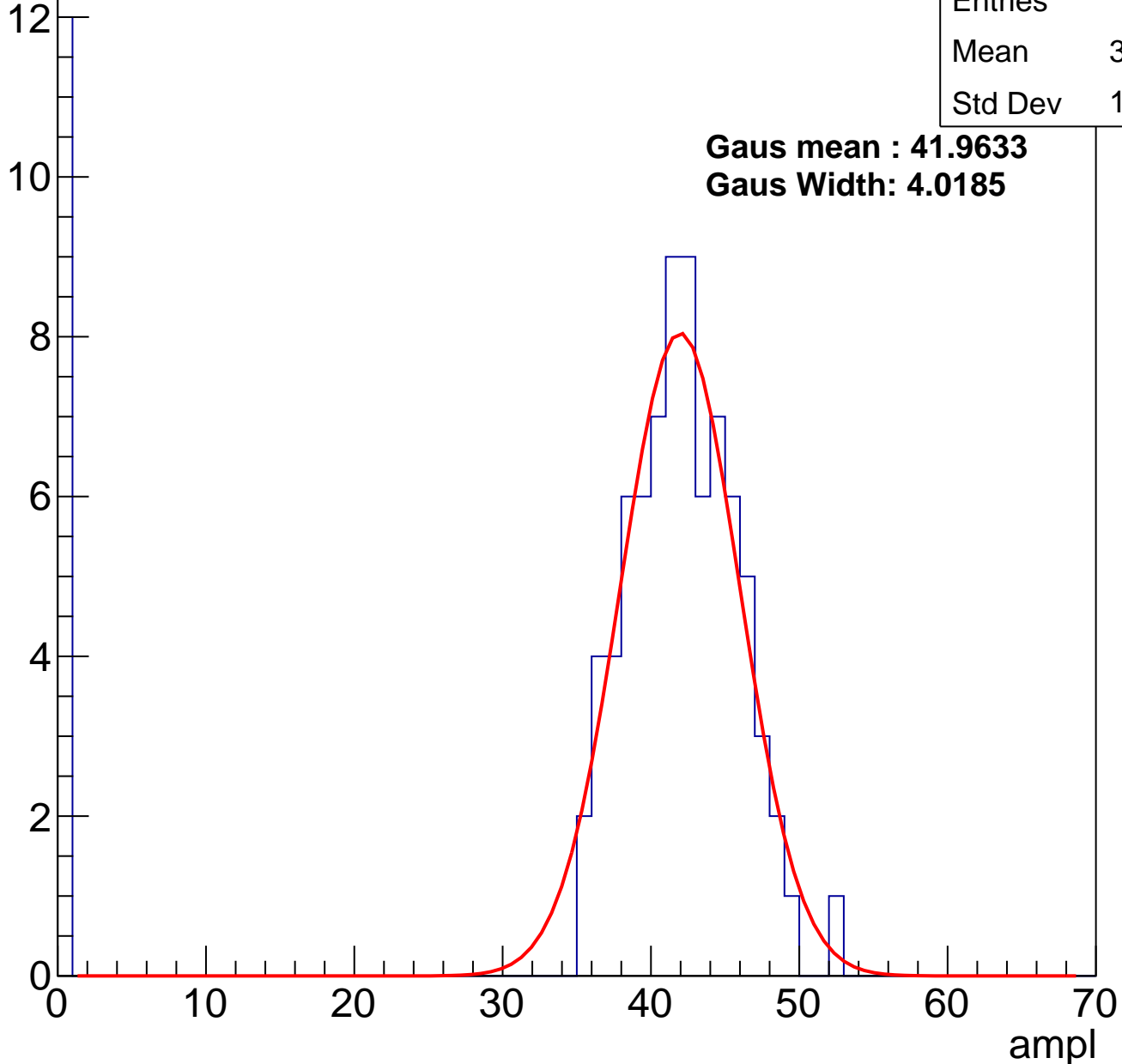
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	36.17
Std Dev	14.57

Gaus mean : 41.9633

Gaus Width: 4.0185

Entry

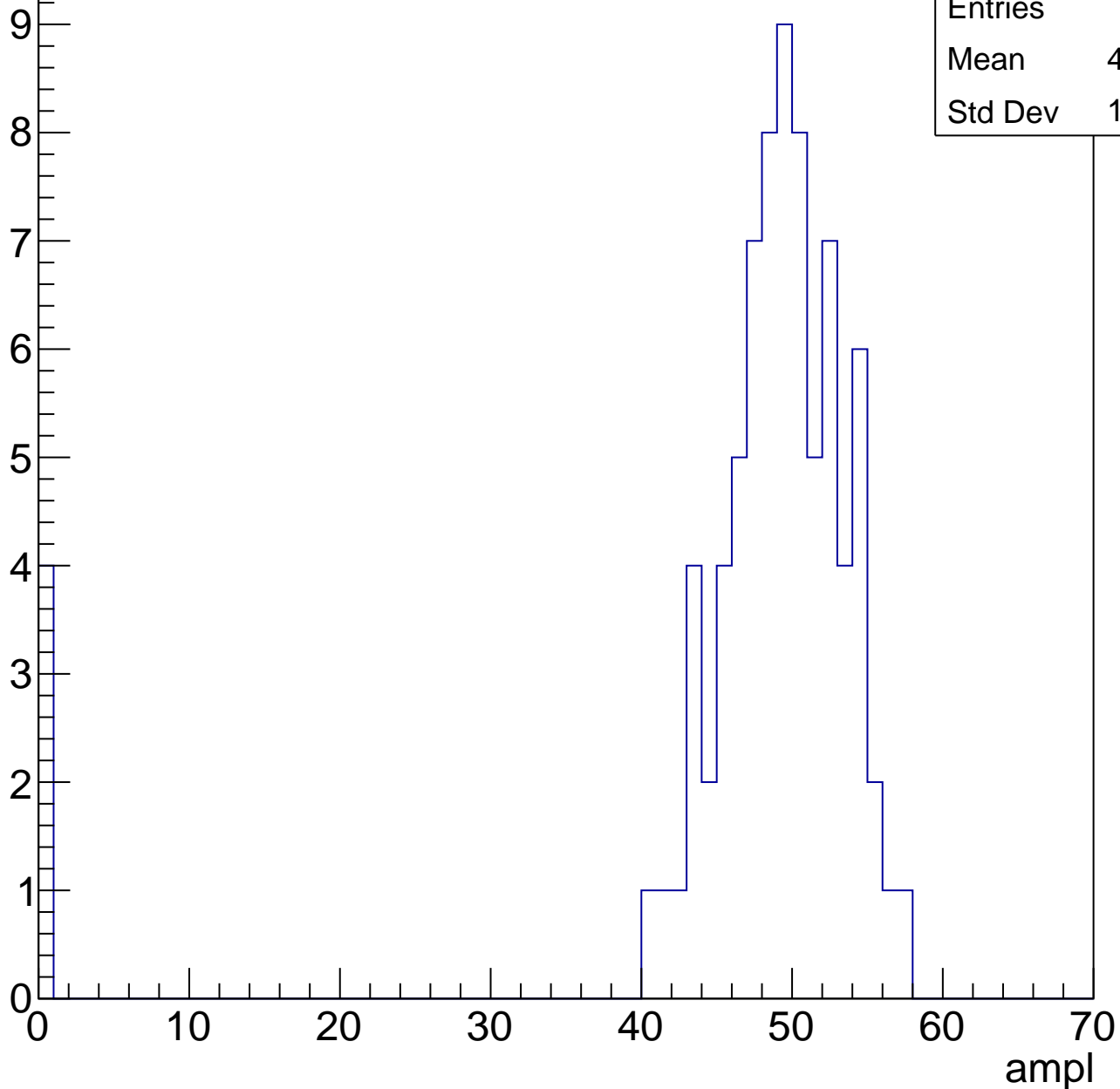


B1L103S, U10-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	46.56
Std Dev	11.26

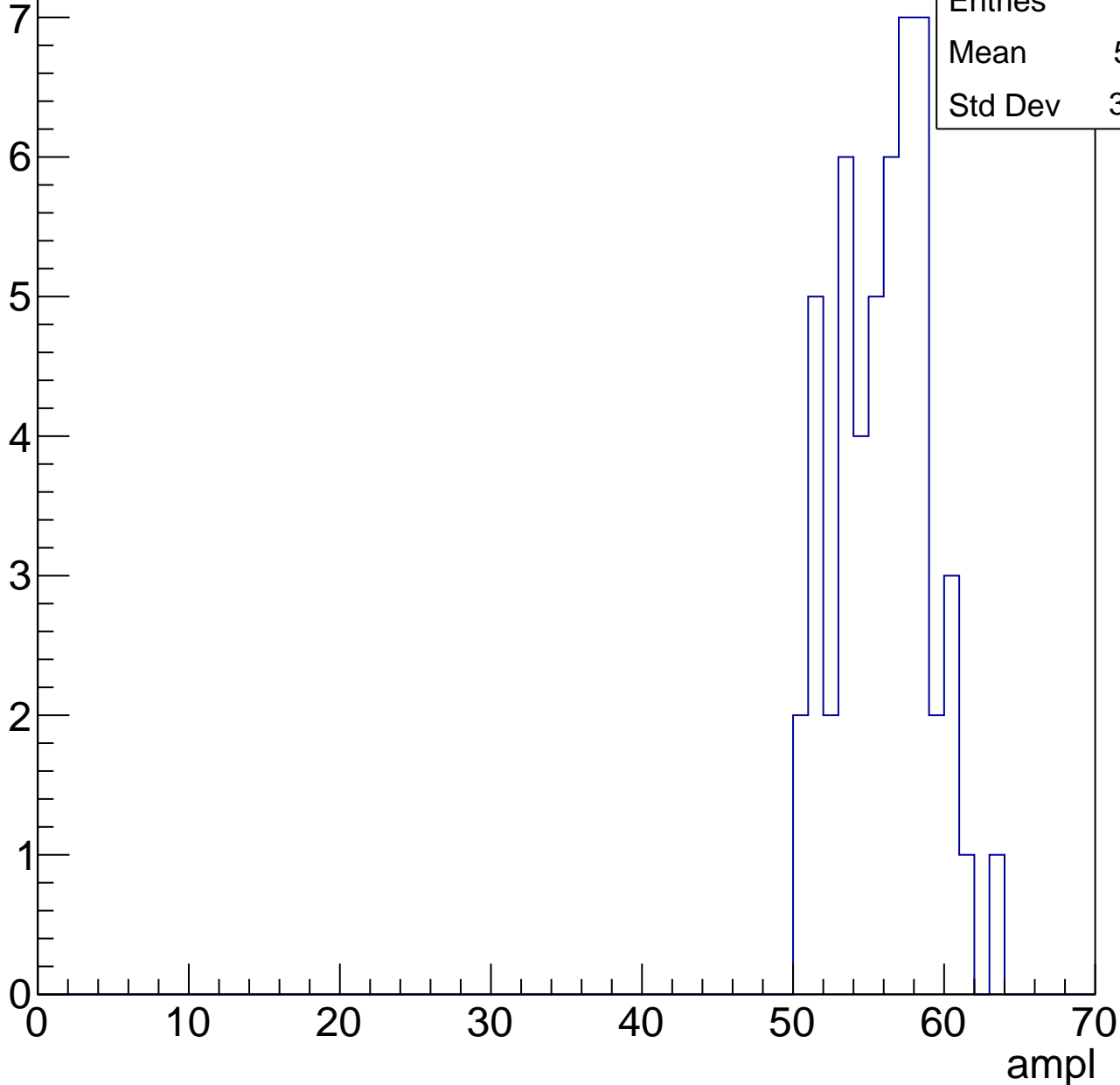


B1L103S, U10-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

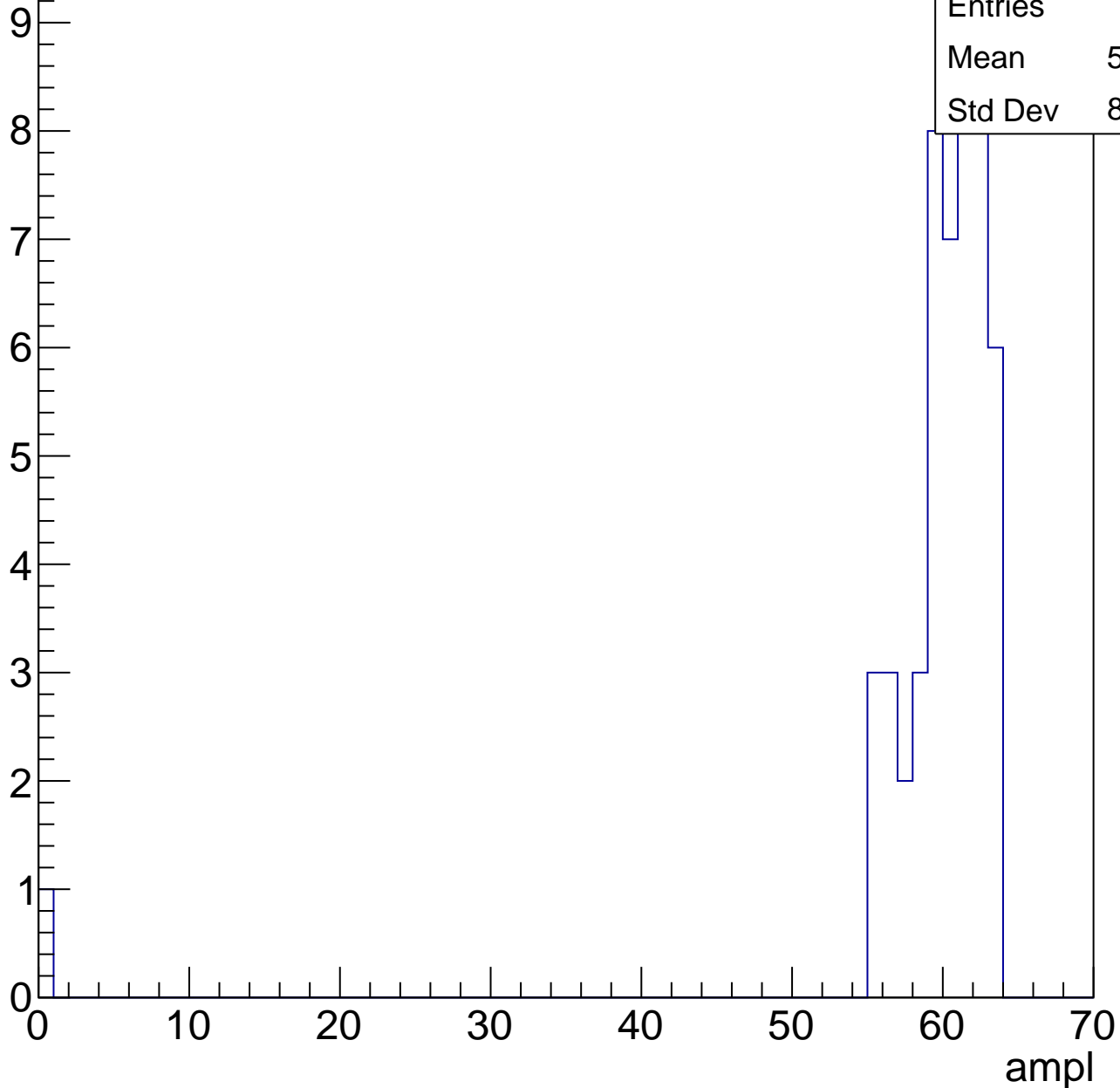
Entries	51
Mean	55.51
Std Dev	3.025



B1L103S, U10-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch96, adc0

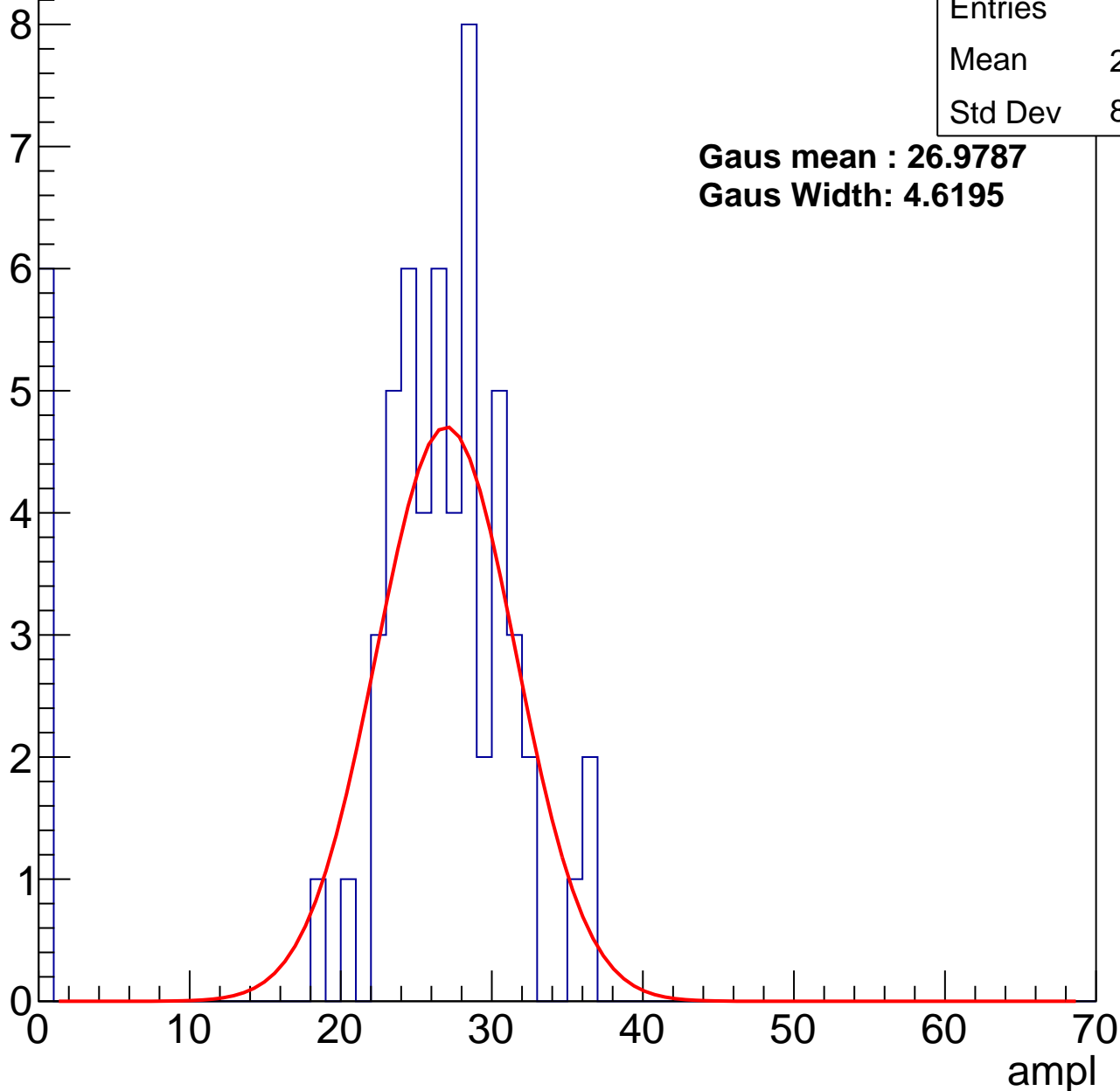
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	24.12
Std Dev	8.859

Gaus mean : 26.9787

Gaus Width: 4.6195



B1L103S, U10-ch96, adc1

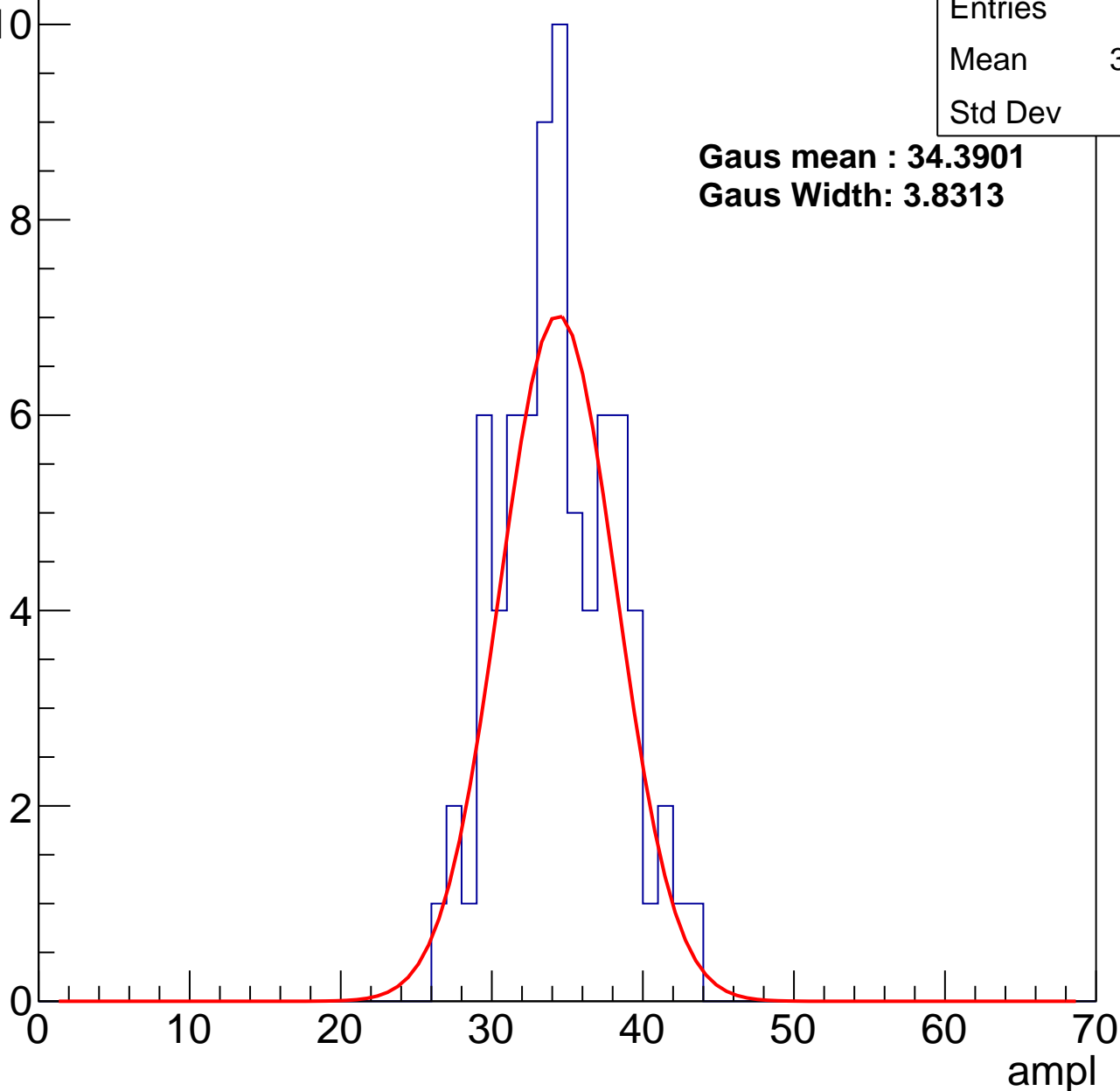
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.99
Std Dev	3.74

Gaus mean : 34.3901

Gaus Width: 3.8313



B1L103S, U10-ch96, adc2

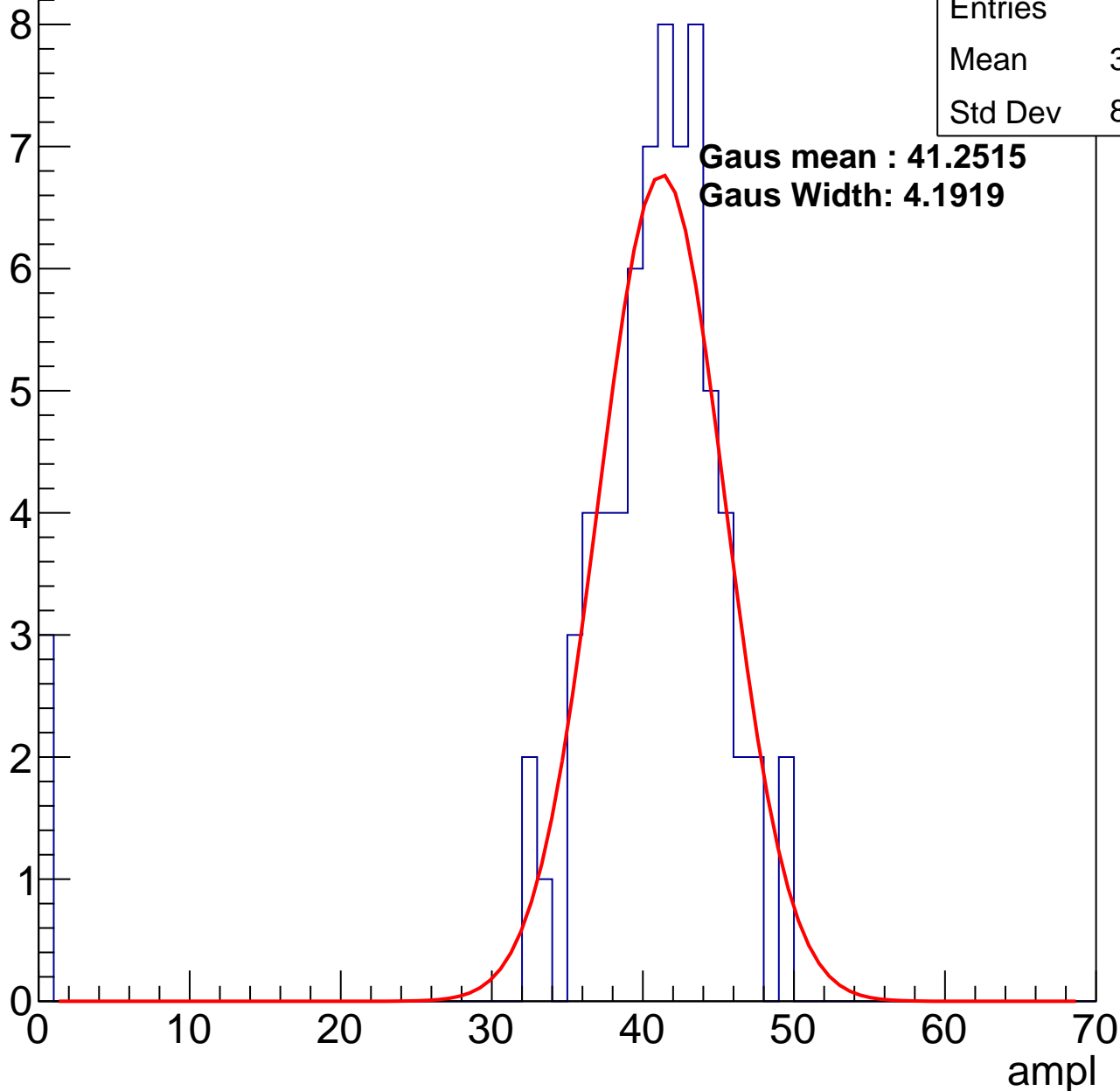
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	39.03
Std Dev	8.918

Gaus mean : 41.2515

Gaus Width: 4.1919

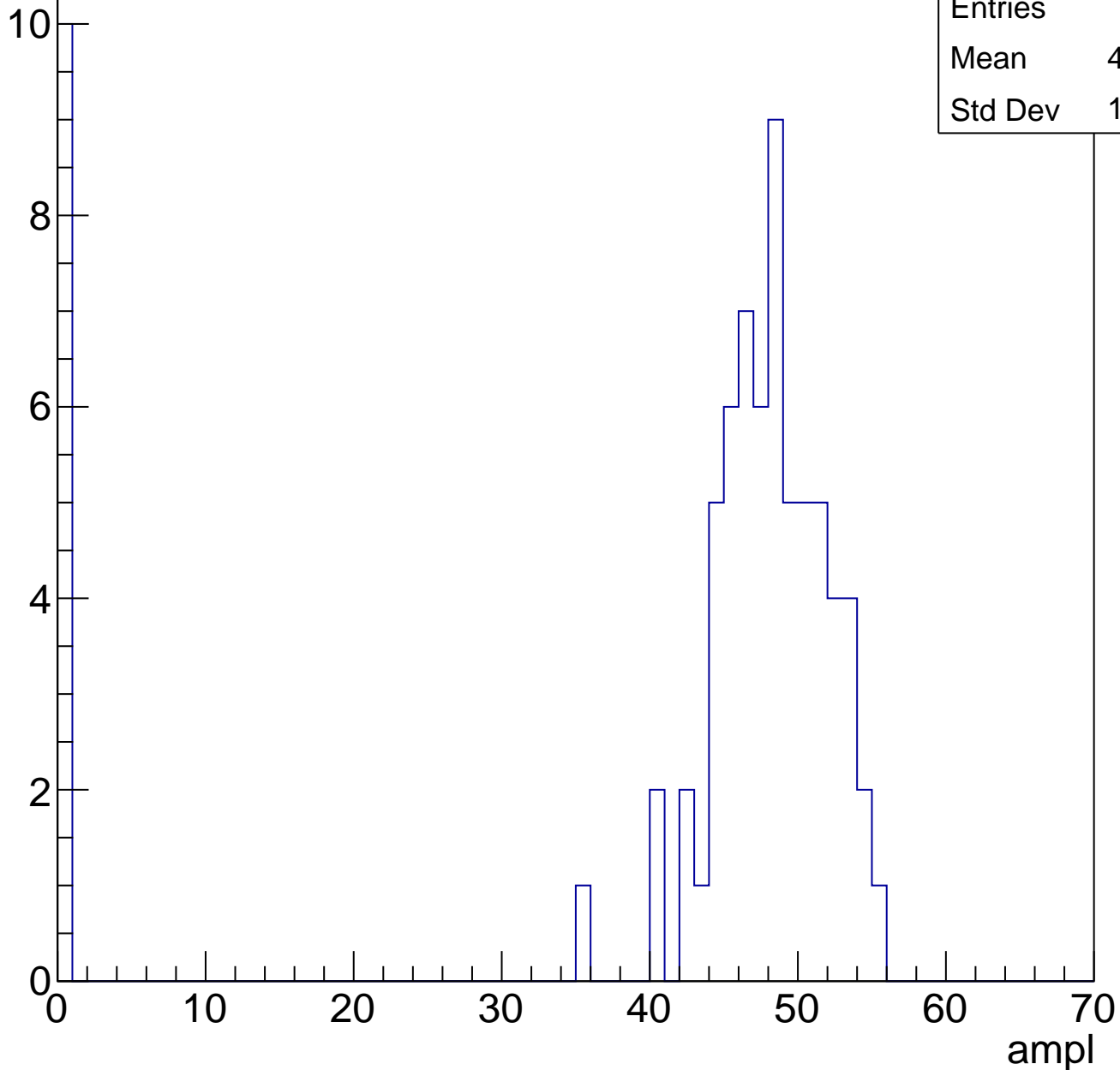


B1L103S, U10-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	41.35
Std Dev	16.59

Entry

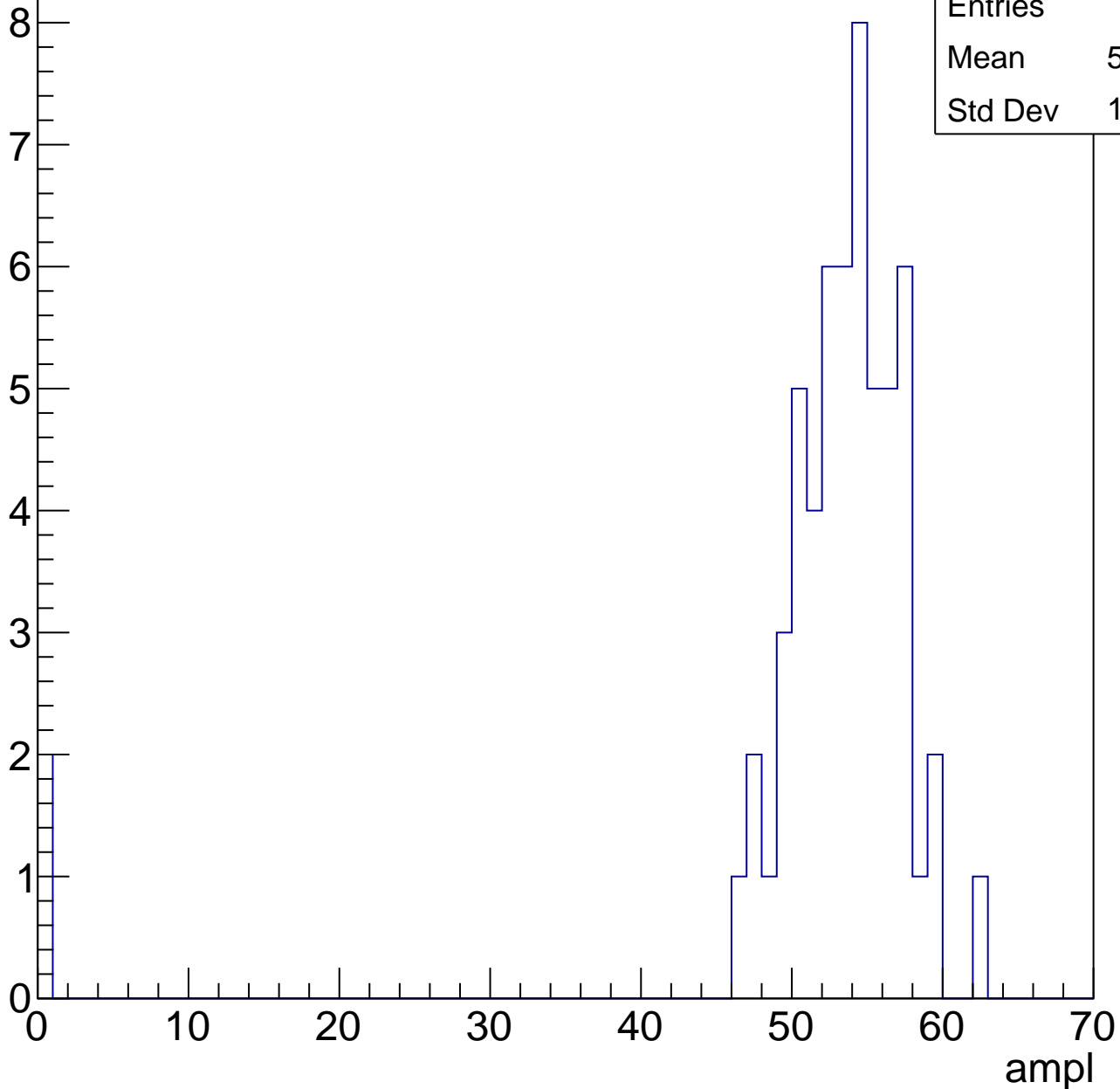


B1L103S, U10-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

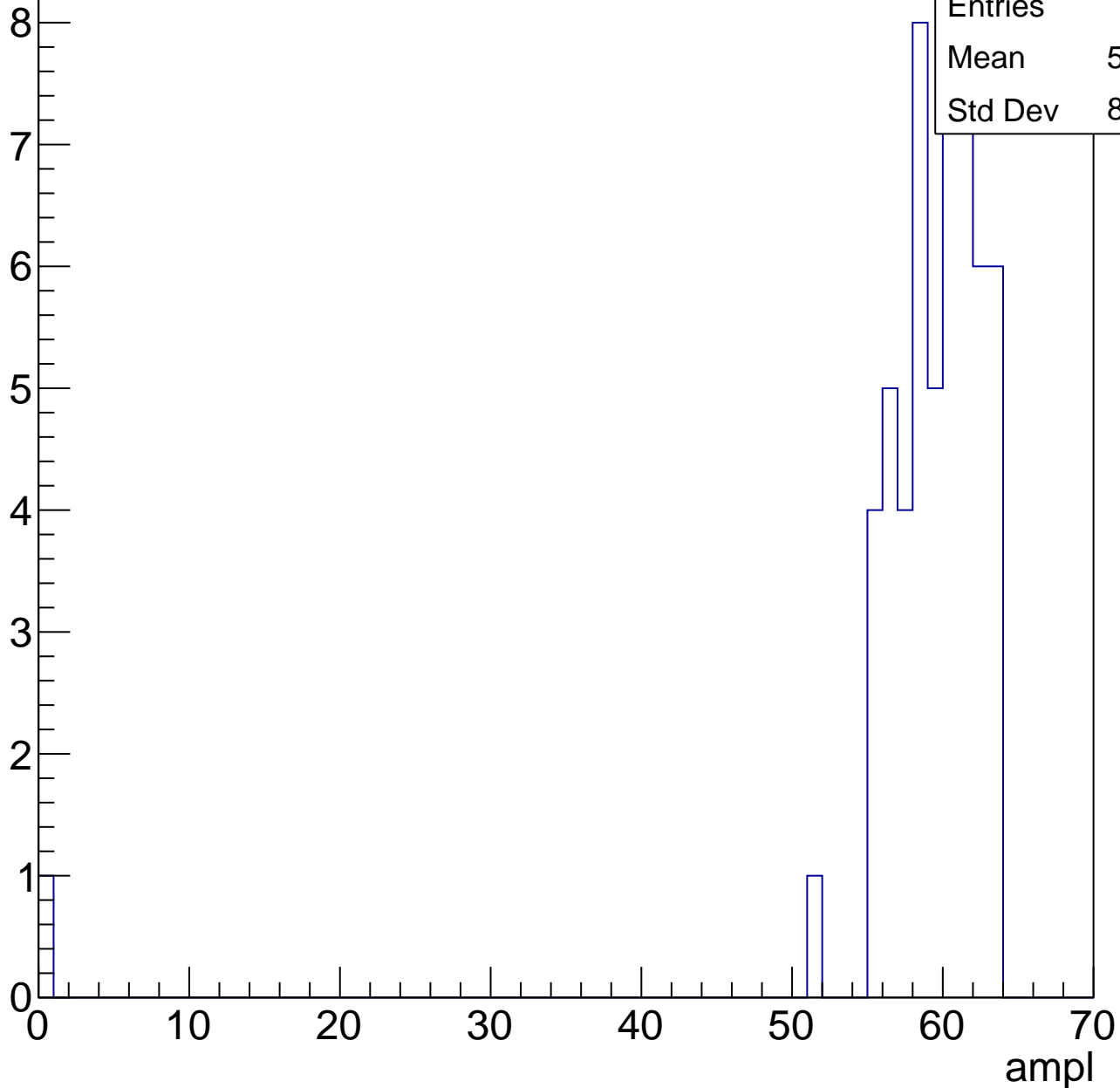
Entries	58
Mean	51.48
Std Dev	10.25



B1L103S, U10-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

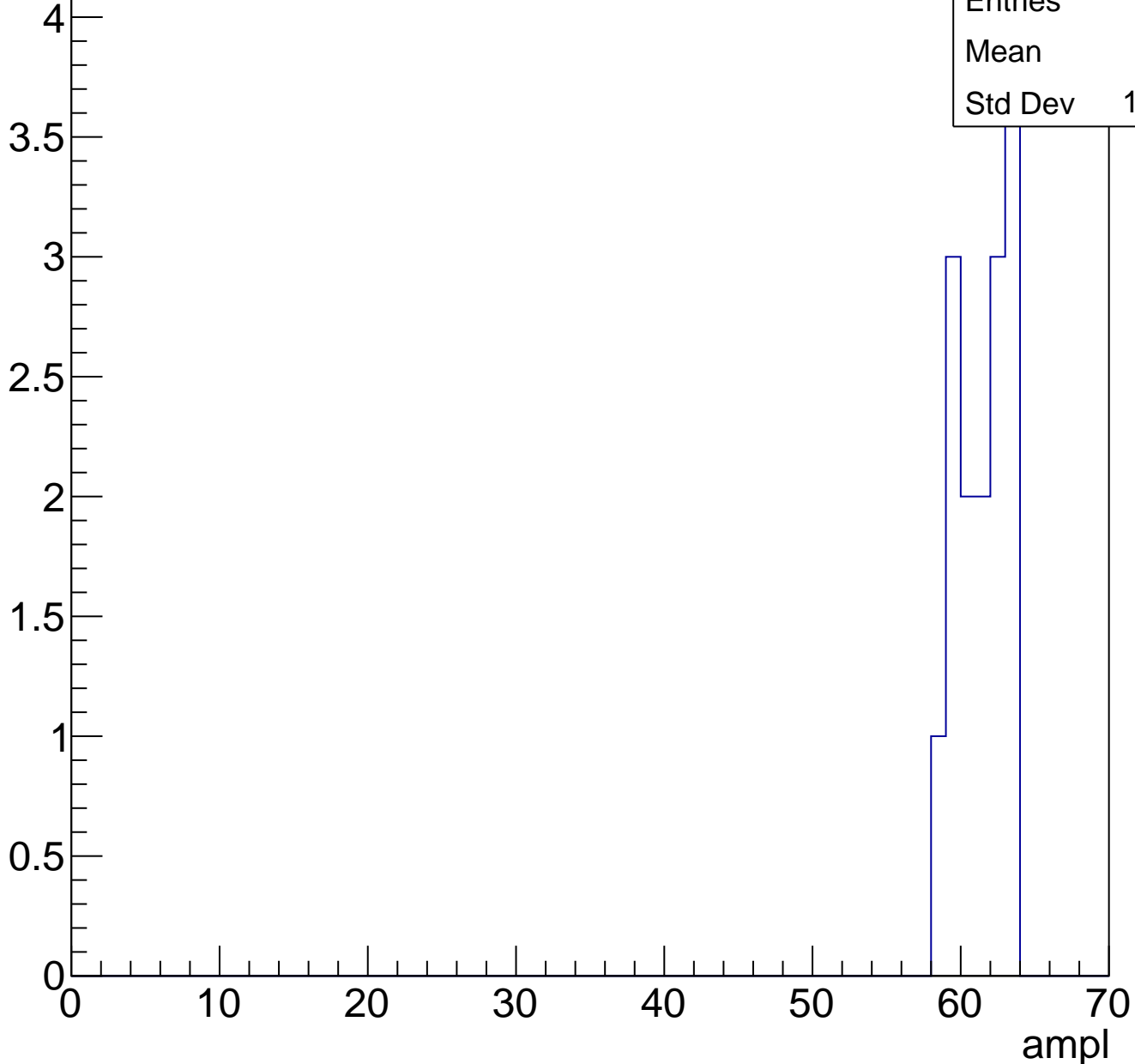
Entry



B1L103S, U10-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

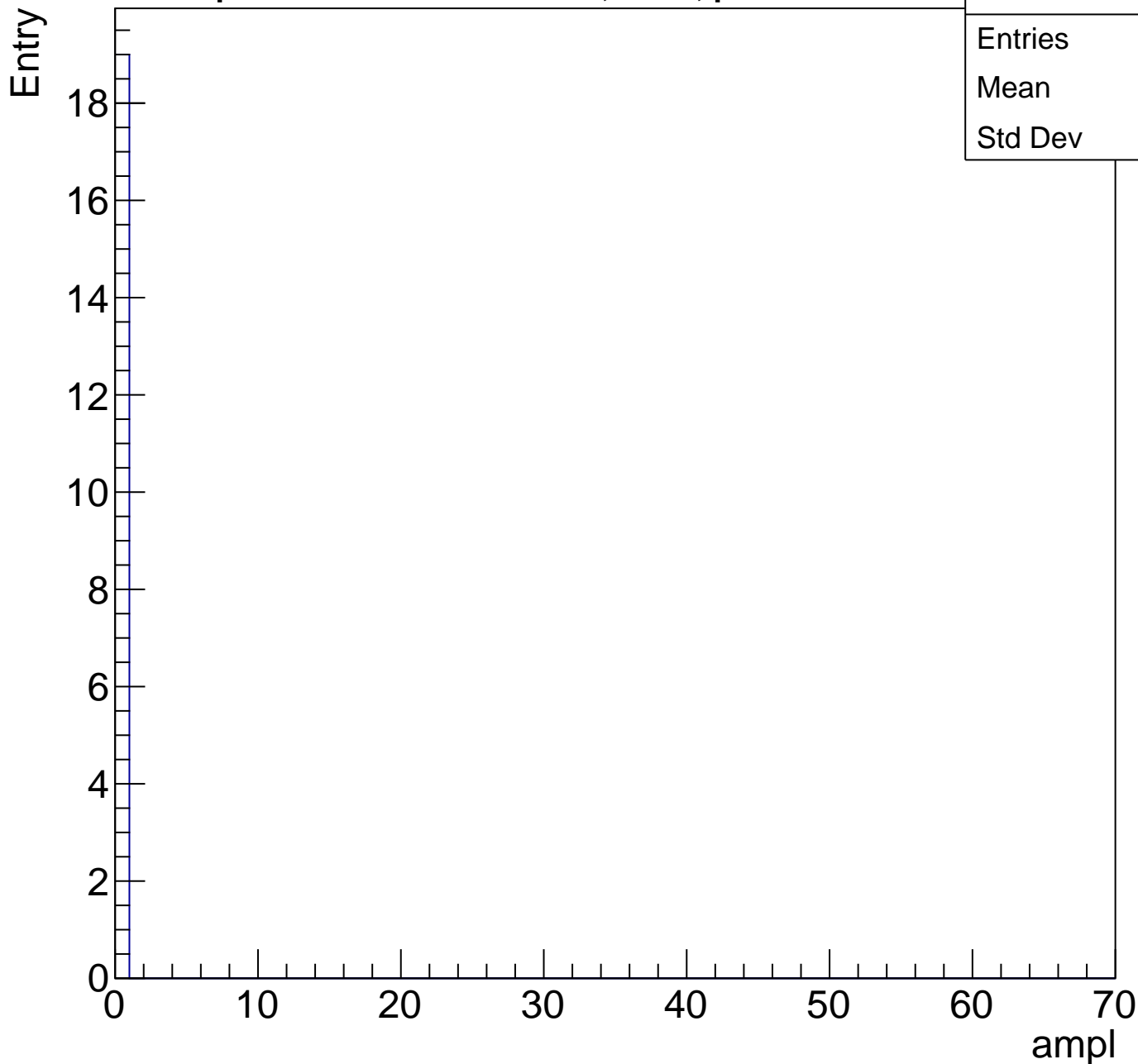


Entries	15
Mean	61
Std Dev	1.673

B1L103S, U10-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0



B1L103S, U10-ch97, adc0

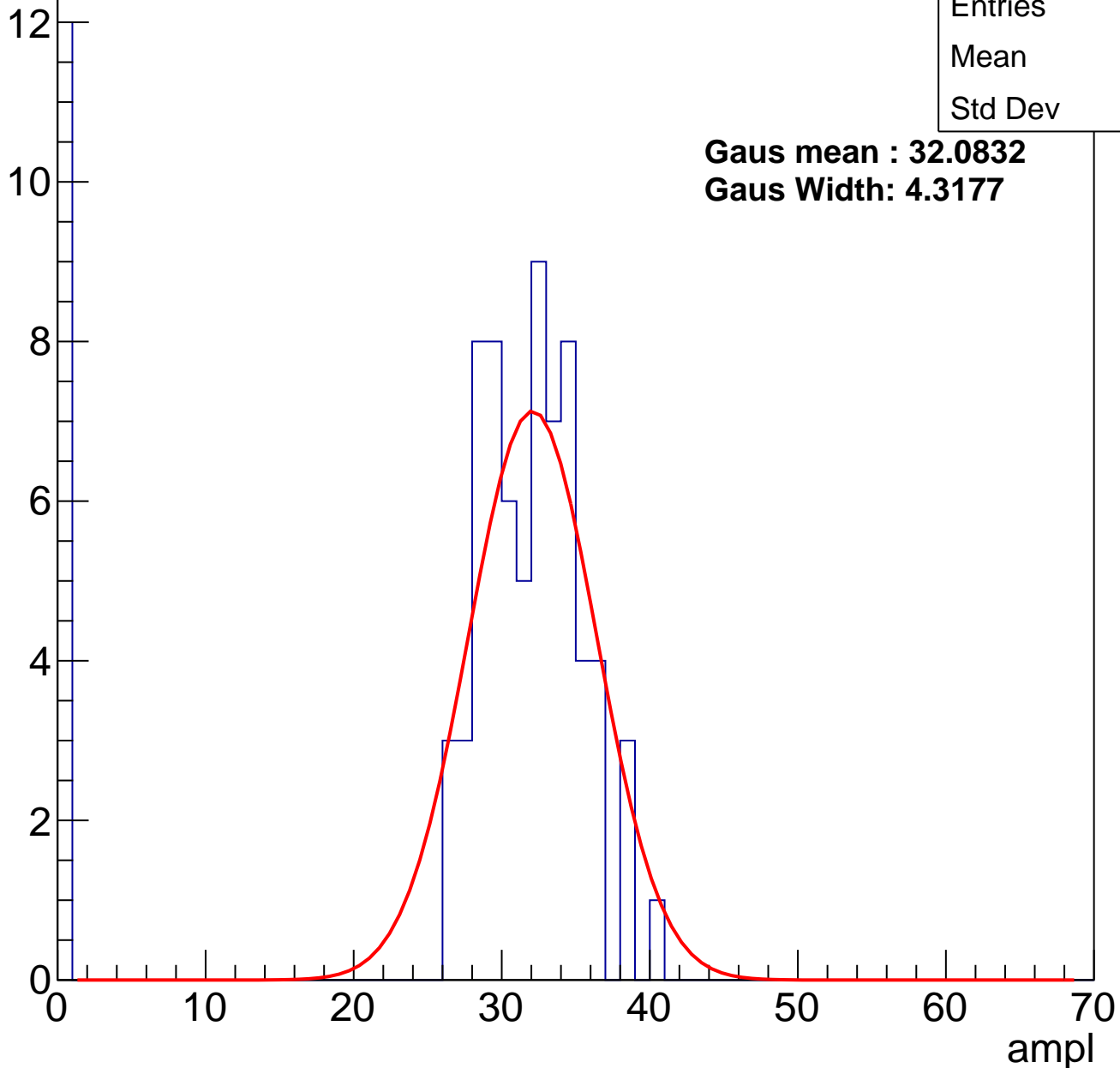
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	26.9
Std Dev	11.6

Gaus mean : 32.0832

Gaus Width: 4.3177

Entry



B1L103S, U10-ch97, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	34.97
Std Dev	11.7

Gaus mean : 39.0515

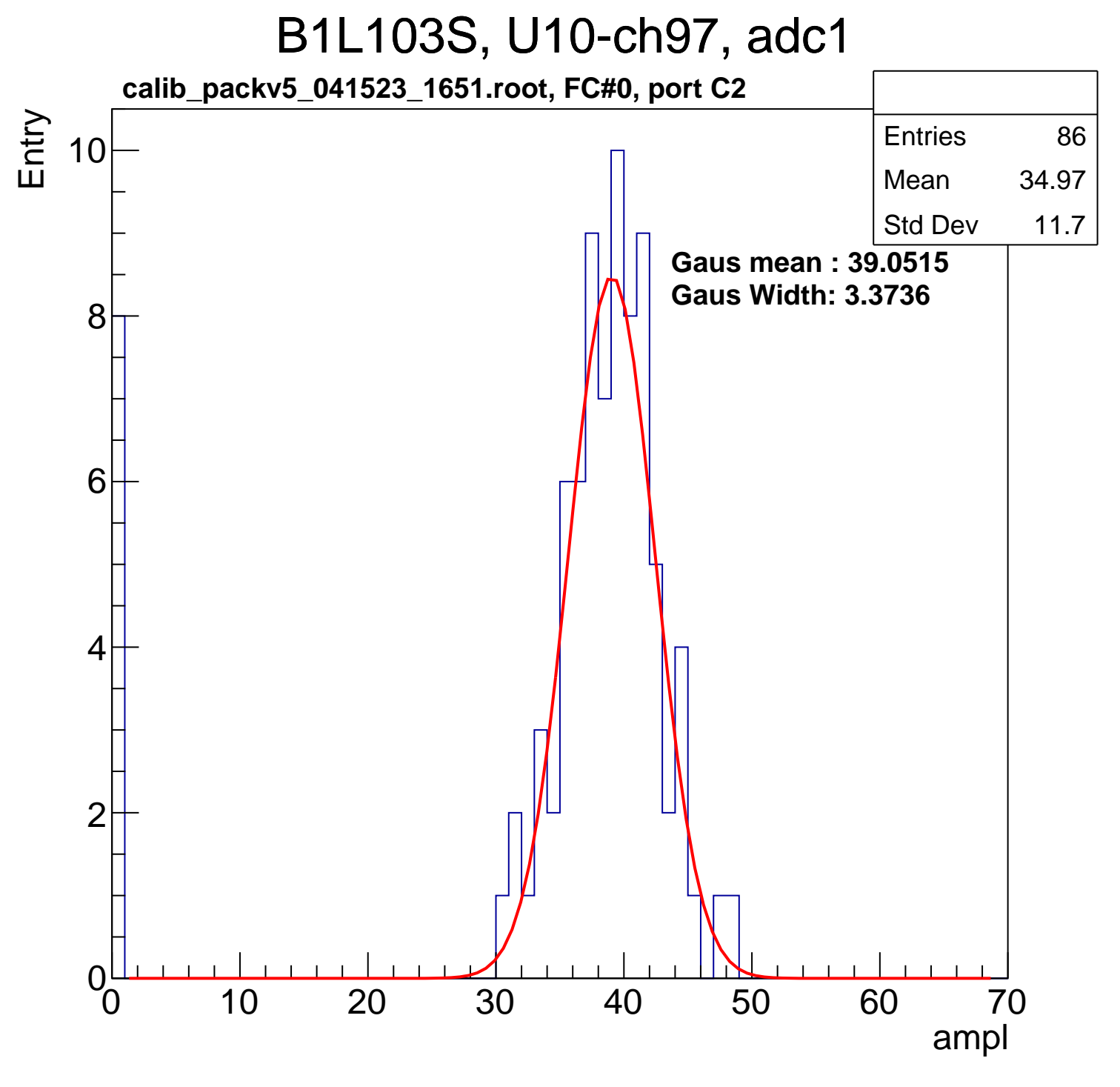
Gaus Width: 3.3736

Entry

10
8
6
4
2
0

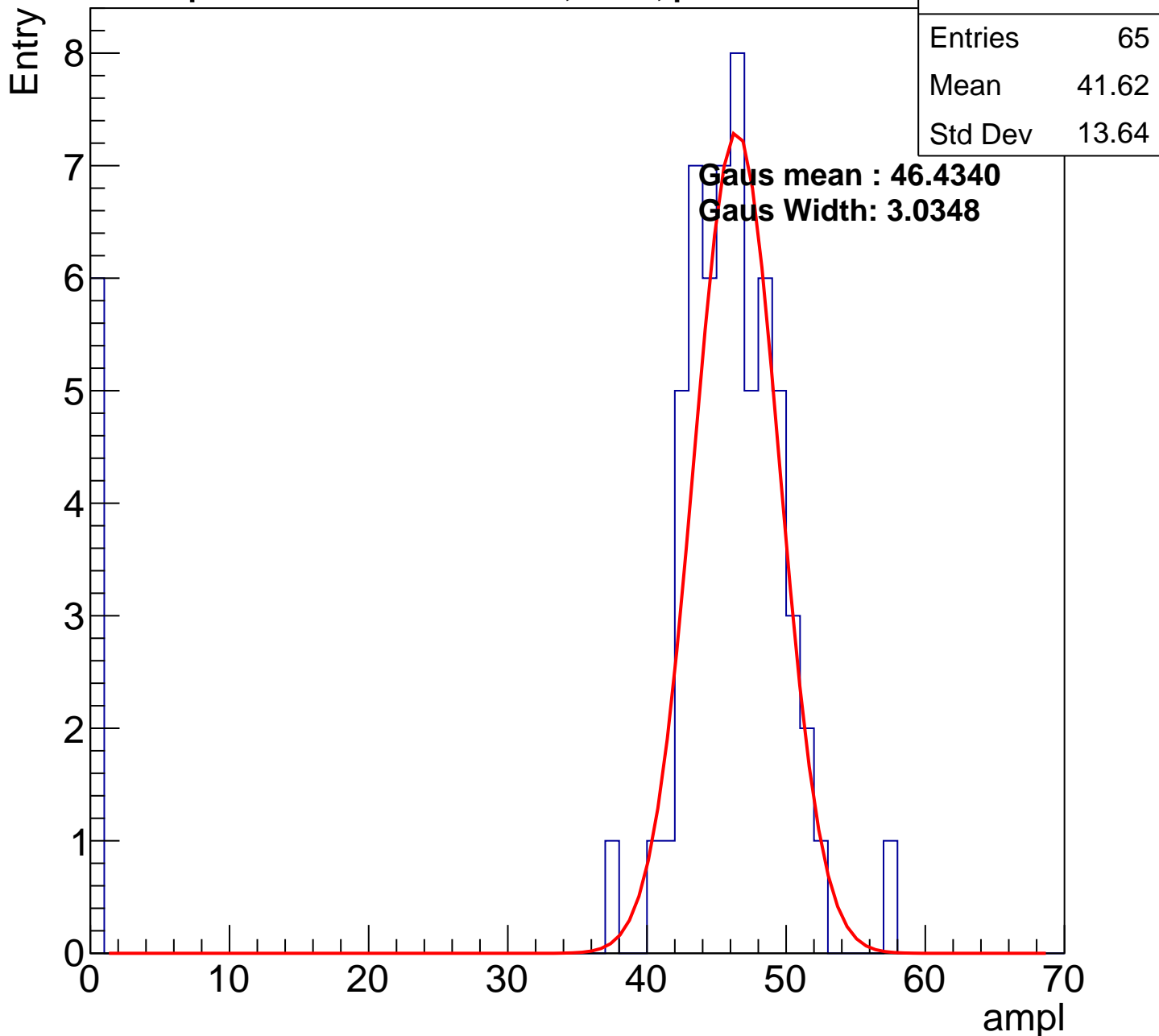
ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch97, adc2

calib_packv5_041523_1651.root, FC#0, port C2

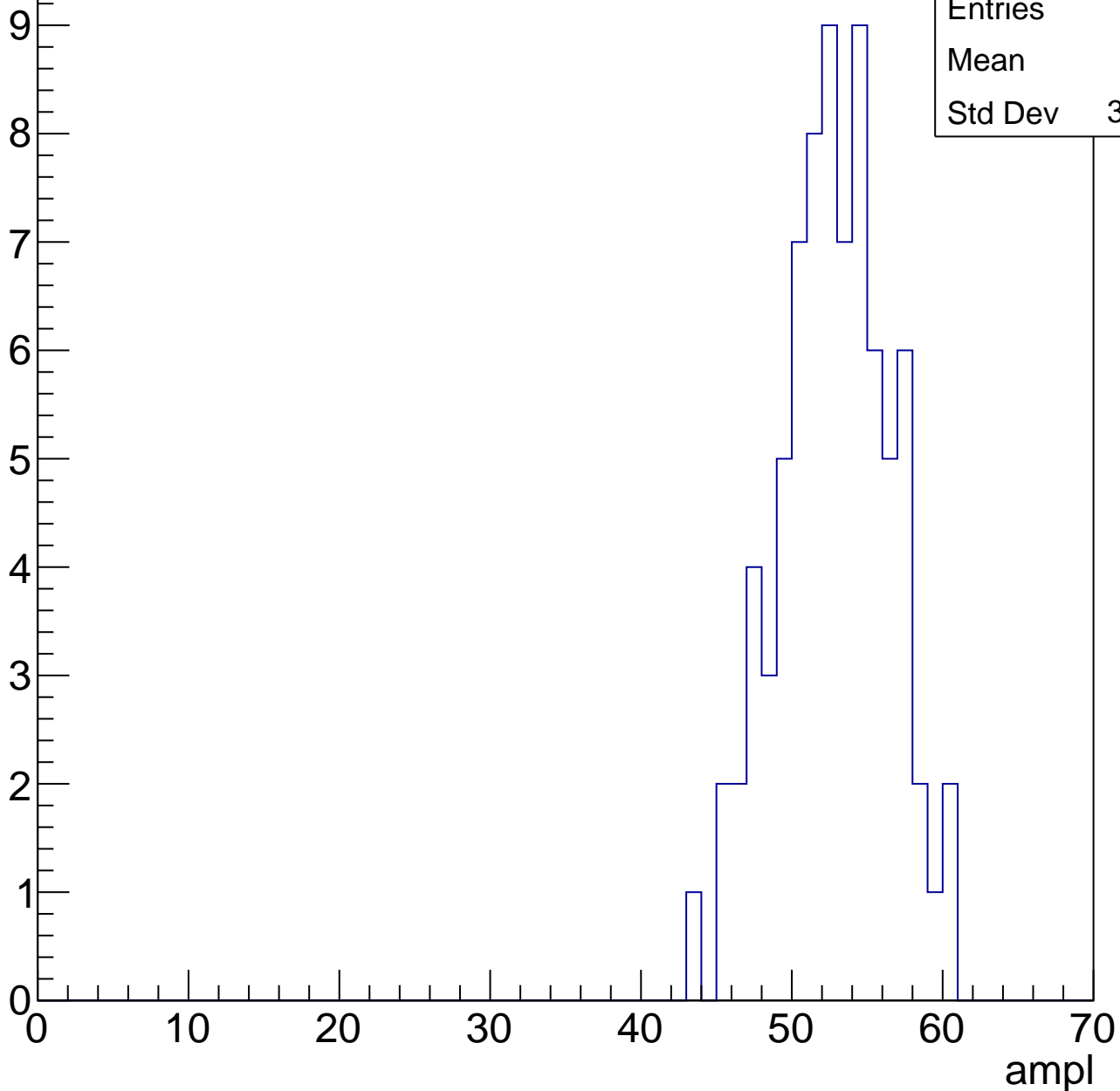


B1L103S, U10-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

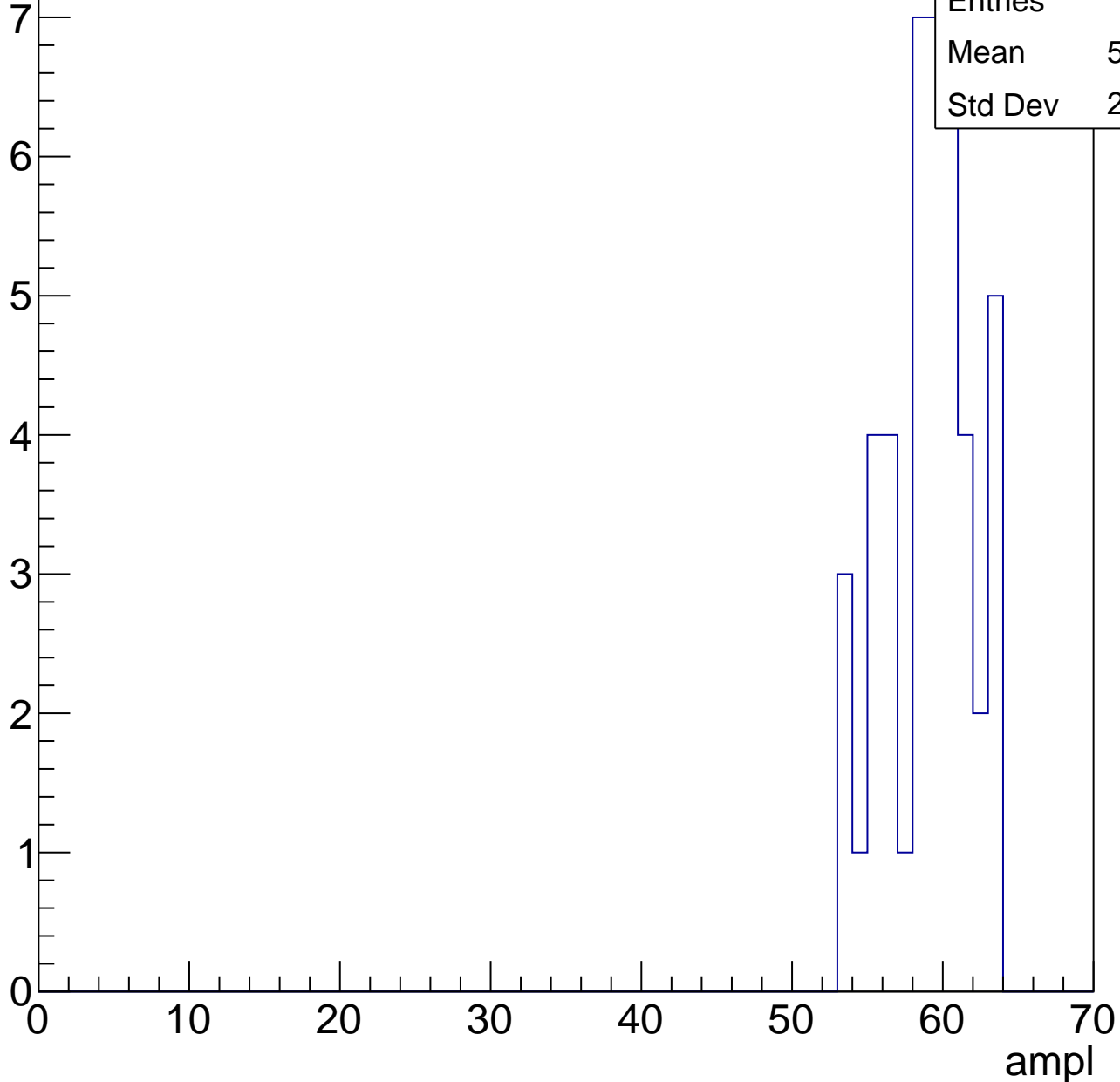
Entries	79
Mean	52.3
Std Dev	3.643



B1L103S, U10-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



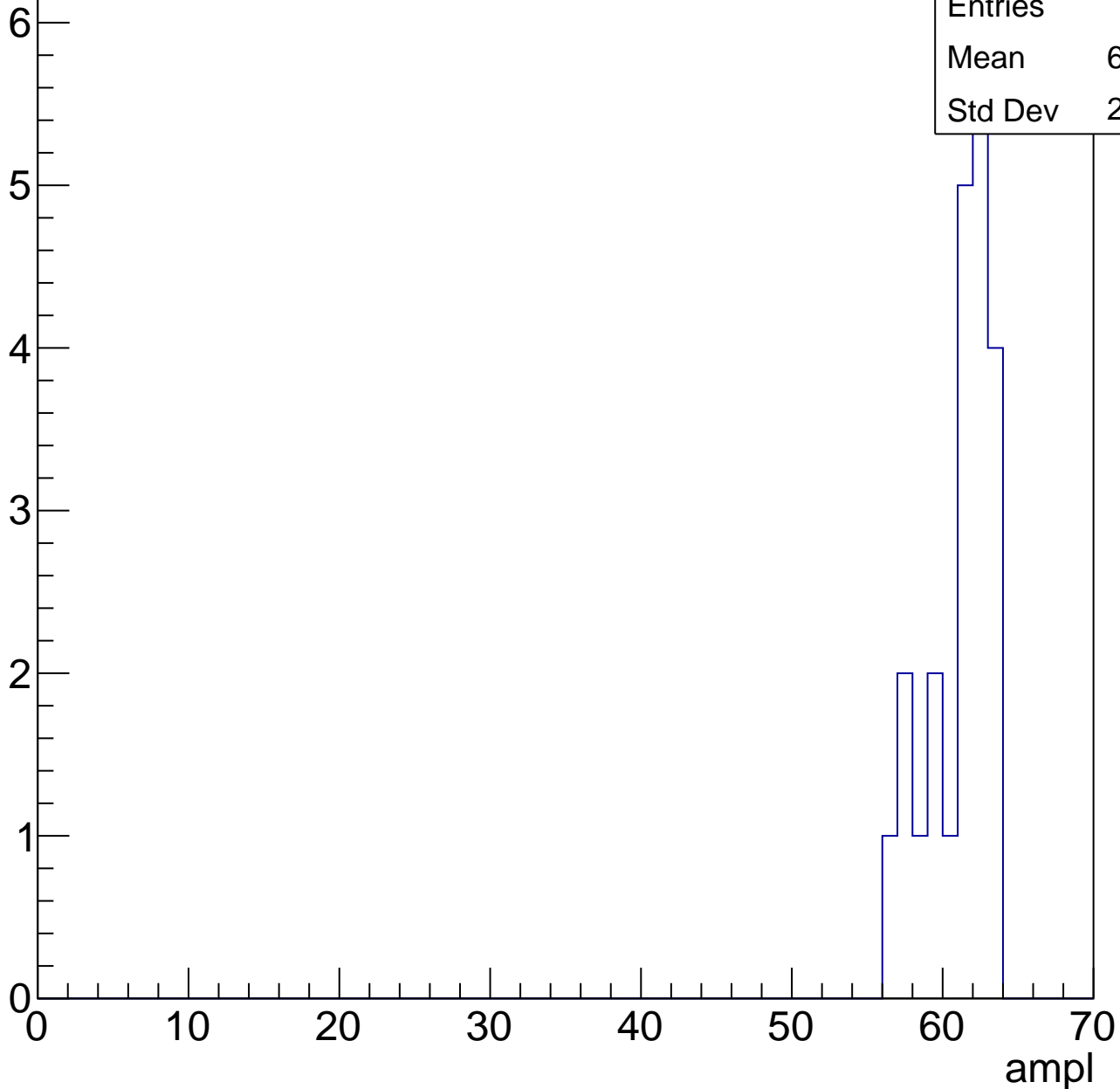
Entries	45
Mean	58.58
Std Dev	2.817

B1L103S, U10-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	60.68
Std Dev	2.076



B1L103S, U10-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

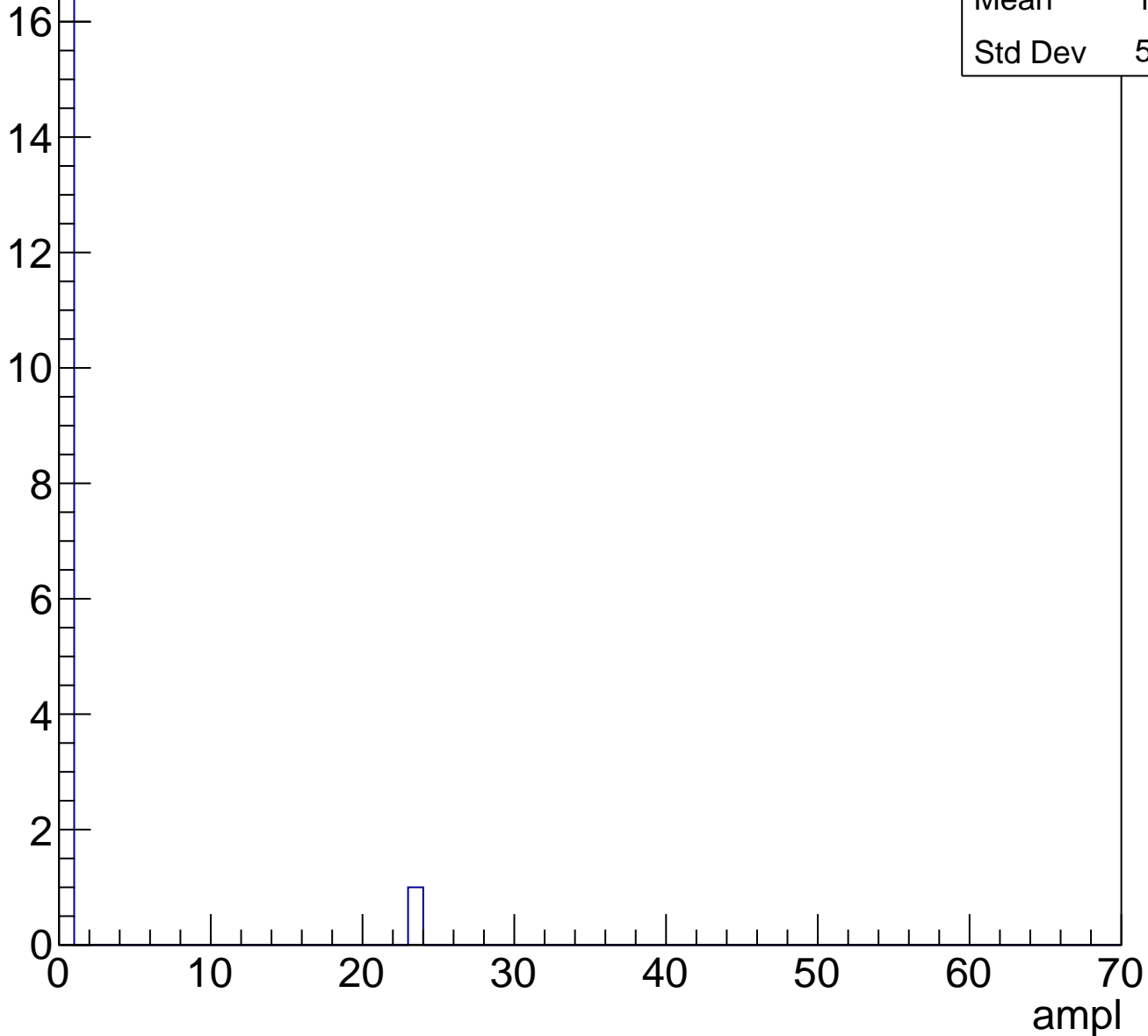


B1L103S, U10-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.278
Std Dev	5.268

Entry



B1L103S, U10-ch98, adc0

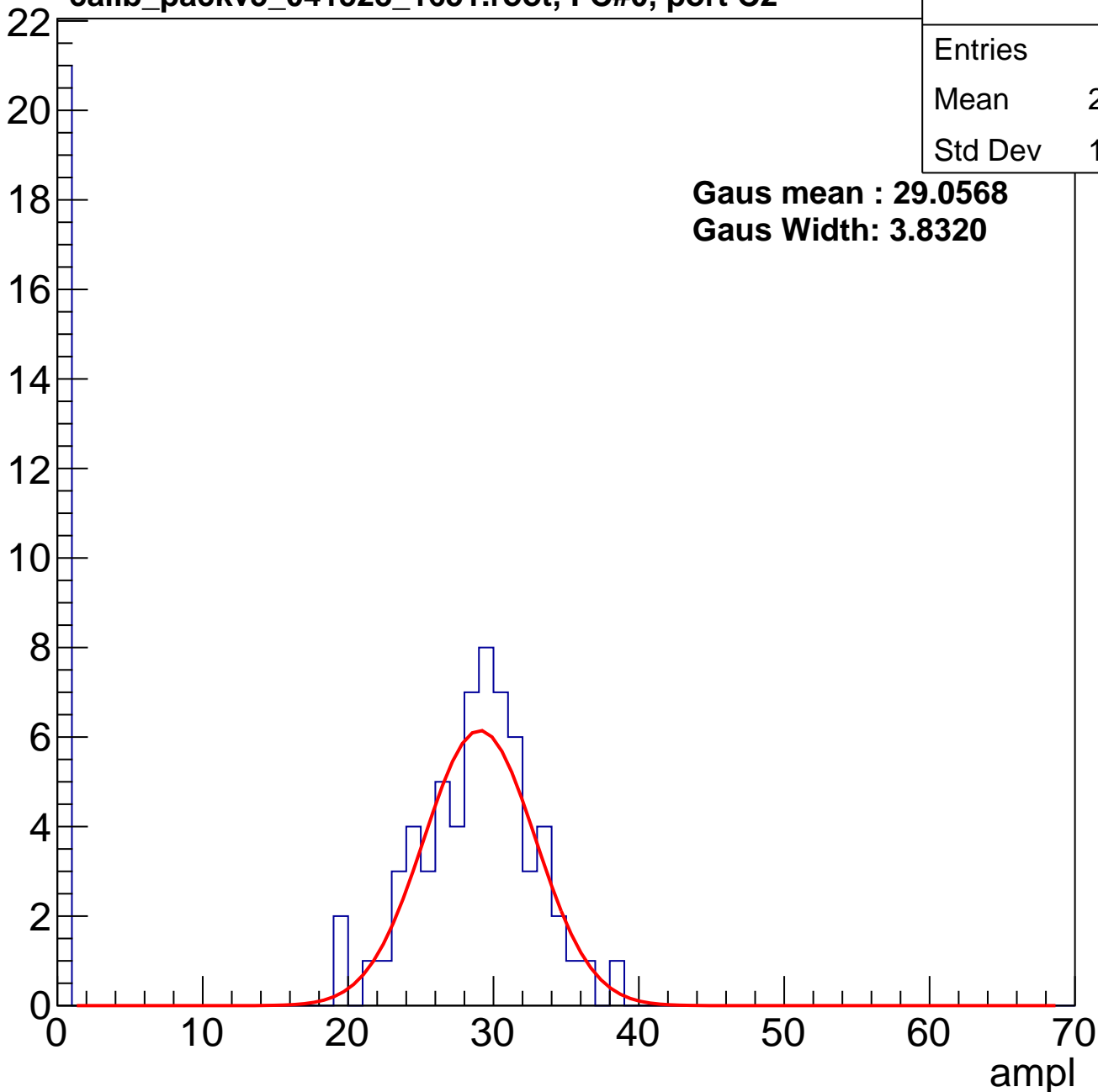
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	21.29
Std Dev	12.74

Gaus mean : 29.0568

Gaus Width: 3.8320

Entry



B1L103S, U10-ch98, adc1

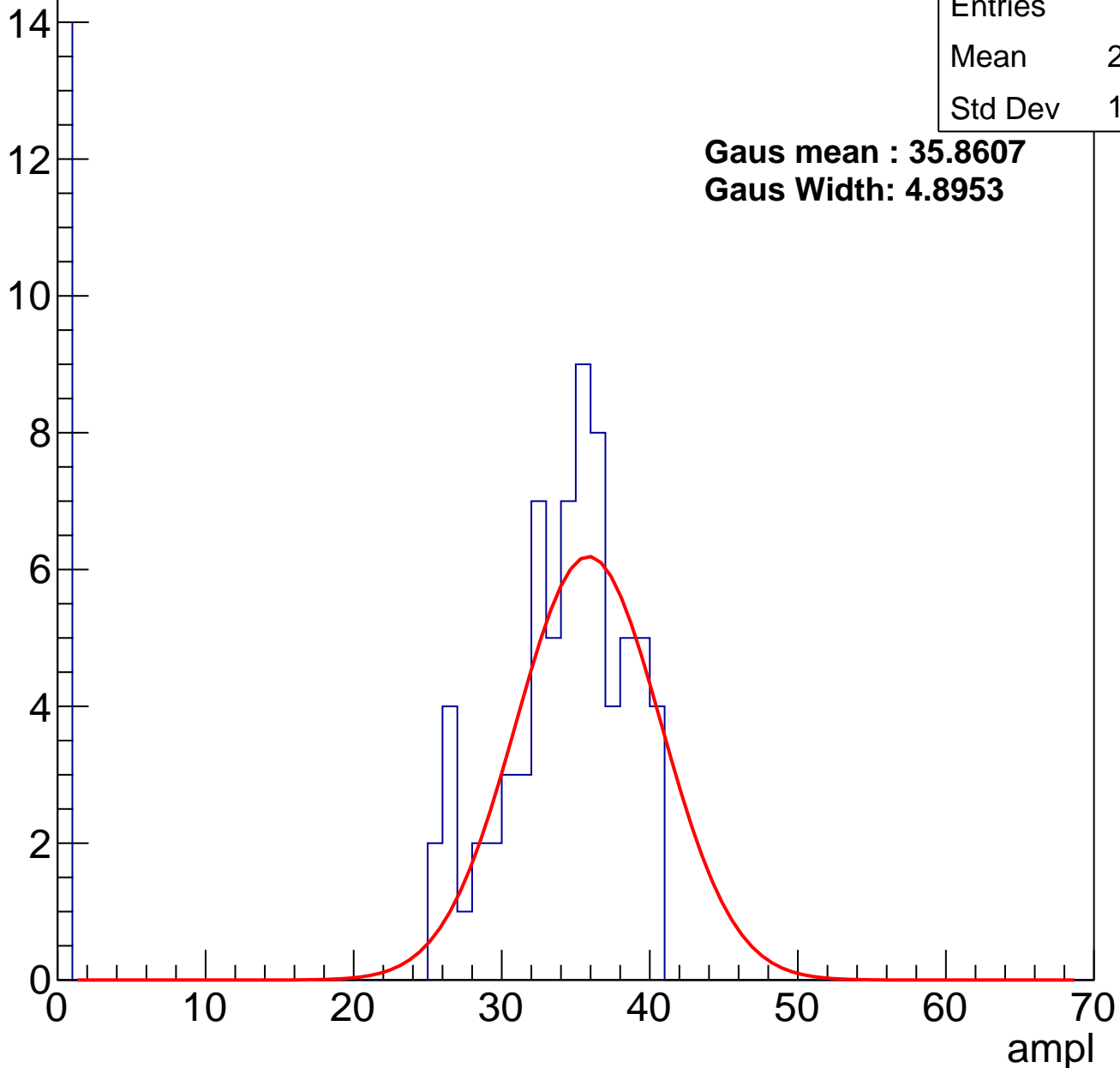
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	28.25
Std Dev	13.06

Gaus mean : 35.8607

Gaus Width: 4.8953

Entry



B1L103S, U10-ch98, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	40.32
Std Dev	7.738

Gaus mean : 42.2843

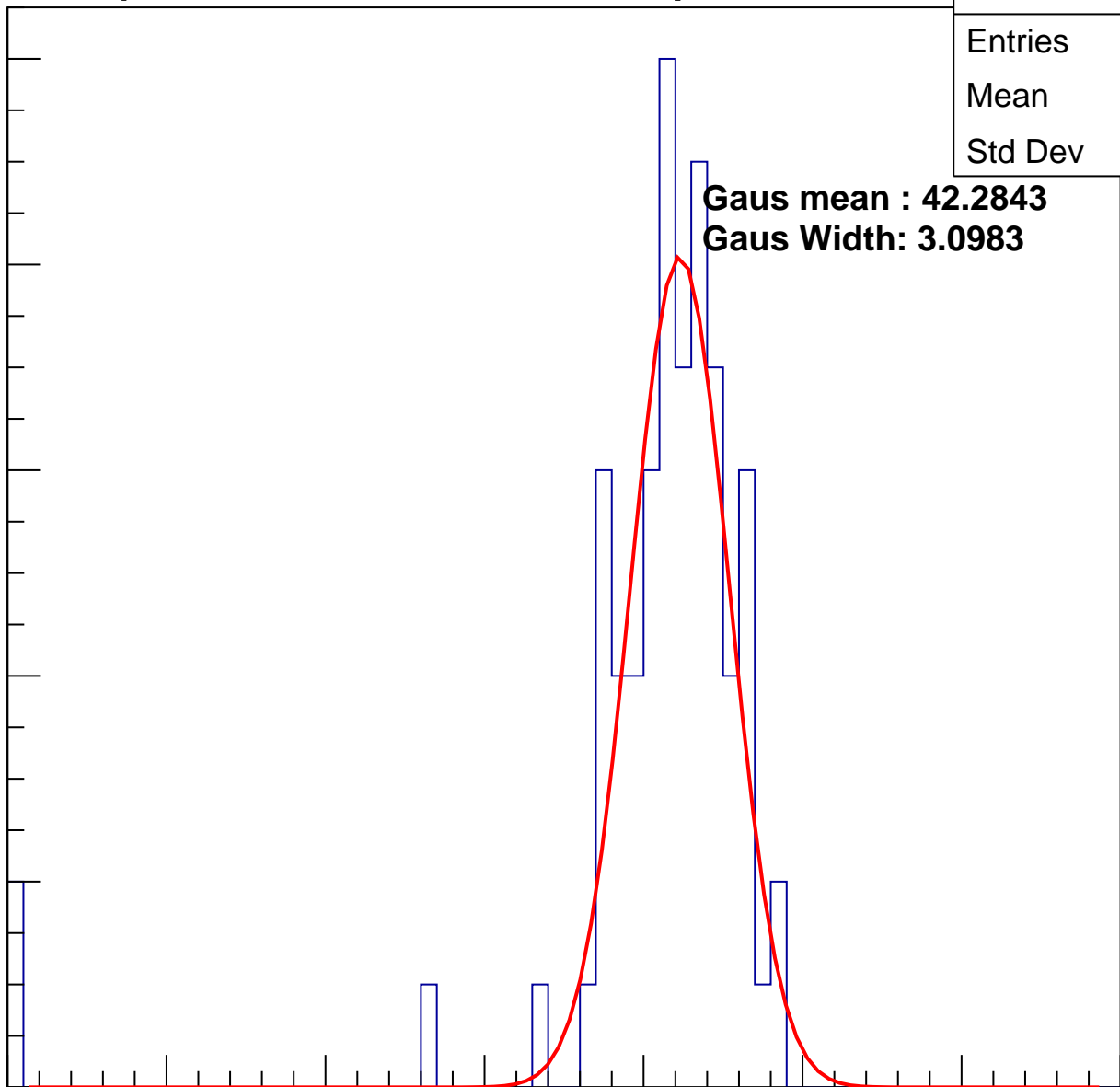
Gaus Width: 3.0983

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

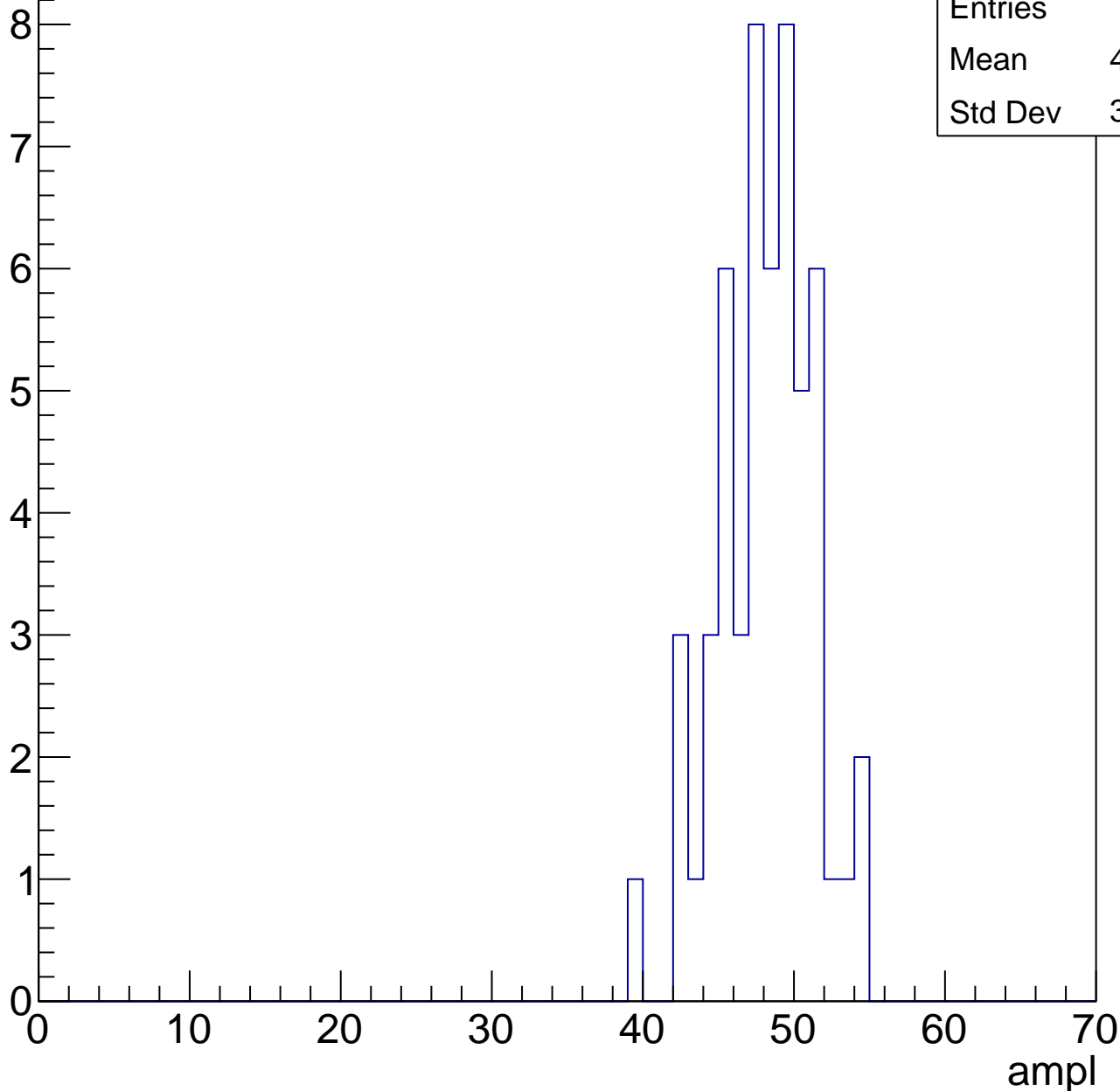


B1L103S, U10-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	47.65
Std Dev	3.116

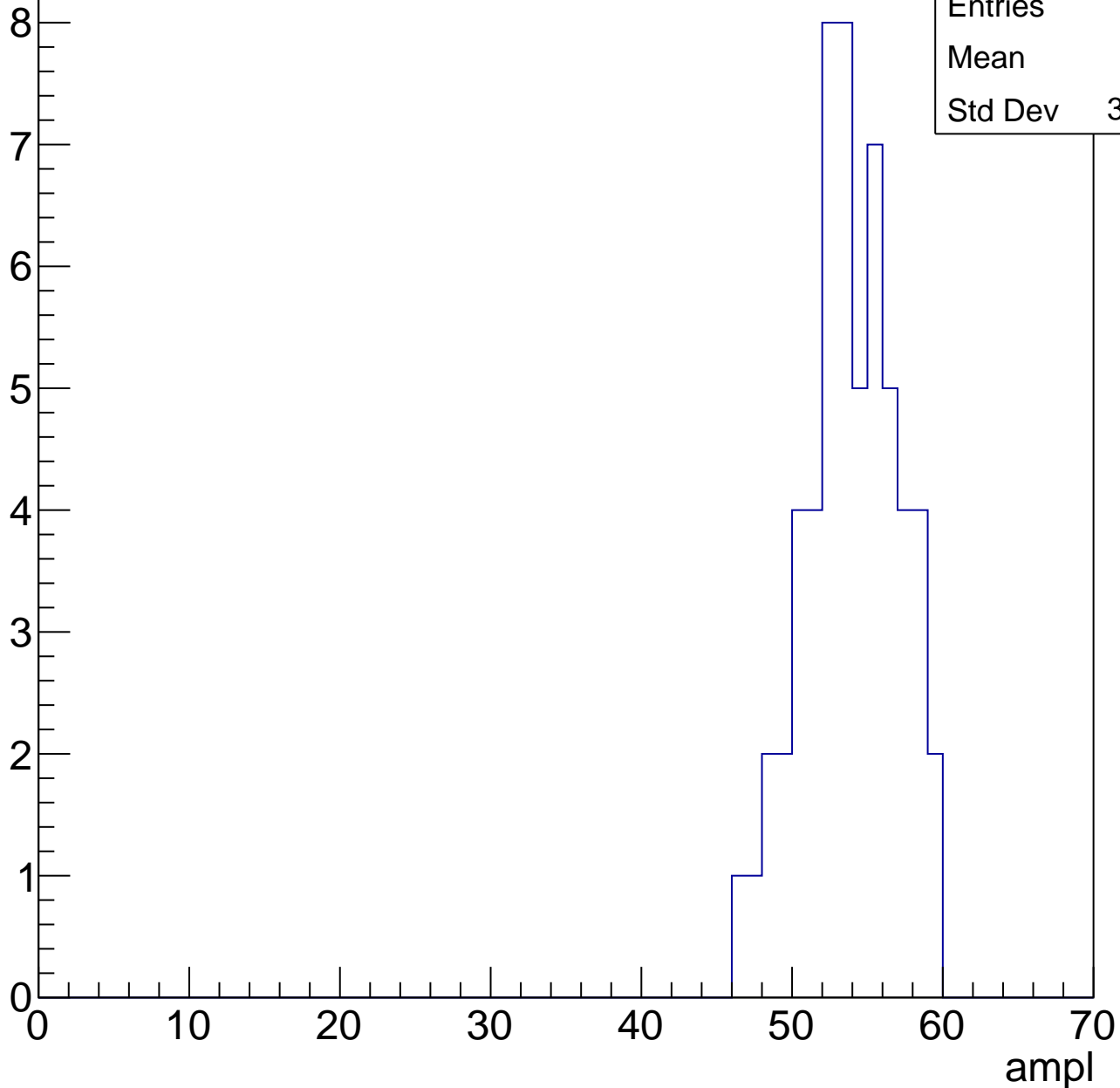


B1L103S, U10-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	53.4
Std Dev	3.054



B1L103S, U10-ch98, adc5

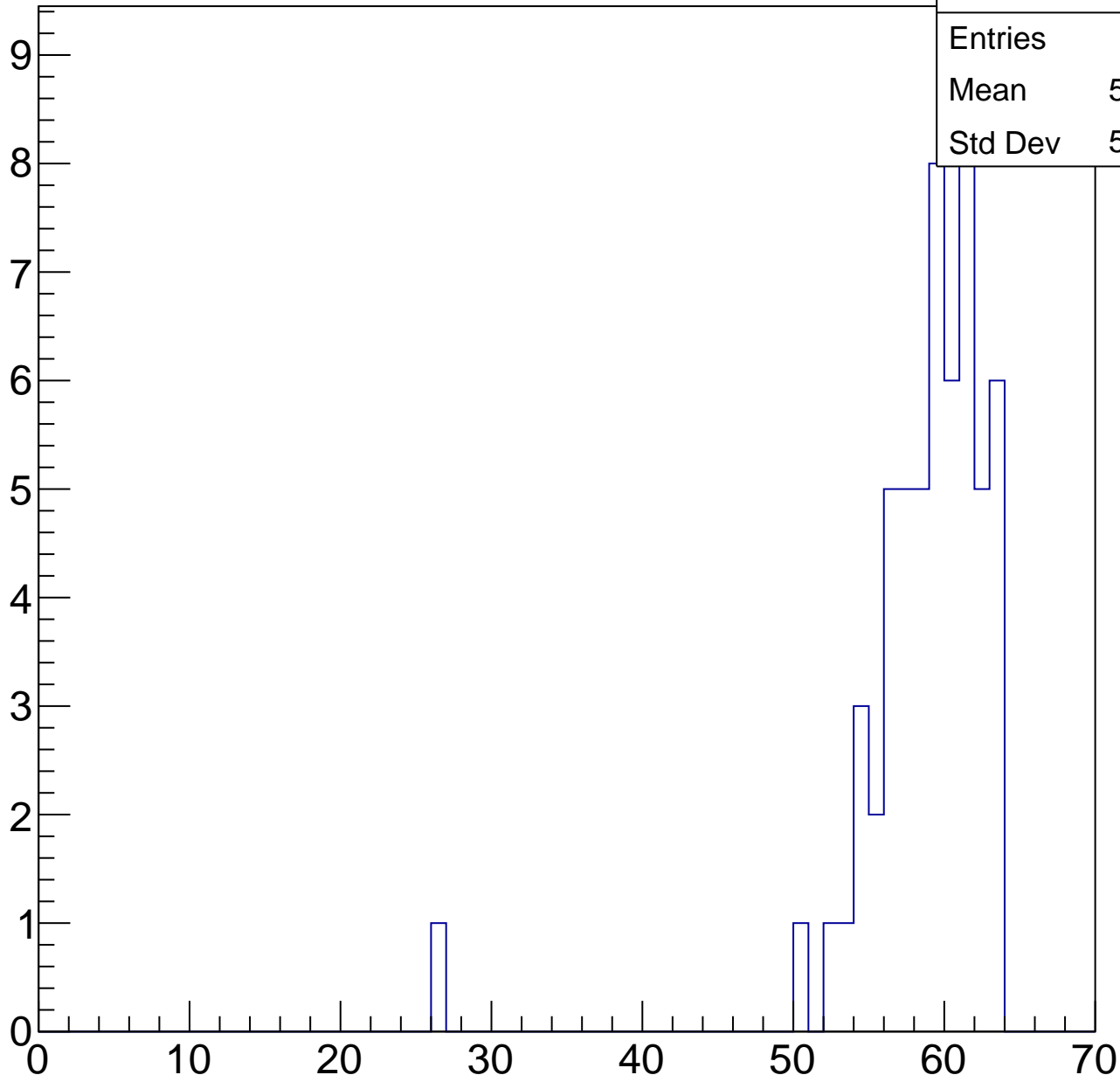
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.22
Std Dev	5.219

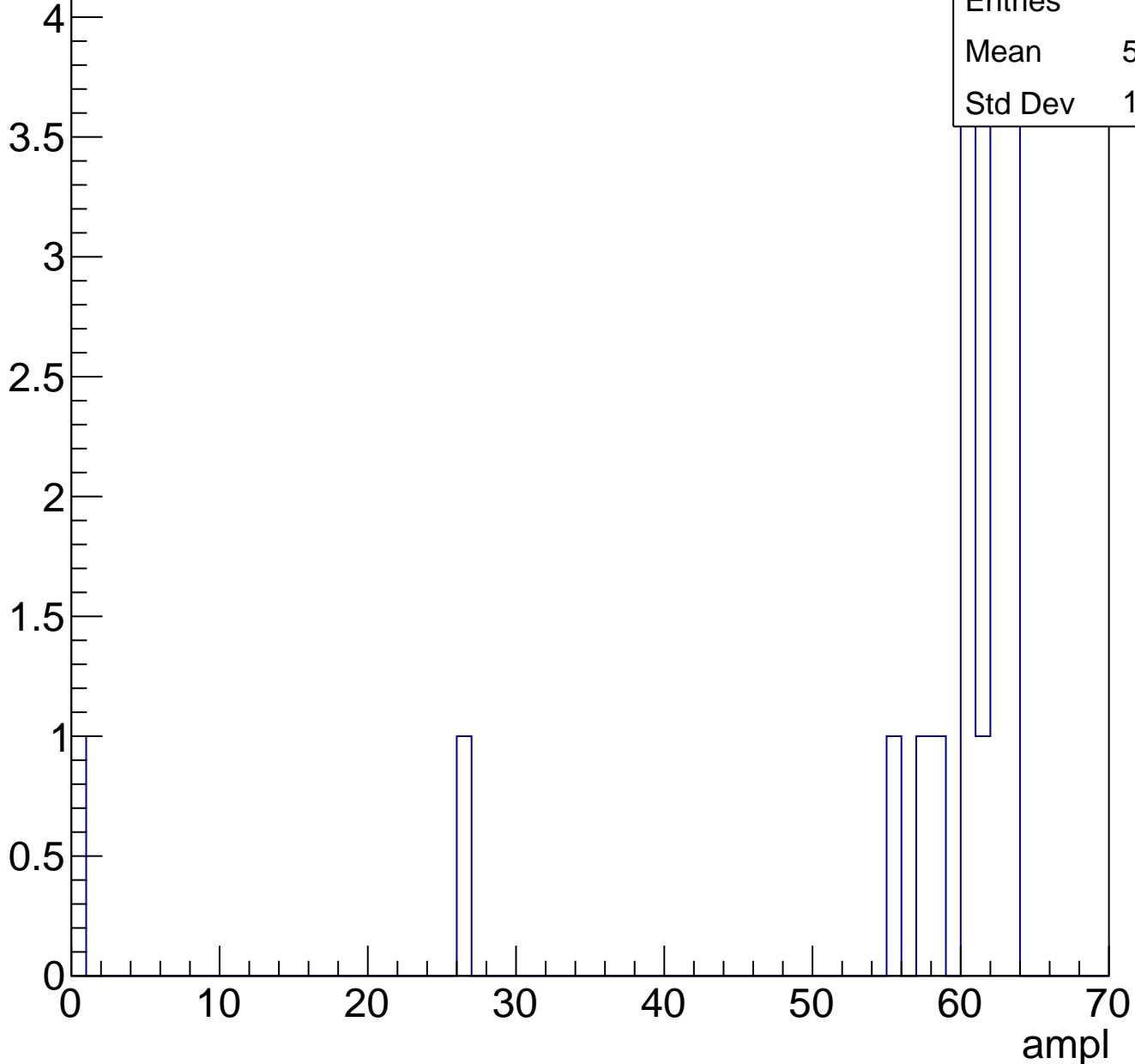
ampl



B1L103S, U10-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U10-ch99, adc0

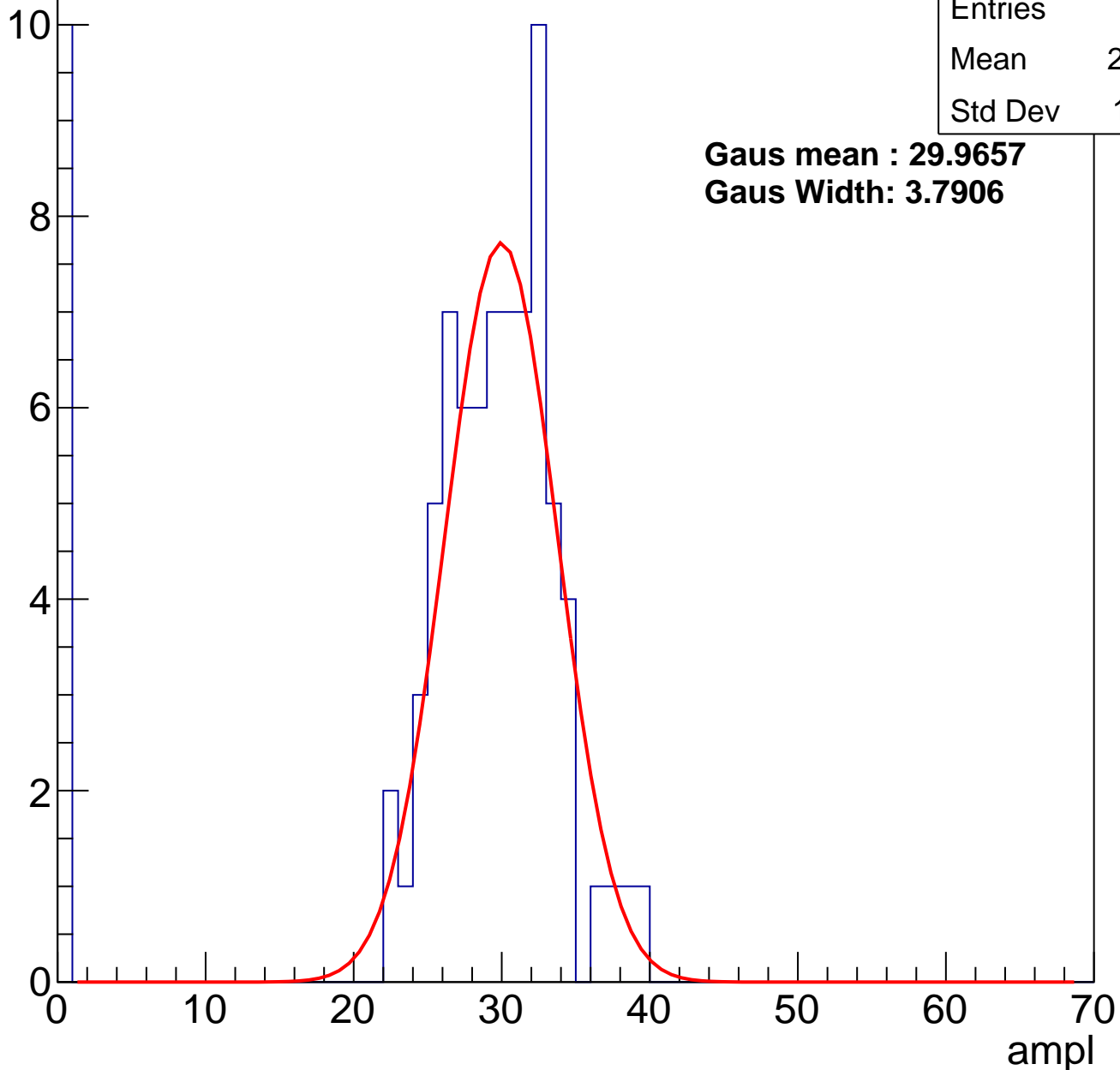
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	25.92
Std Dev	10.11

Gaus mean : 29.9657

Gaus Width: 3.7906

Entry



B1L103S, U10-ch99, adc1

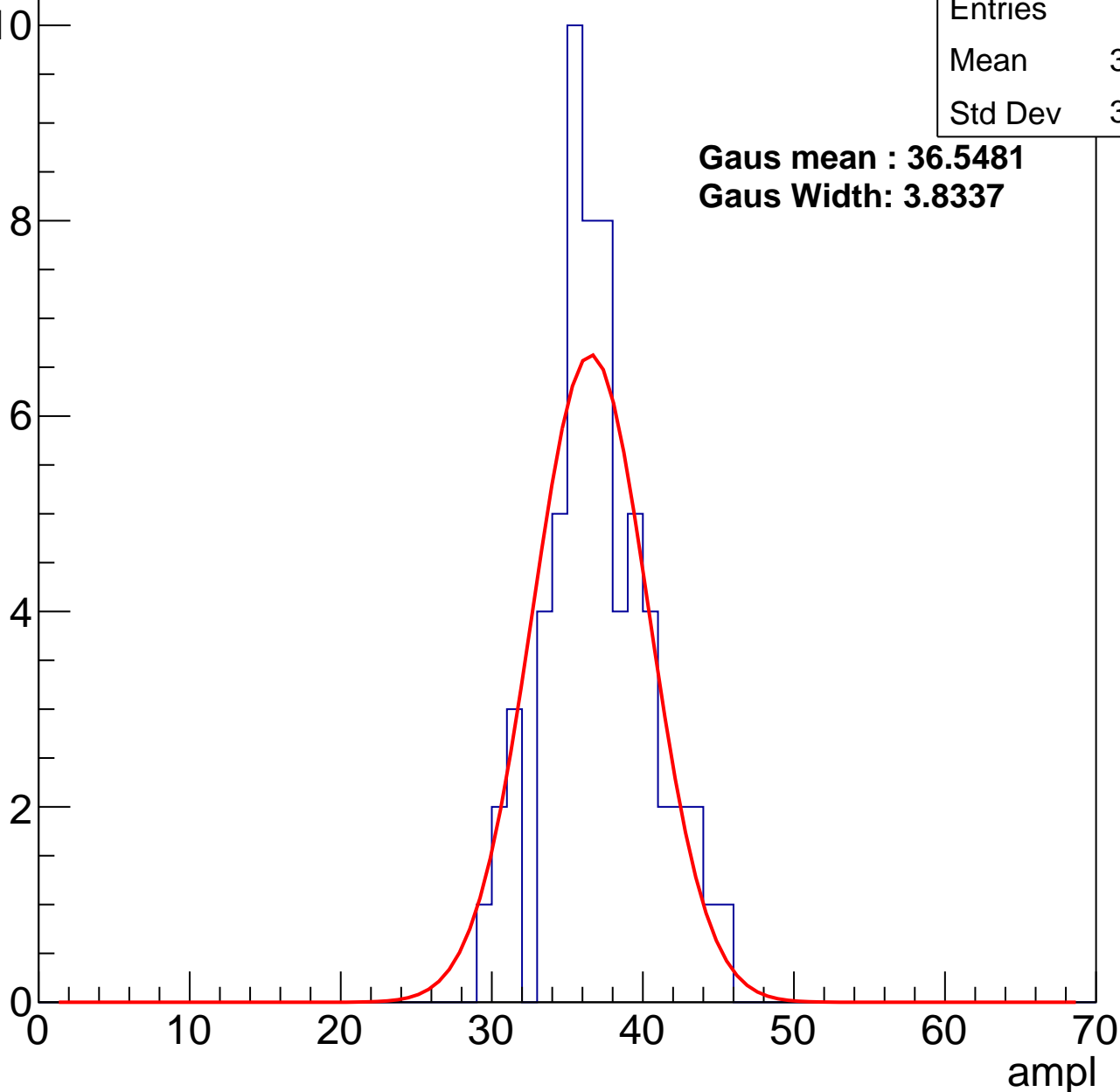
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.55
Std Dev	3.458

Gaus mean : 36.5481

Gaus Width: 3.8337



B1L103S, U10-ch99, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	37.16
Std Dev	14.93

Gaus mean : 43.6166

Gaus Width: 3.8456

10

8

6

4

2

0

0

10

20

30

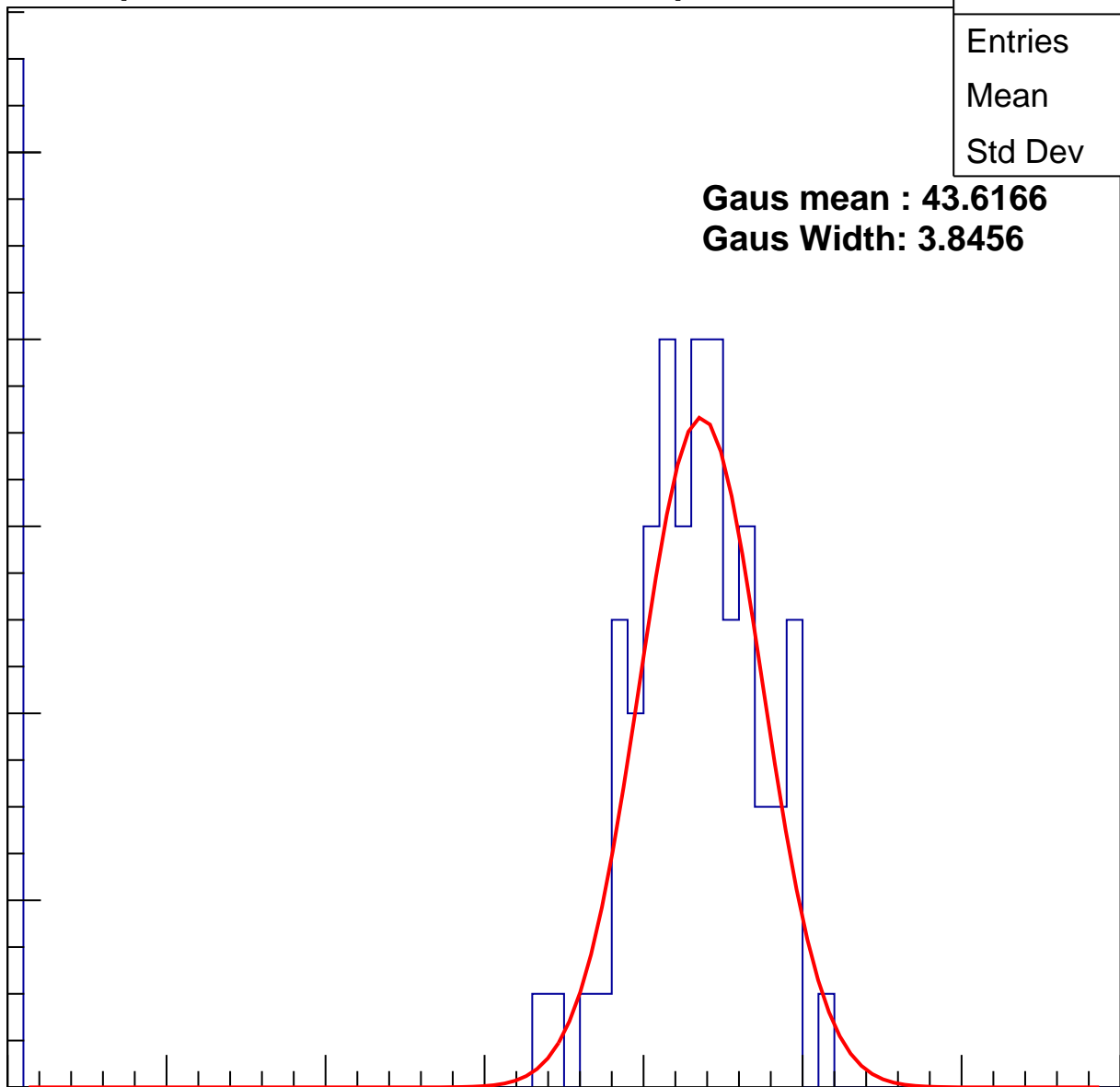
40

50

60

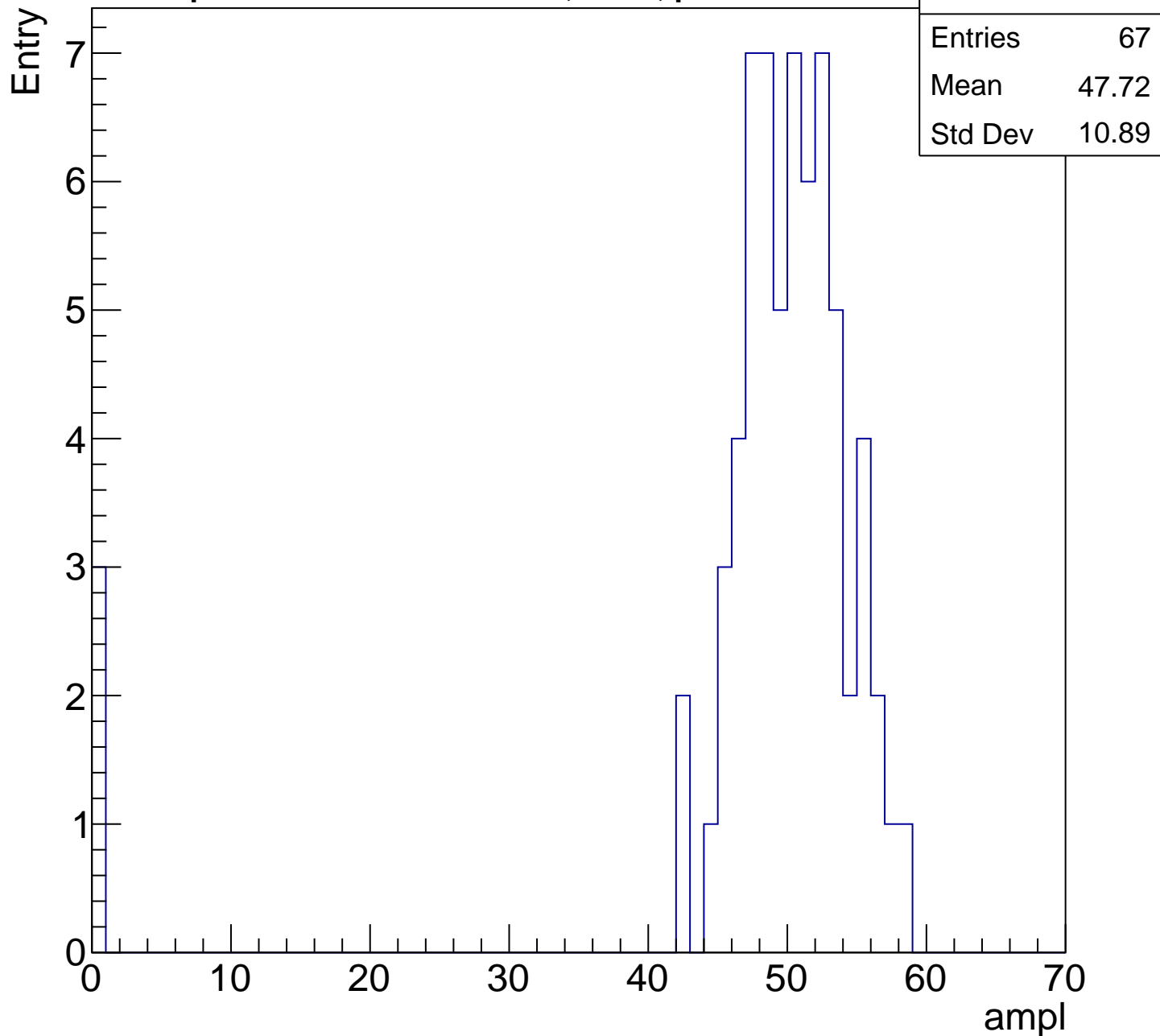
70

ampl



B1L103S, U10-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

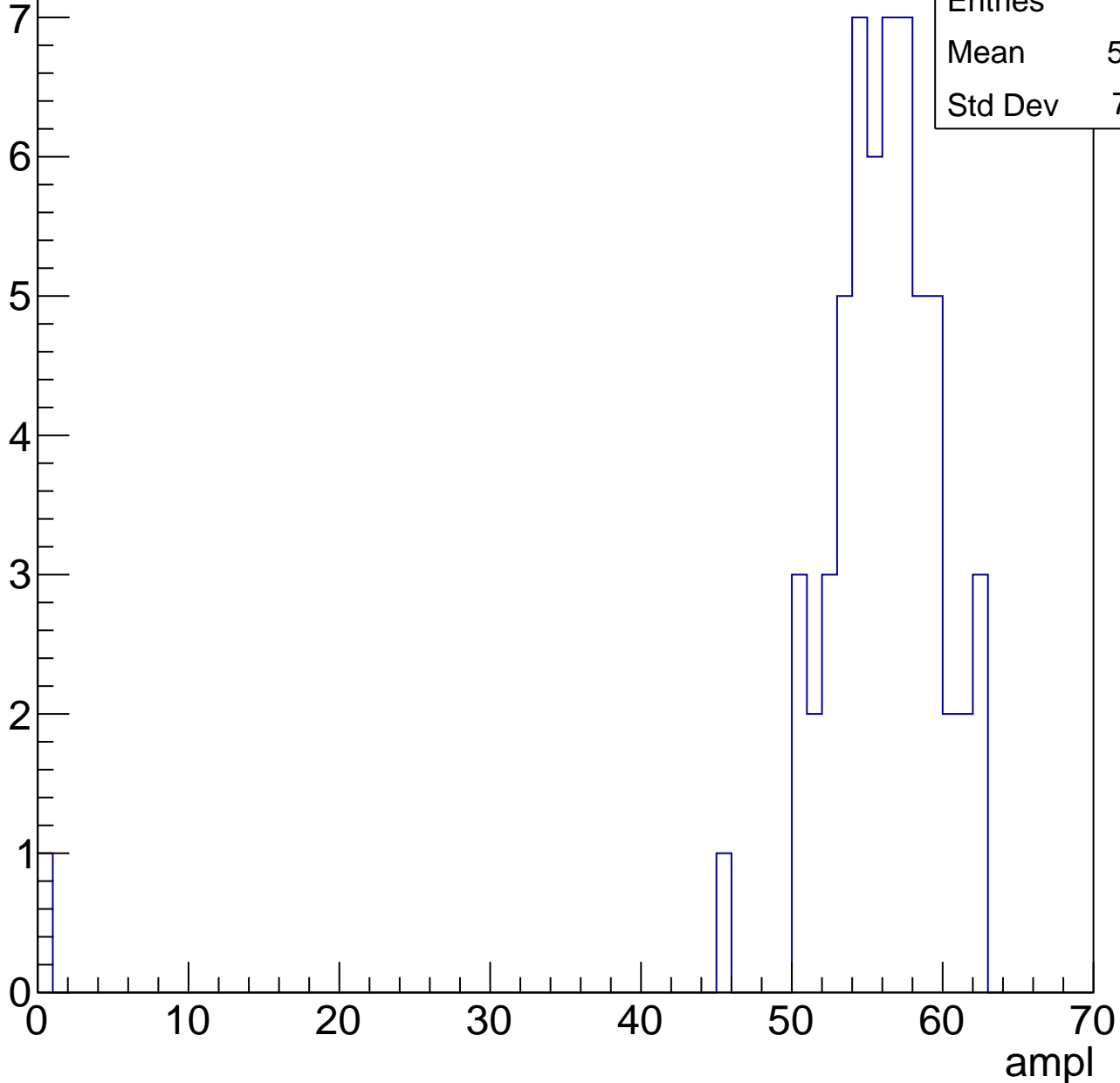


B1L103S, U10-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.75
Std Dev	7.931

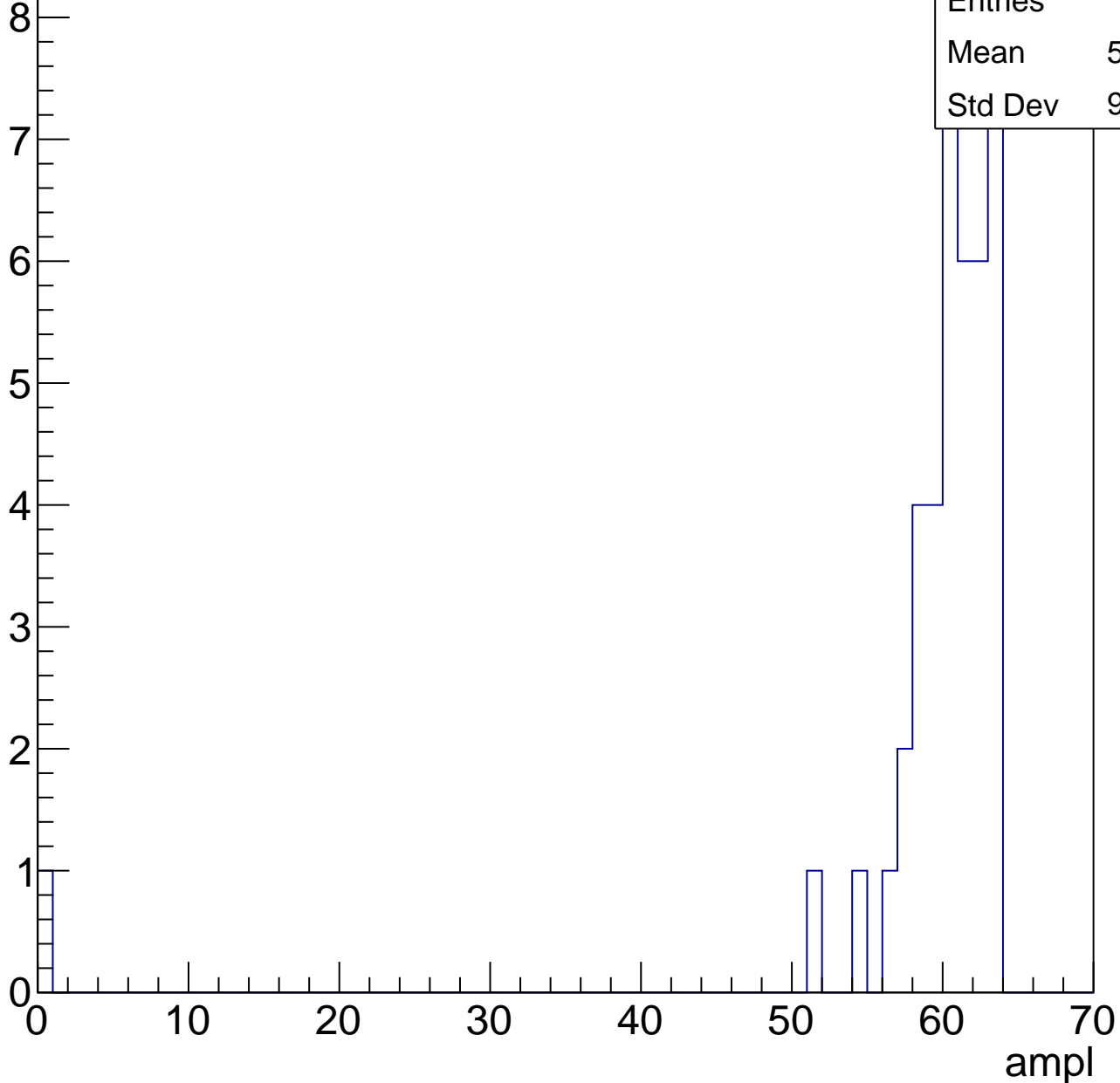


B1L103S, U10-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.69
Std Dev	9.513



B1L103S, U10-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

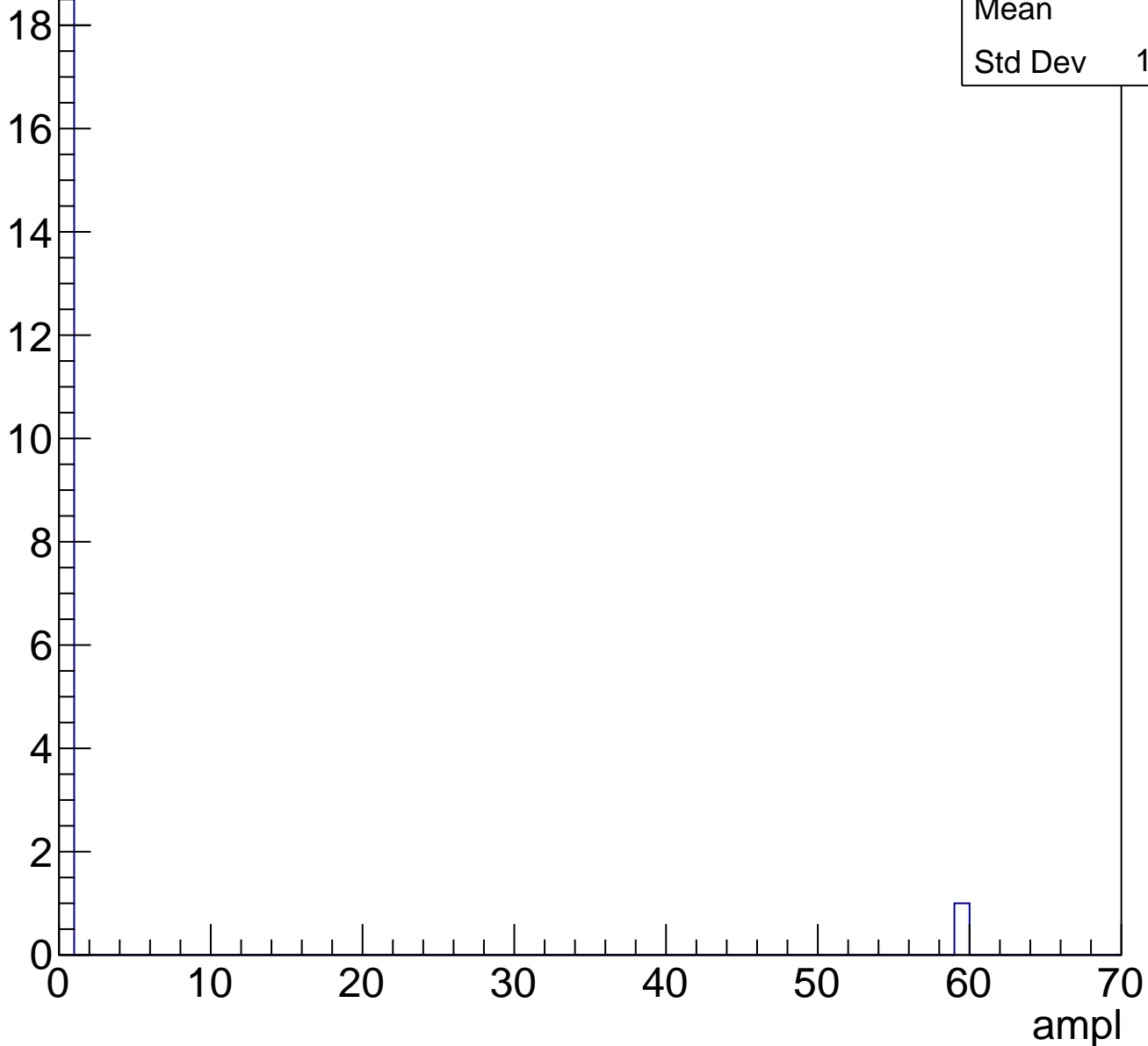
0 10 20 30 40 50 60 70

B1L103S, U10-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	2.95
Std Dev	12.86

Entry



B1L103S, U10-ch100, adc0

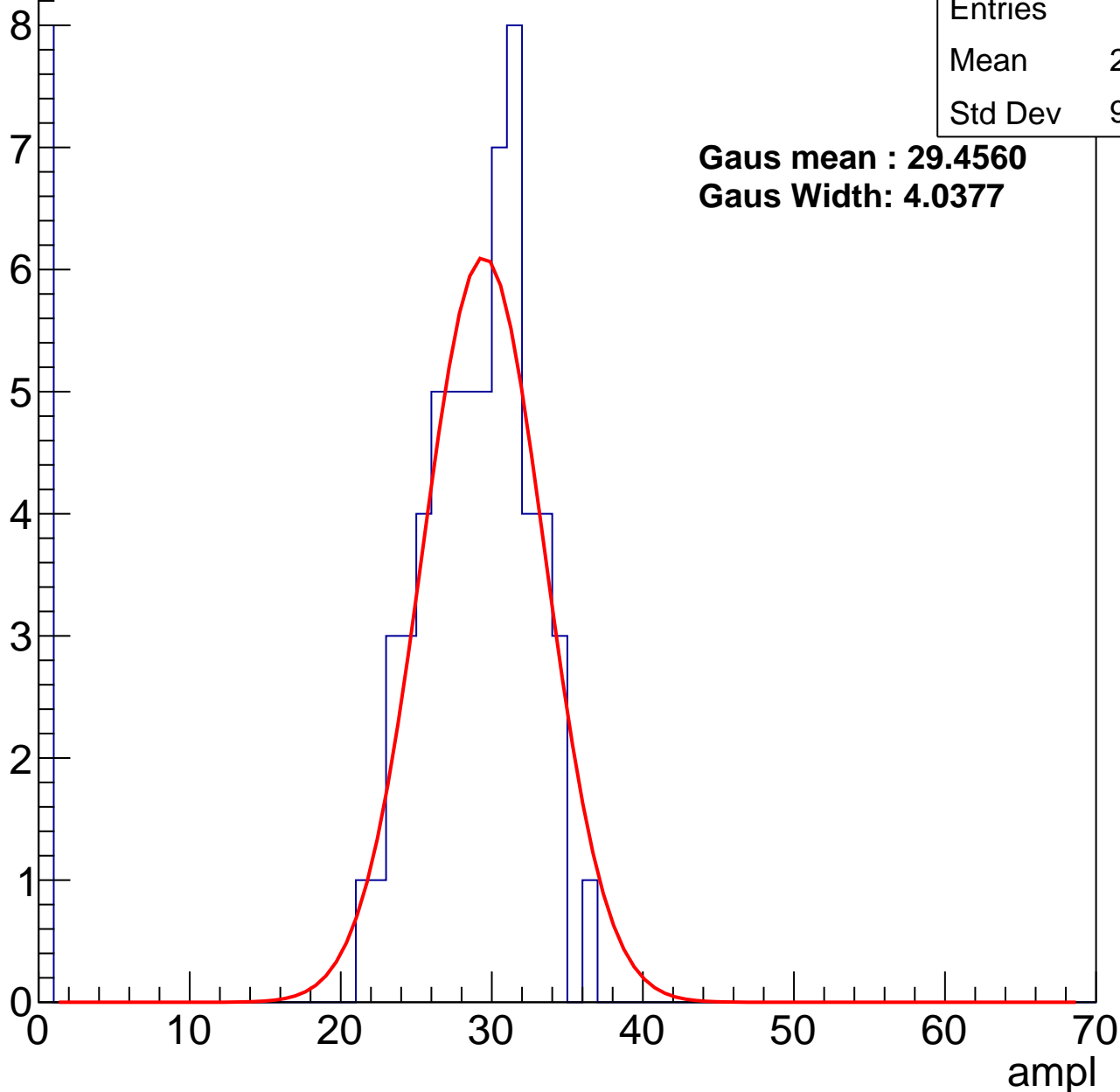
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	25.22
Std Dev	9.824

Gaus mean : 29.4560

Gaus Width: 4.0377



B1L103S, U10-ch100, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	29.48
Std Dev	13.31

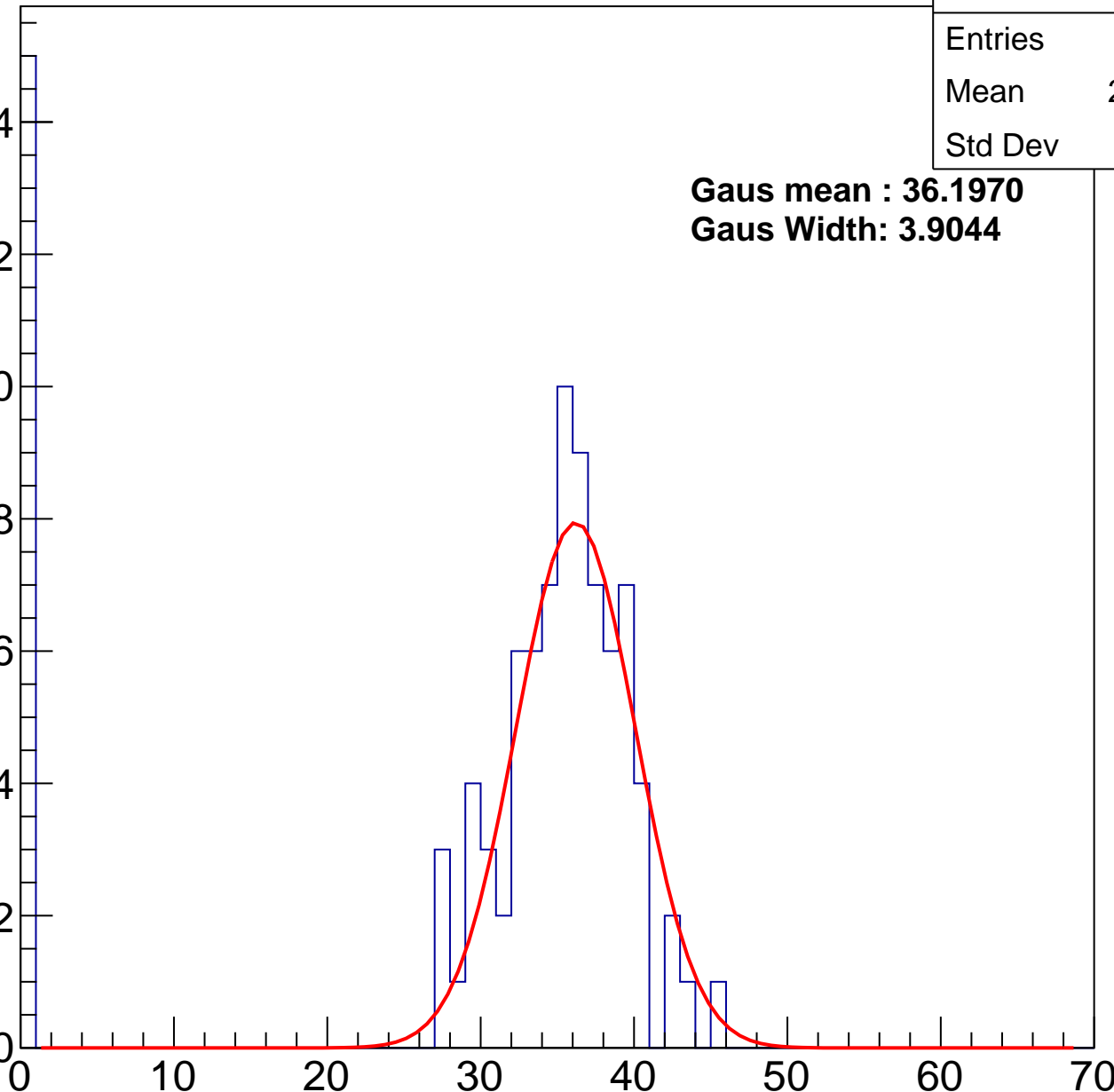
Gaus mean : 36.1970

Gaus Width: 3.9044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U10-ch100, adc2

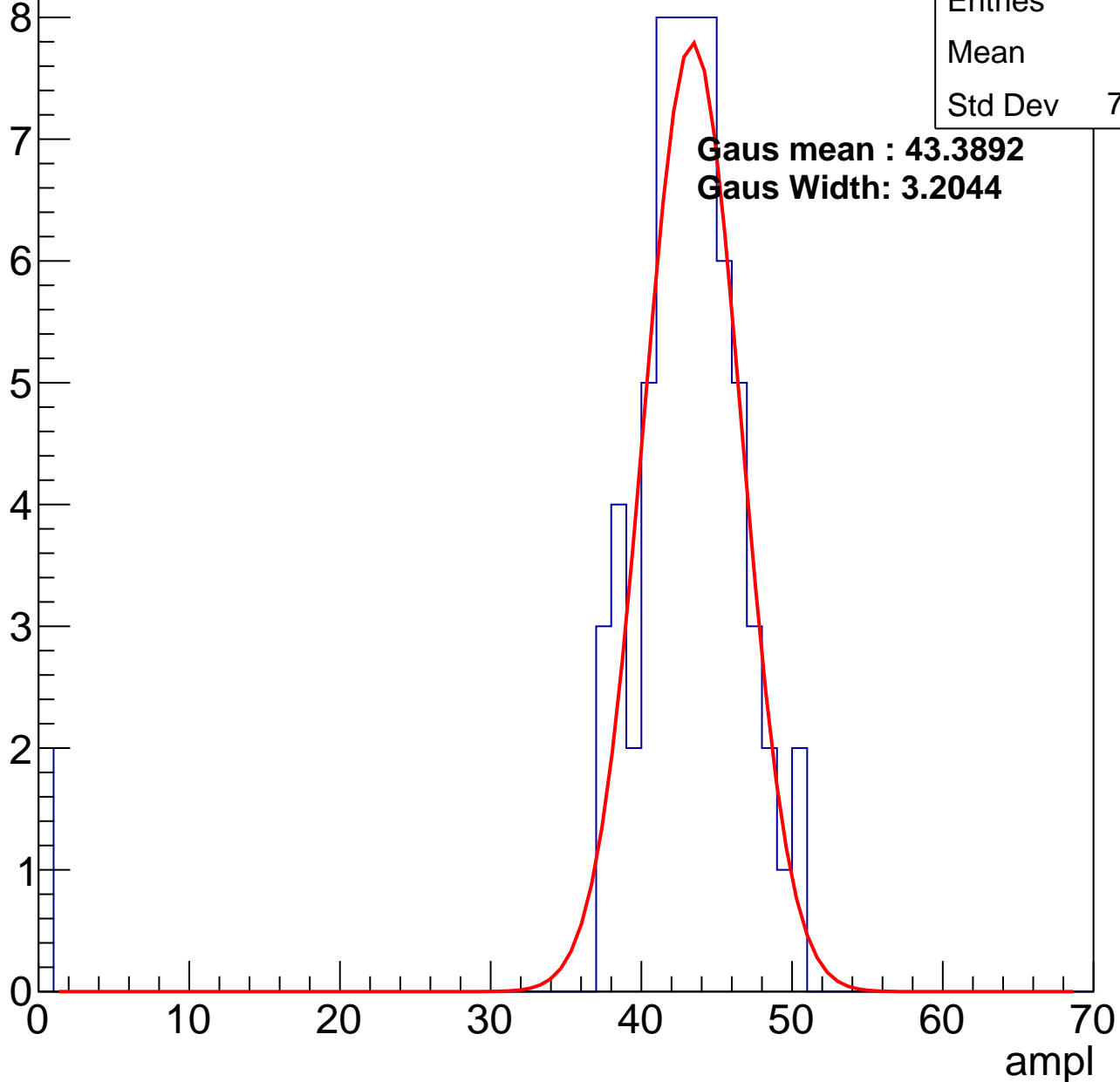
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	41.6
Std Dev	7.918

Gaus mean : 43.3892

Gaus Width: 3.2044

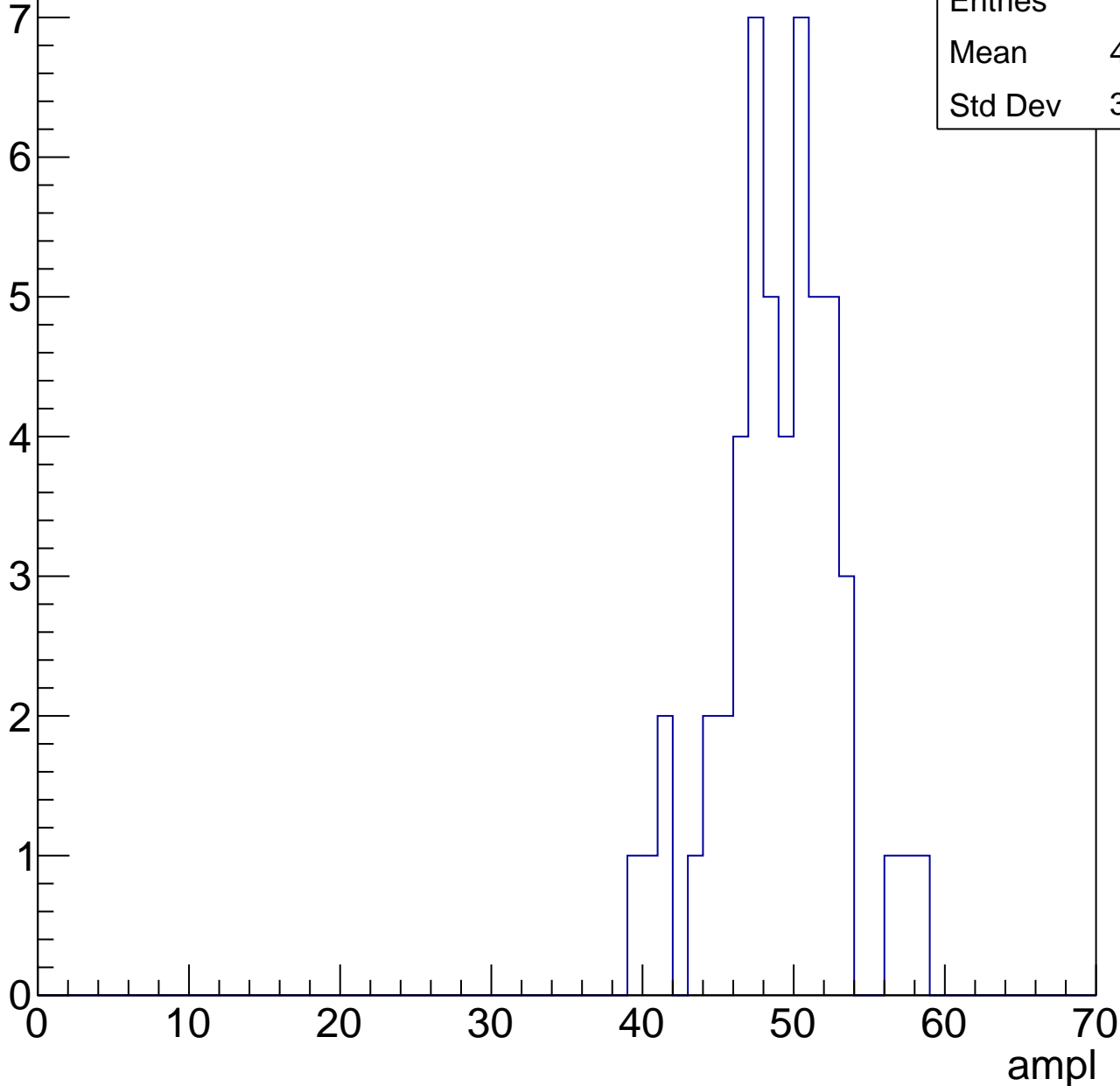


B1L103S, U10-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

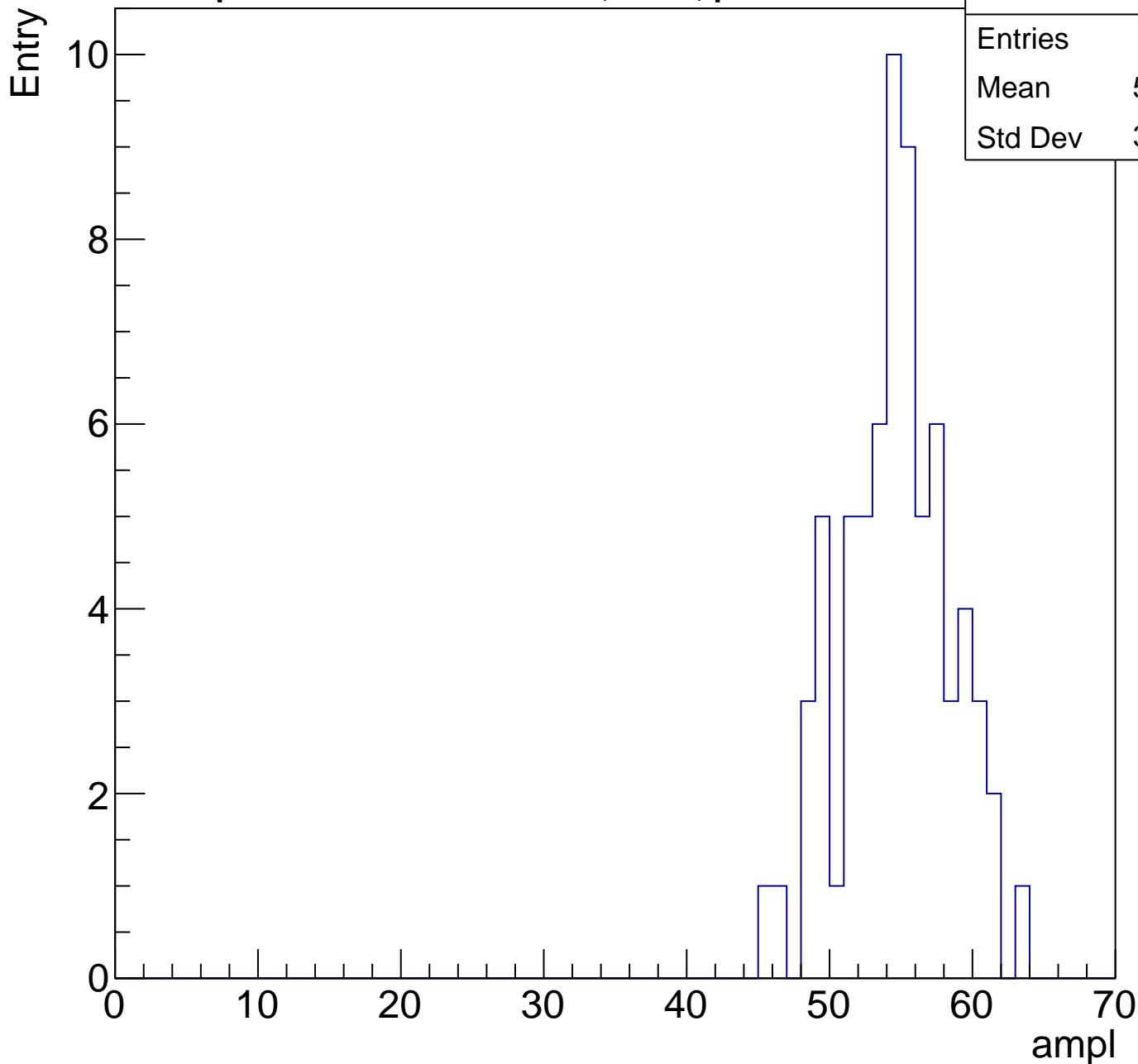
Entries	52
Mean	48.58
Std Dev	3.924



B1L103S, U10-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

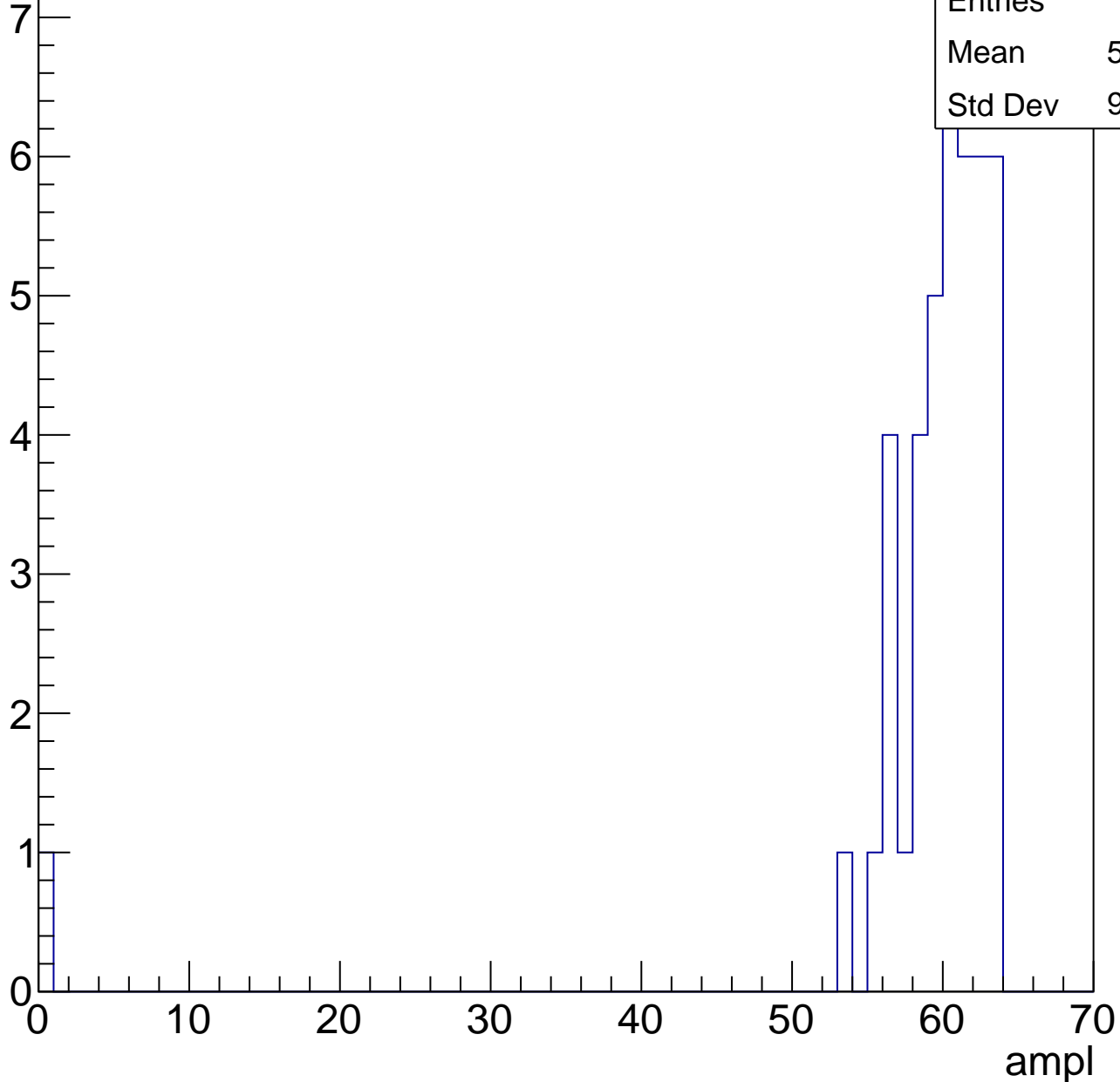
Entries	70
Mean	54.21
Std Dev	3.741



B1L103S, U10-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

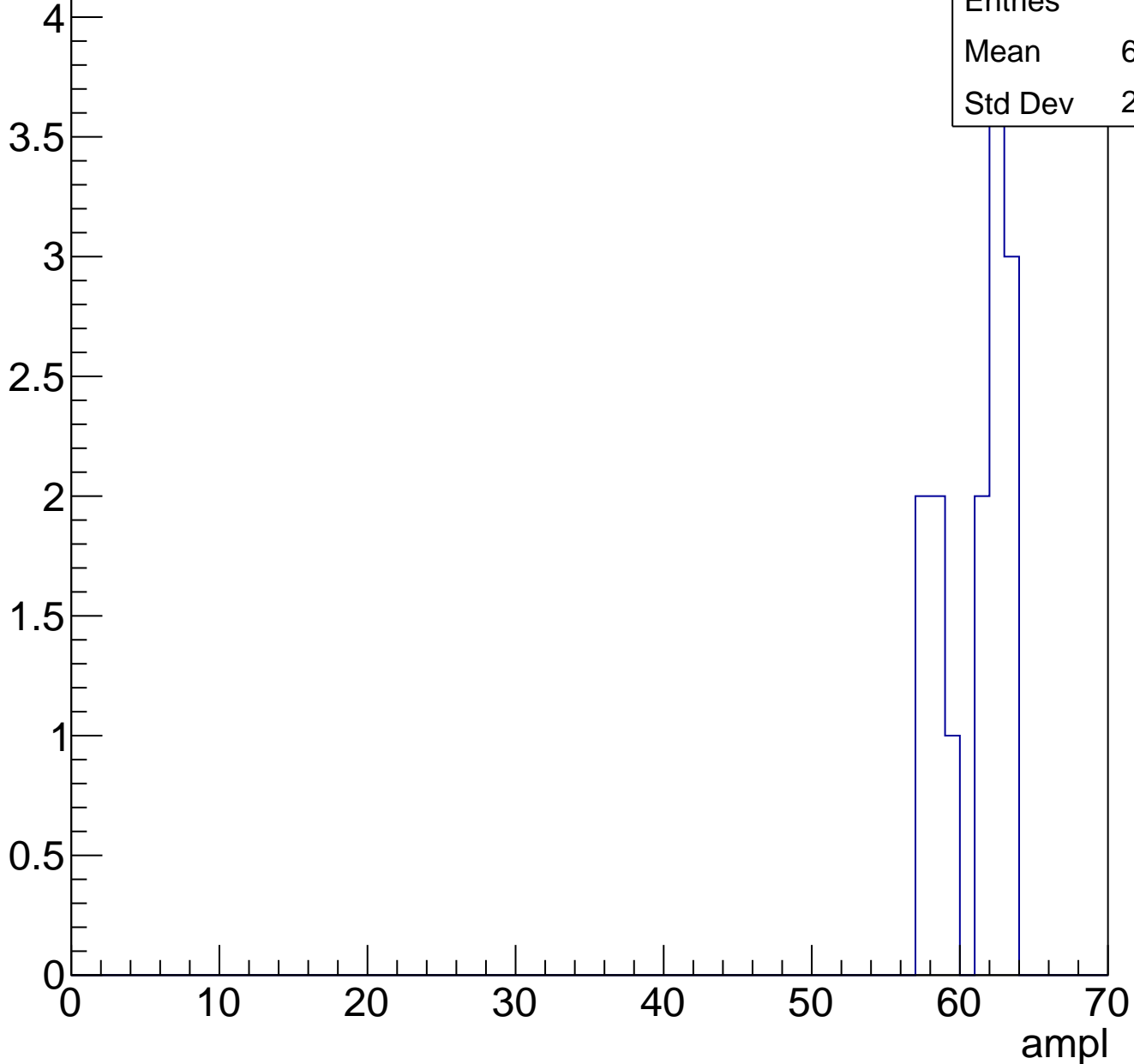
Entry



B1L103S, U10-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch101, adc0

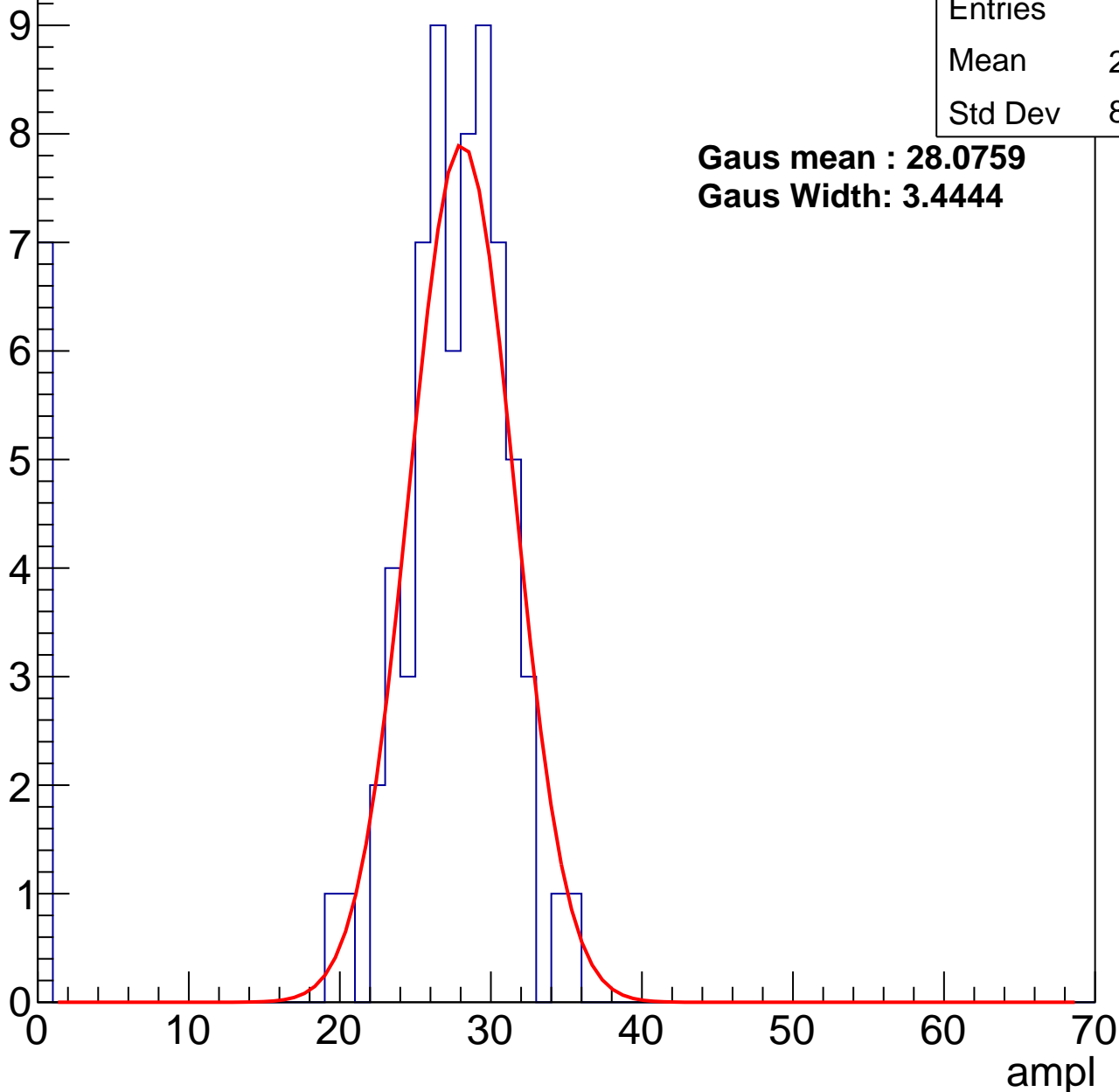
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	24.77
Std Dev	8.543

Gaus mean : 28.0759

Gaus Width: 3.4444



B1L103S, U10-ch101, adc1

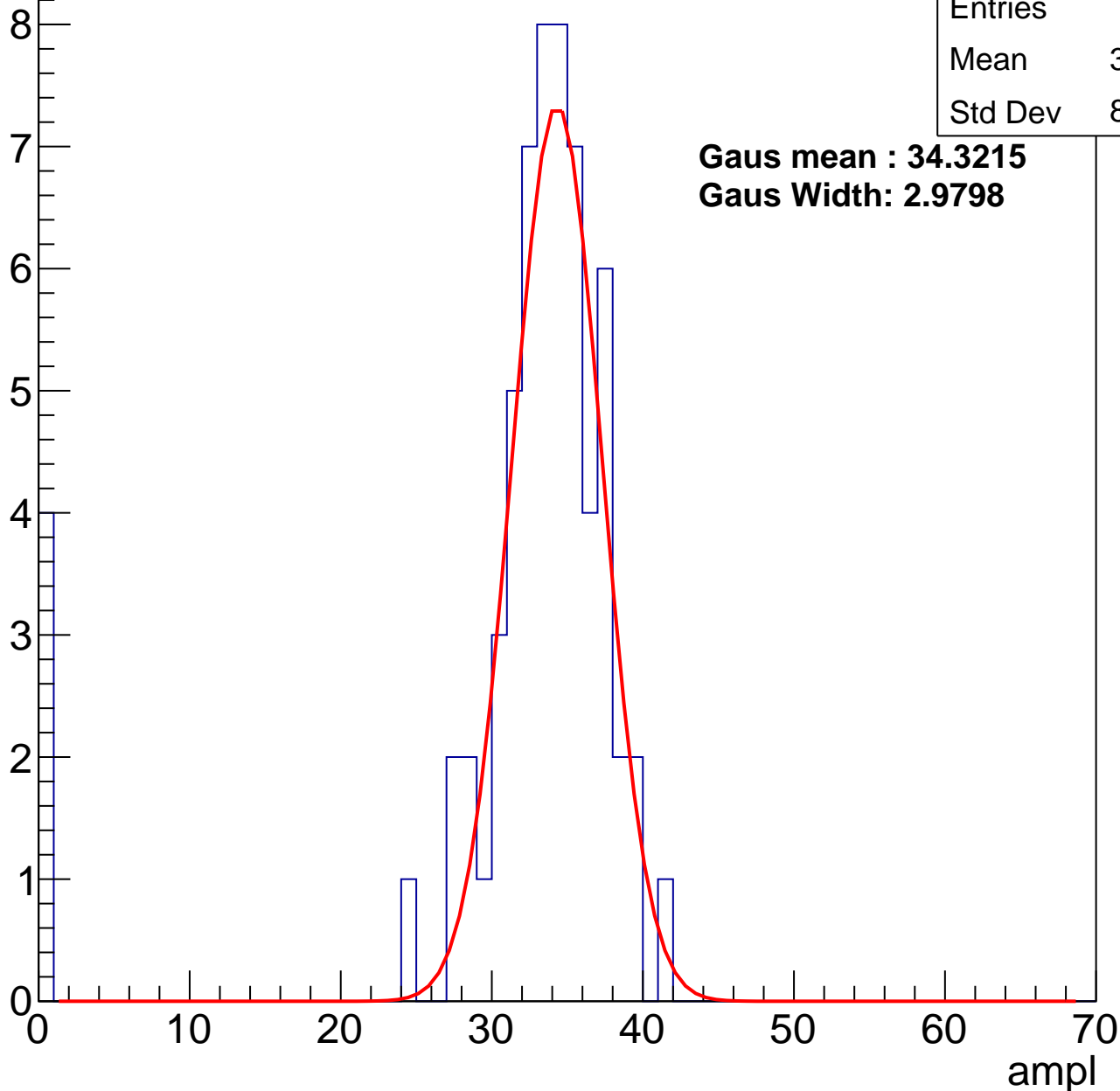
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	31.33
Std Dev	8.736

Gaus mean : 34.3215

Gaus Width: 2.9798



B1L103S, U10-ch101, adc2

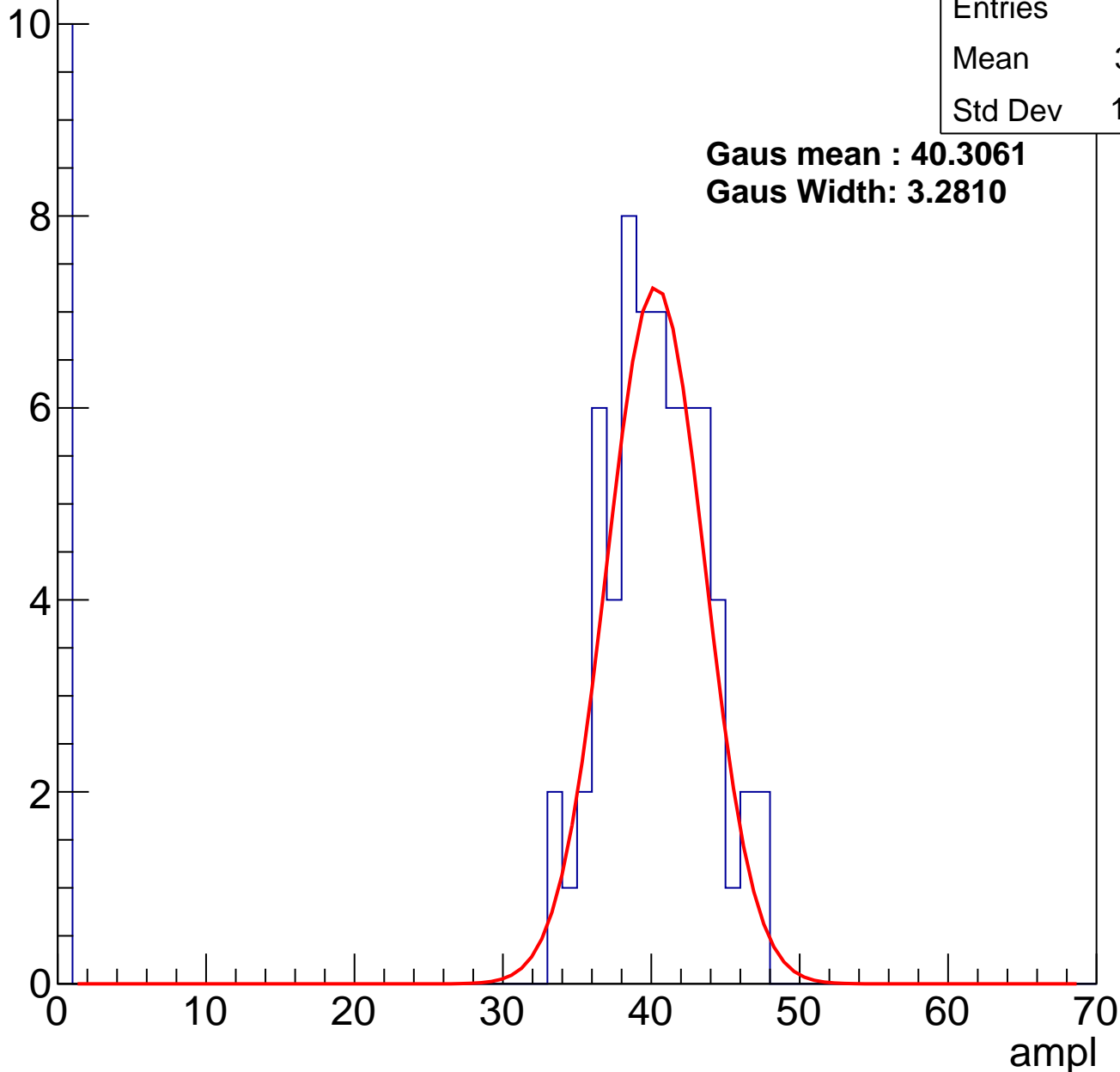
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	34.51
Std Dev	13.98

Gaus mean : 40.3061

Gaus Width: 3.2810

Entry

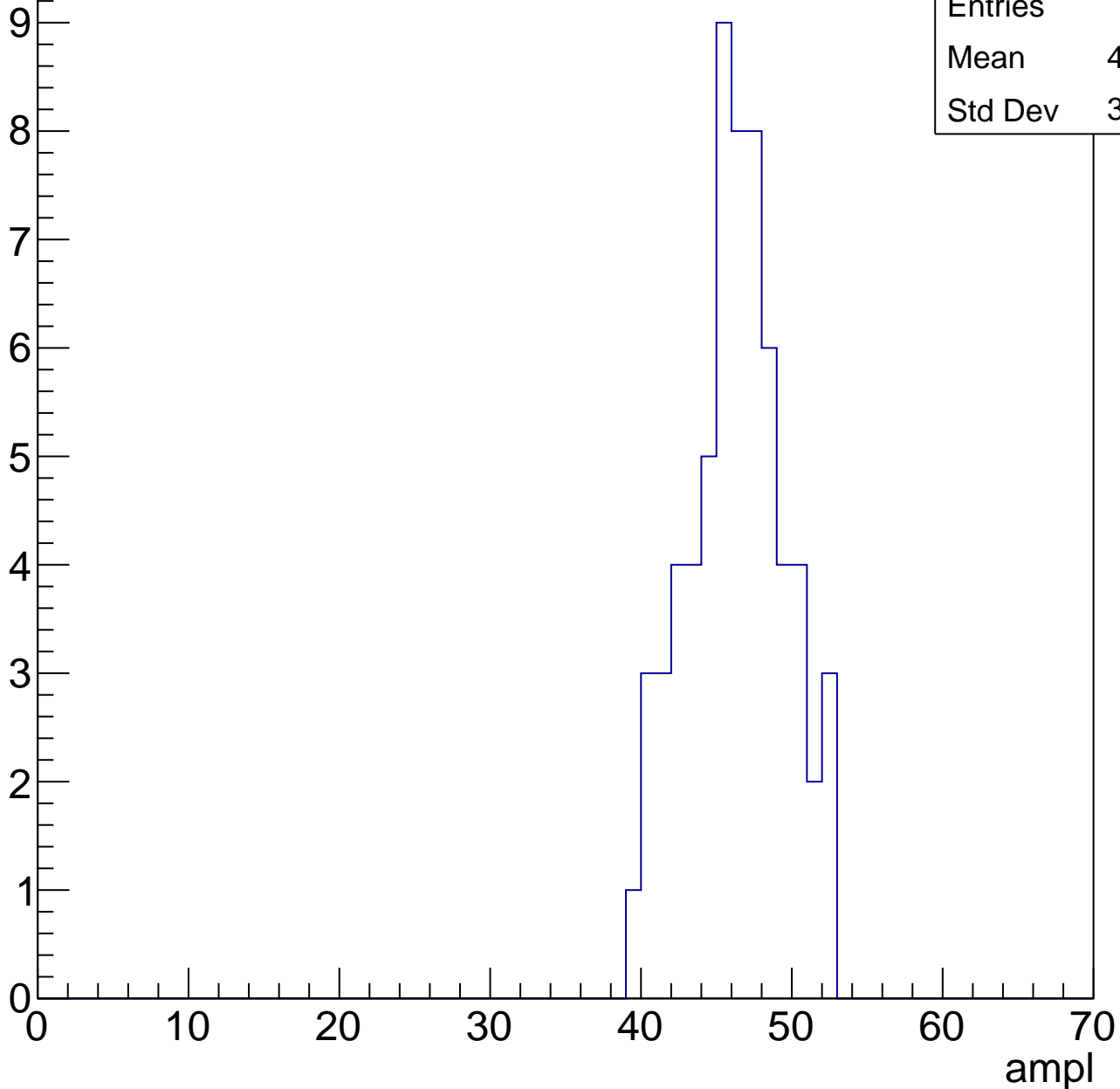


B1L103S, U10-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	45.83
Std Dev	3.185

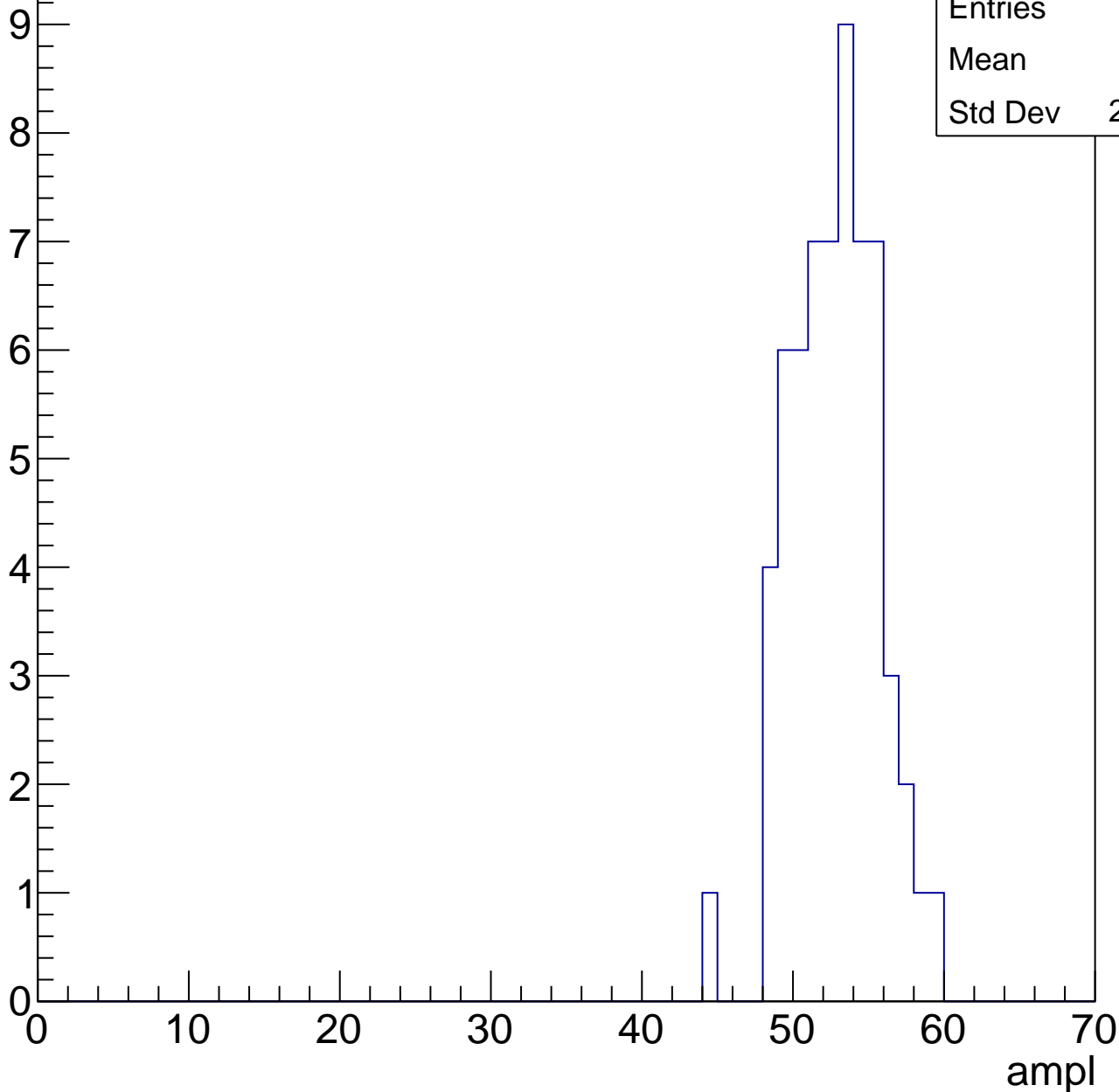


B1L103S, U10-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	52.3
Std Dev	2.836

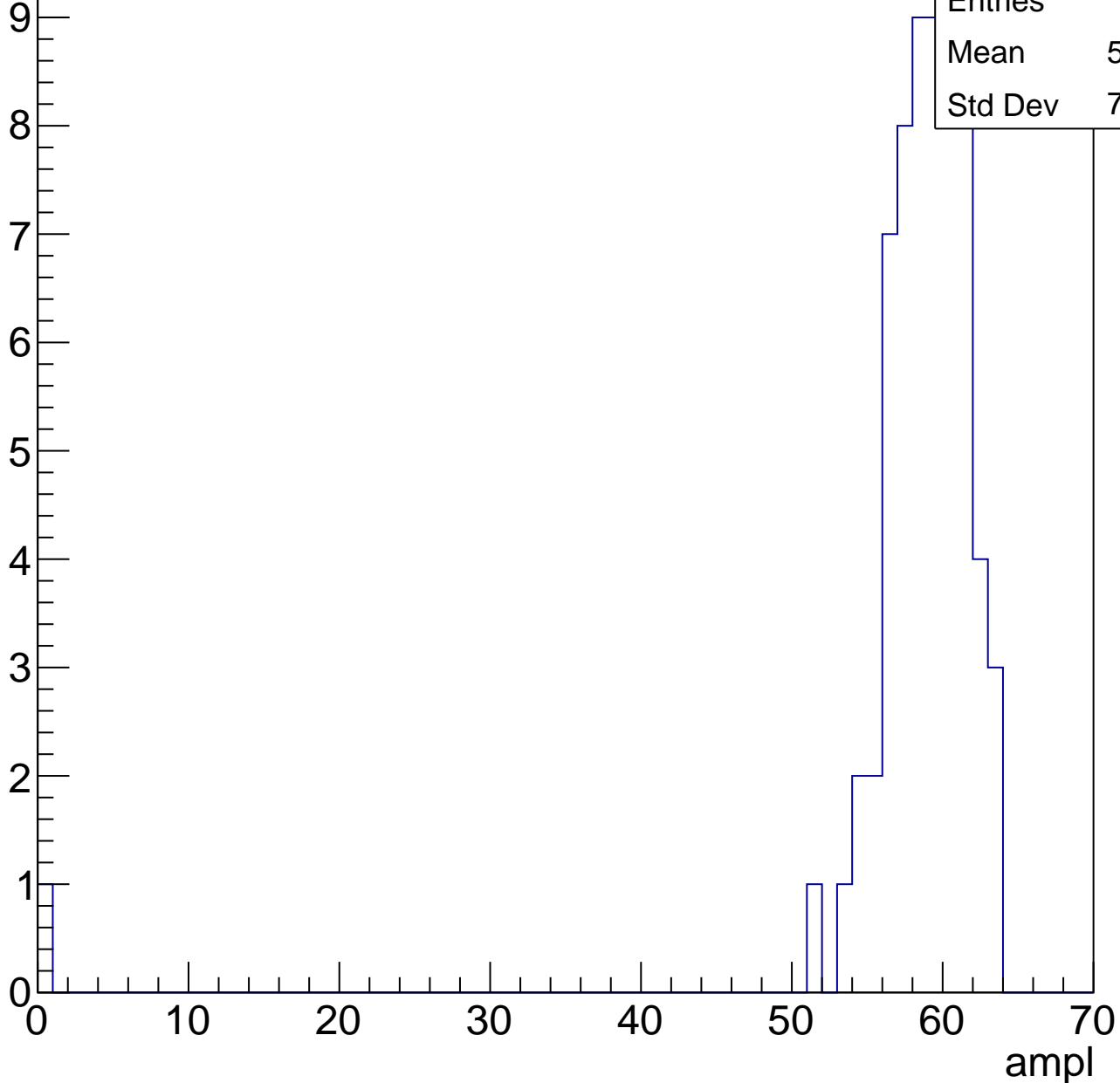


B1L103S, U10-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	57.64
Std Dev	7.682

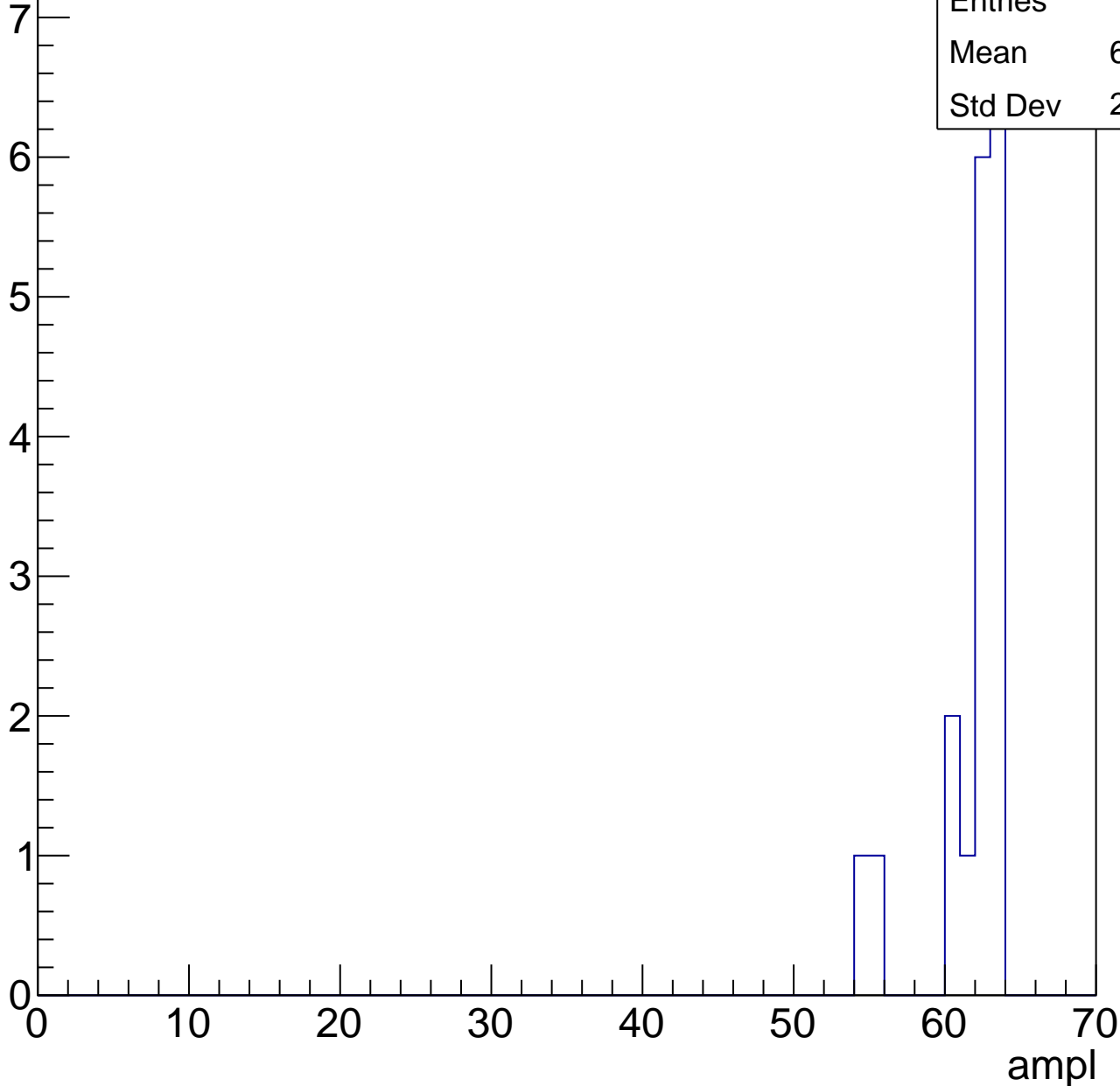


B1L103S, U10-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.28
Std Dev	2.578

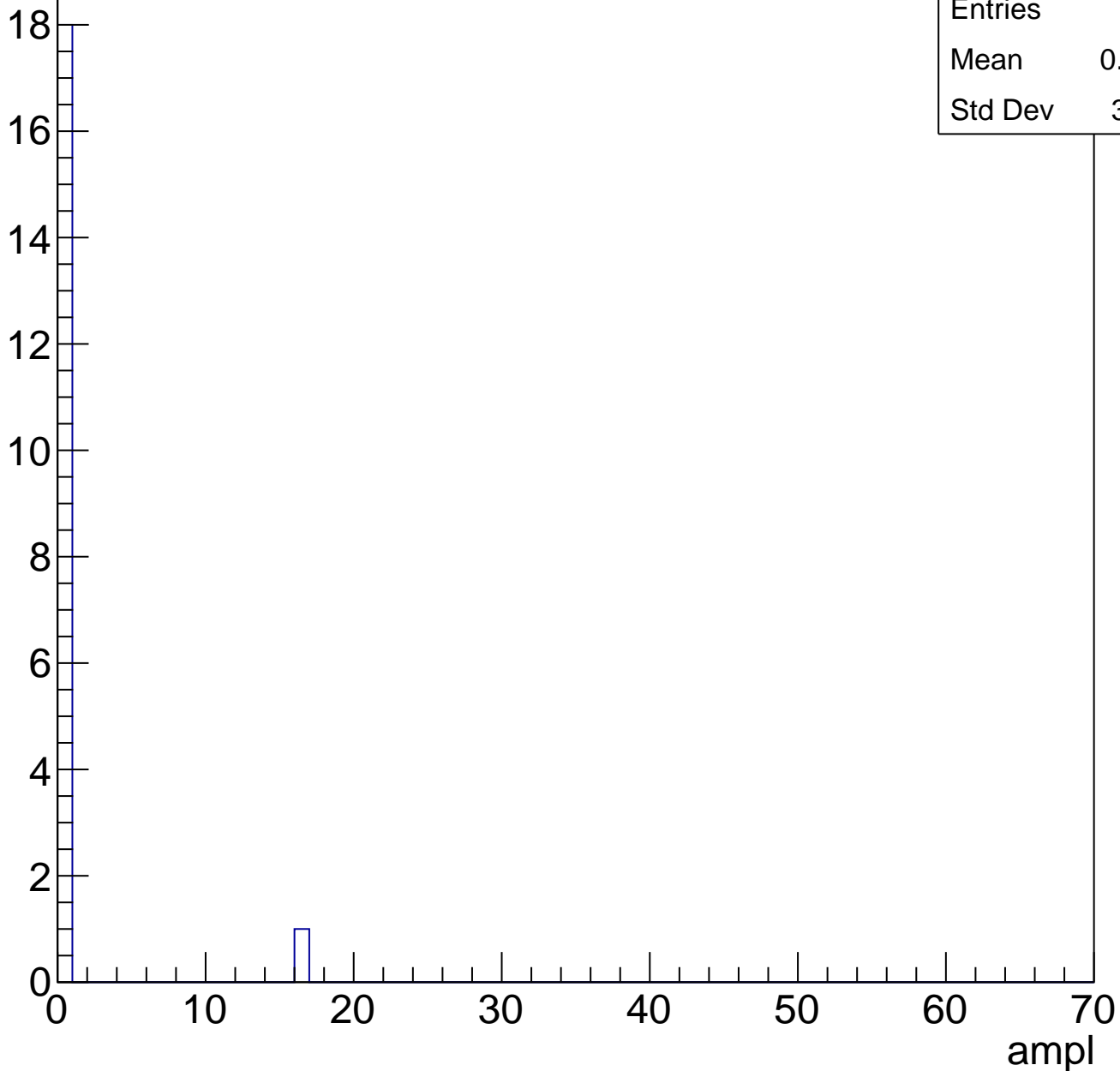


B1L103S, U10-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0.8421
Std Dev	3.573

Entry



B1L103S, U10-ch102, adc0

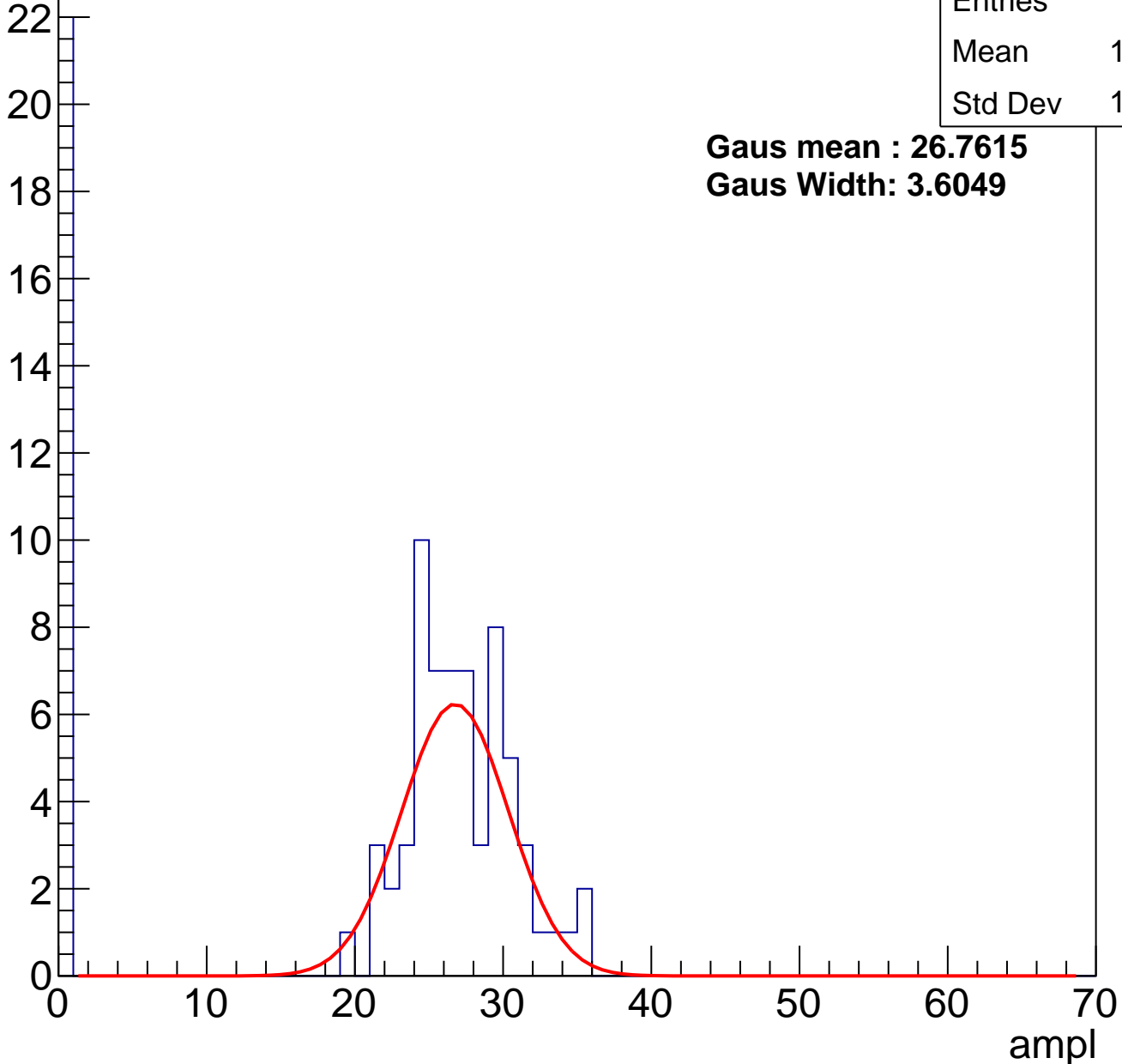
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	19.87
Std Dev	12.02

Gaus mean : 26.7615

Gaus Width: 3.6049

Entry



B1L103S, U10-ch102, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	28.97
Std Dev	12.13

Gaus mean : 34.3547

Gaus Width: 4.0403

Entry

12

10

8

6

4

2

0

0

10

20

30

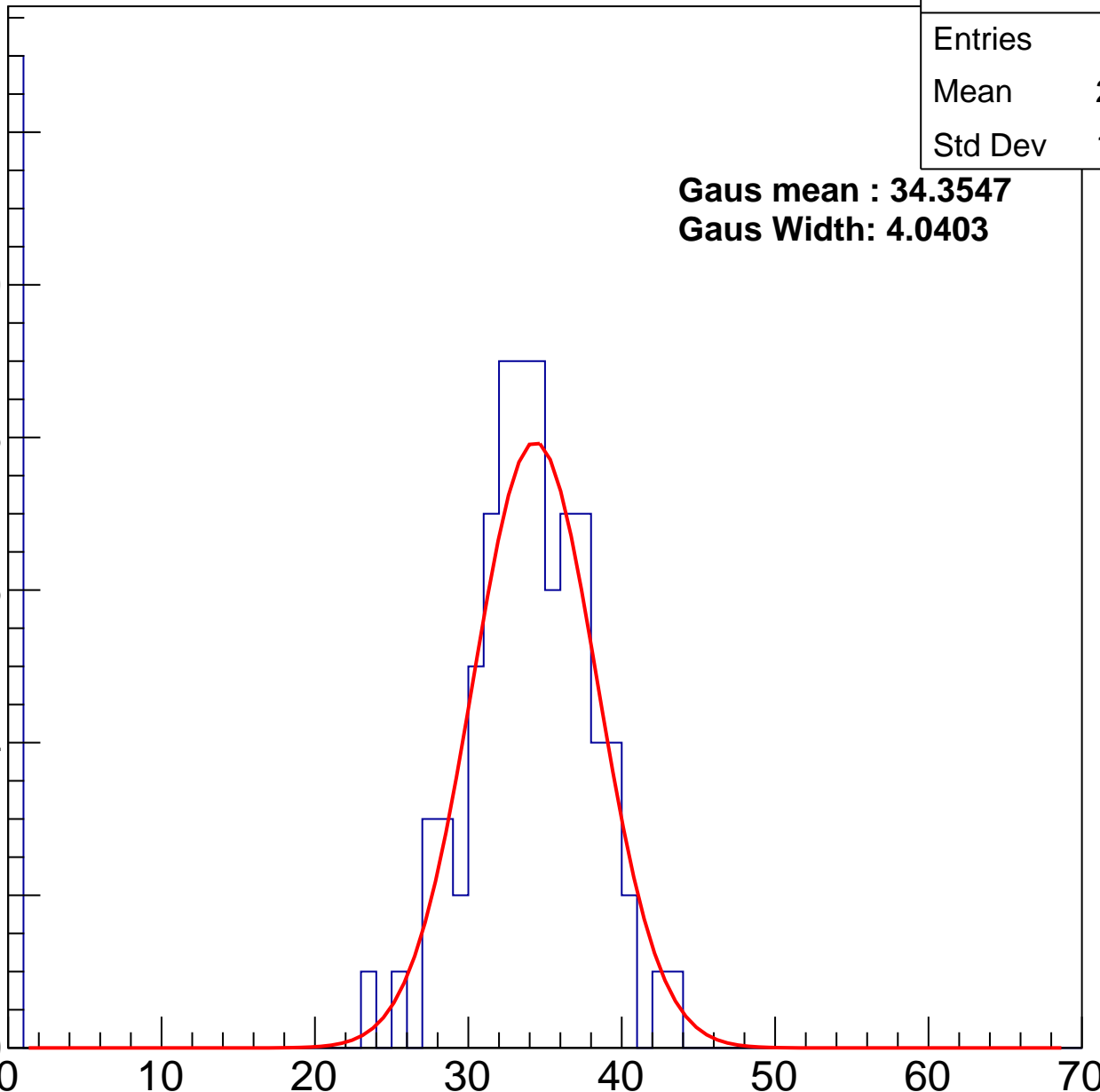
40

50

60

70

ampl



B1L103S, U10-ch102, adc2

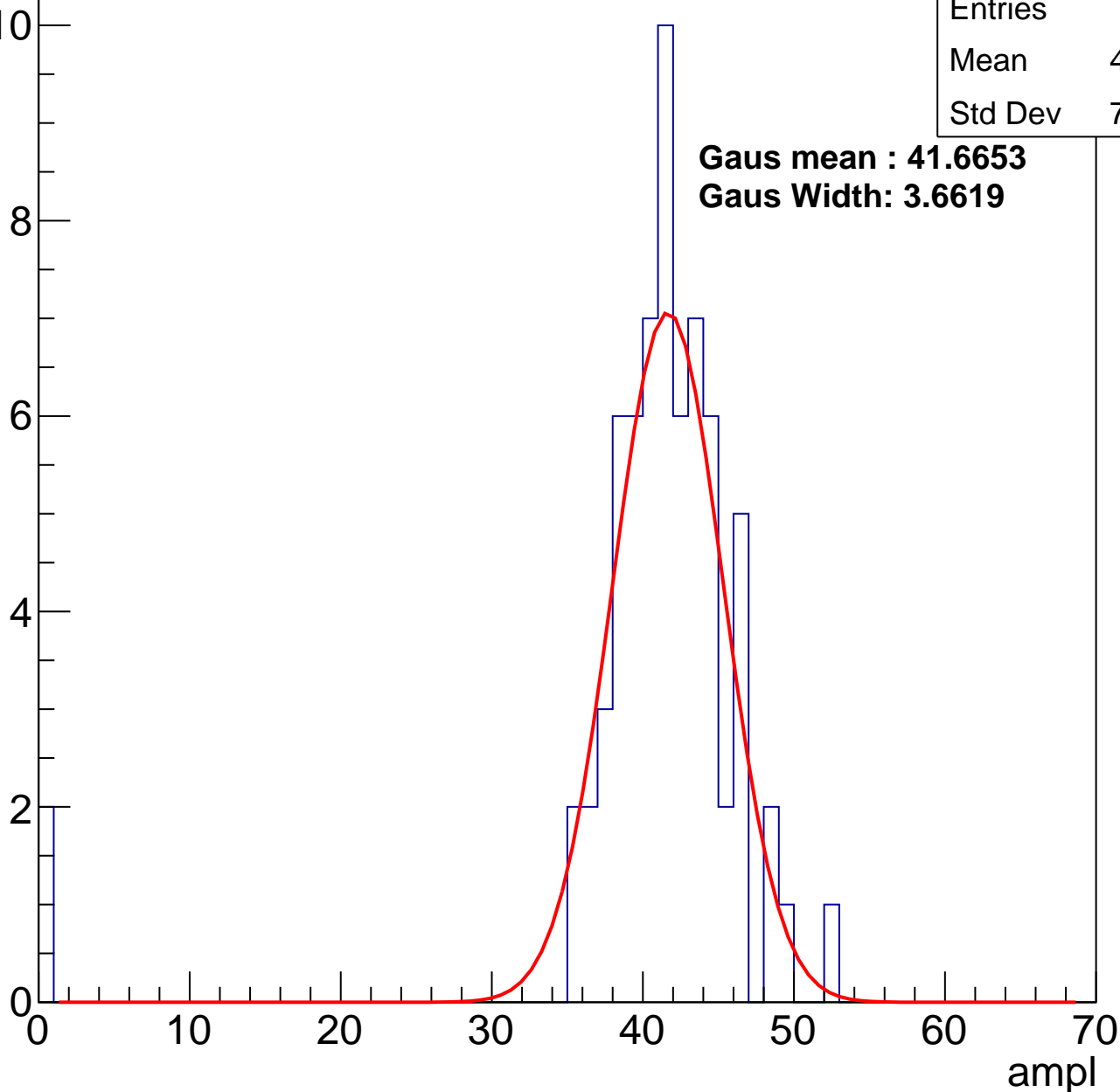
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.28
Std Dev	7.774

Gaus mean : 41.6653

Gaus Width: 3.6619

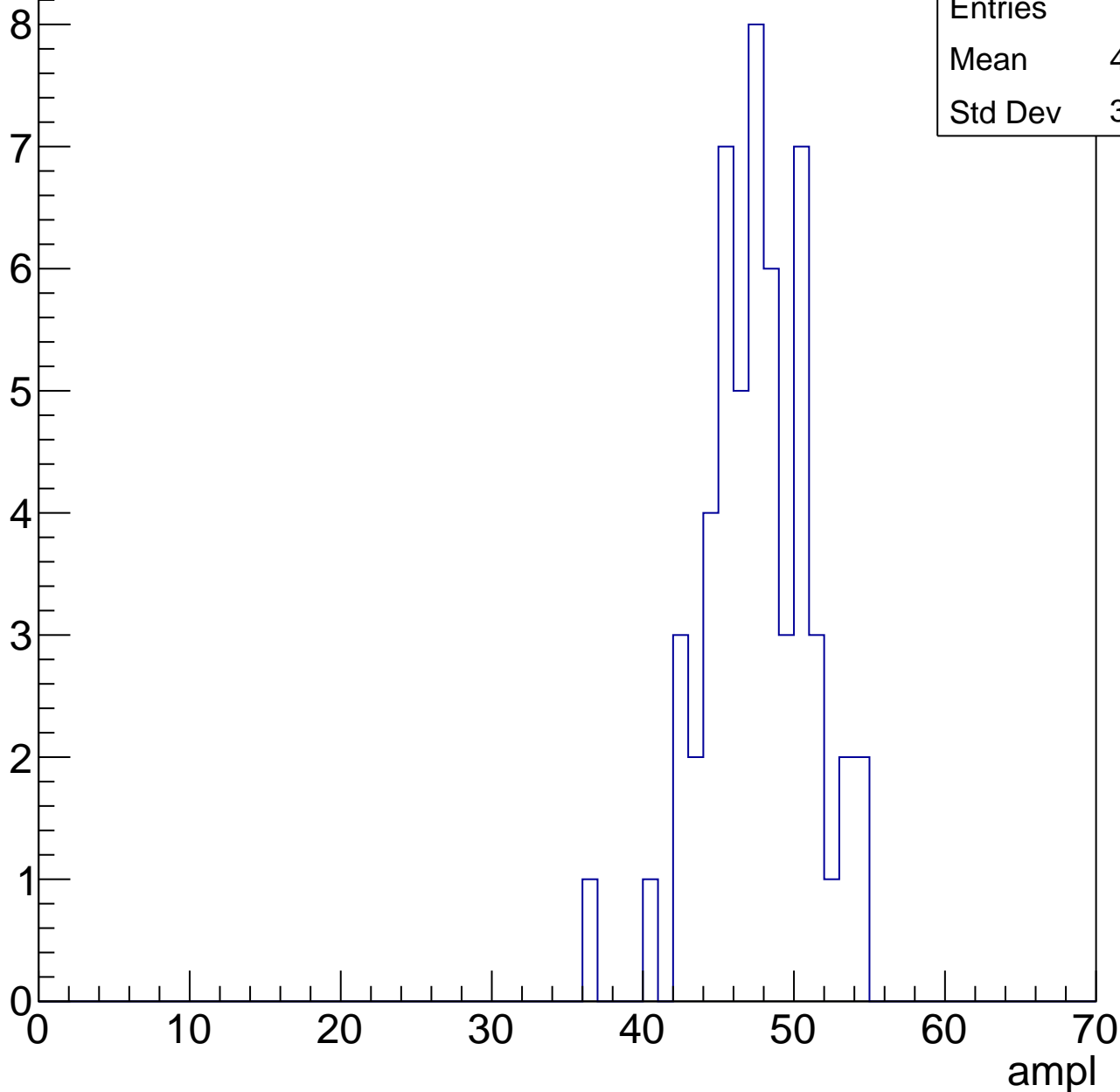


B1L103S, U10-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

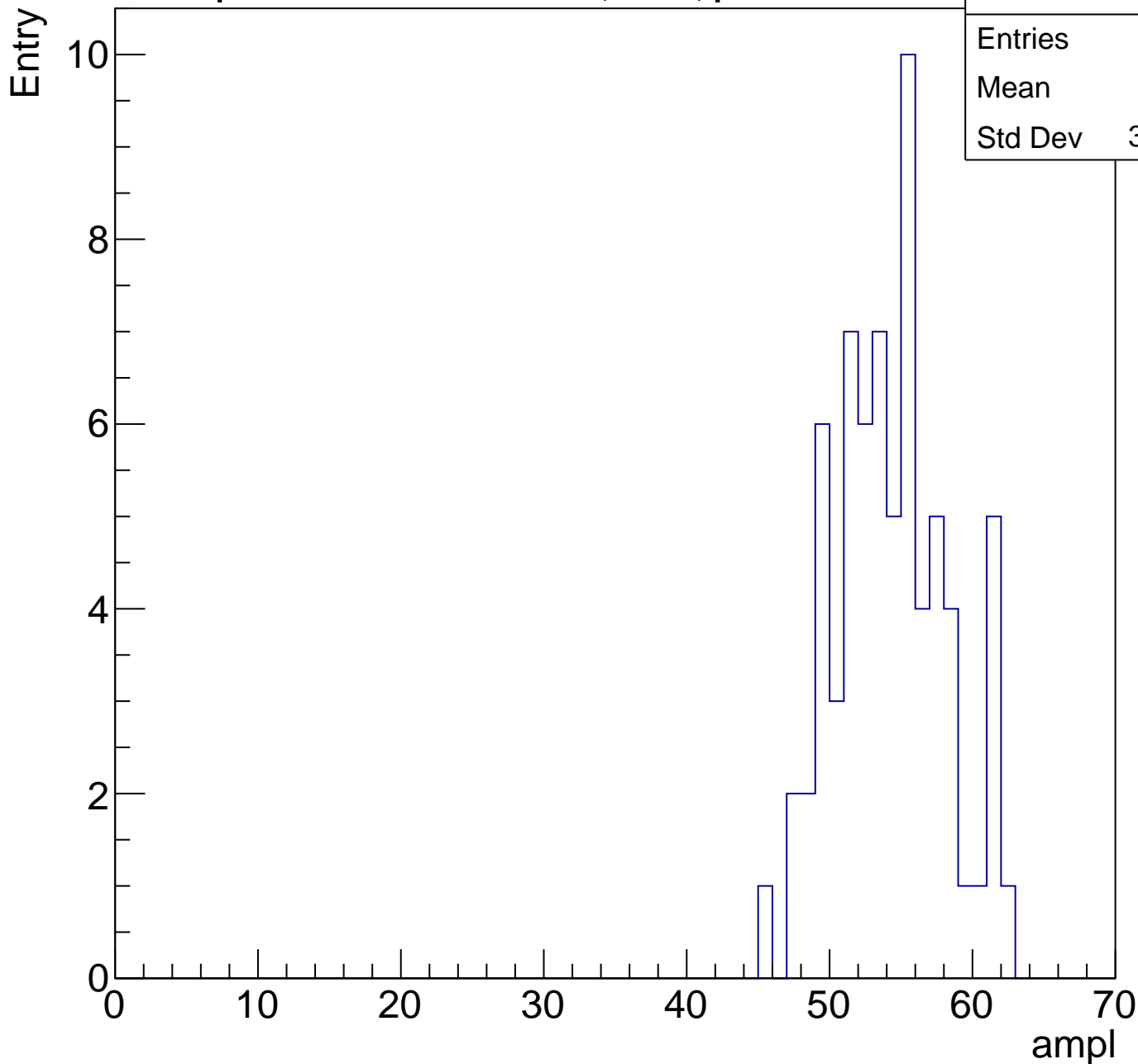
Entries	55
Mean	47.07
Std Dev	3.489



B1L103S, U10-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	53.8
Std Dev	3.864

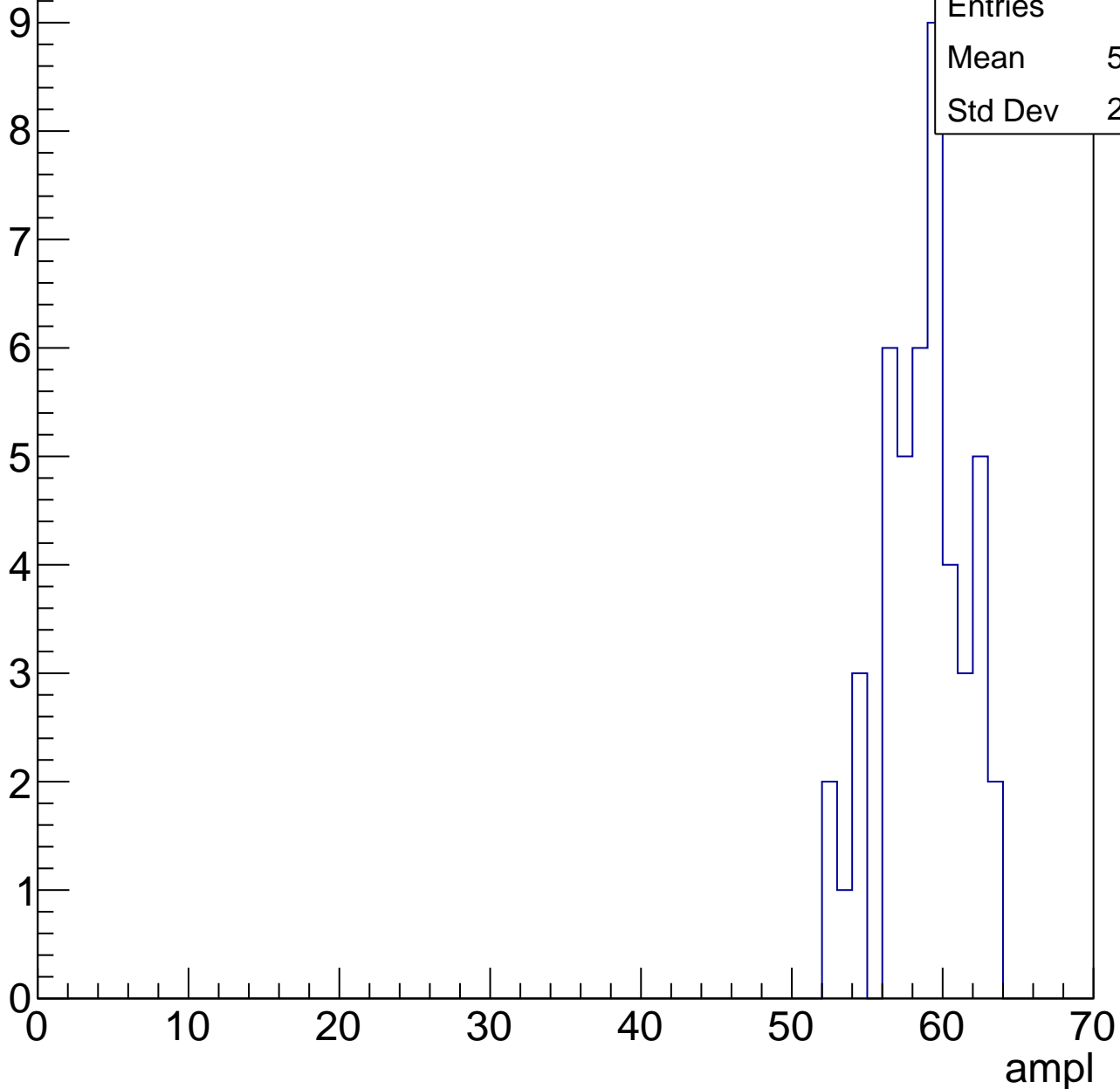


B1L103S, U10-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.22
Std Dev	2.773

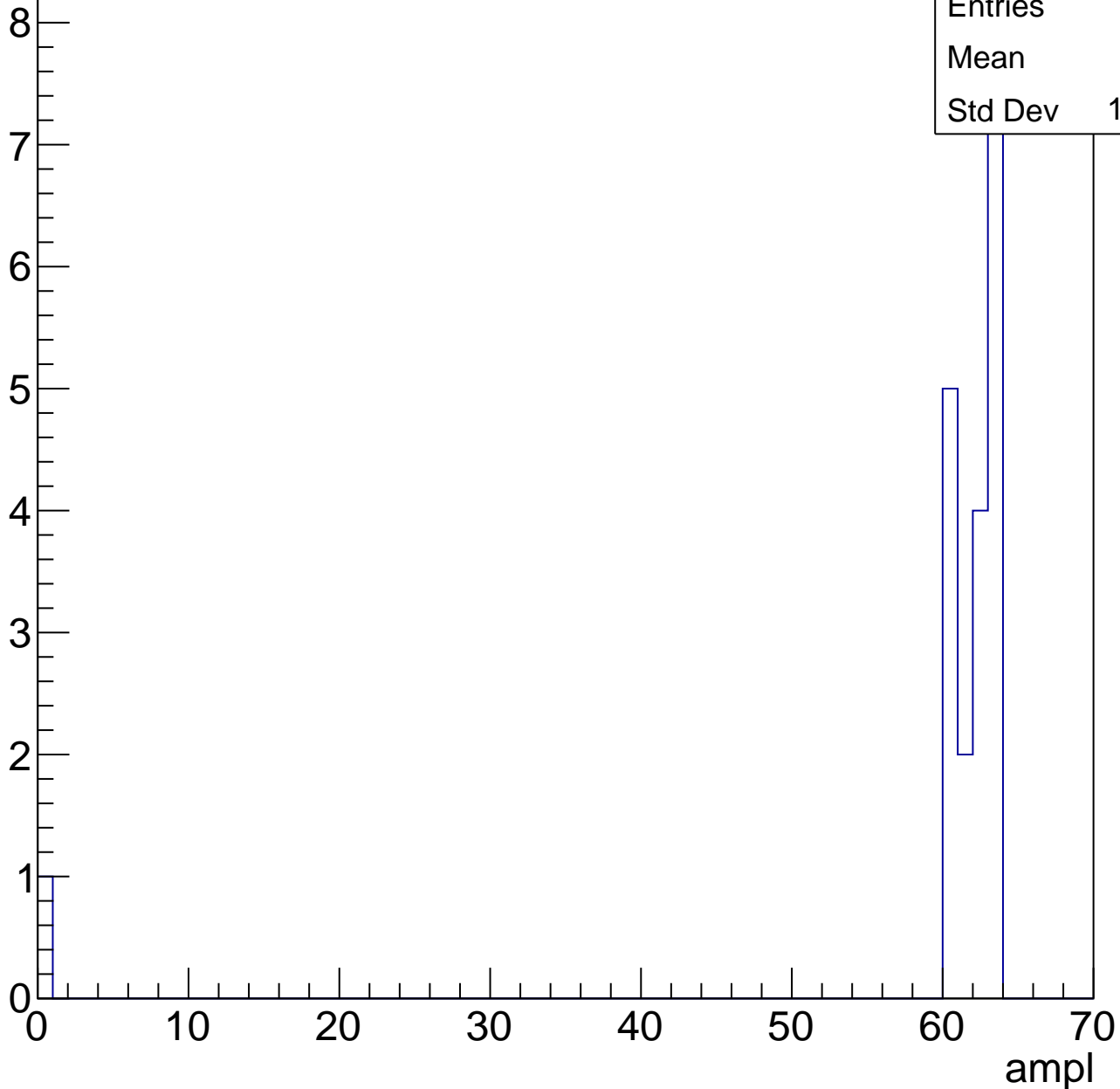


B1L103S, U10-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.7
Std Dev	13.52



B1L103S, U10-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

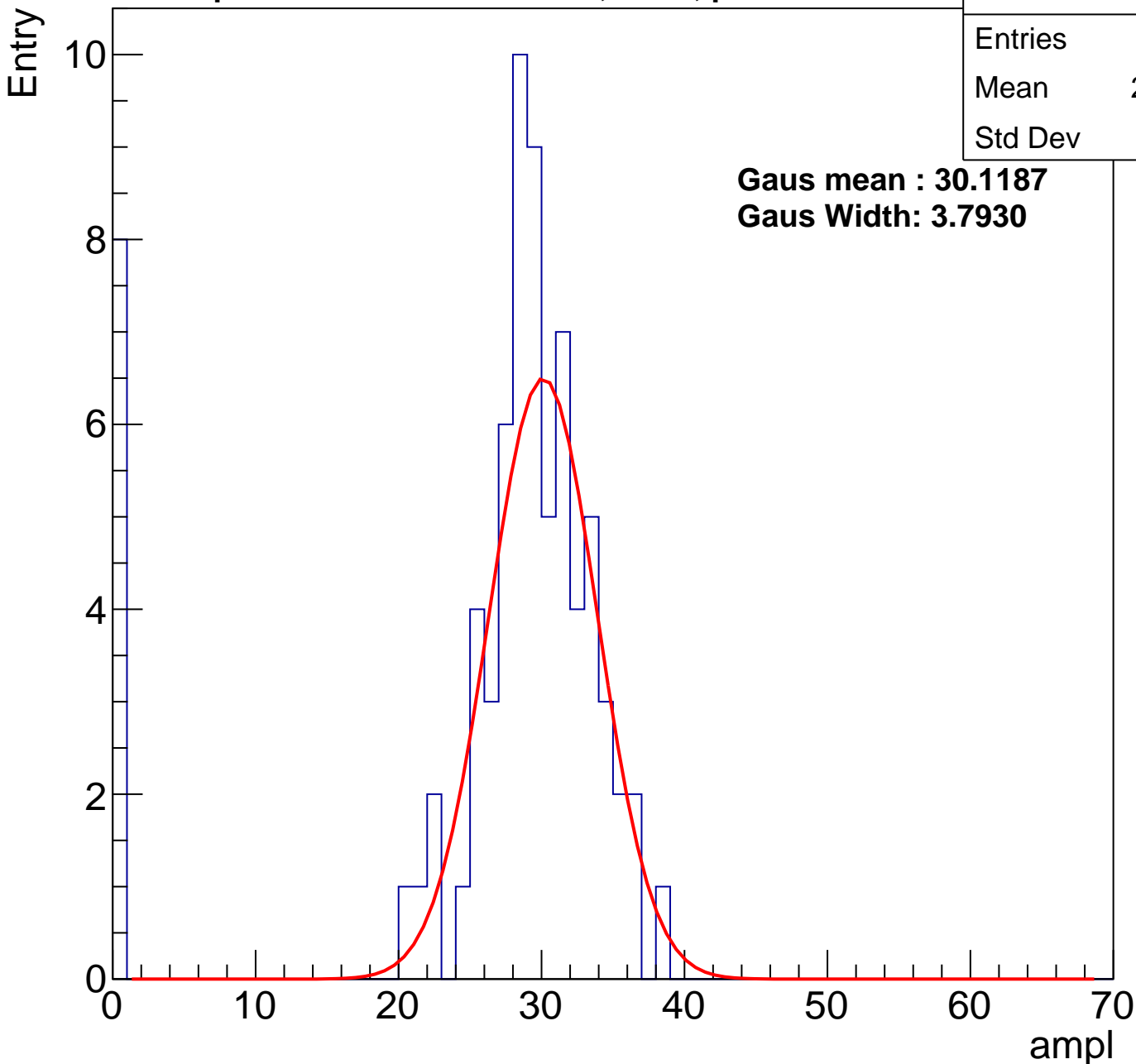
ampl

B1L103S, U10-ch103, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	26.11
Std Dev	9.71

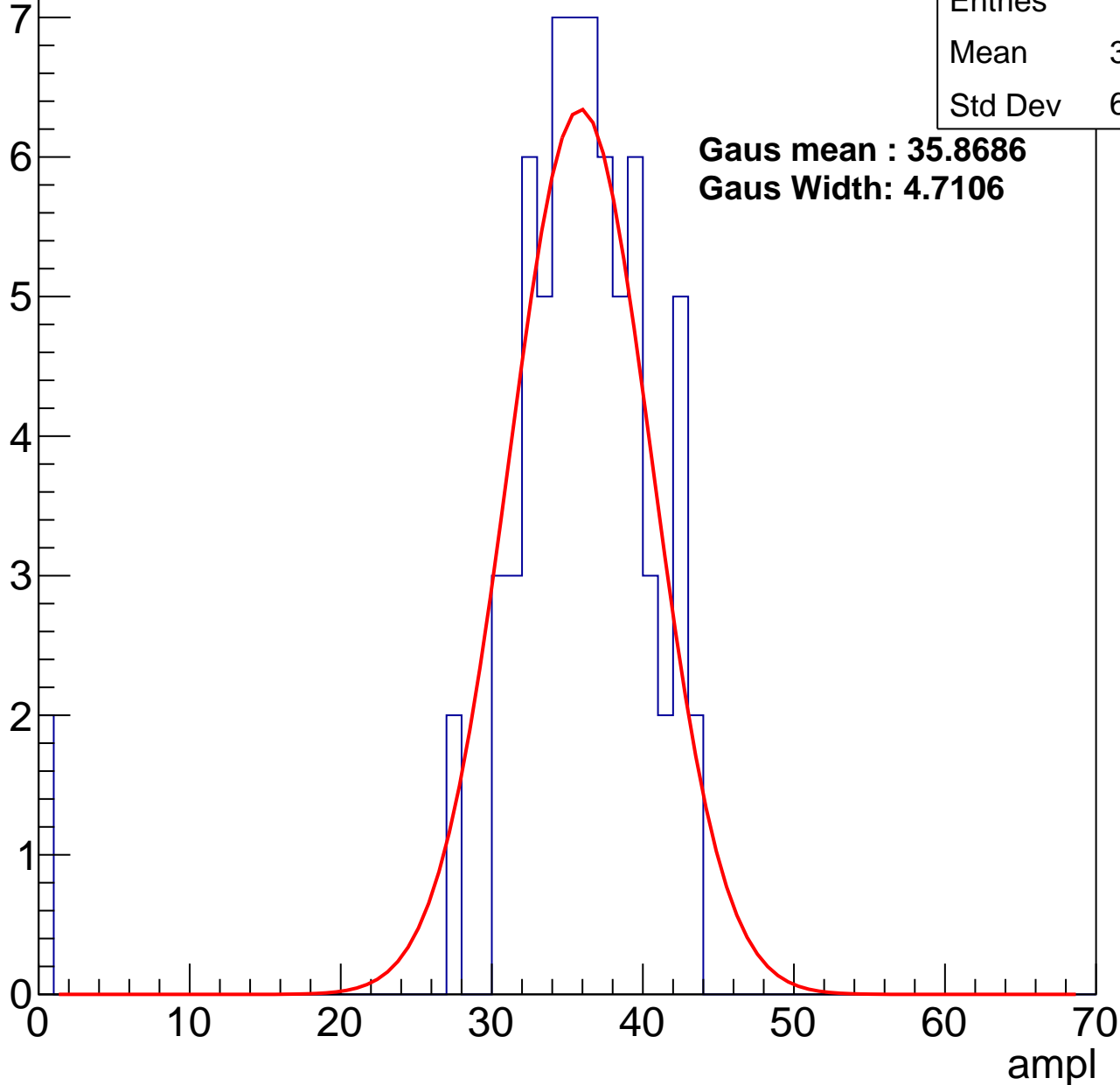
Gaus mean : 30.1187
Gaus Width: 3.7930



B1L103S, U10-ch103, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch103, adc2

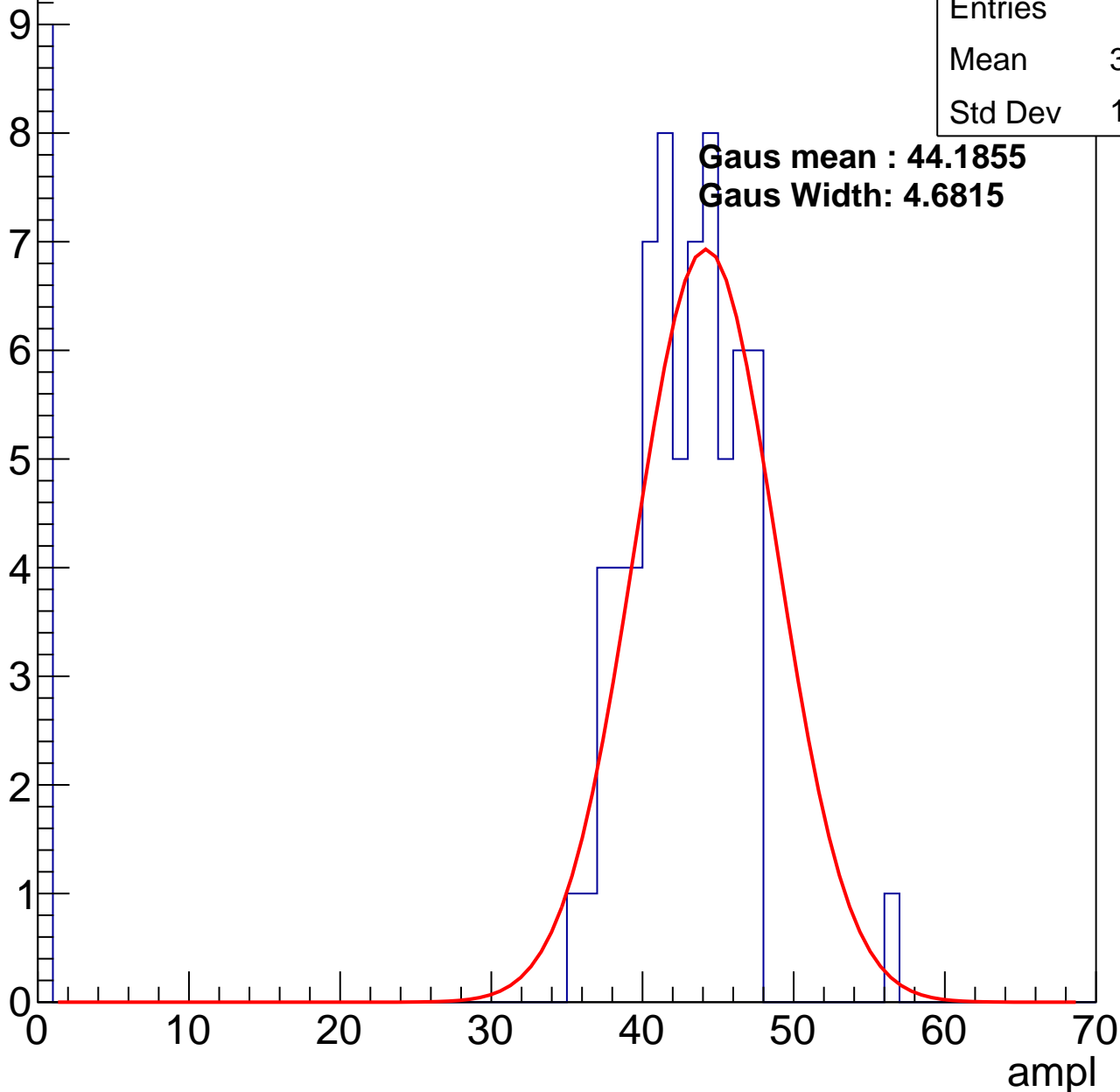
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	37.33
Std Dev	14.08

Gaus mean : 44.1855

Gaus Width: 4.6815

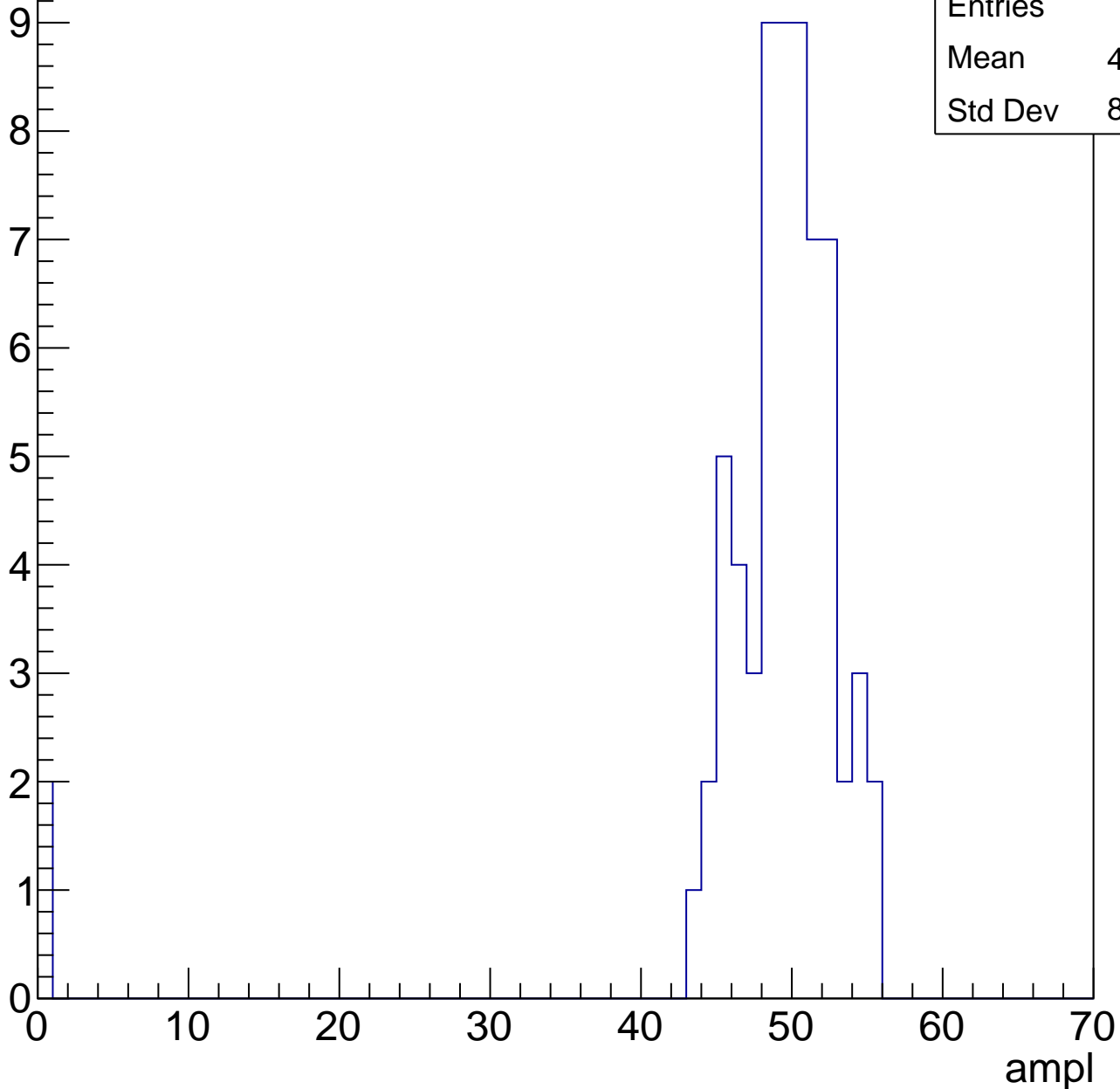


B1L103S, U10-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	47.74
Std Dev	8.945

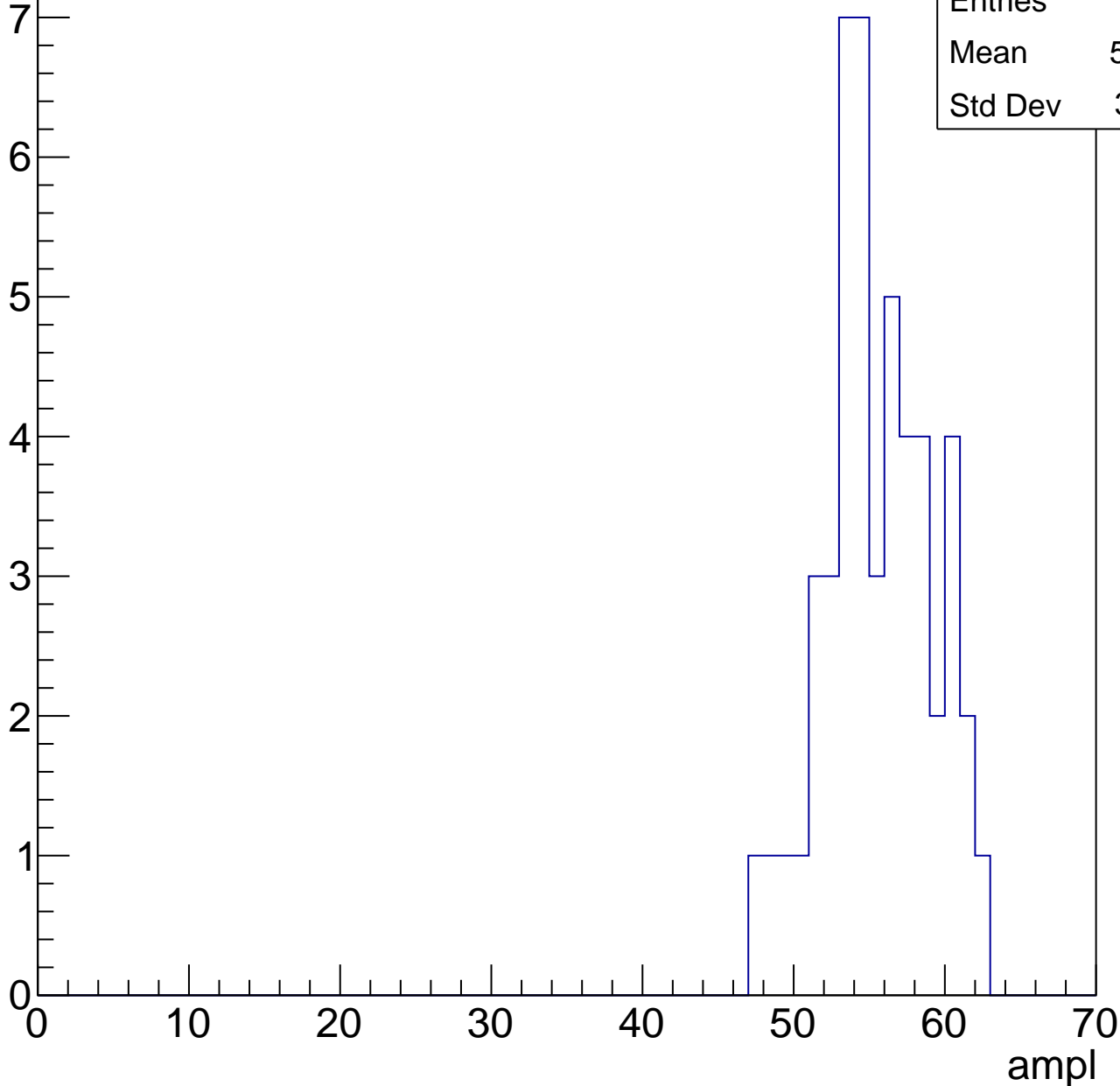


B1L103S, U10-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.08
Std Dev	3.481

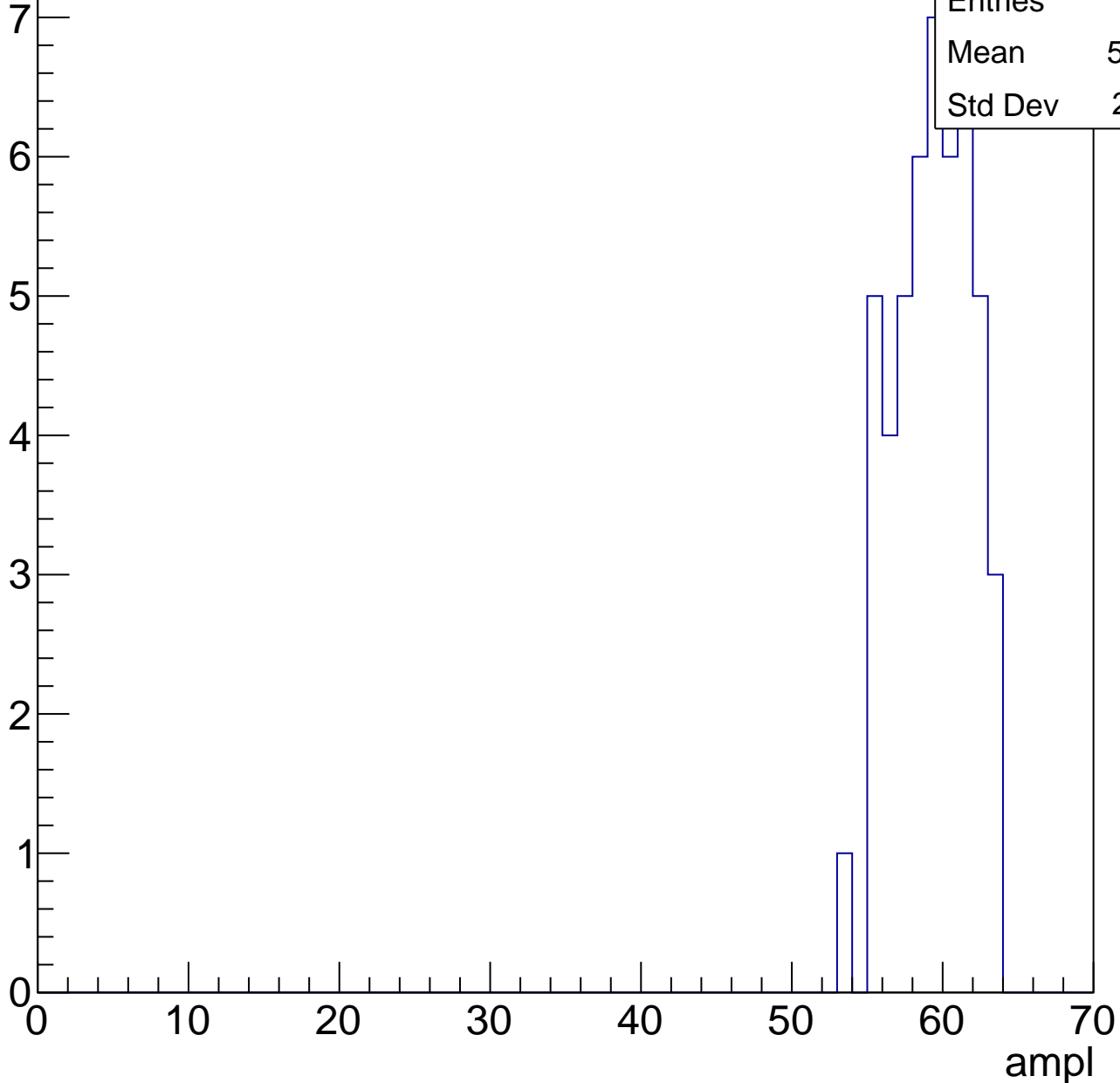


B1L103S, U10-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.86
Std Dev	2.491

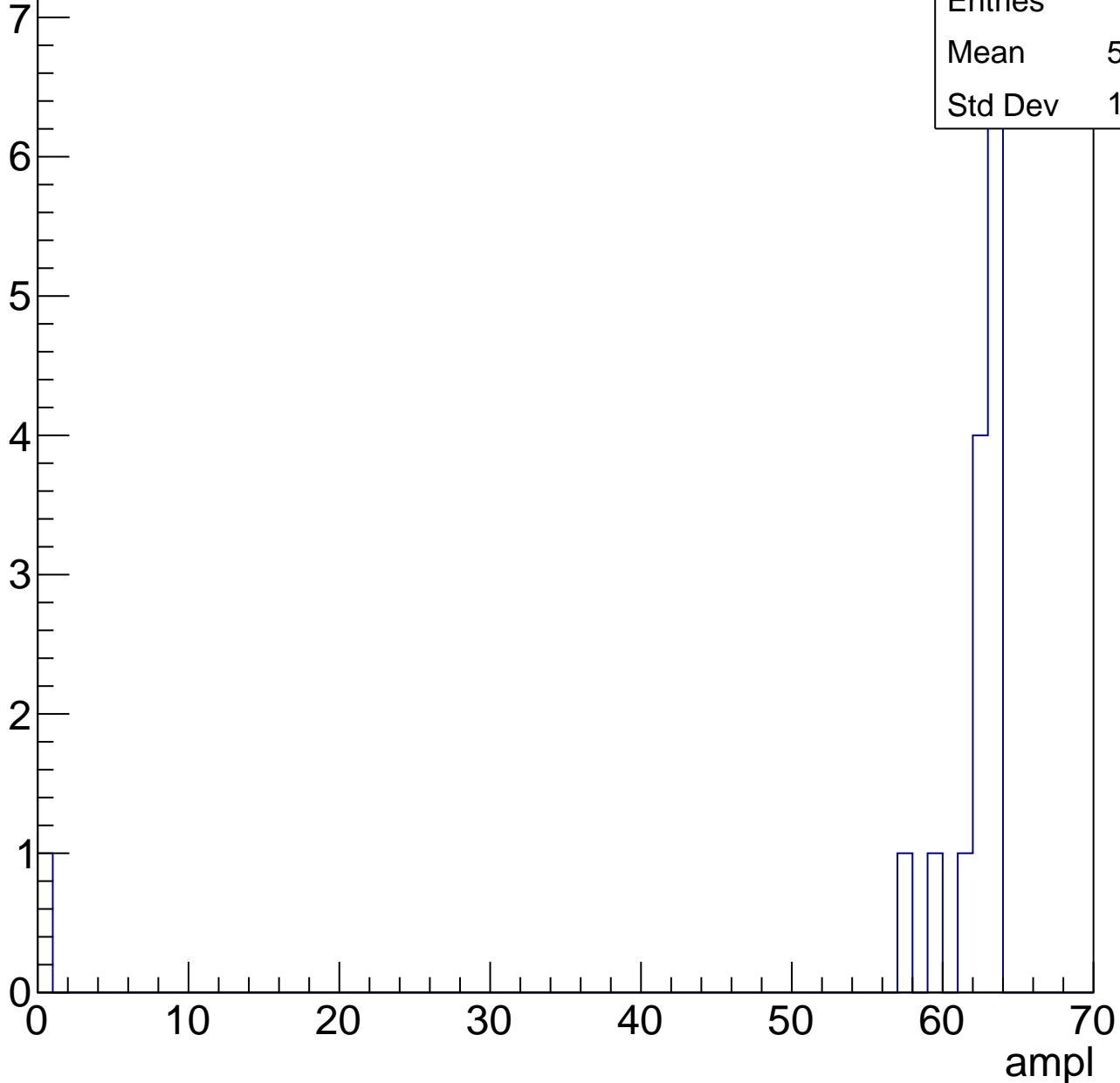


B1L103S, U10-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.73
Std Dev	15.52



B1L103S, U10-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch104, adc0

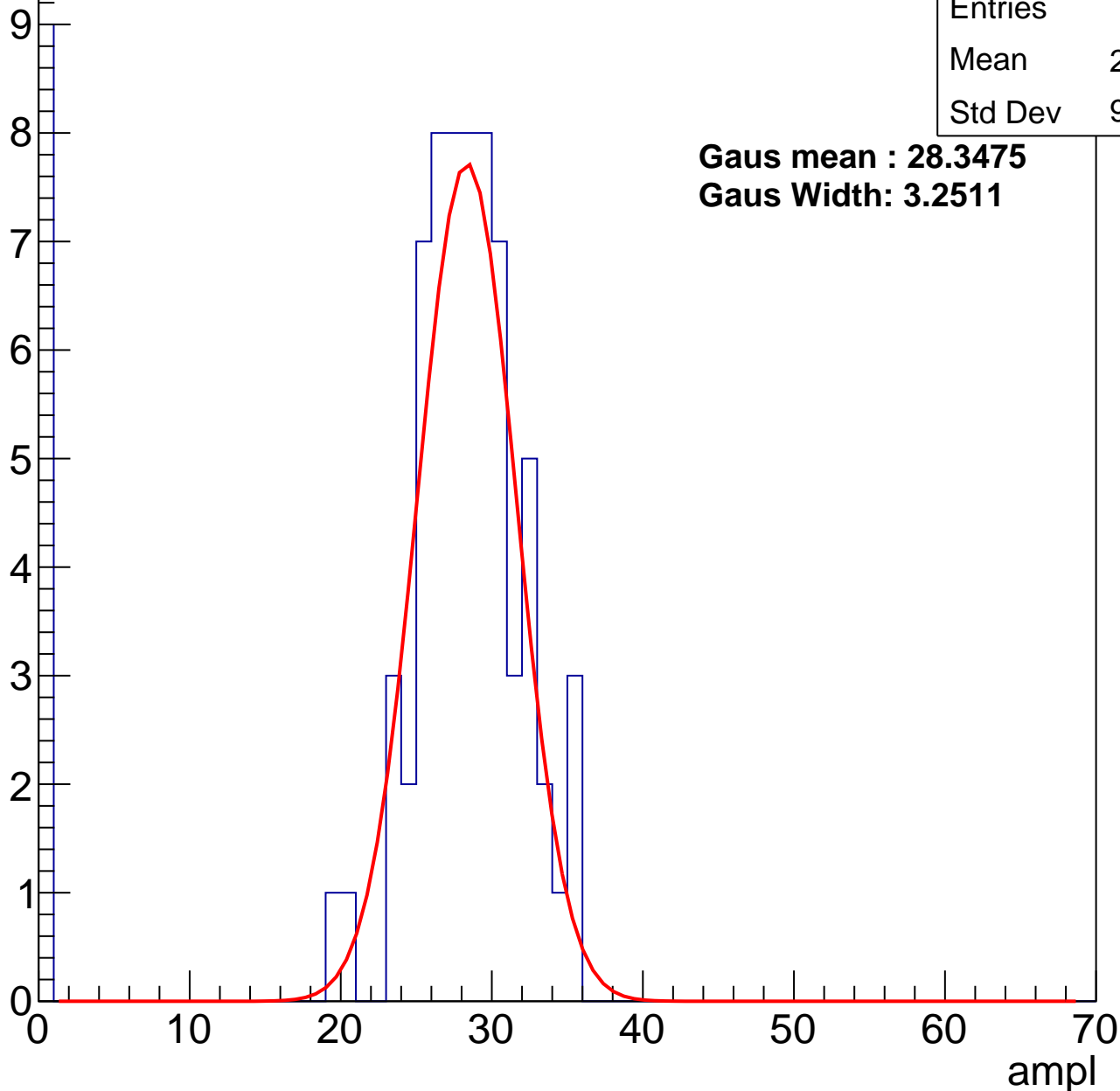
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	24.72
Std Dev	9.577

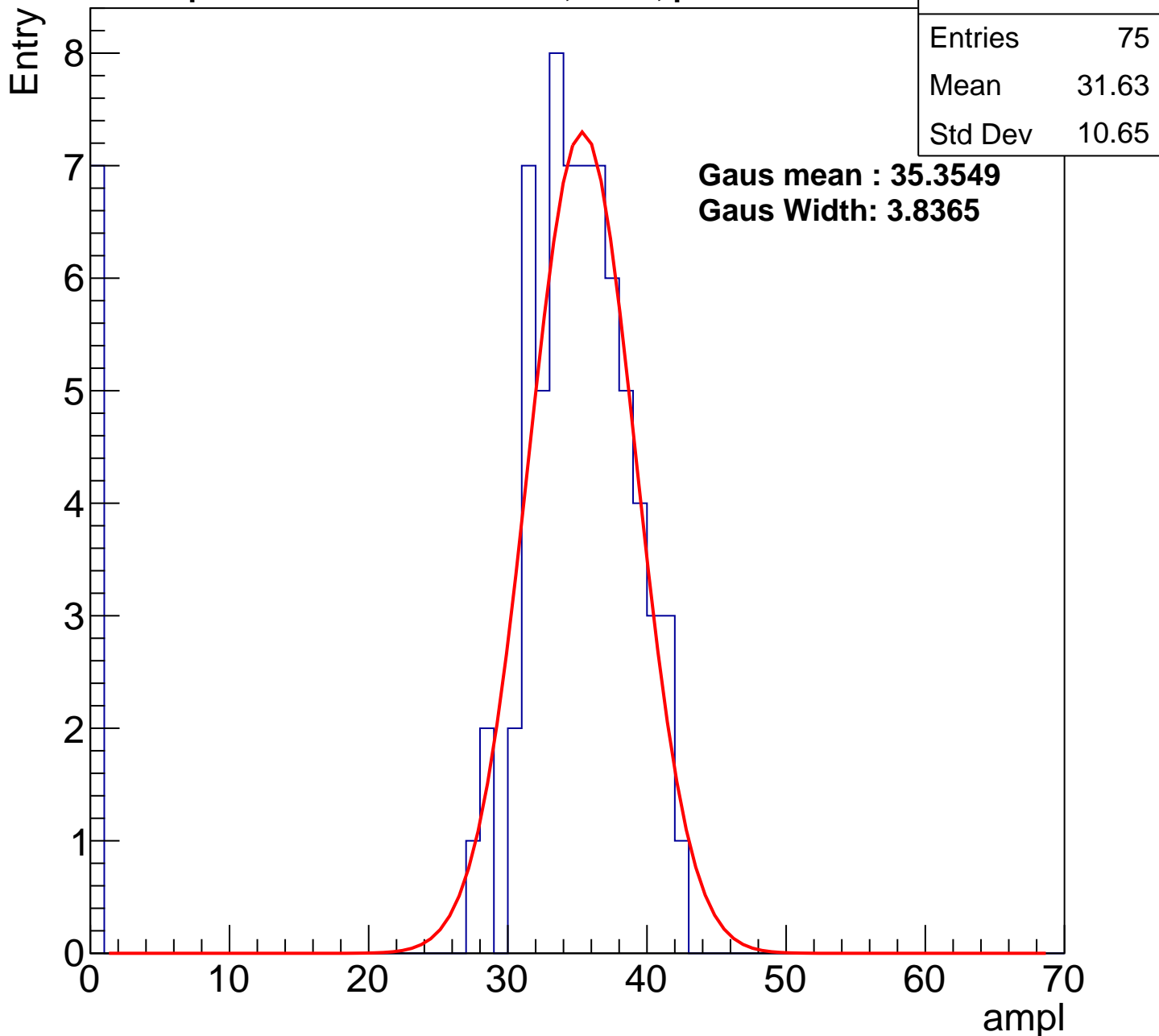
Gaus mean : 28.3475

Gaus Width: 3.2511



B1L103S, U10-ch104, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch104, adc2

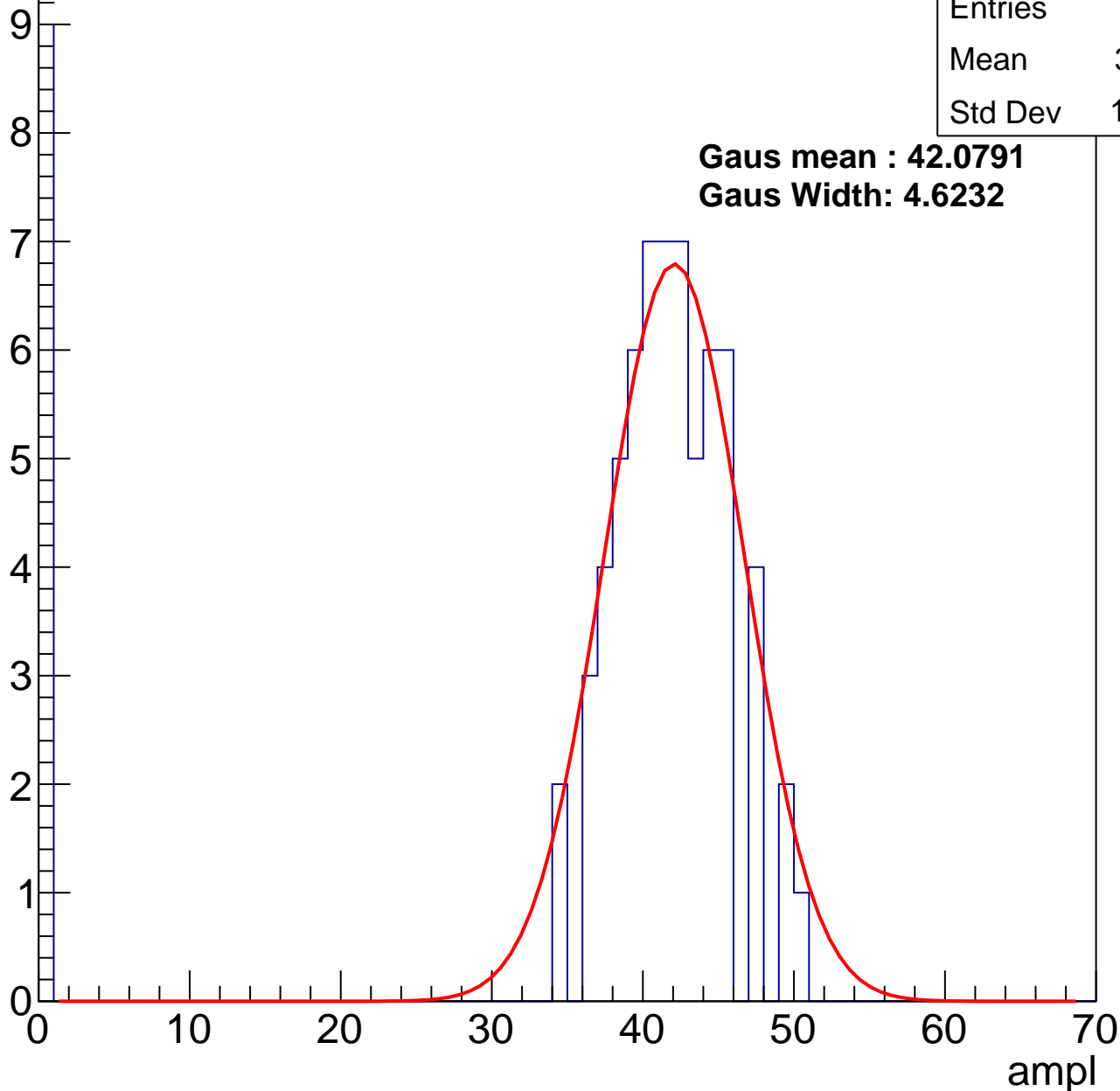
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.41
Std Dev	13.95

Gaus mean : 42.0791

Gaus Width: 4.6232

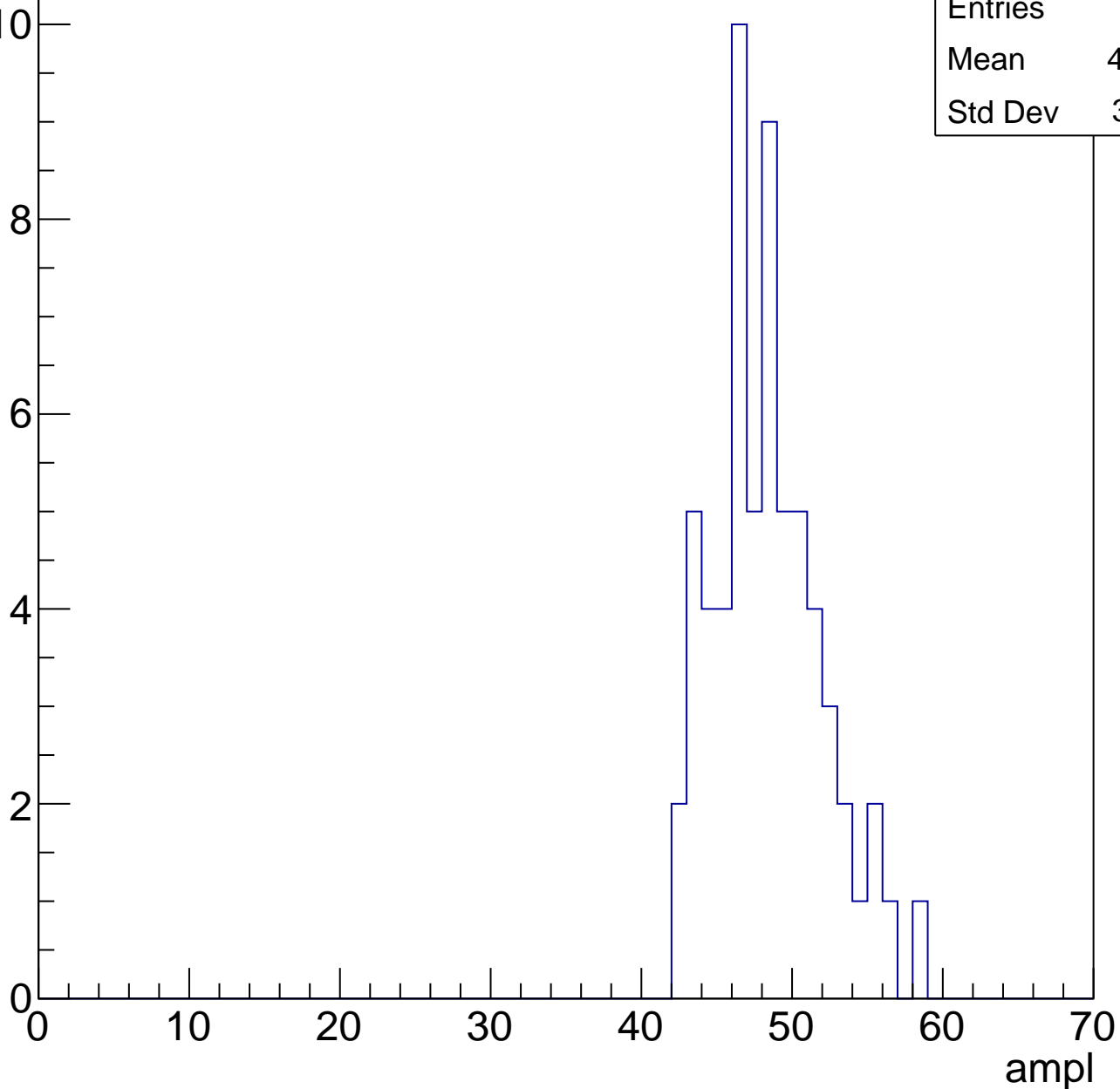


B1L103S, U10-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.95
Std Dev	3.561

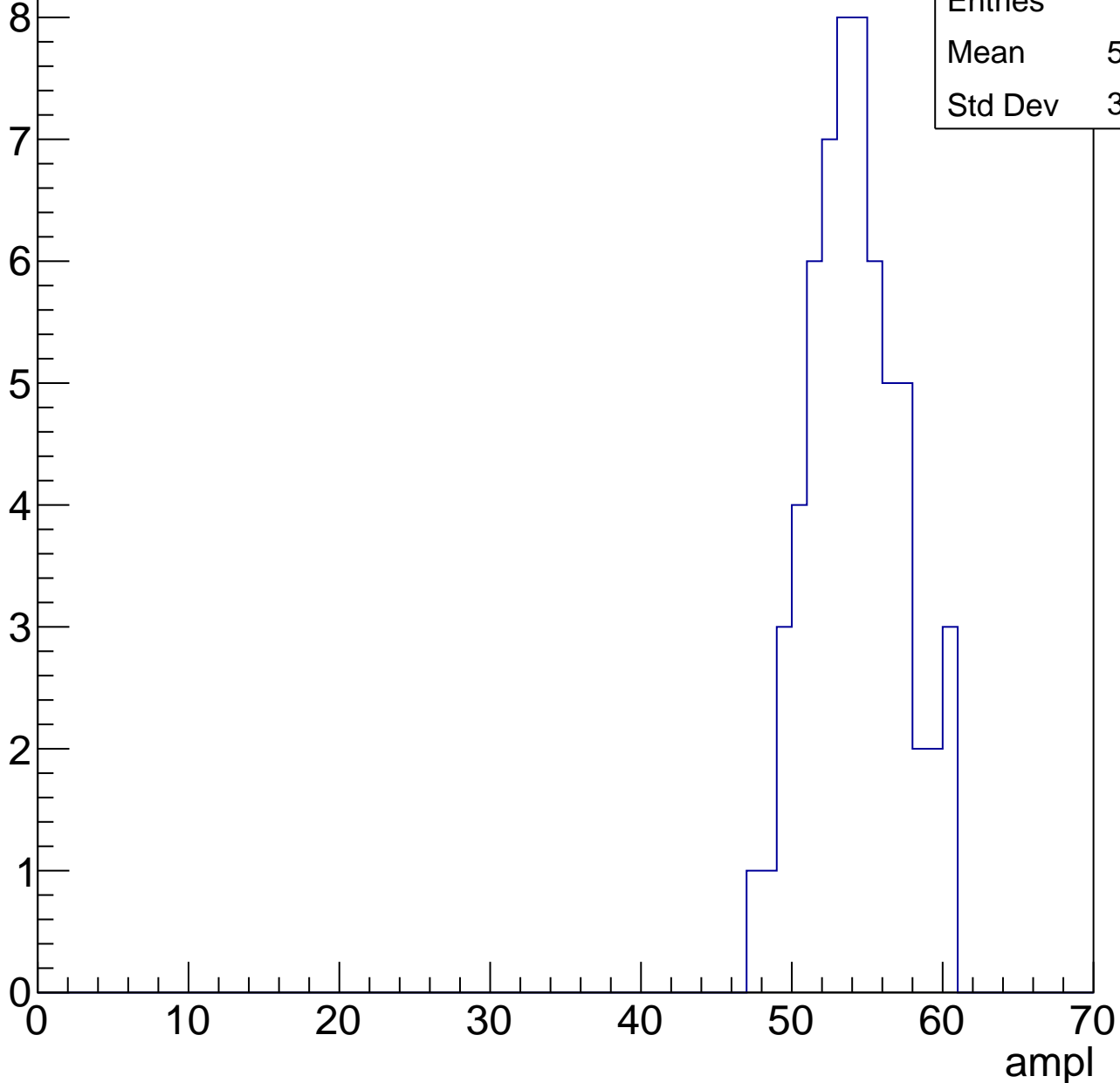


B1L103S, U10-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.72
Std Dev	3.058

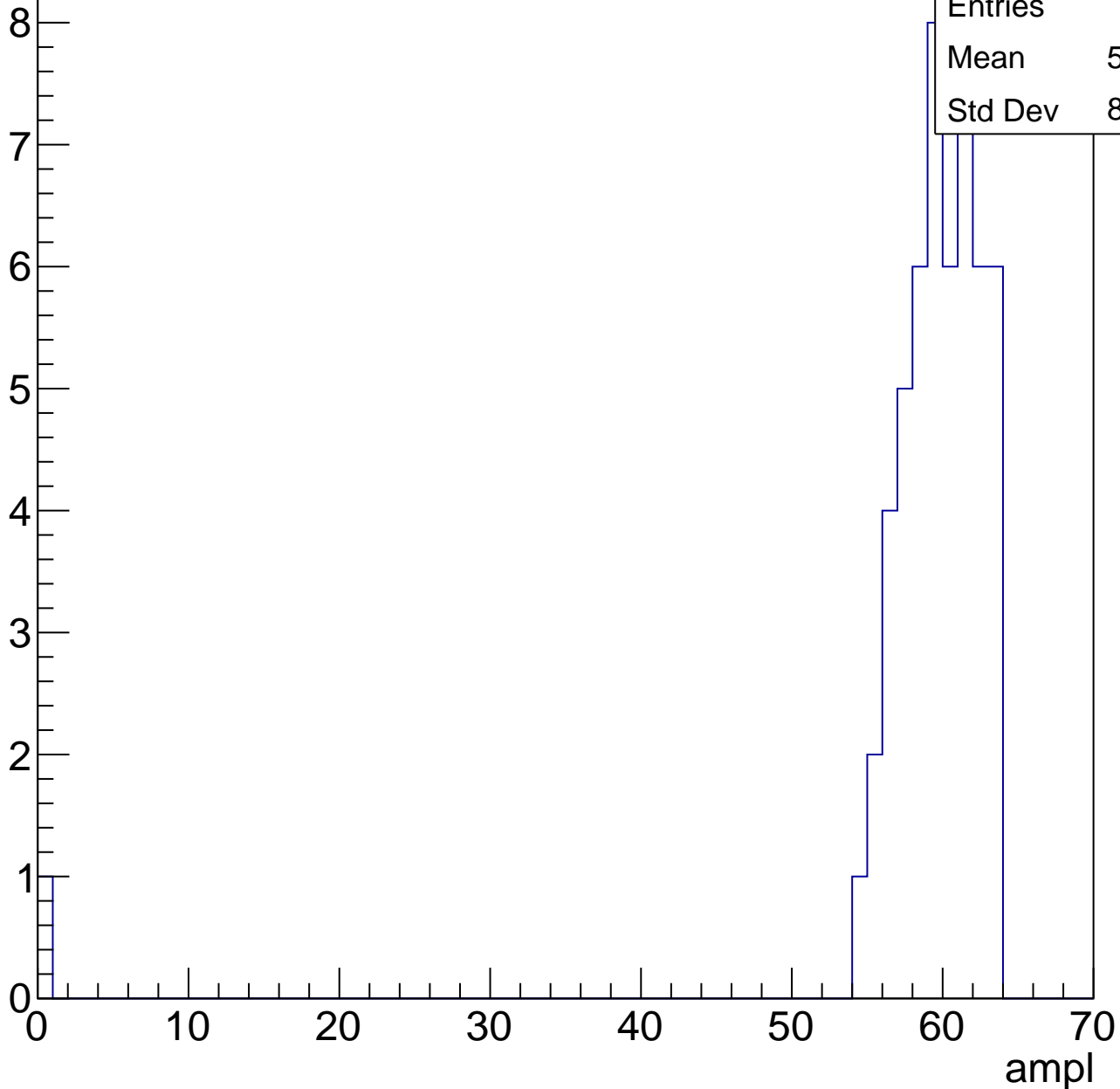


B1L103S, U10-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

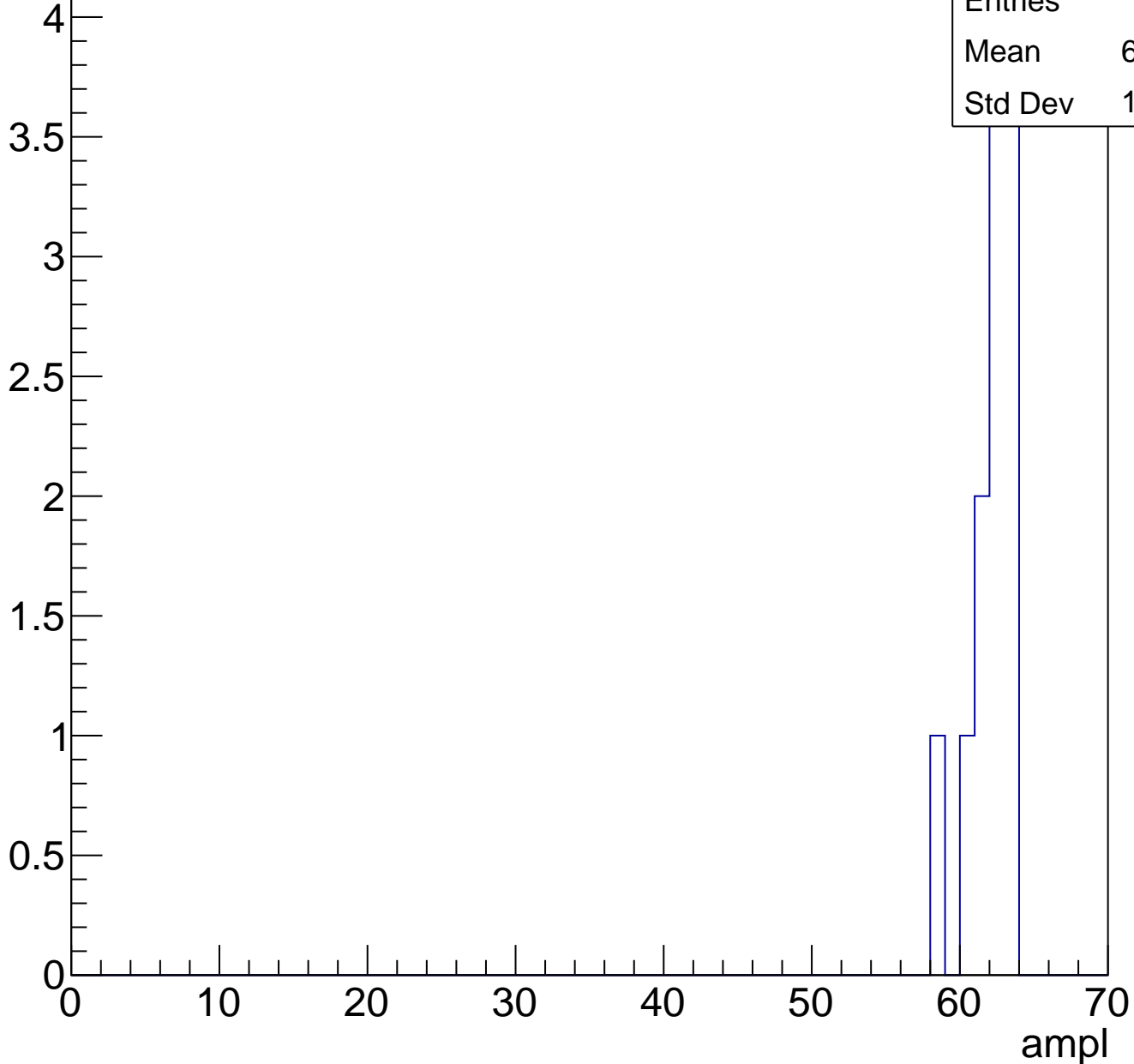
Entries	53
Mean	58.32
Std Dev	8.427



B1L103S, U10-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch105, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	19.93
Std Dev	12.71

Gaus mean : 27.8927

Gaus Width: 4.0033

Entry

25

20

15

10

5

0

0

10

20

30

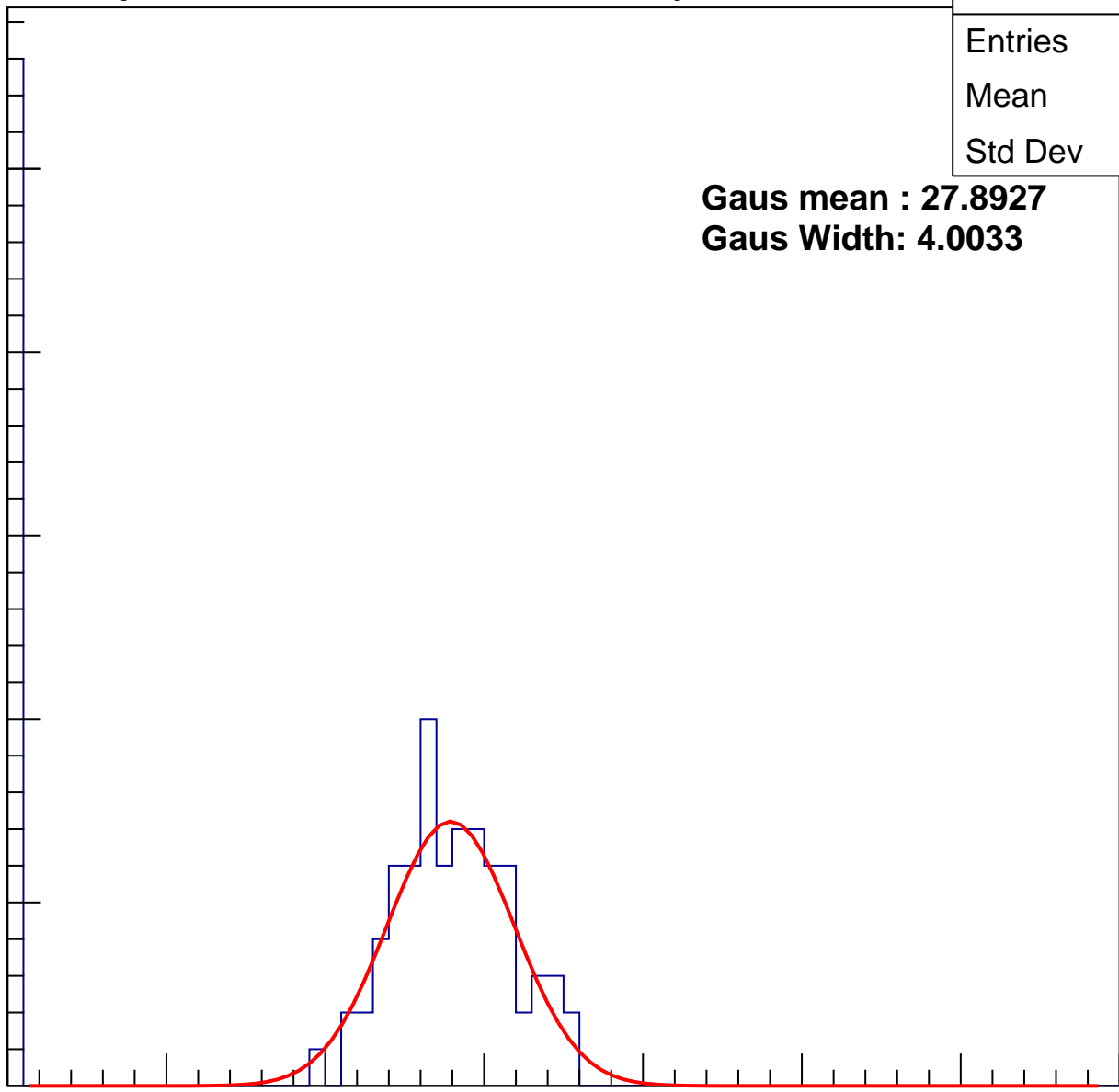
40

50

60

70

ampl



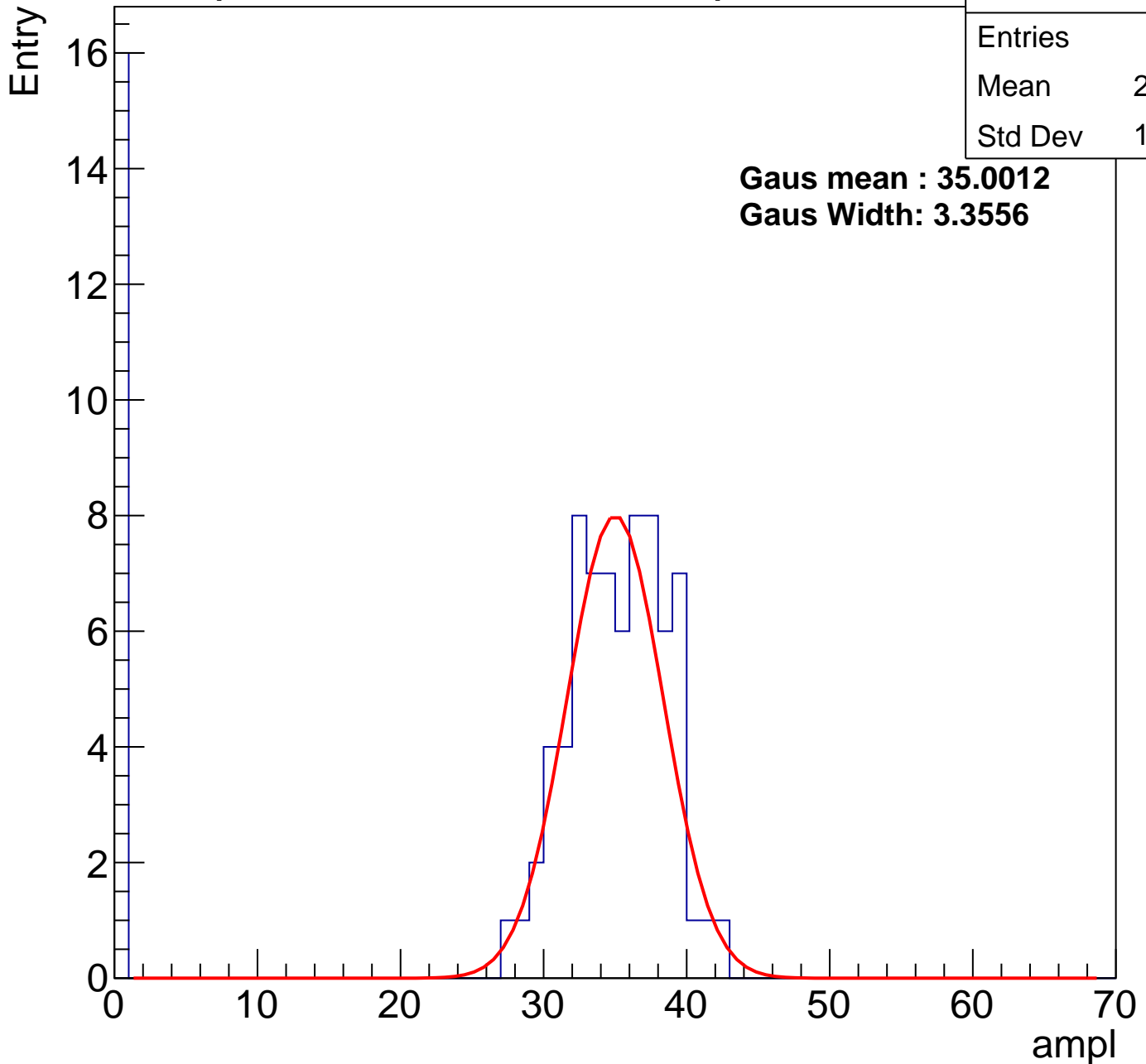
B1L103S, U10-ch105, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	28.41
Std Dev	13.71

Gaus mean : 35.0012

Gaus Width: 3.3556



B1L103S, U10-ch105, adc2

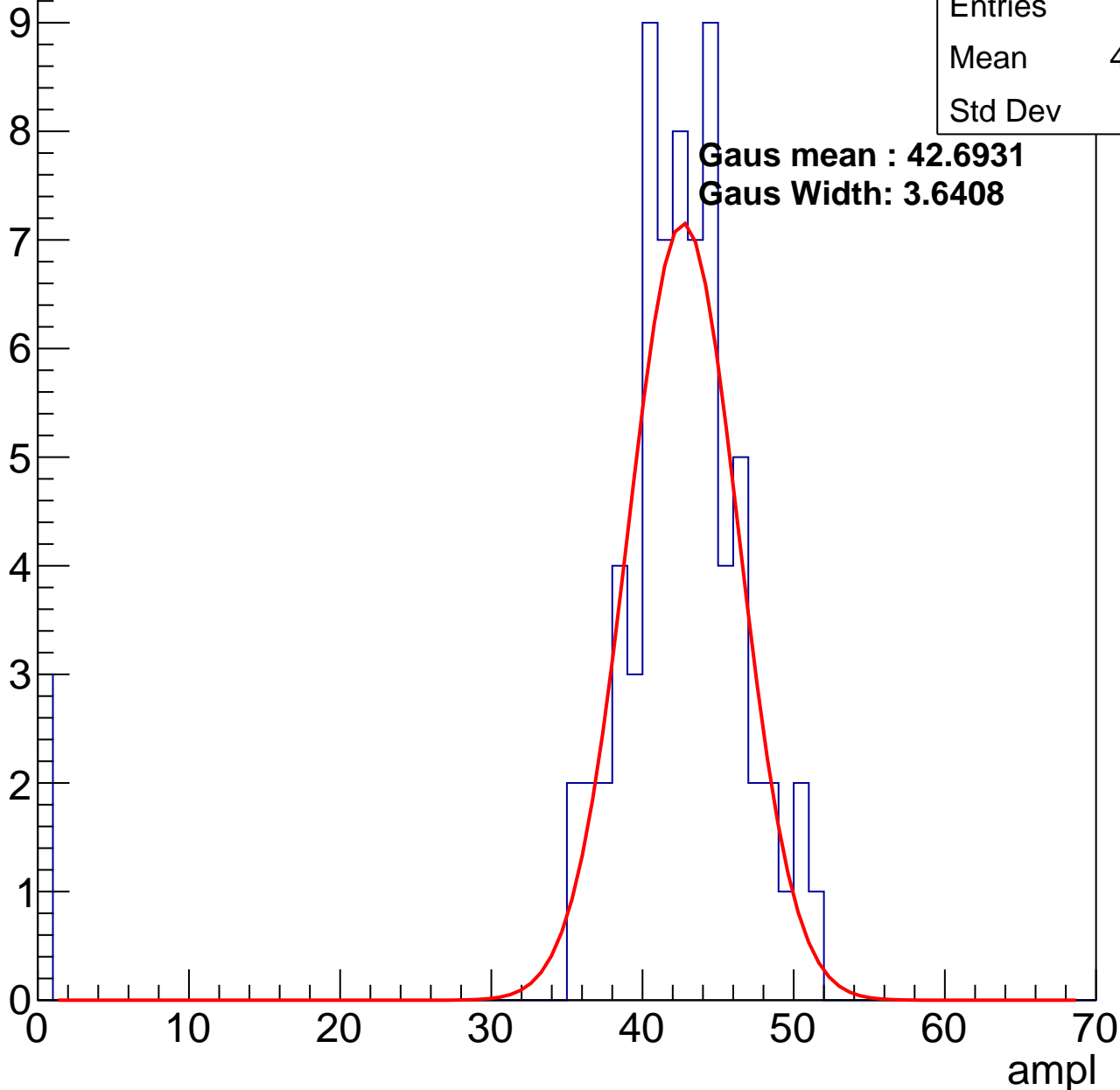
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.62
Std Dev	9.1

Gaus mean : 42.6931

Gaus Width: 3.6408

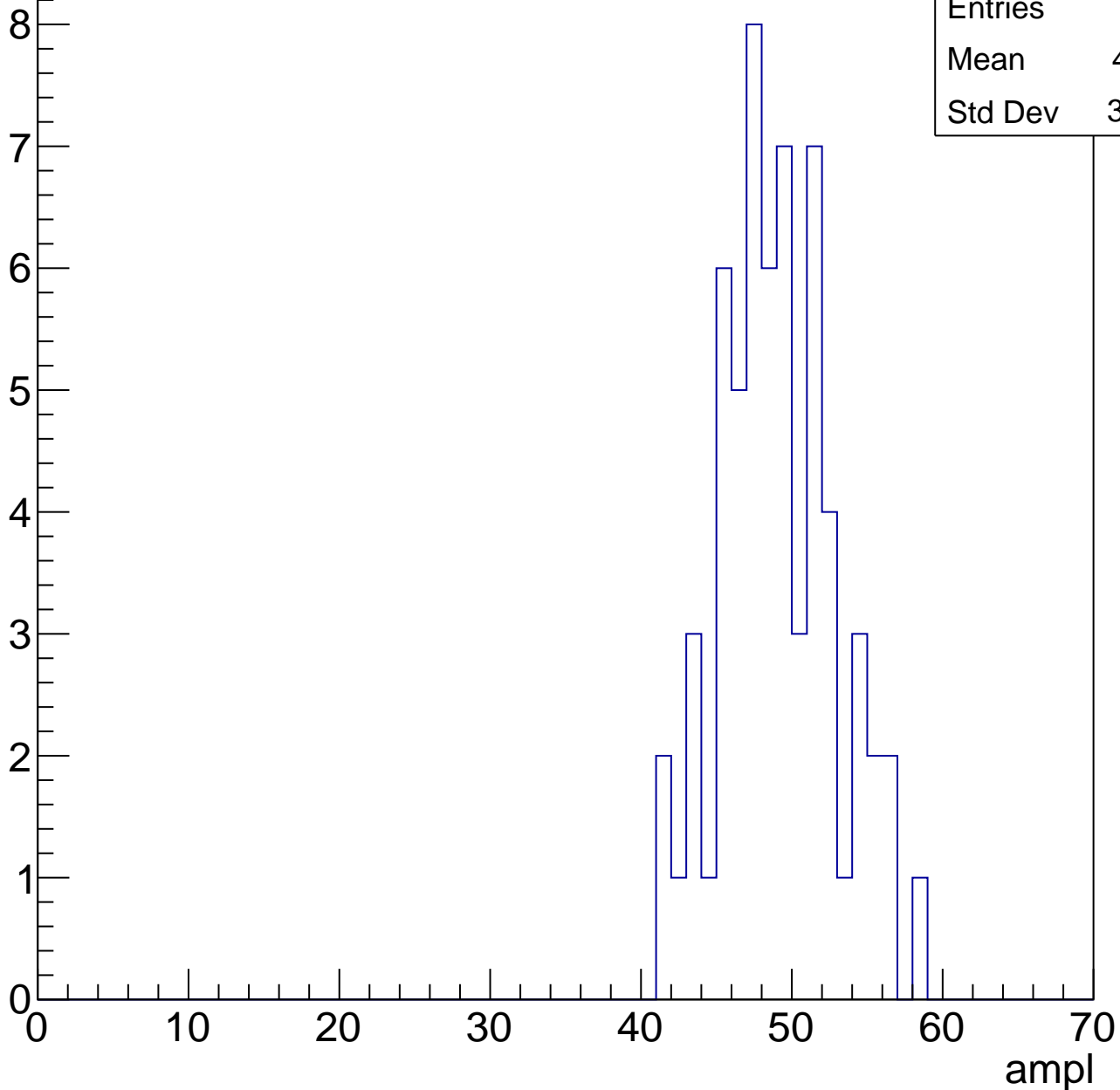


B1L103S, U10-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.61
Std Dev	3.786

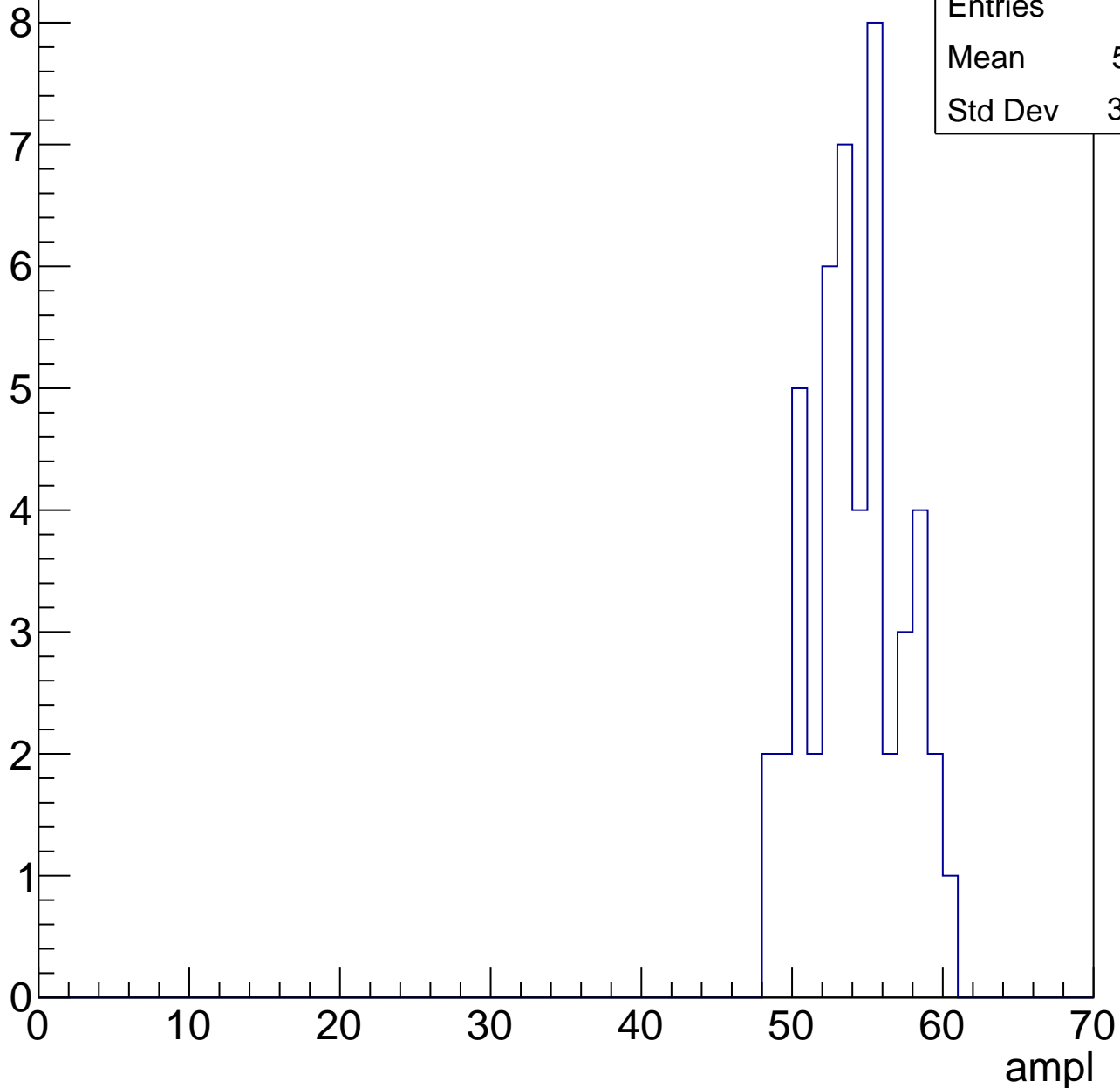


B1L103S, U10-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.71
Std Dev	3.027



B1L103S, U10-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	54
Mean	59
Std Dev	2.618

Entry

10

8

6

4

2

0

0

10

20

30

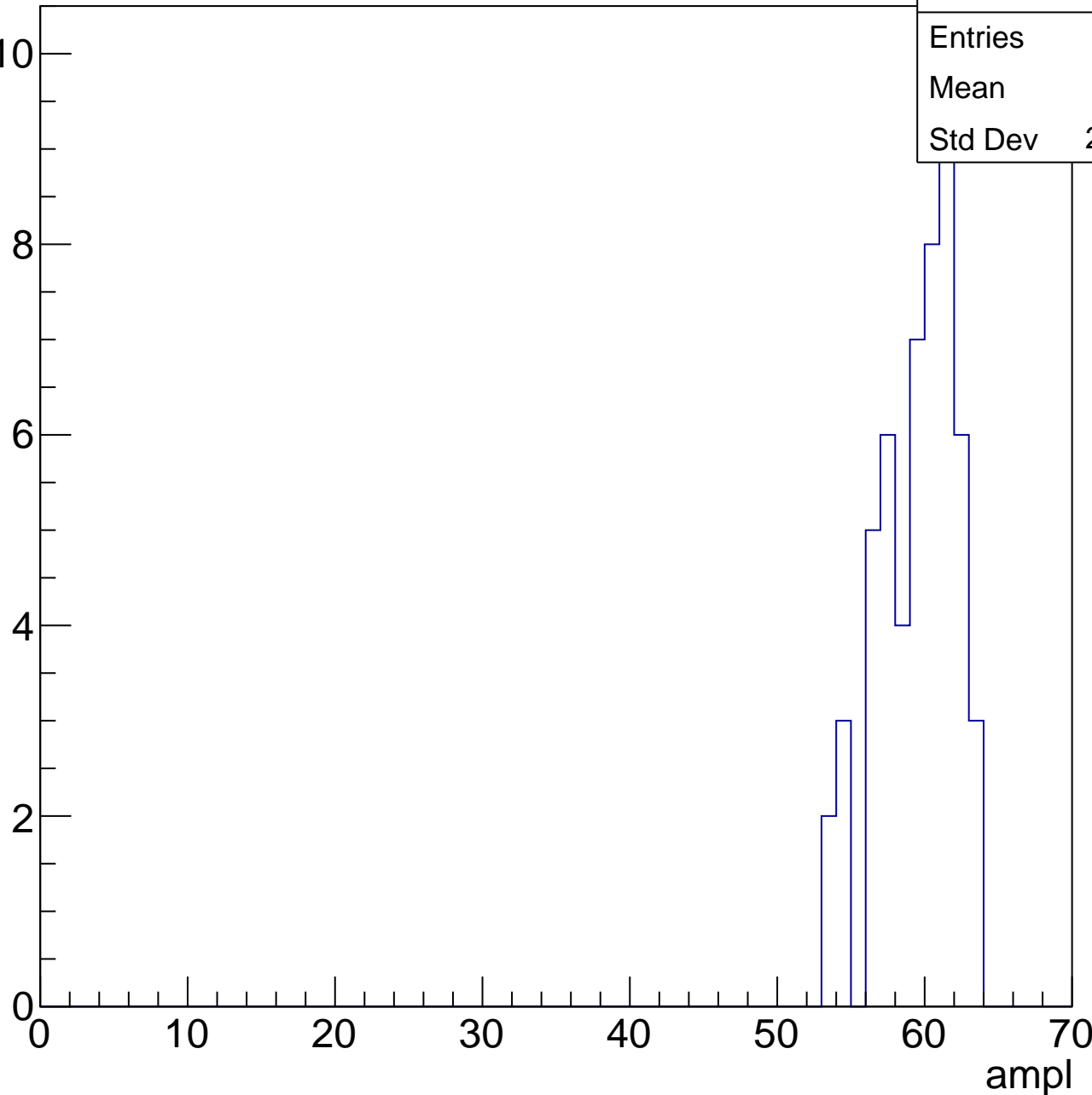
40

50

60

70

ampl

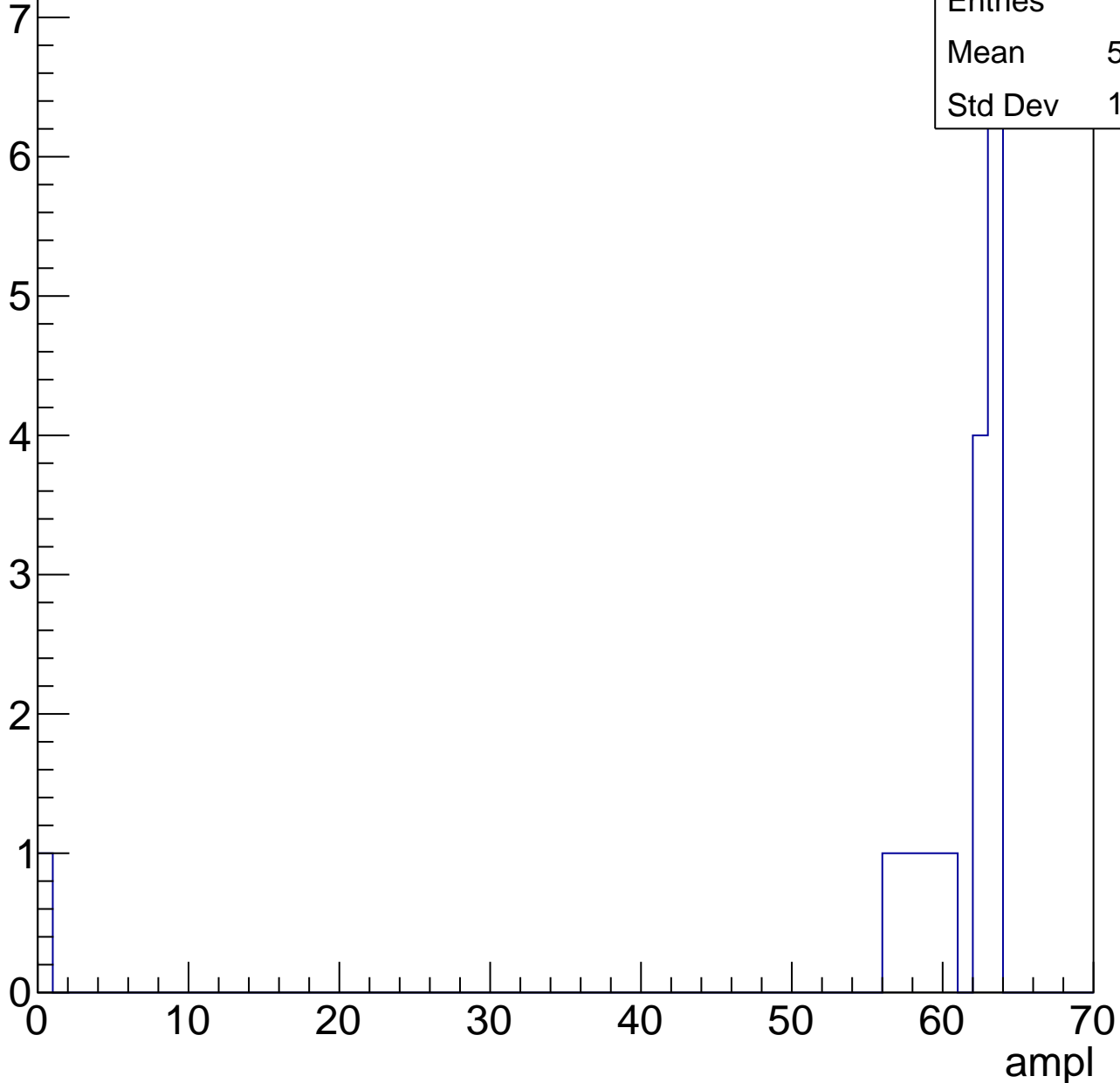


B1L103S, U10-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.59
Std Dev	14.57



B1L103S, U10-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch106, adc0

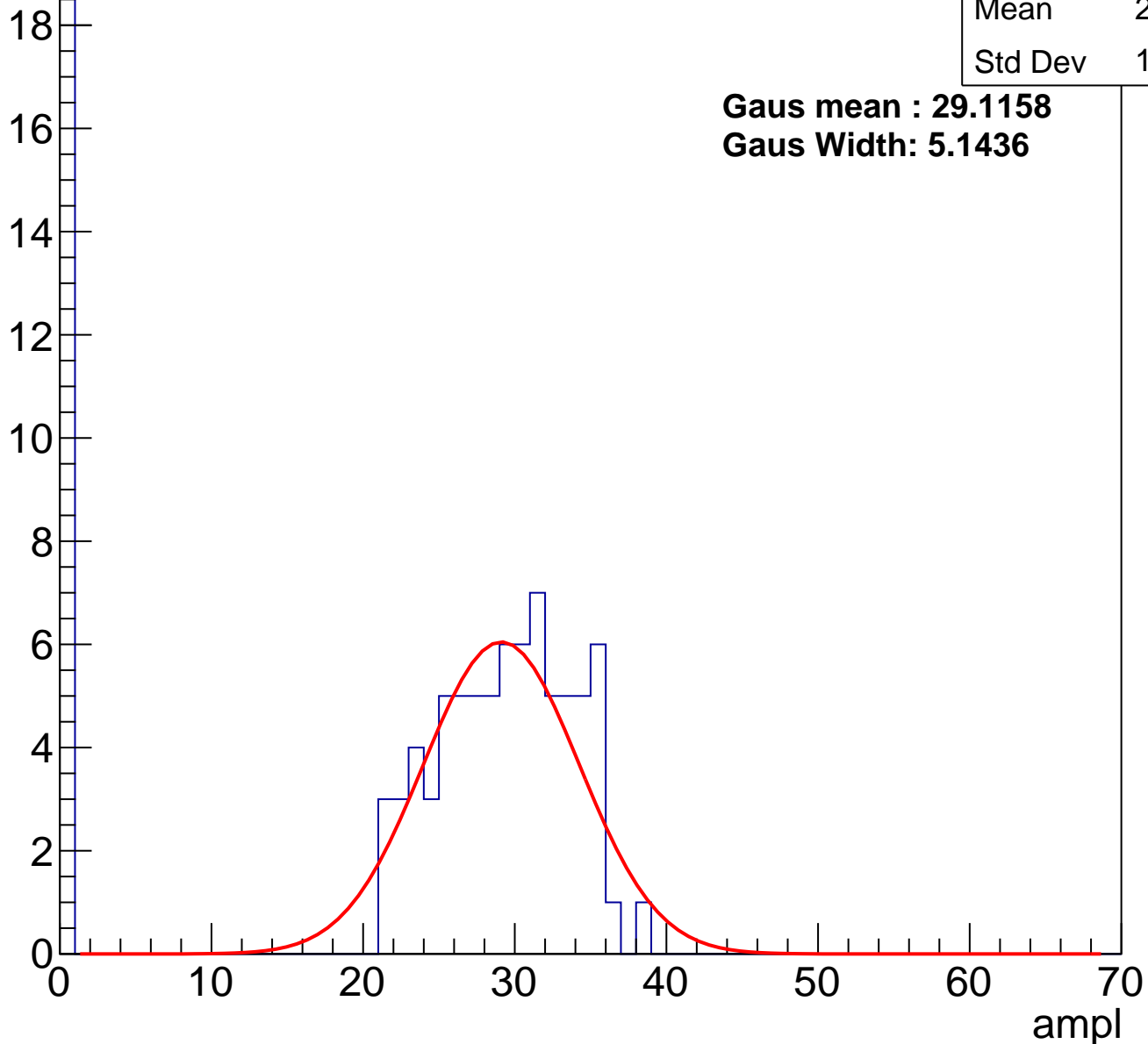
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	23.12
Std Dev	12.23

Gaus mean : 29.1158

Gaus Width: 5.1436

Entry



B1L103S, U10-ch106, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	30.31
Std Dev	13.95

Gaus mean : 37.9477

Gaus Width: 5.0951

Entry

12

10

8

6

4

2

0

0

10

20

30

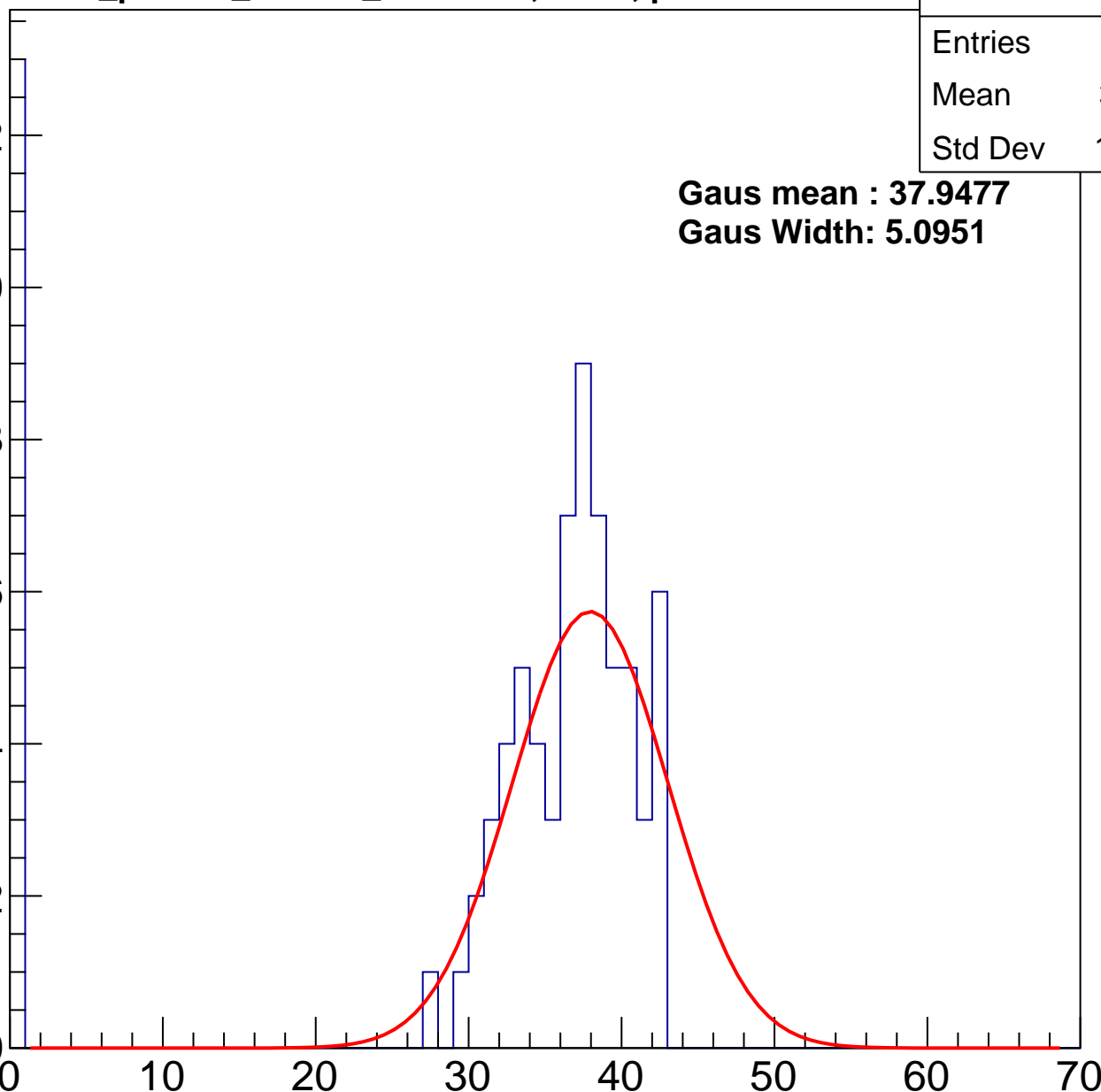
40

50

60

70

ampl



B1L103S, U10-ch106, adc2

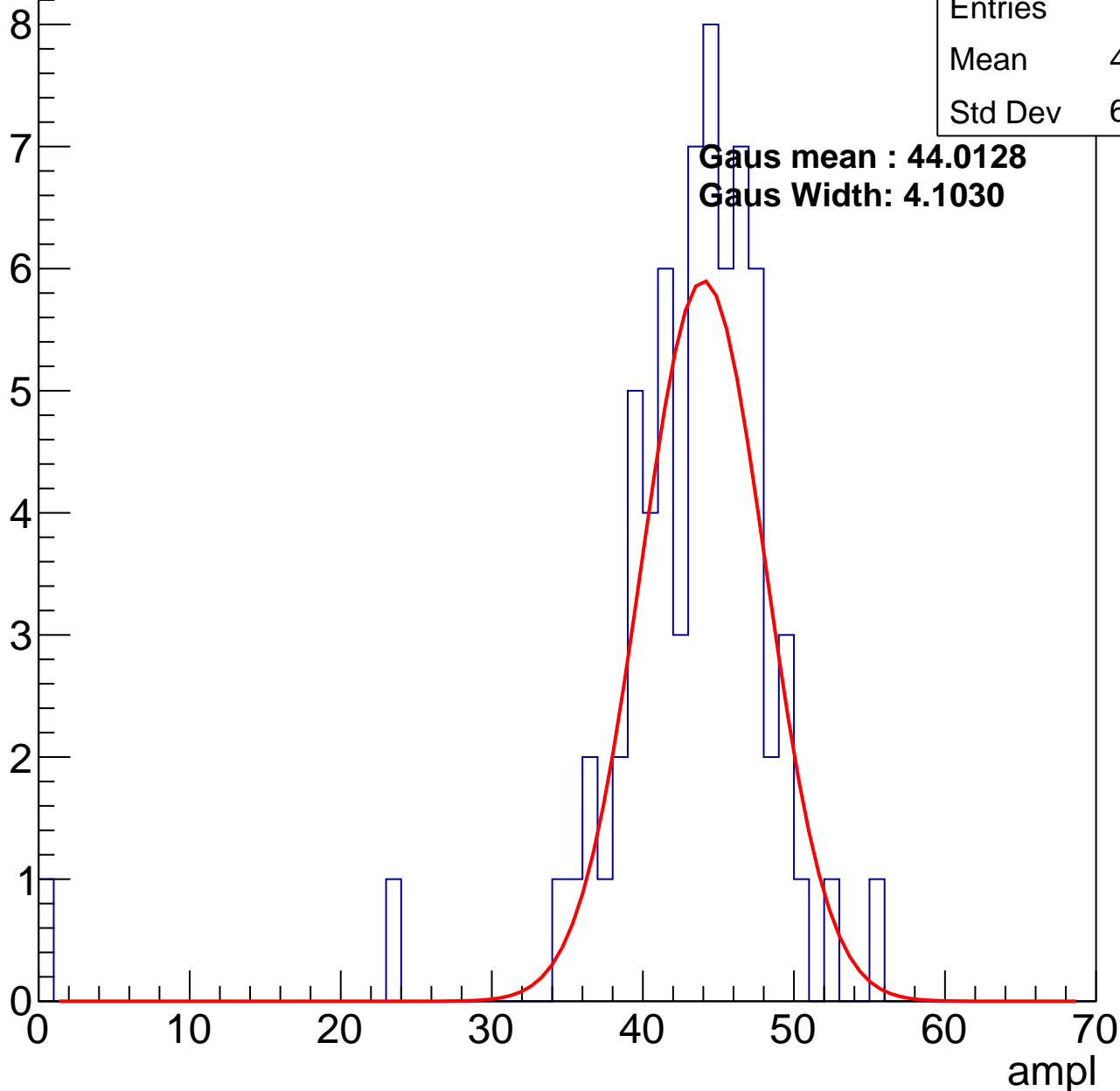
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	42.48
Std Dev	6.942

Gaus mean : 44.0128

Gaus Width: 4.1030

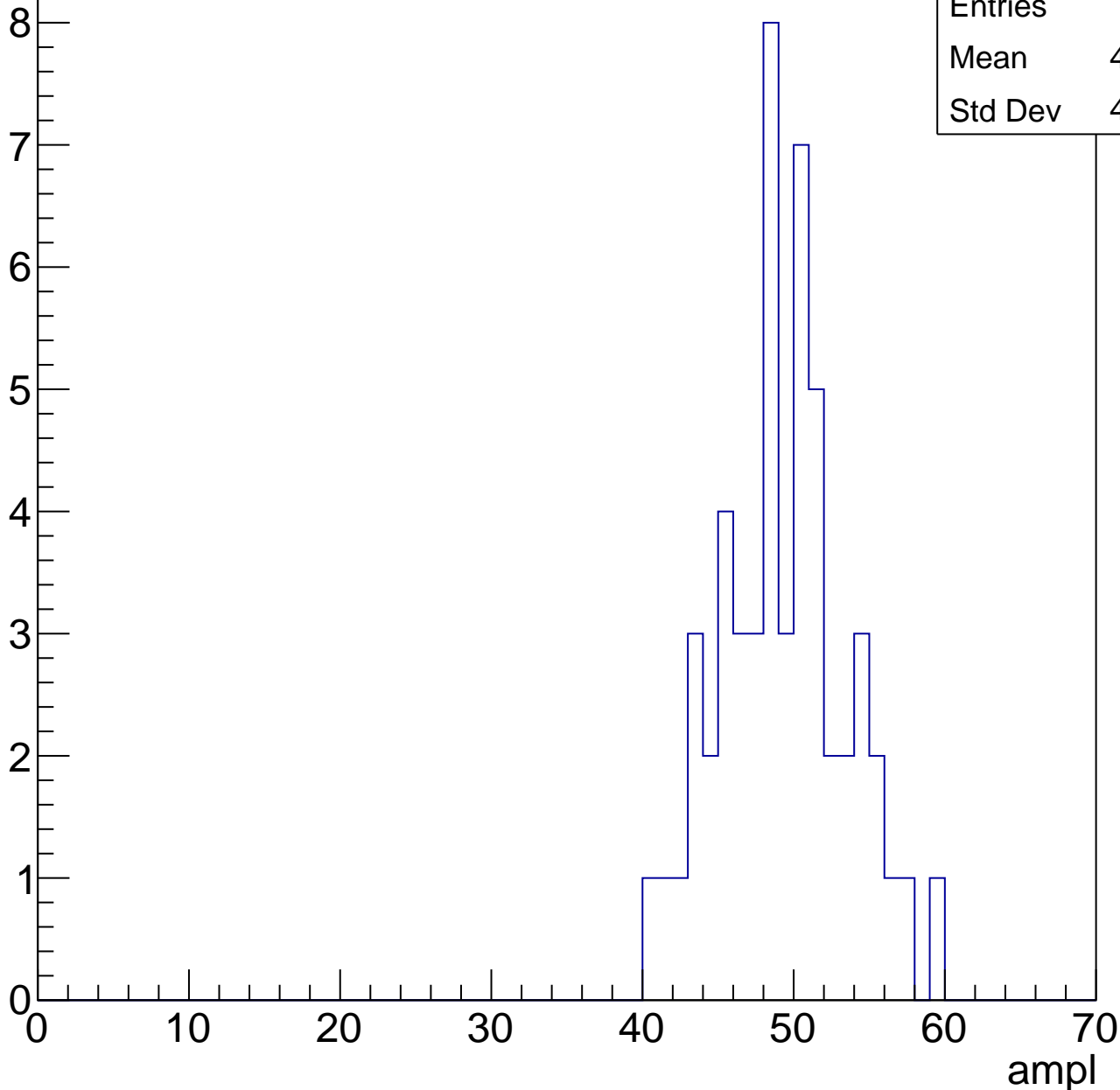


B1L103S, U10-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	48.85
Std Dev	4.118

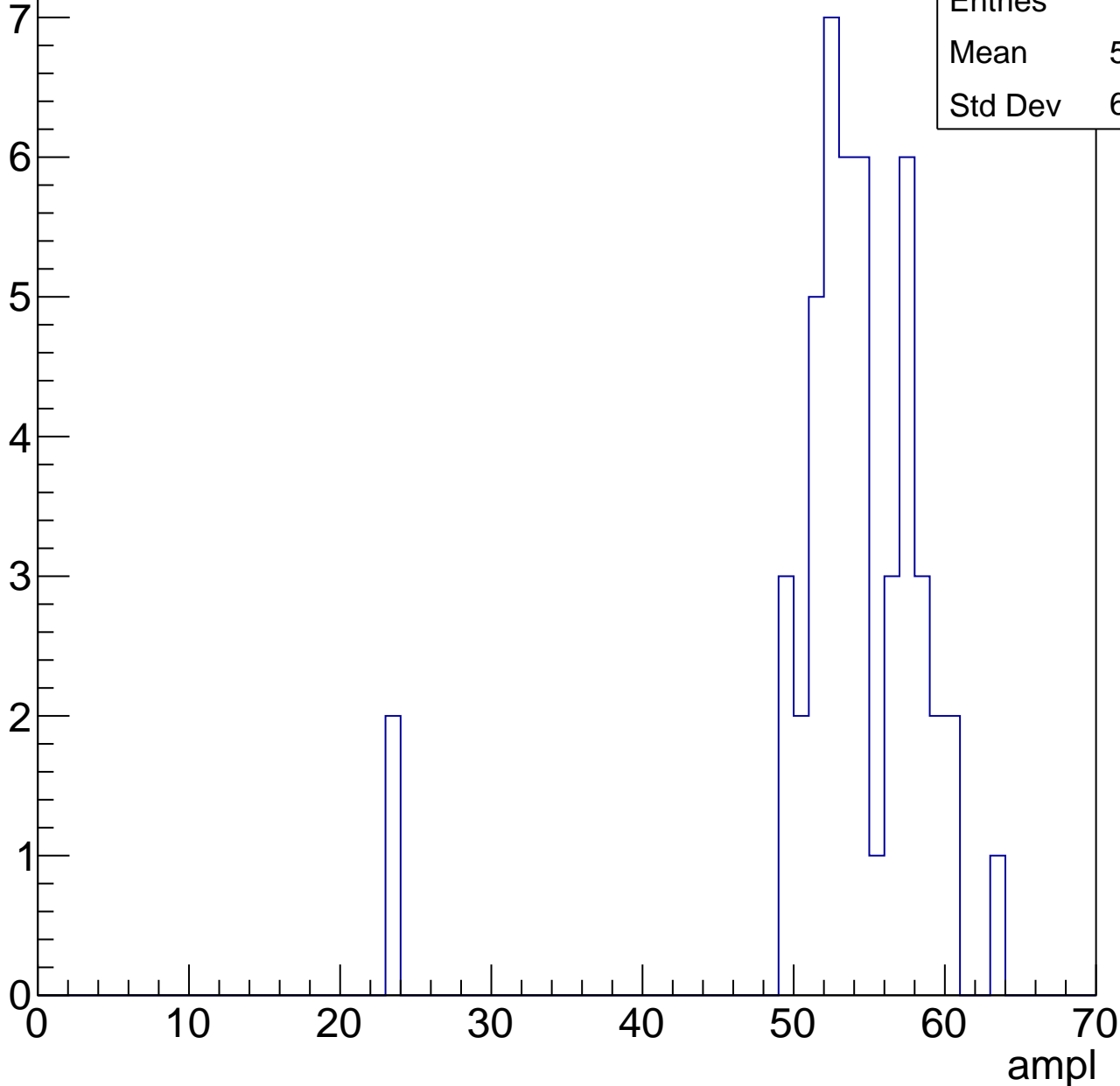


B1L103S, U10-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.94
Std Dev	6.956

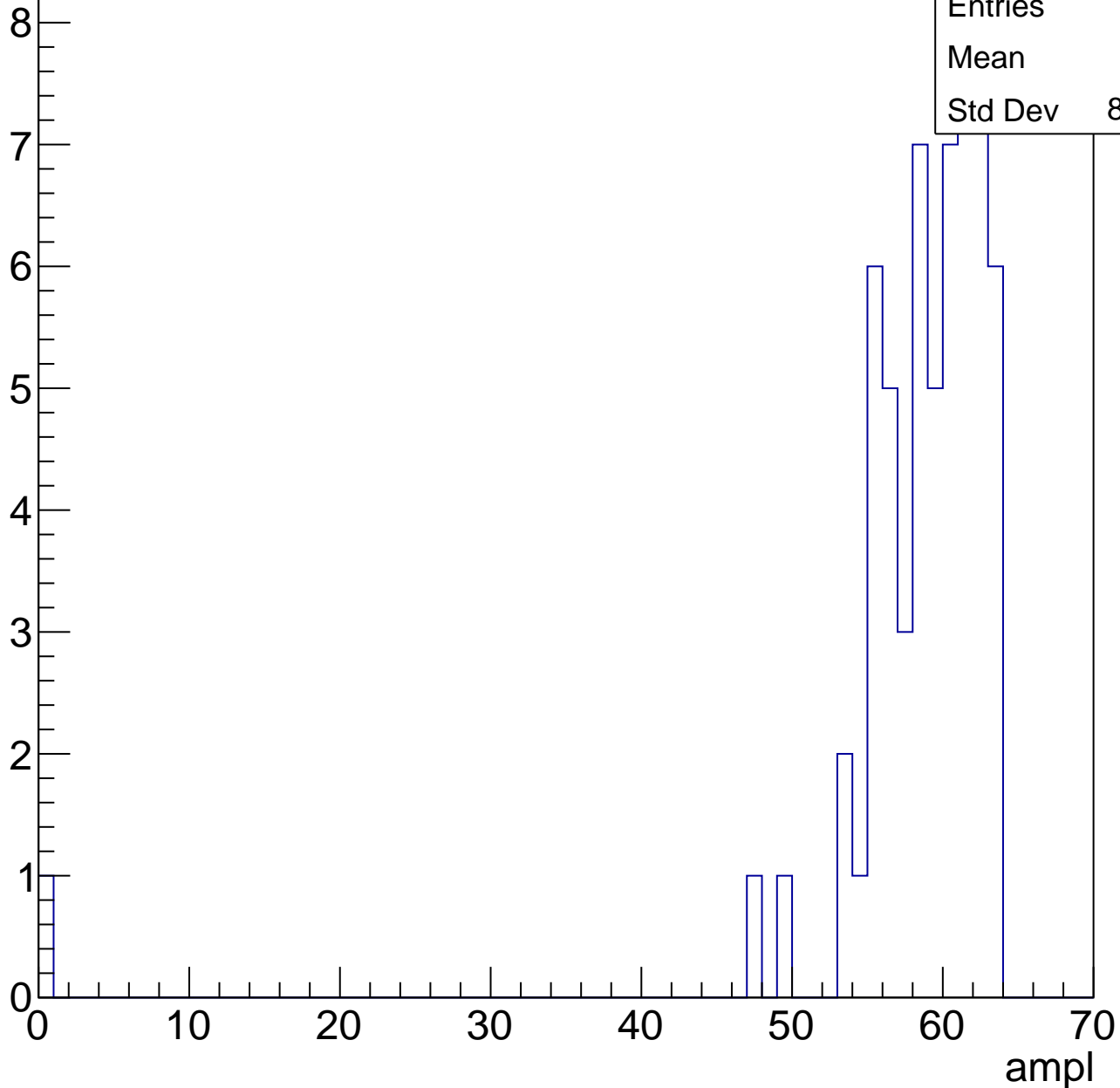


B1L103S, U10-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.7
Std Dev	8.185



B1L103S, U10-ch106, adc6

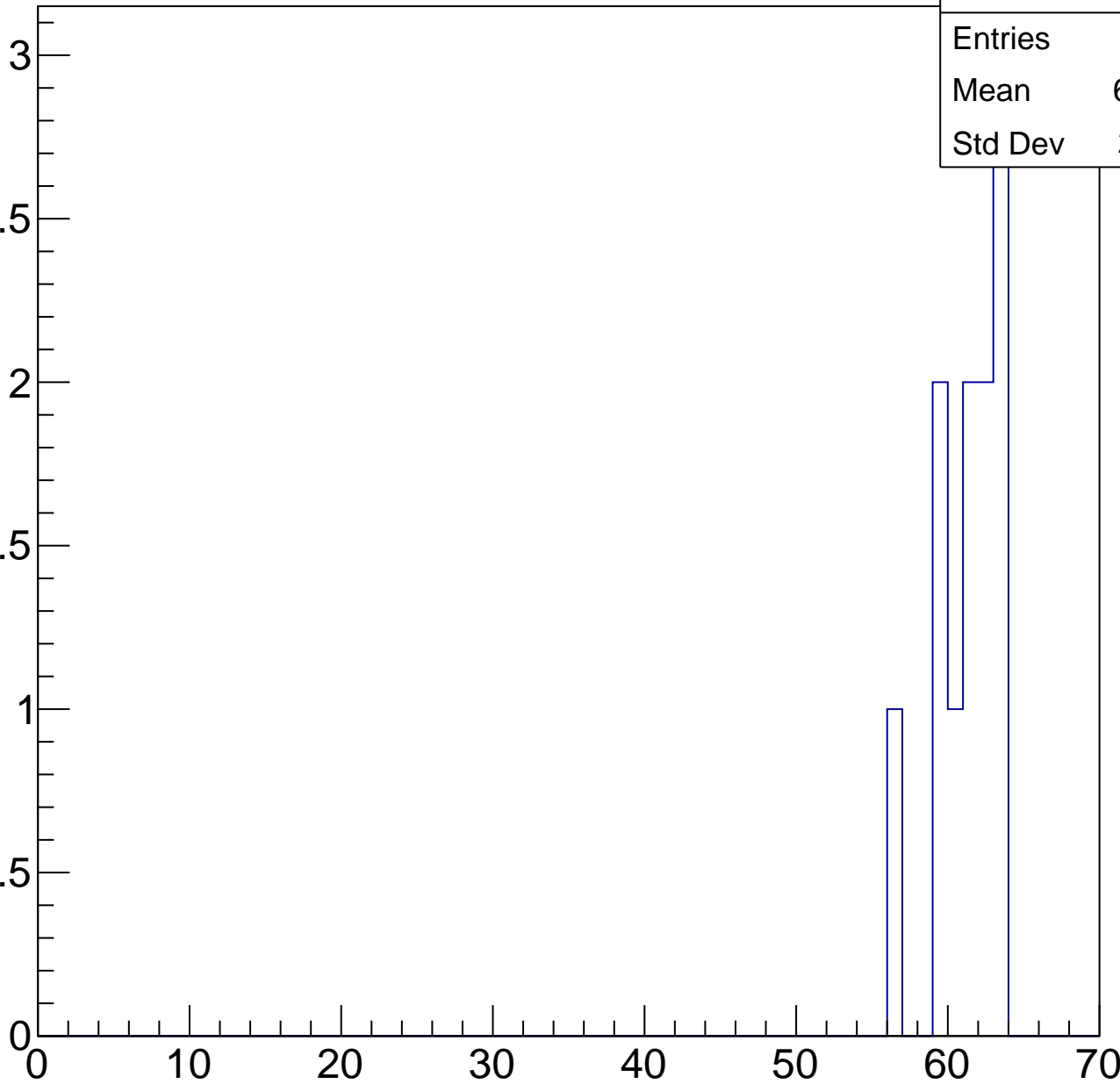
calib_packv5_041523_1651.root, FC#0, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	60.82
Std Dev	2.081

ampl



B1L103S, U10-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch107, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	24.33
Std Dev	9.803

Gaus mean : 28.4340

Gaus Width: 3.7756

Entry

12

10

8

6

4

2

0

0

10

20

30

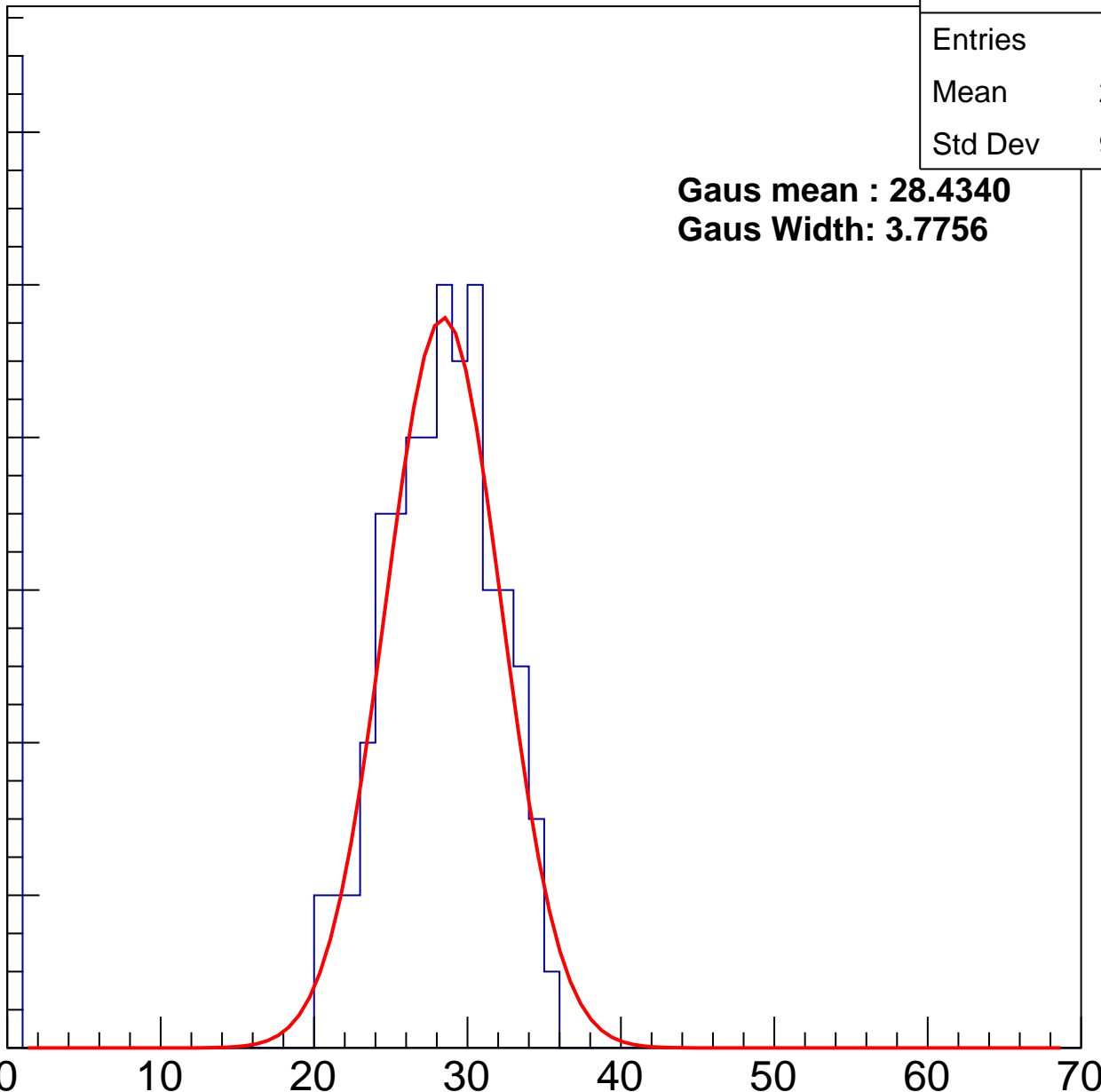
40

50

60

70

ampl



B1L103S, U10-ch107, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

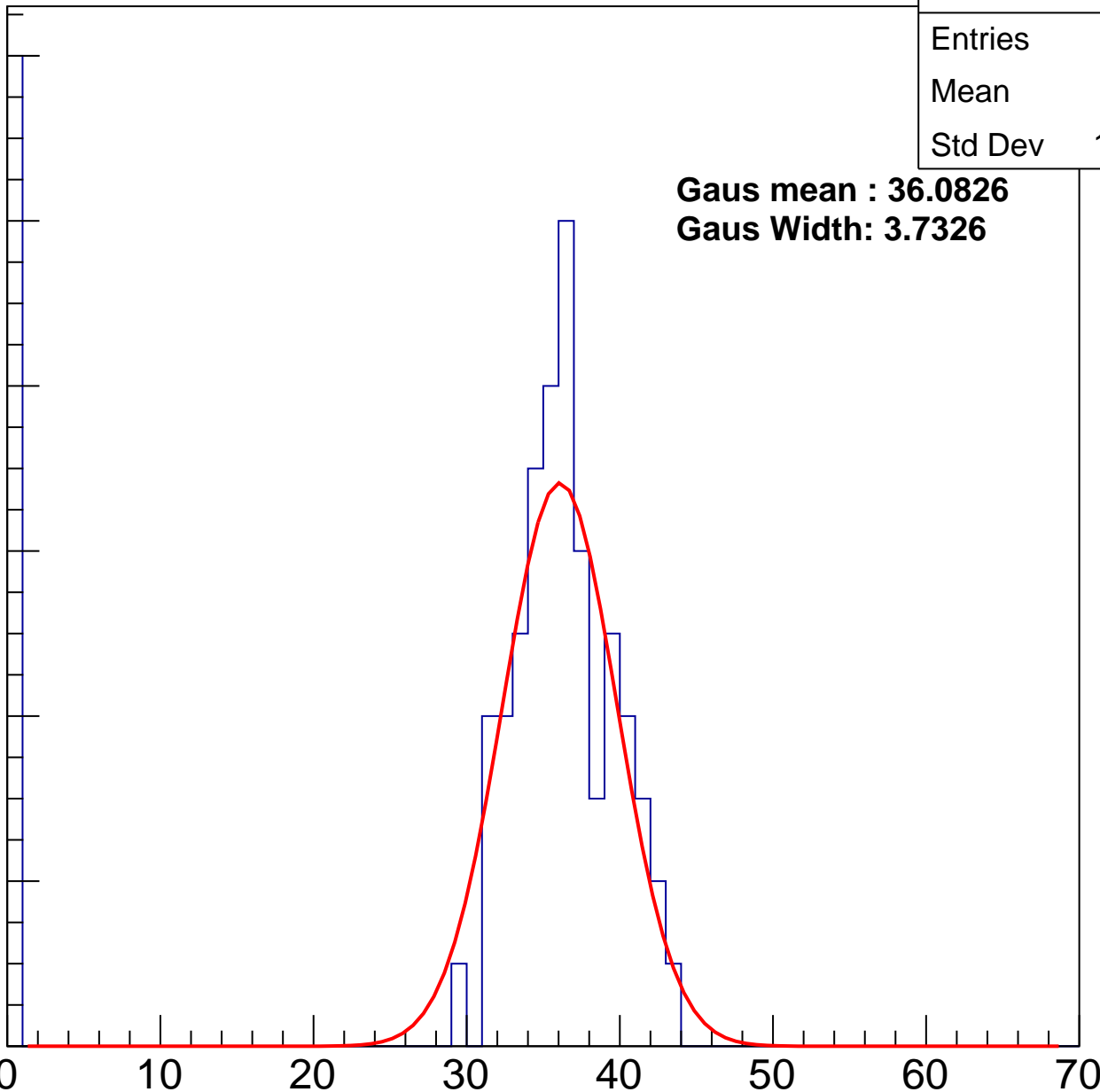
12
10
8
6
4
2
0

Entries	75
Mean	30.2
Std Dev	13.49

Gaus mean : 36.0826

Gaus Width: 3.7326

ampl



B1L103S, U10-ch107, adc2

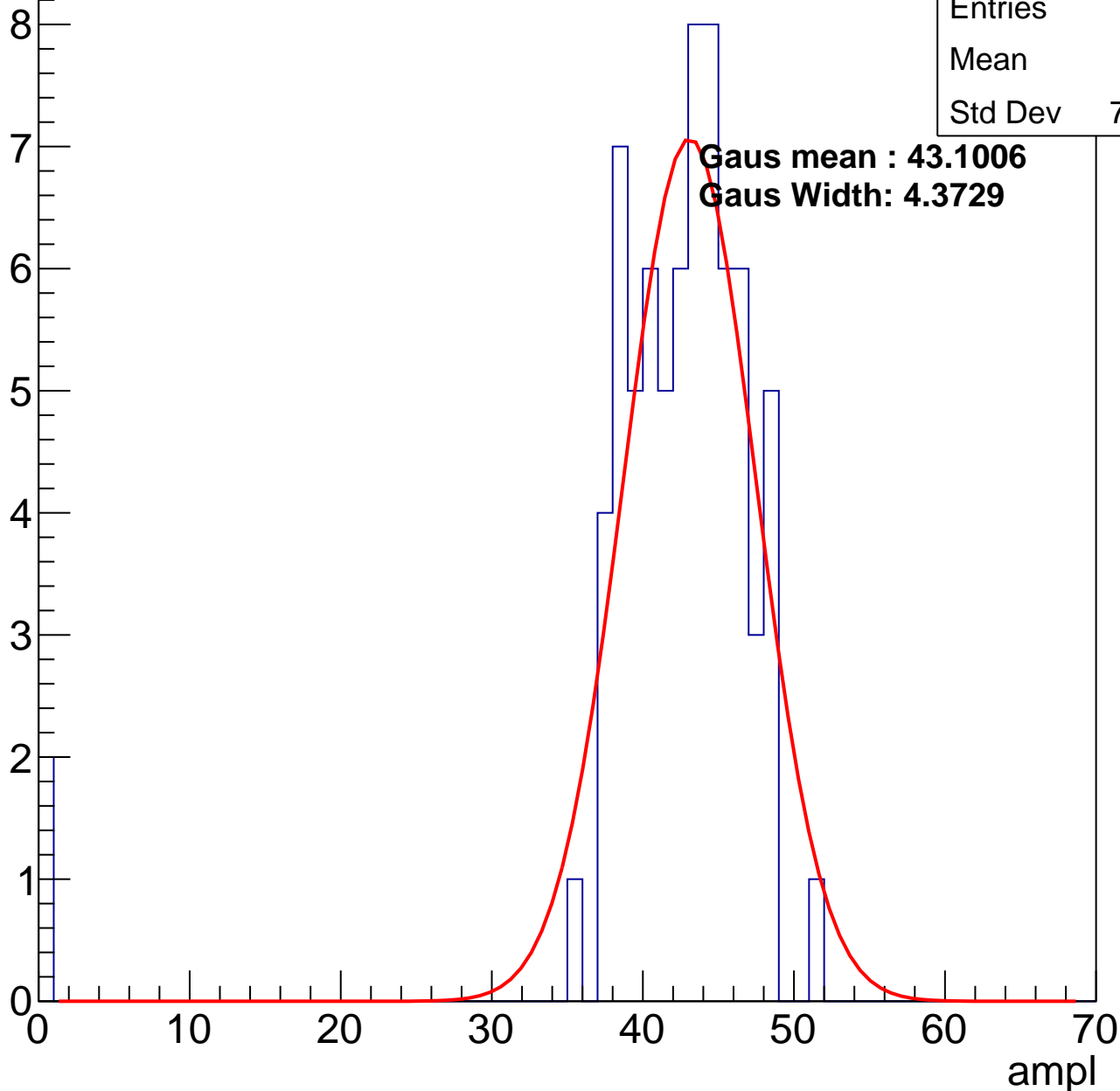
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	41.3
Std Dev	7.722

Gaus mean : 43.1006

Gaus Width: 4.3729

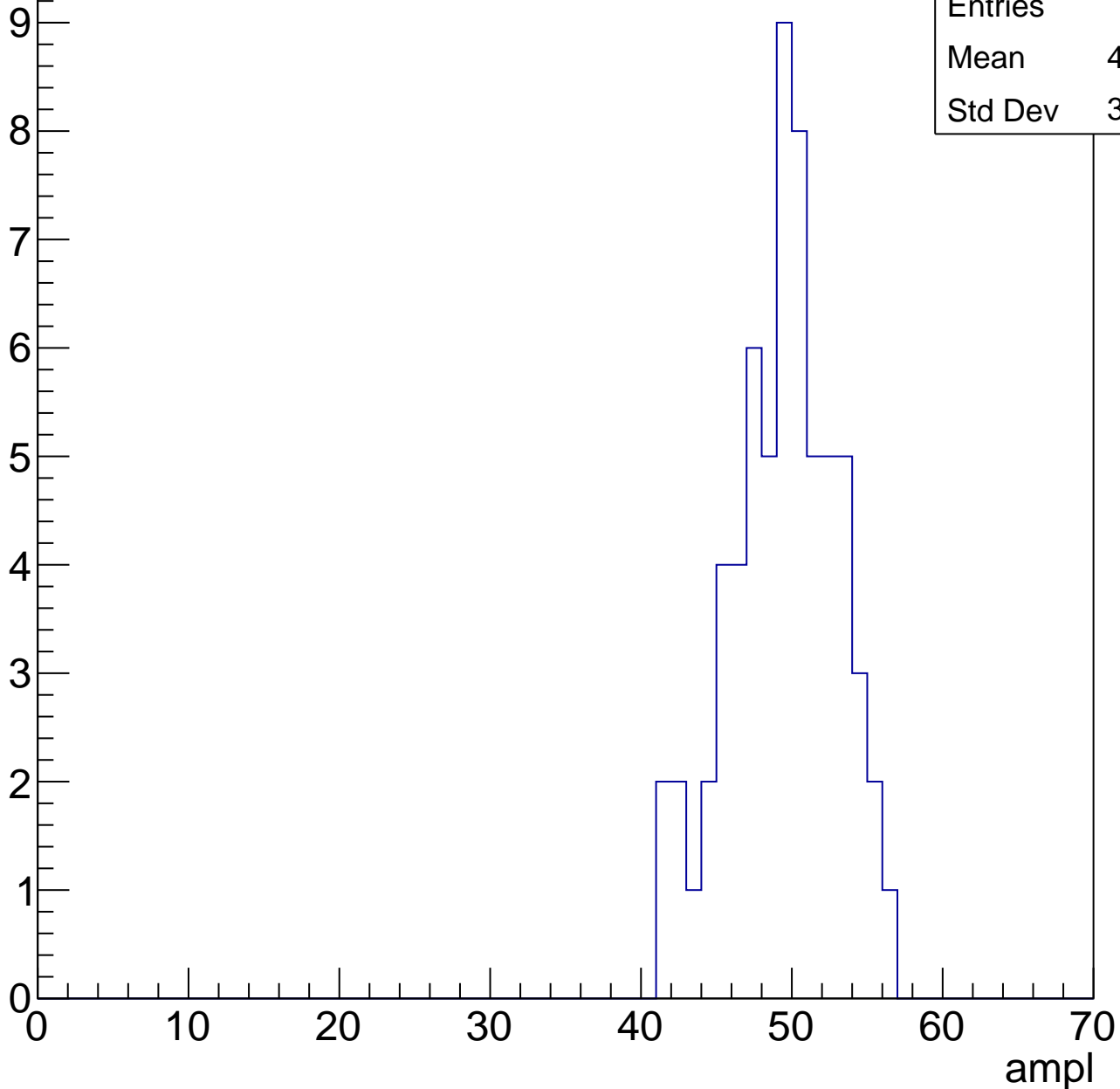


B1L103S, U10-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

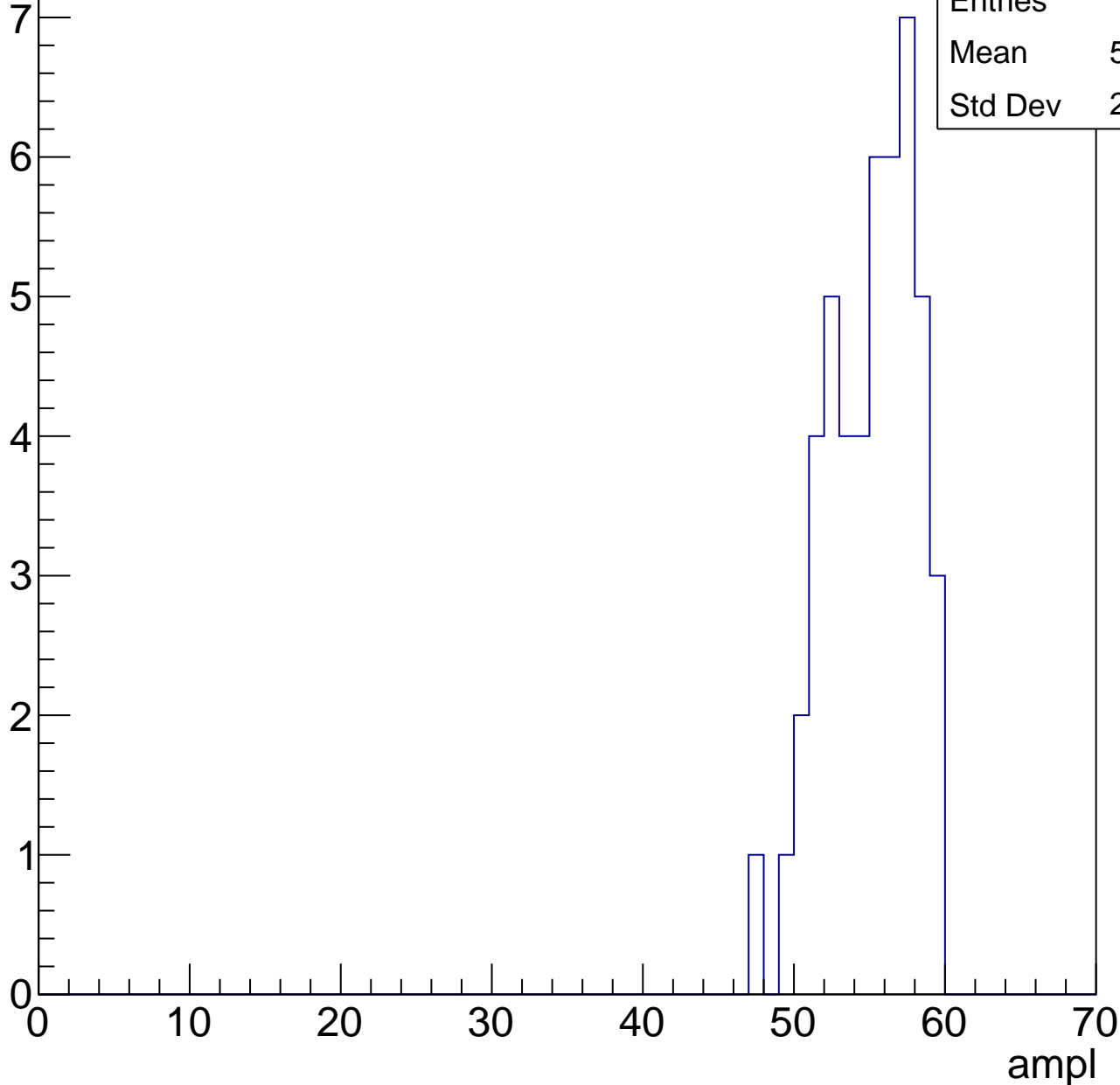
Entries	64
Mean	48.94
Std Dev	3.513



B1L103S, U10-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

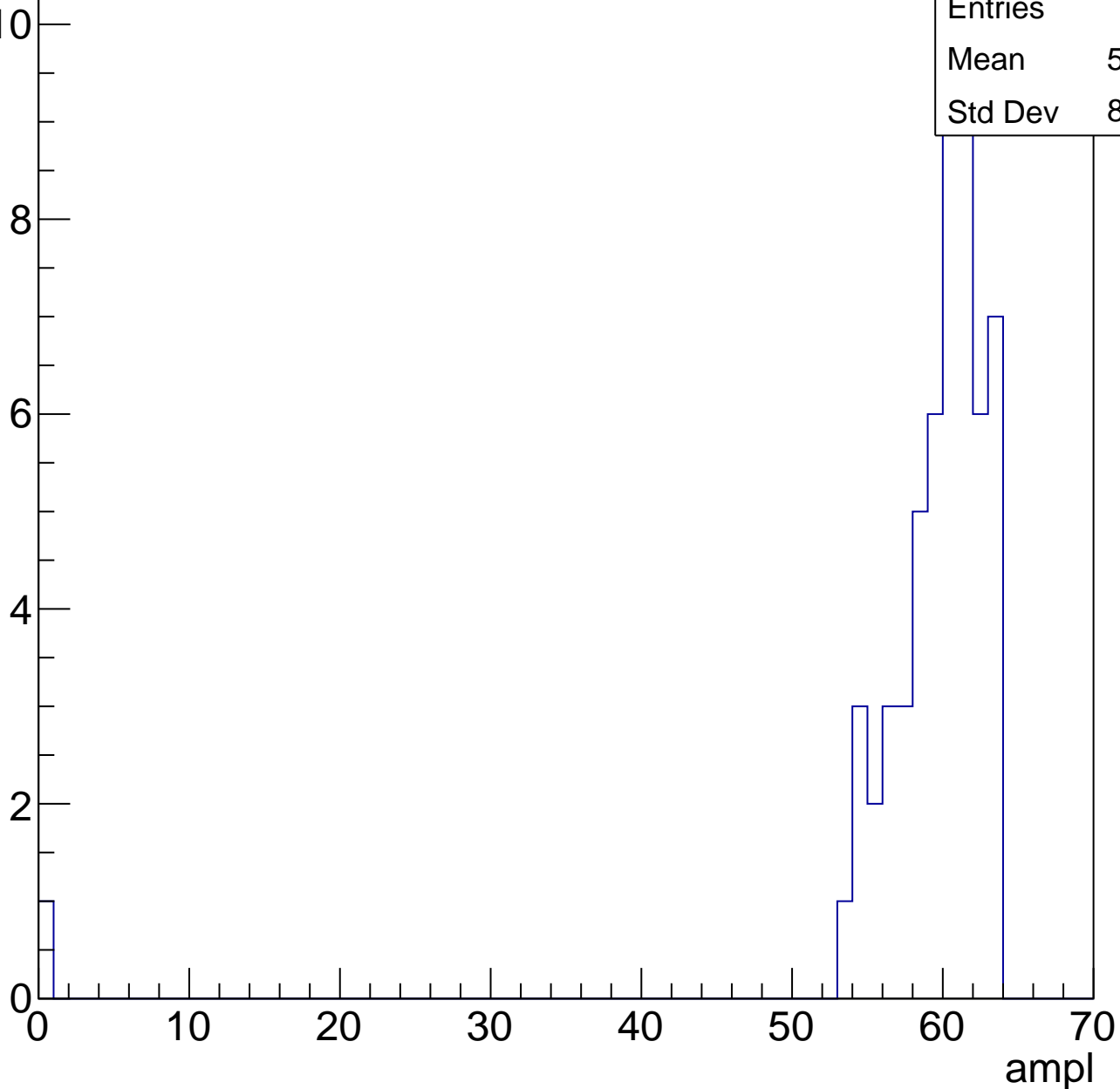


B1L103S, U10-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

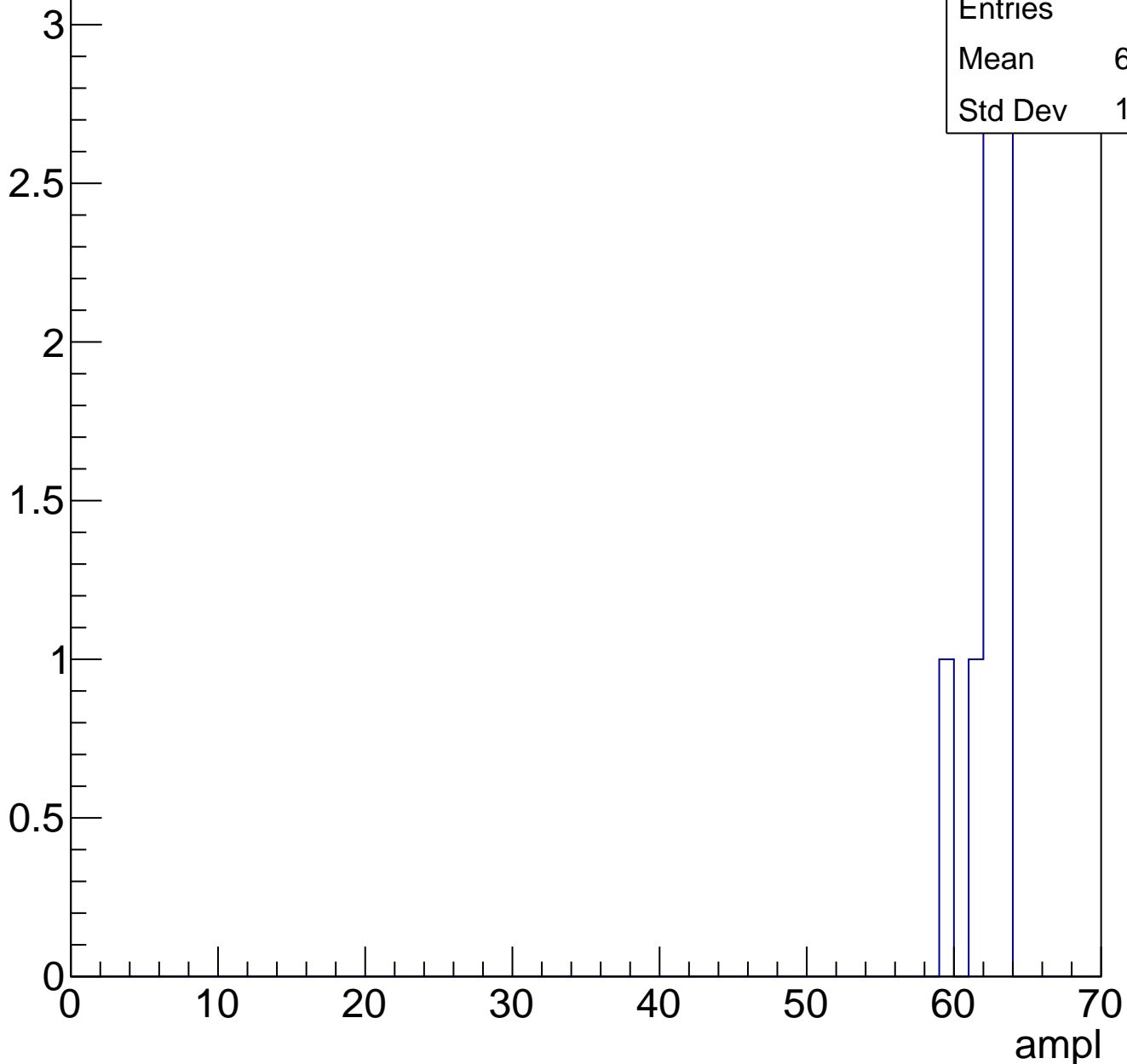
Entries	56
Mean	58.39
Std Dev	8.302



B1L103S, U10-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U10-ch108, adc0

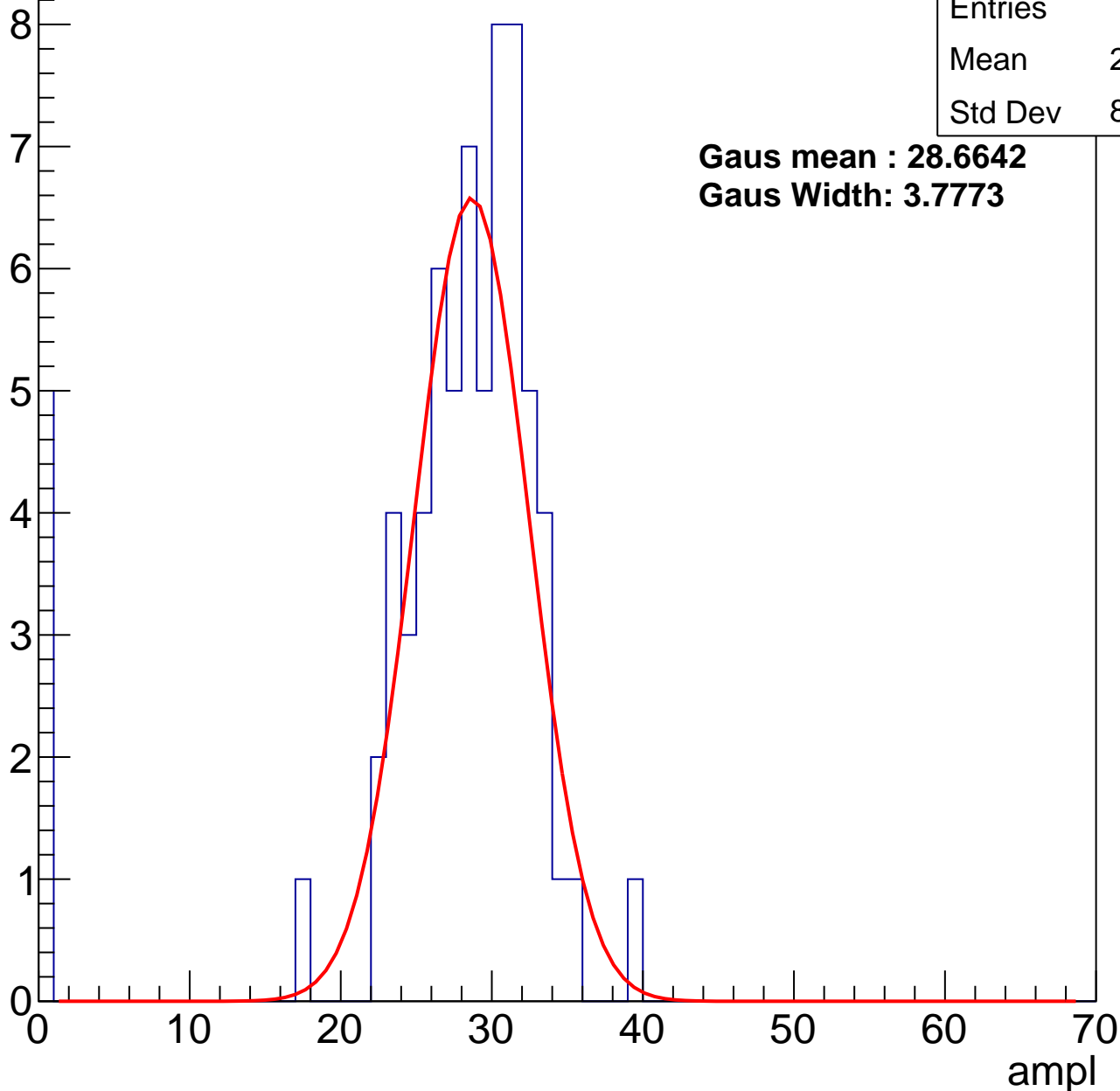
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	26.36
Std Dev	8.135

Gaus mean : 28.6642

Gaus Width: 3.7773



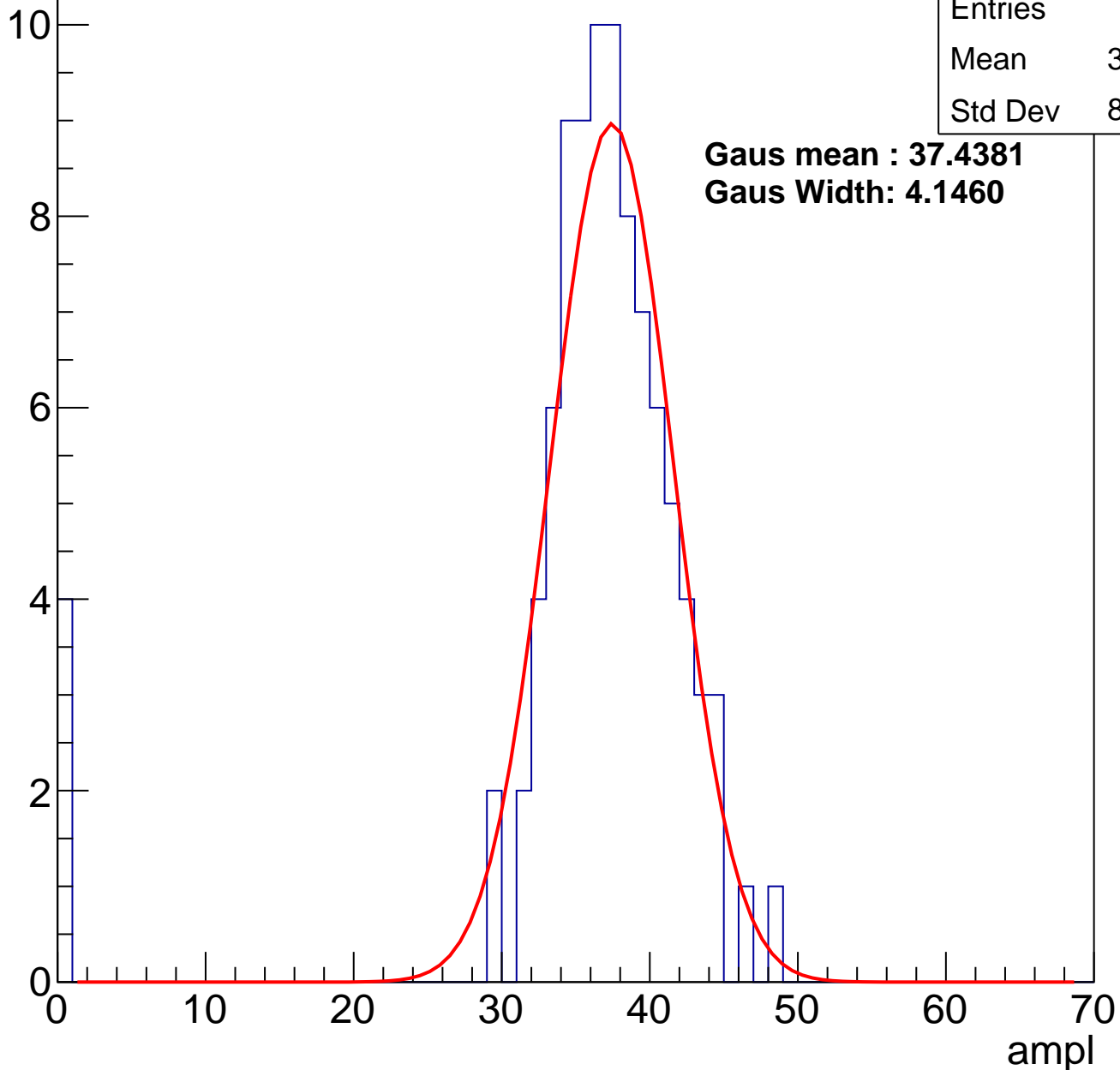
B1L103S, U10-ch108, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	35.55
Std Dev	8.334

Gaus mean : 37.4381
Gaus Width: 4.1460

Entry



B1L103S, U10-ch108, adc2

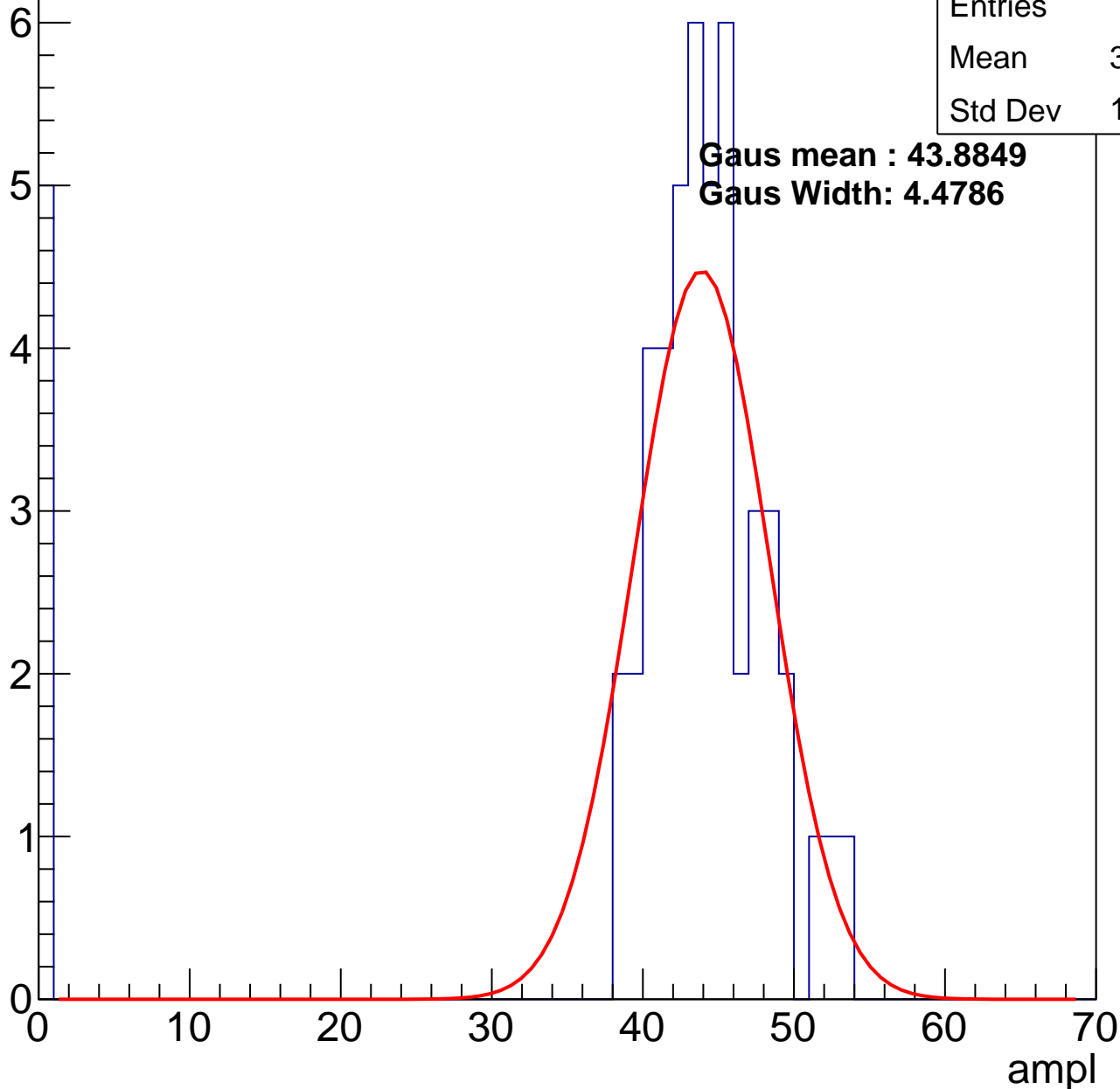
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	39.75
Std Dev	13.39

Gaus mean : 43.8849

Gaus Width: 4.4786

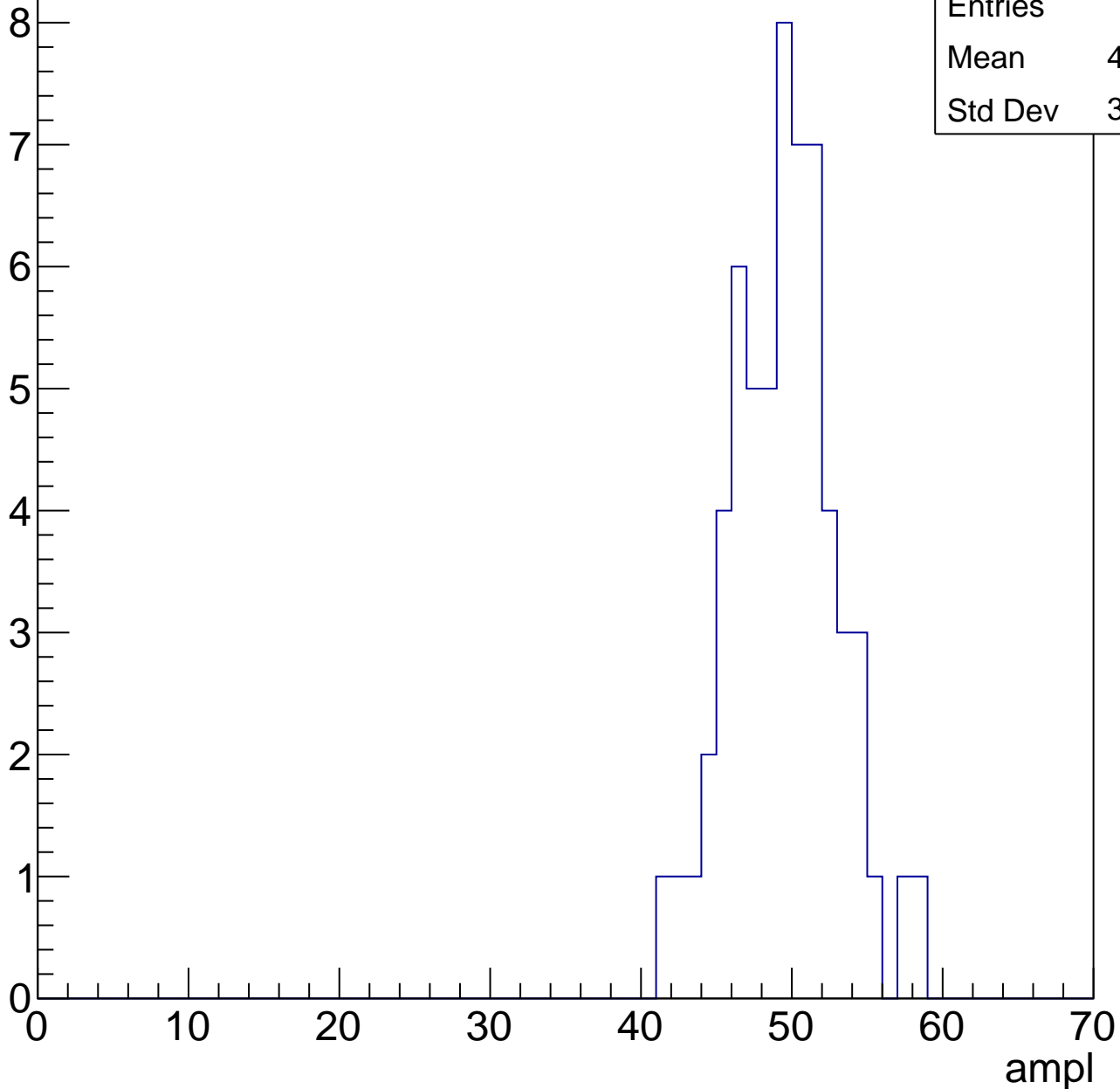


B1L103S, U10-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	49.05
Std Dev	3.457

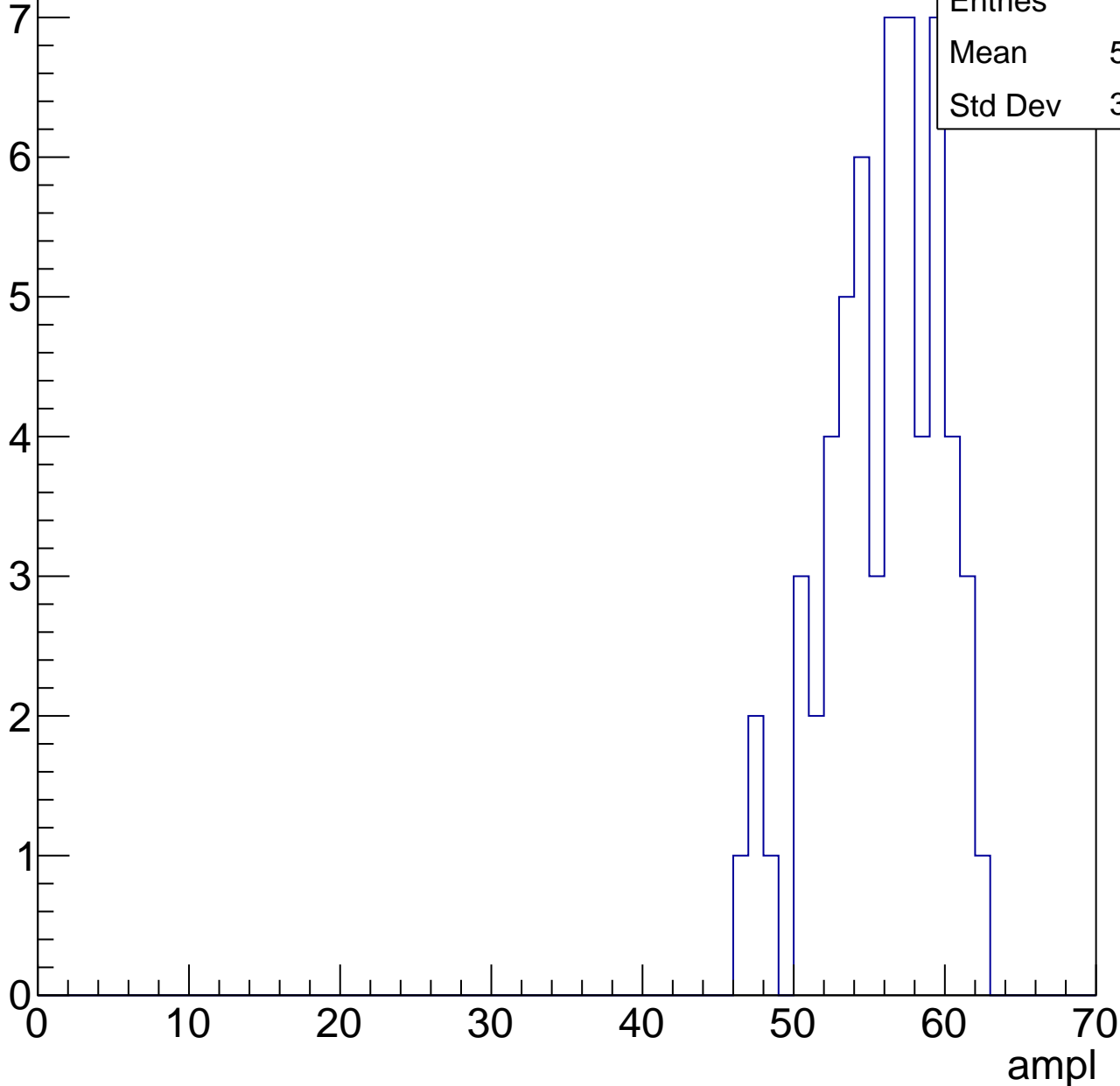


B1L103S, U10-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.38
Std Dev	3.782

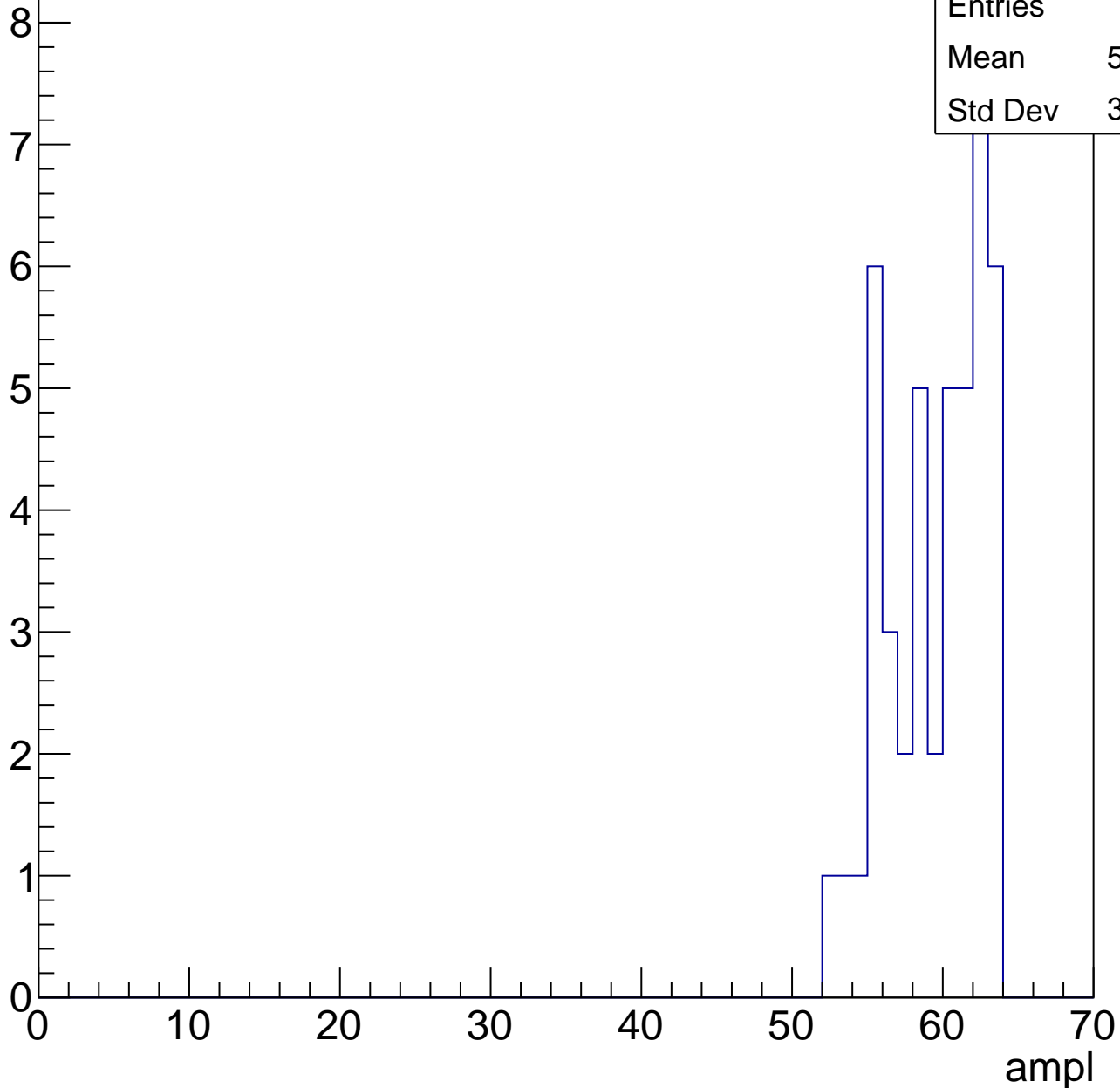


B1L103S, U10-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

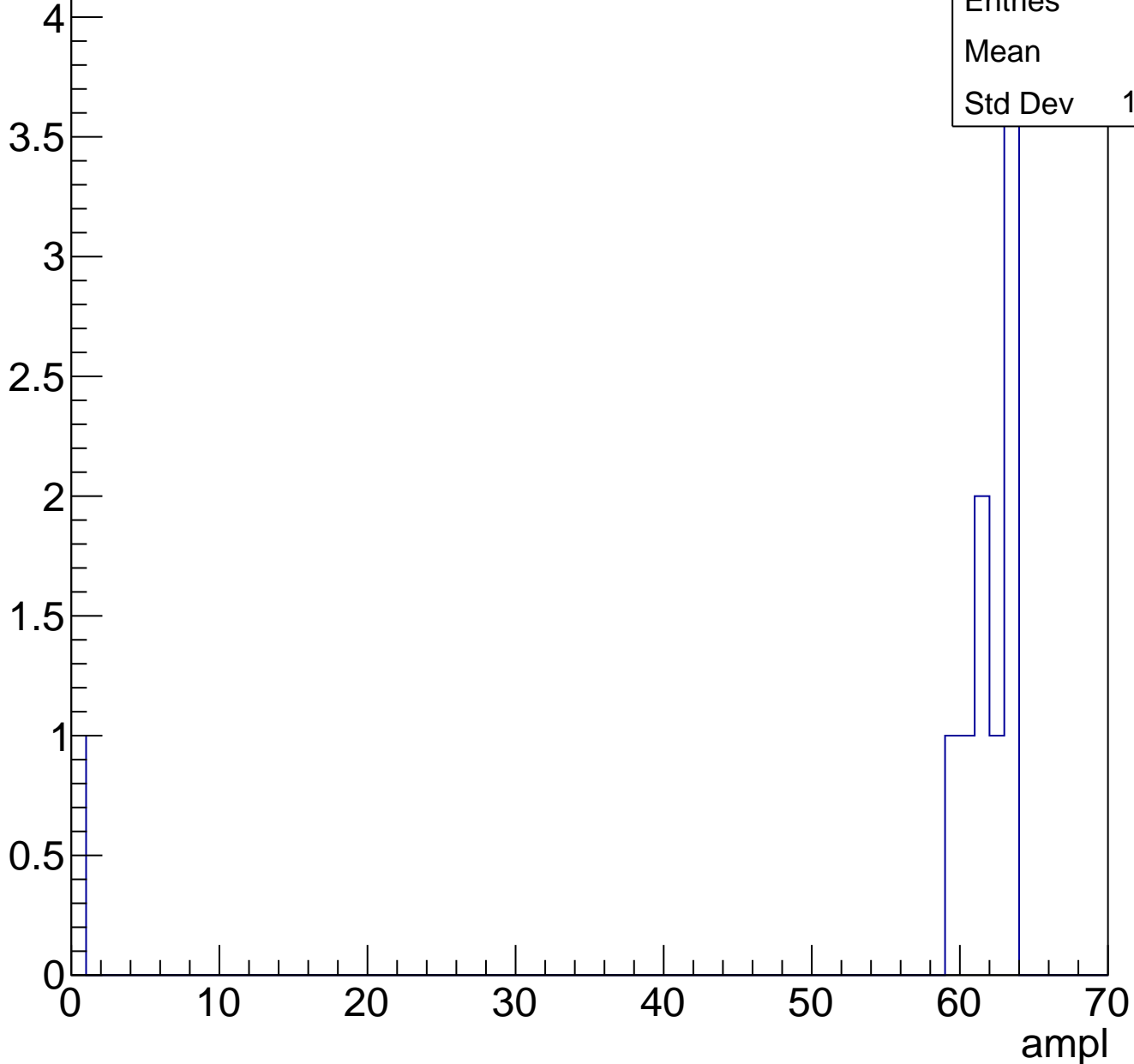
Entries	45
Mean	59.07
Std Dev	3.123



B1L103S, U10-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	10
Mean	55.5
Std Dev	18.55

B1L103S, U10-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch109, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	26.7
Std Dev	11.98

Gaus mean : 32.5471

Gaus Width: 3.9037

Entry

12

10

8

6

4

2

0

0

10

20

30

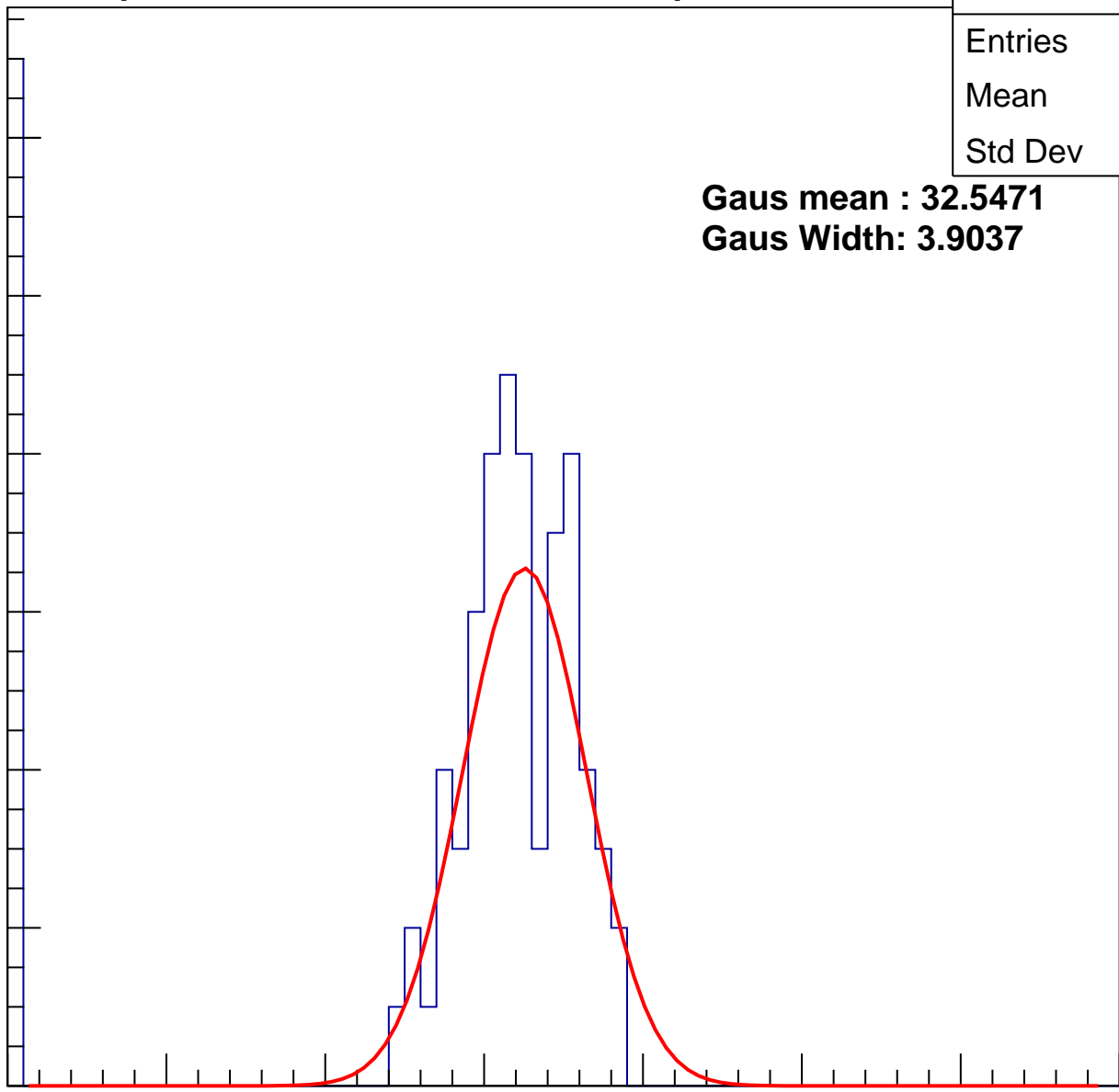
40

50

60

70

ampl



B1L103S, U10-ch109, adc1

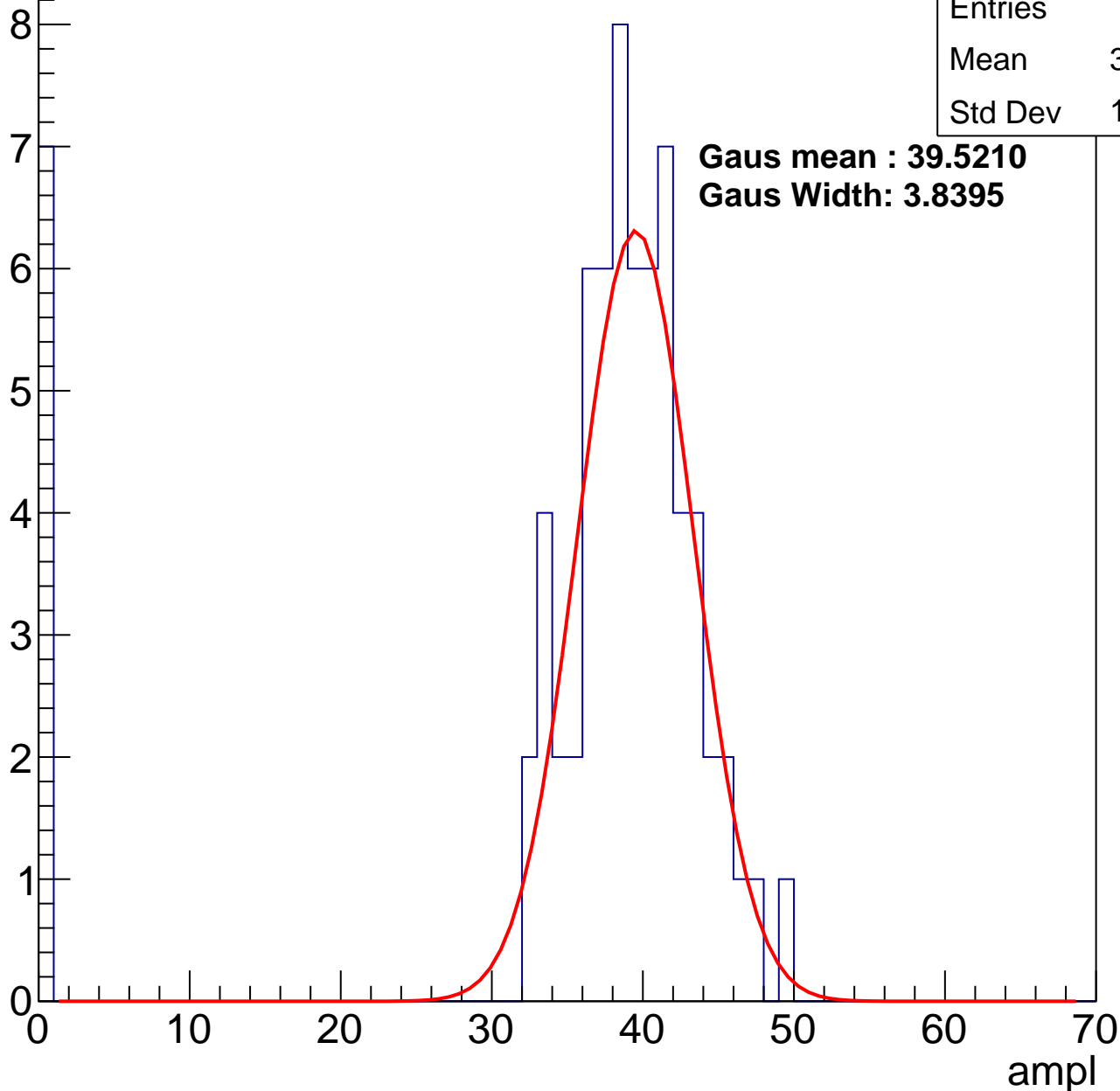
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	35.17
Std Dev	12.15

Gaus mean : 39.5210

Gaus Width: 3.8395



B1L103S, U10-ch109, adc2

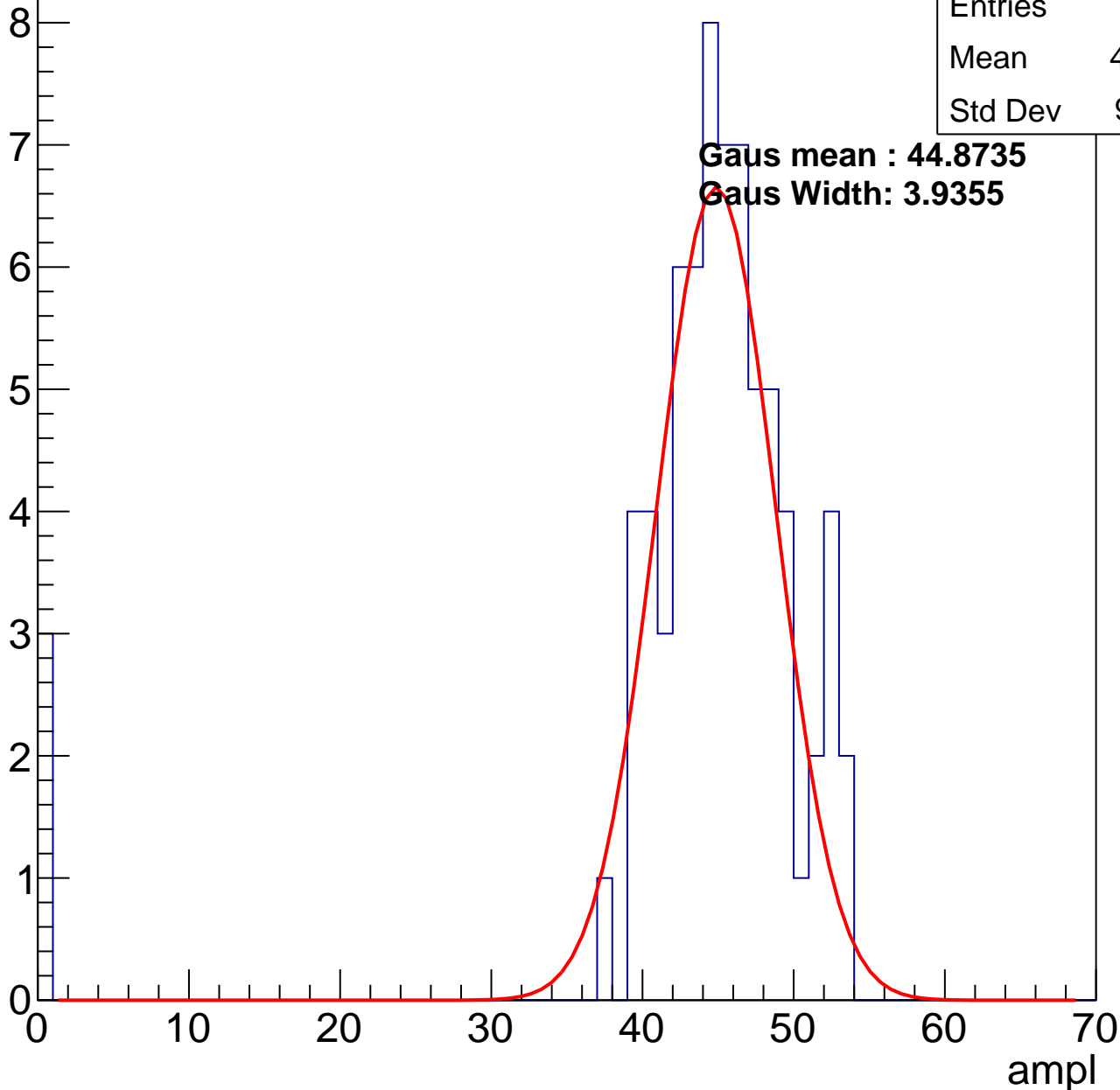
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	43.22
Std Dev	9.751

Gaus mean : 44.8735

Gaus Width: 3.9355



B1L103S, U10-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	50.78
Std Dev	6.799

Entry

10

8

6

4

2

0

0

10

20

30

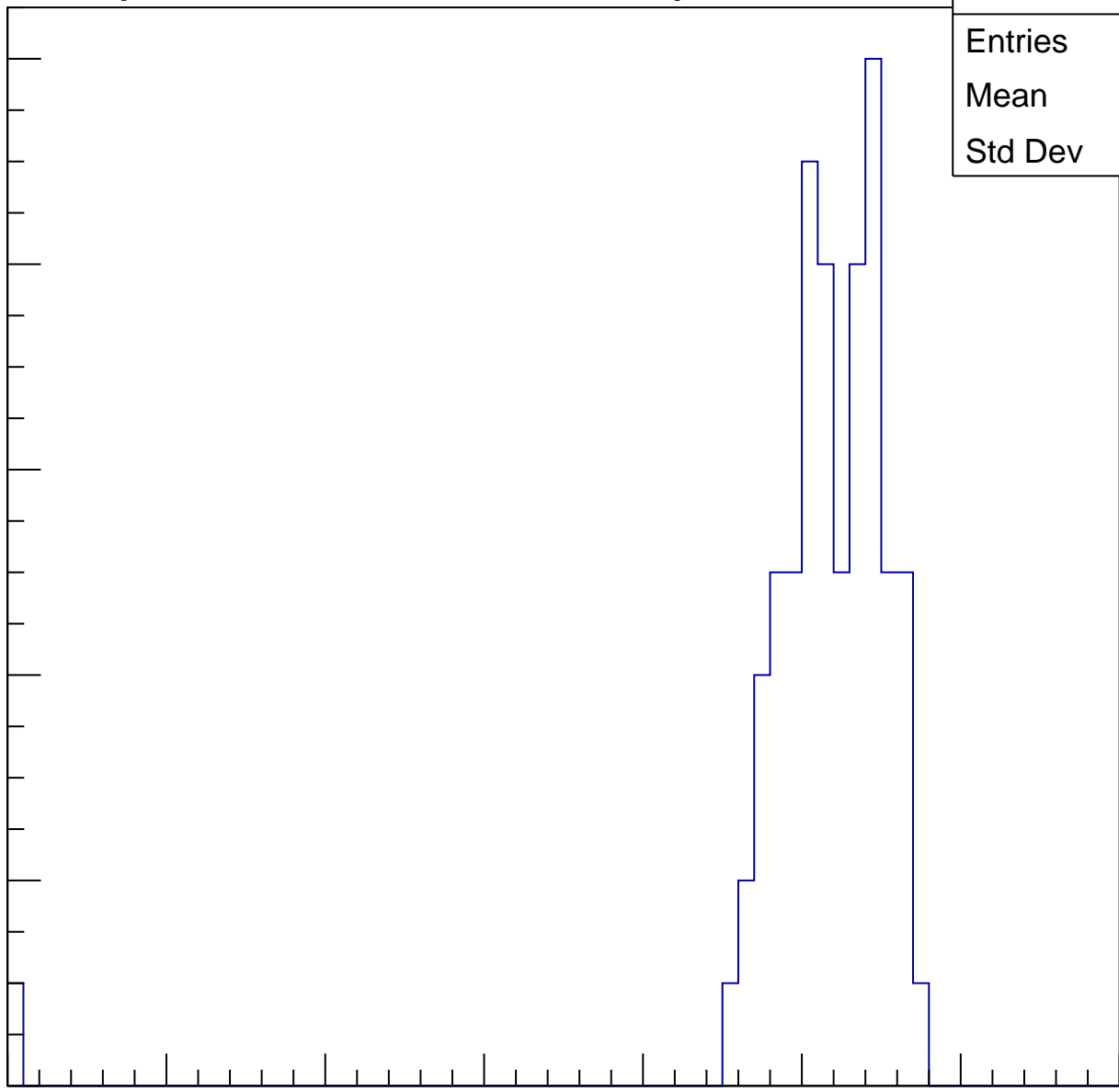
40

50

60

70

ampl

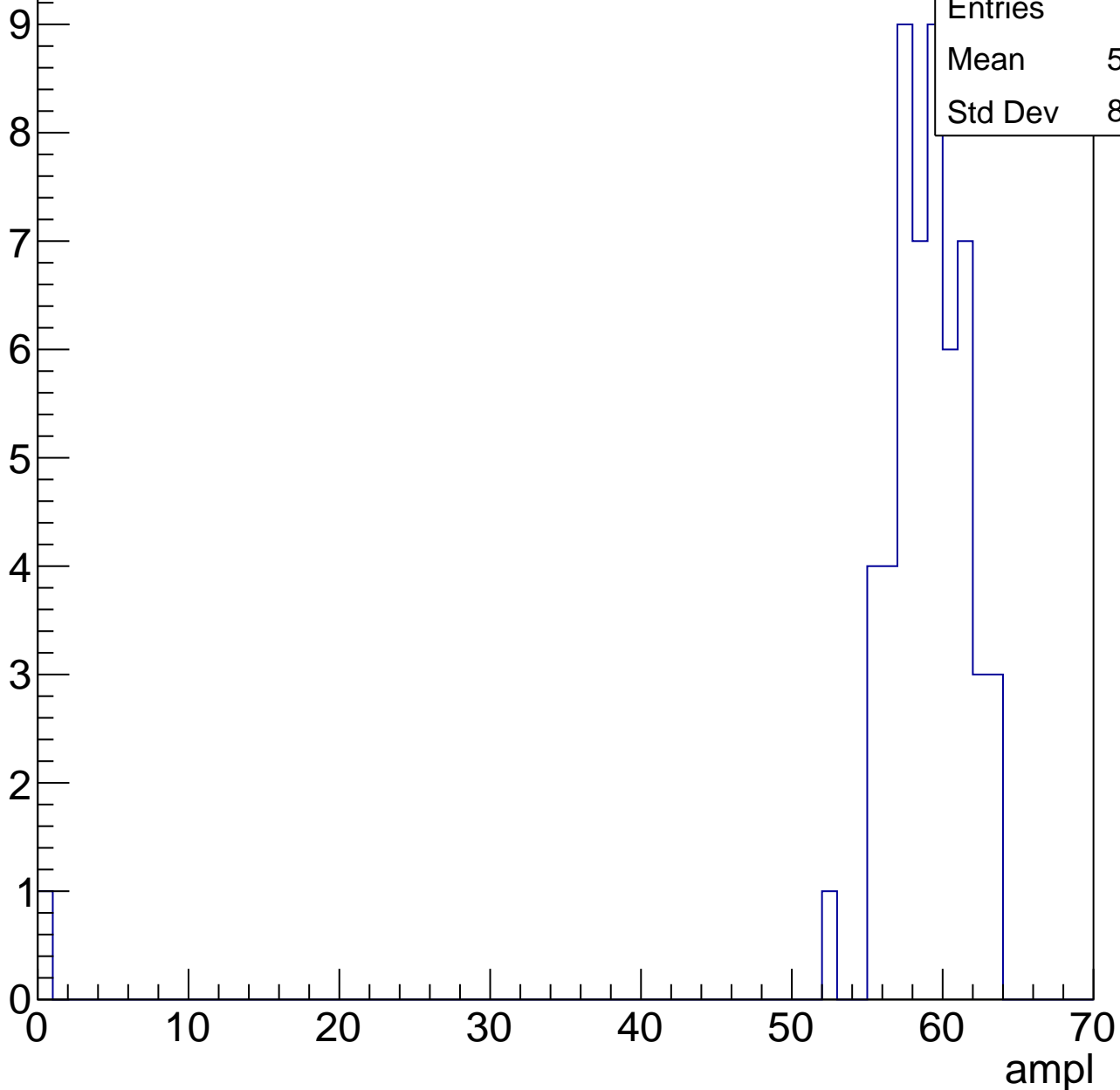


B1L103S, U10-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.56
Std Dev	8.243

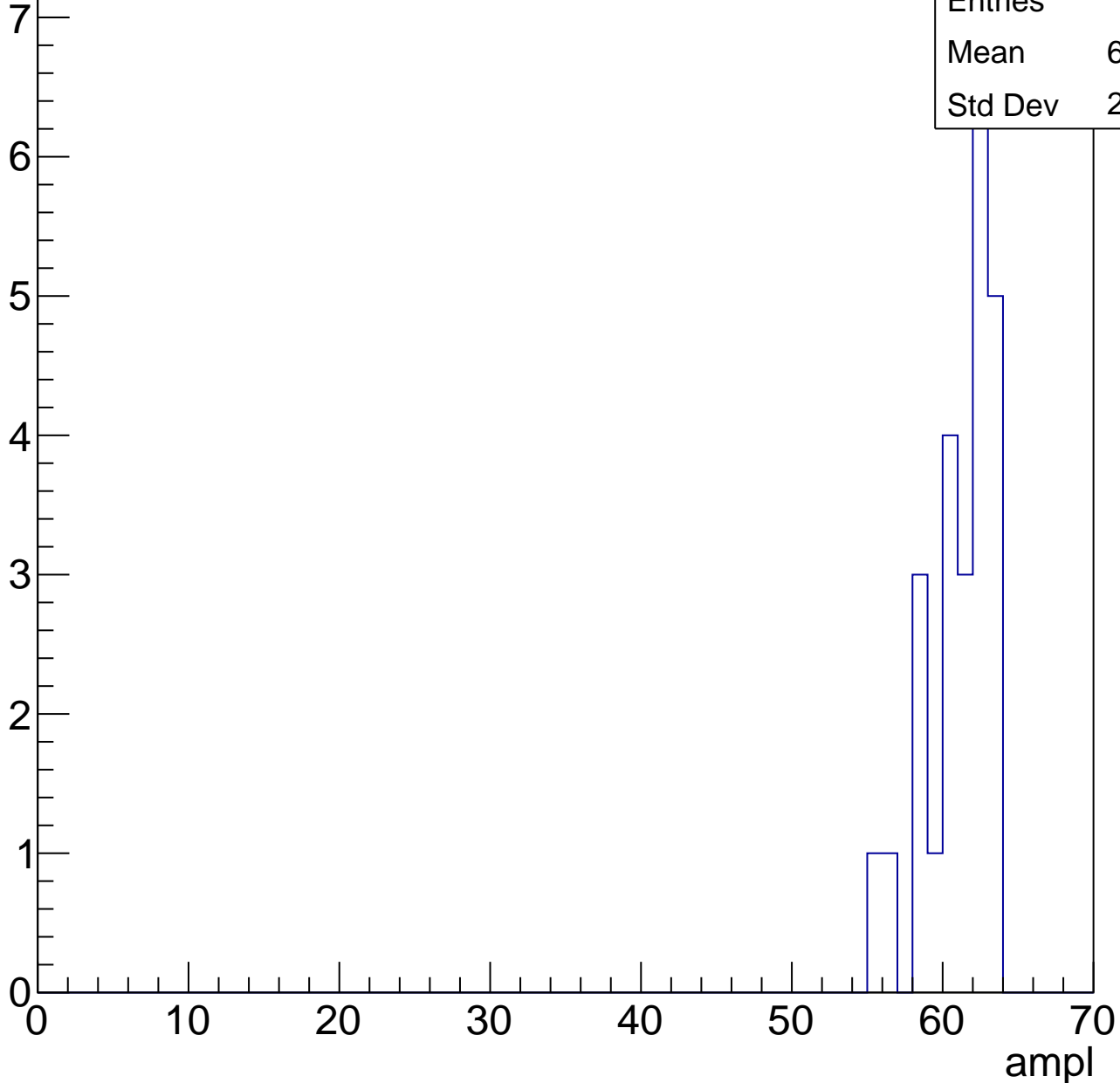


B1L103S, U10-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	60.64
Std Dev	2.189



B1L103S, U10-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch110, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.34
Std Dev	11.04

Gaus mean : 29.5939

Gaus Width: 3.7408

Entry

12

10

8

6

4

2

0

0

10

20

30

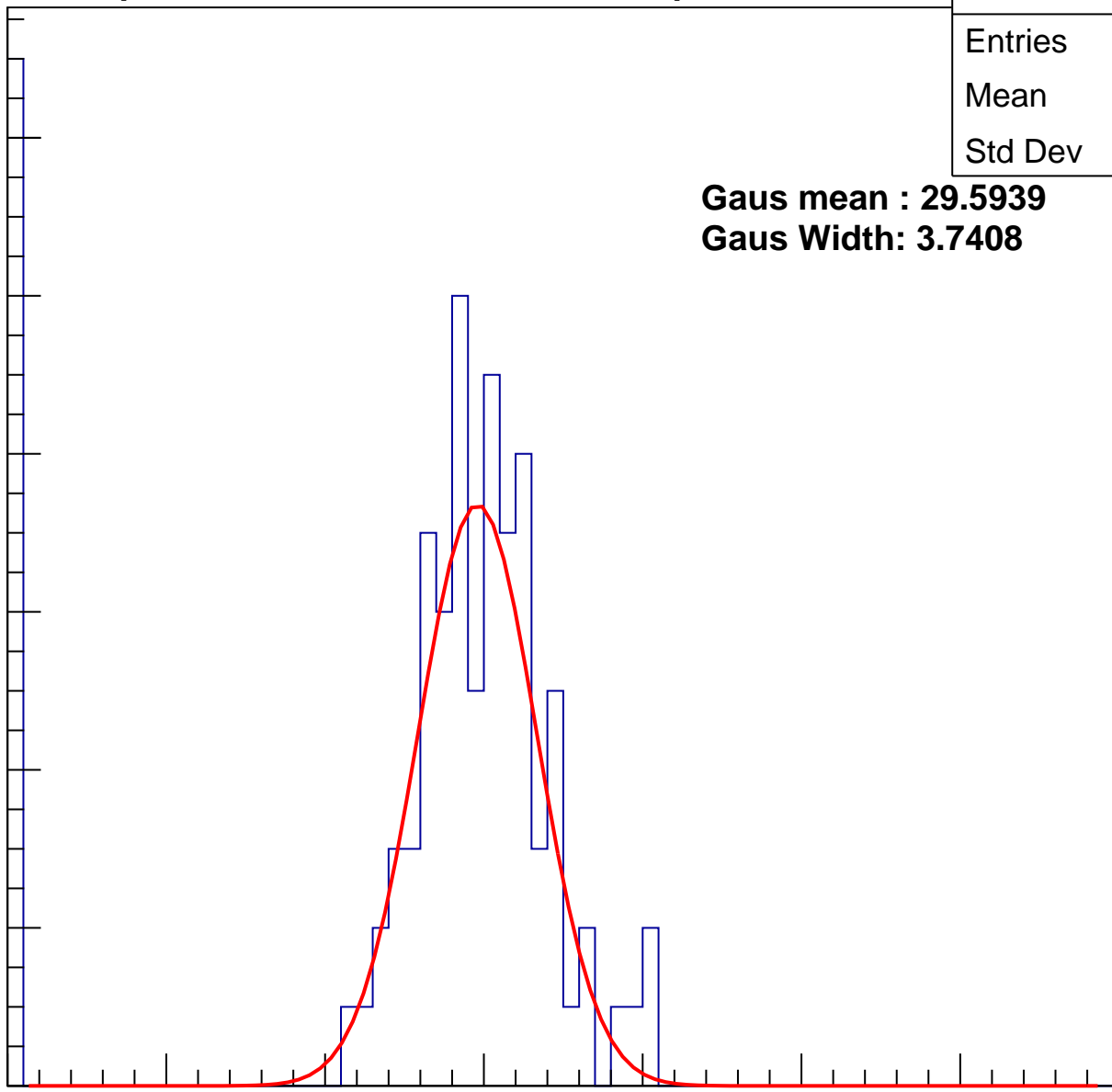
40

50

60

70

ampl



B1L103S, U10-ch110, adc1

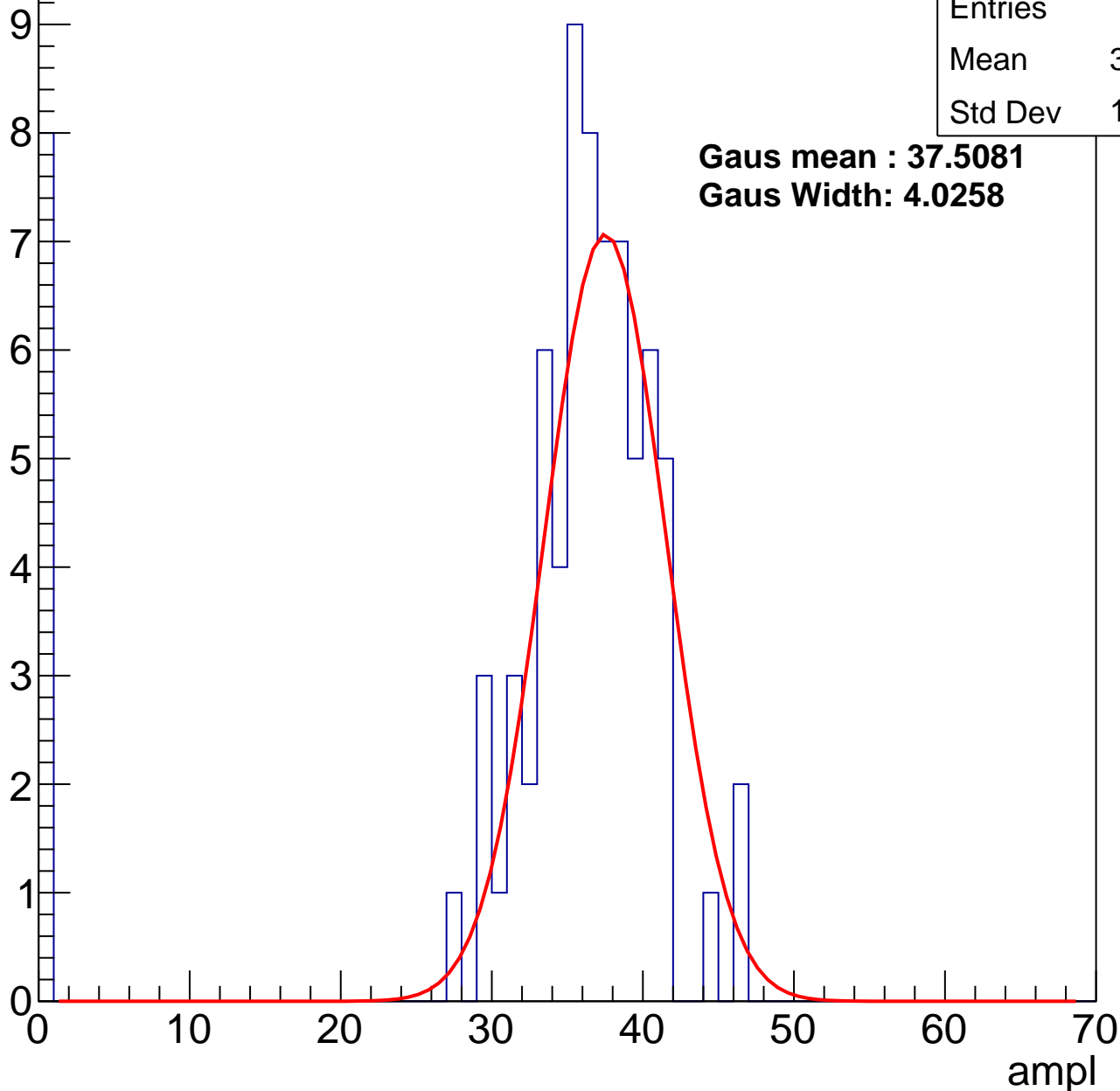
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.55
Std Dev	11.58

Gaus mean : 37.5081

Gaus Width: 4.0258



B1L103S, U10-ch110, adc2

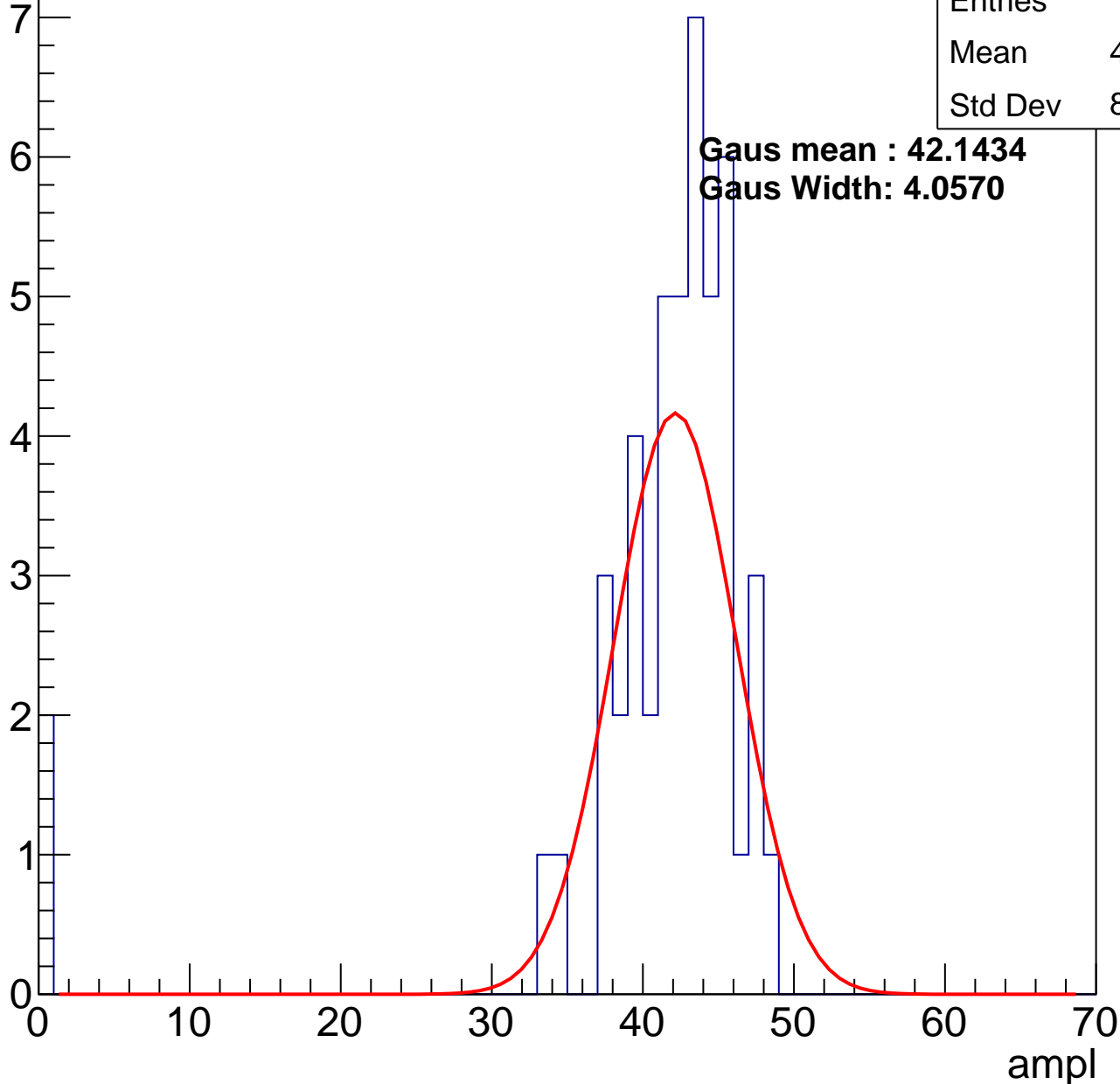
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	40.23
Std Dev	8.998

Gaus mean : 42.1434

Gaus Width: 4.0570

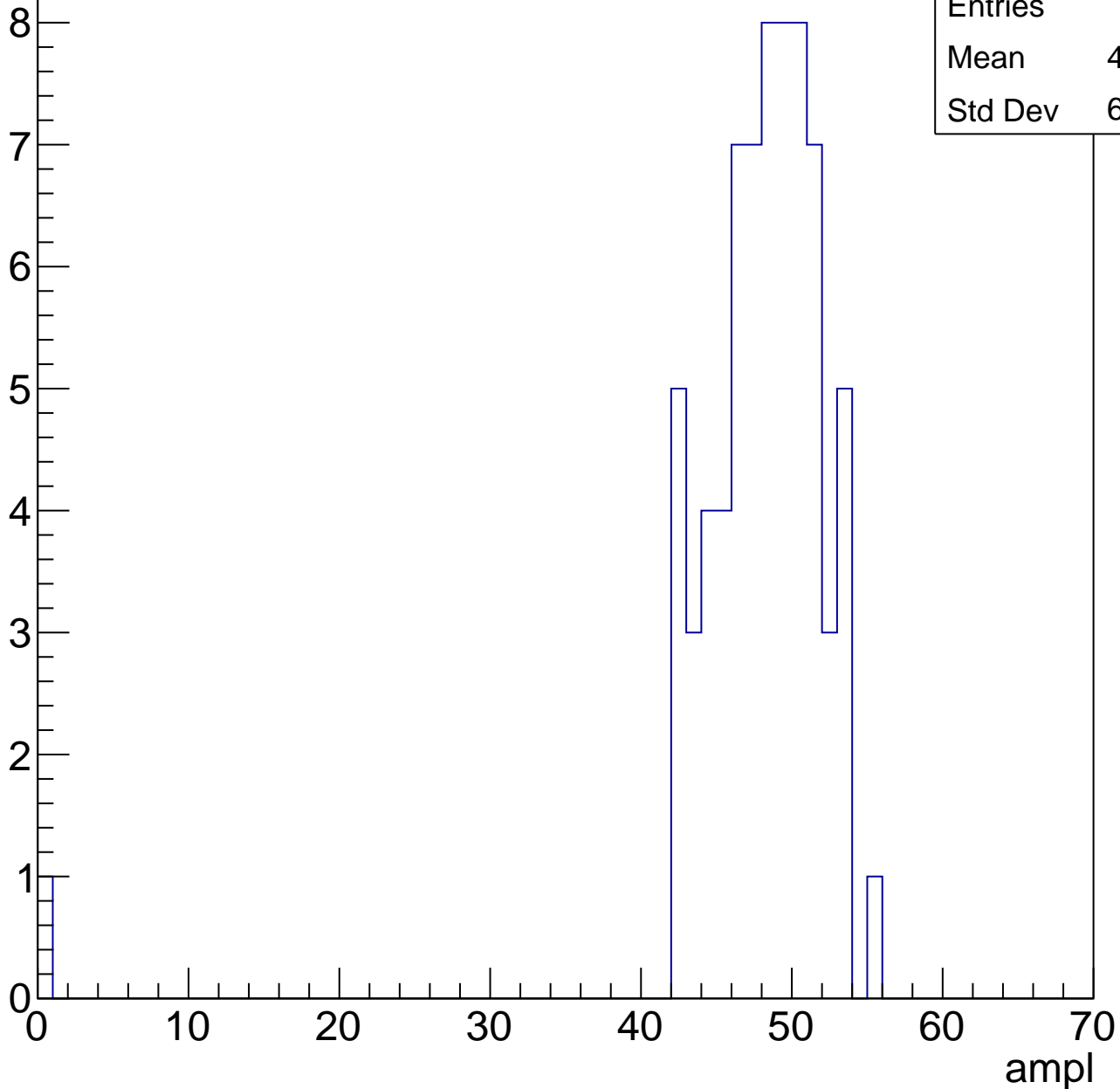


B1L103S, U10-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47.25
Std Dev	6.478

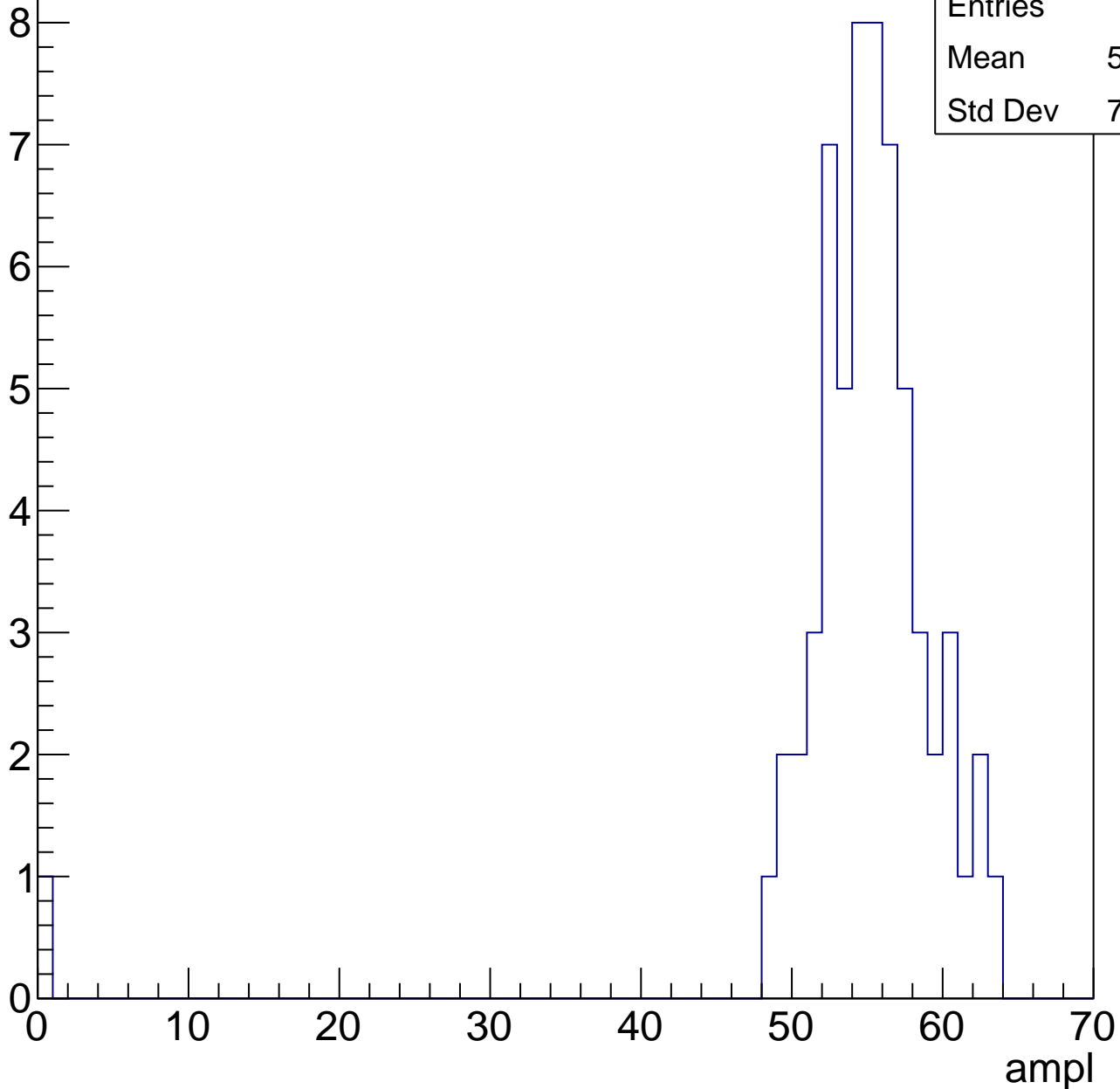


B1L103S, U10-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

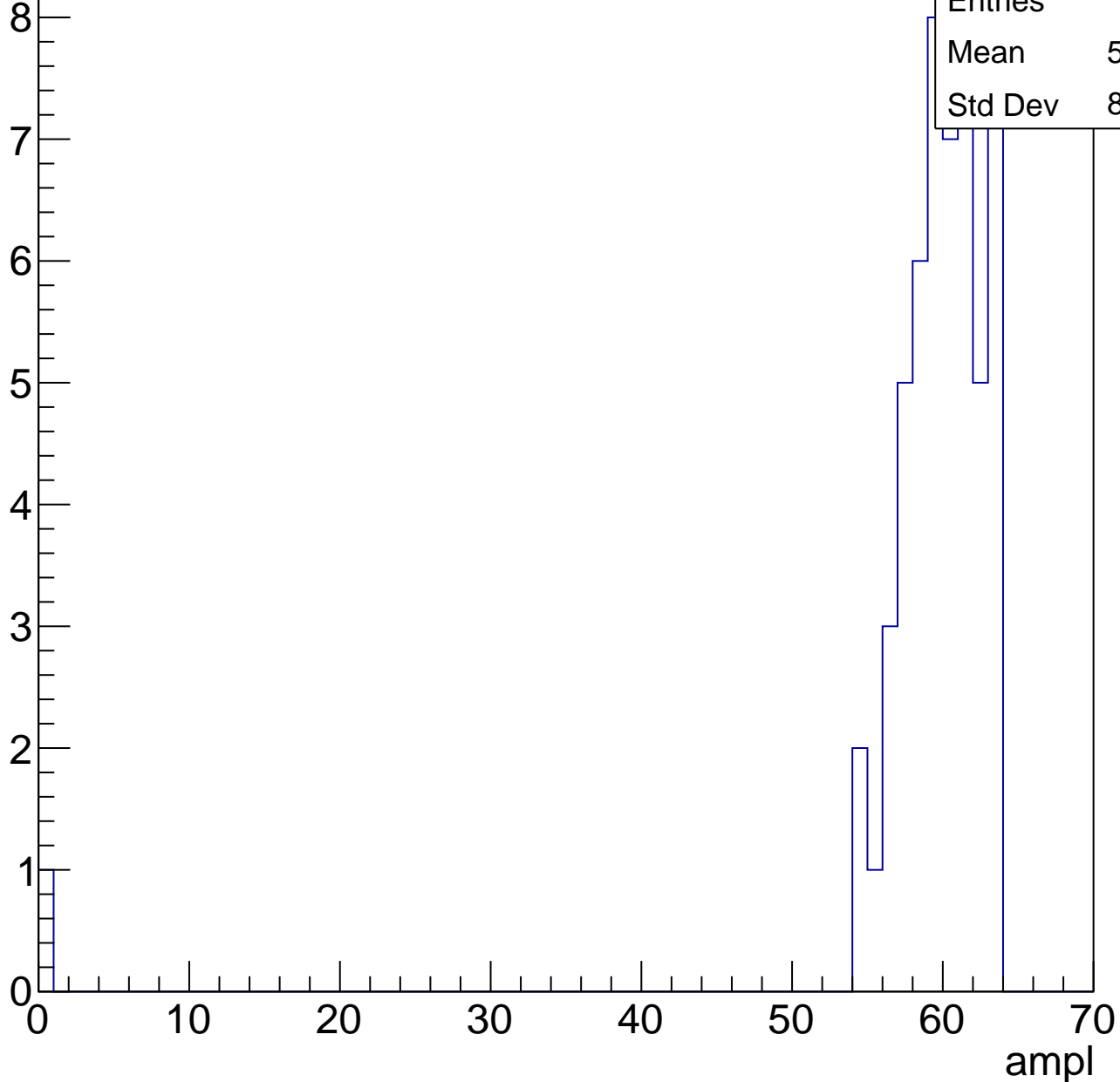
Entries	61
Mean	54.05
Std Dev	7.724



B1L103S, U10-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

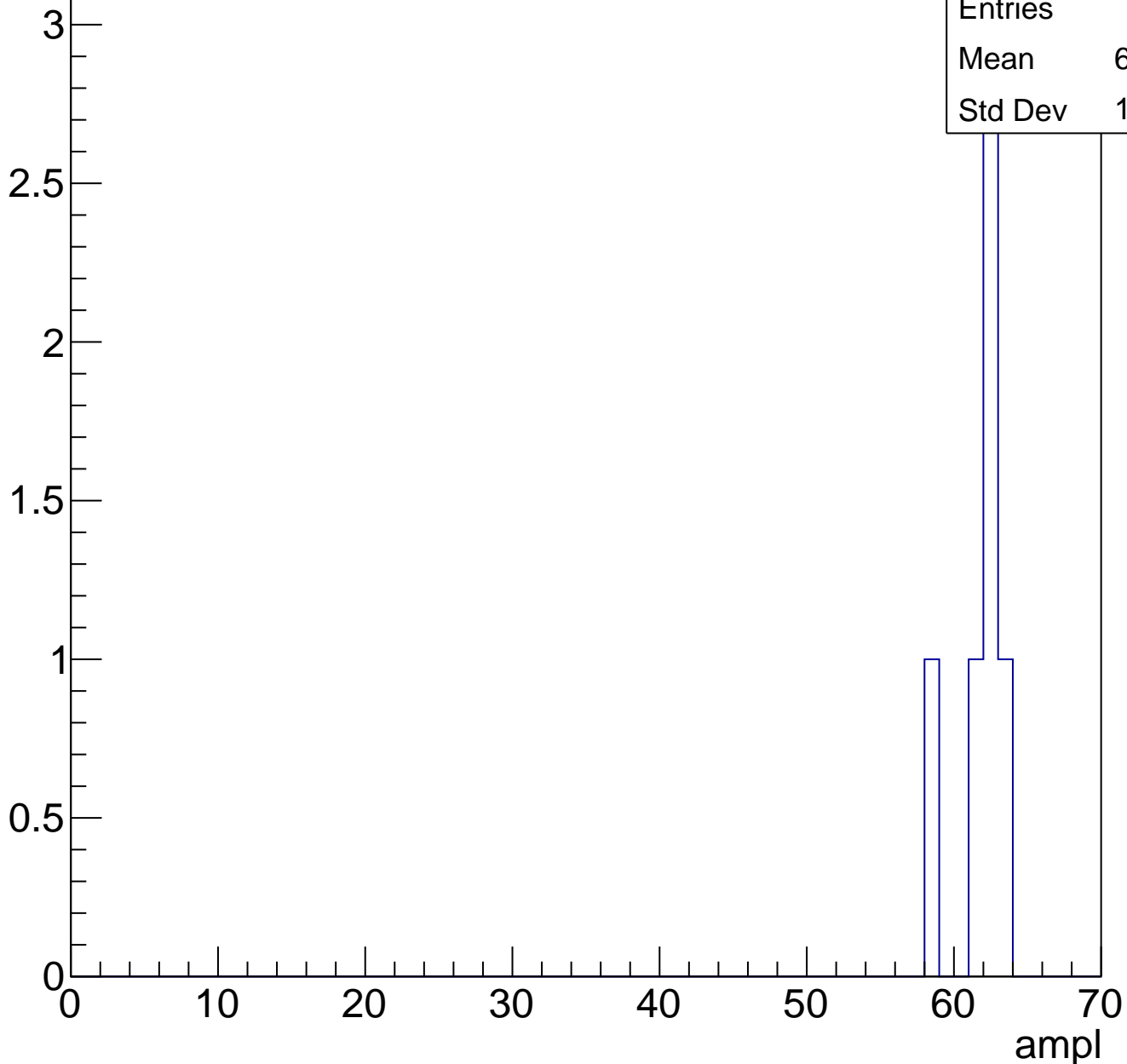
Entry



B1L103S, U10-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

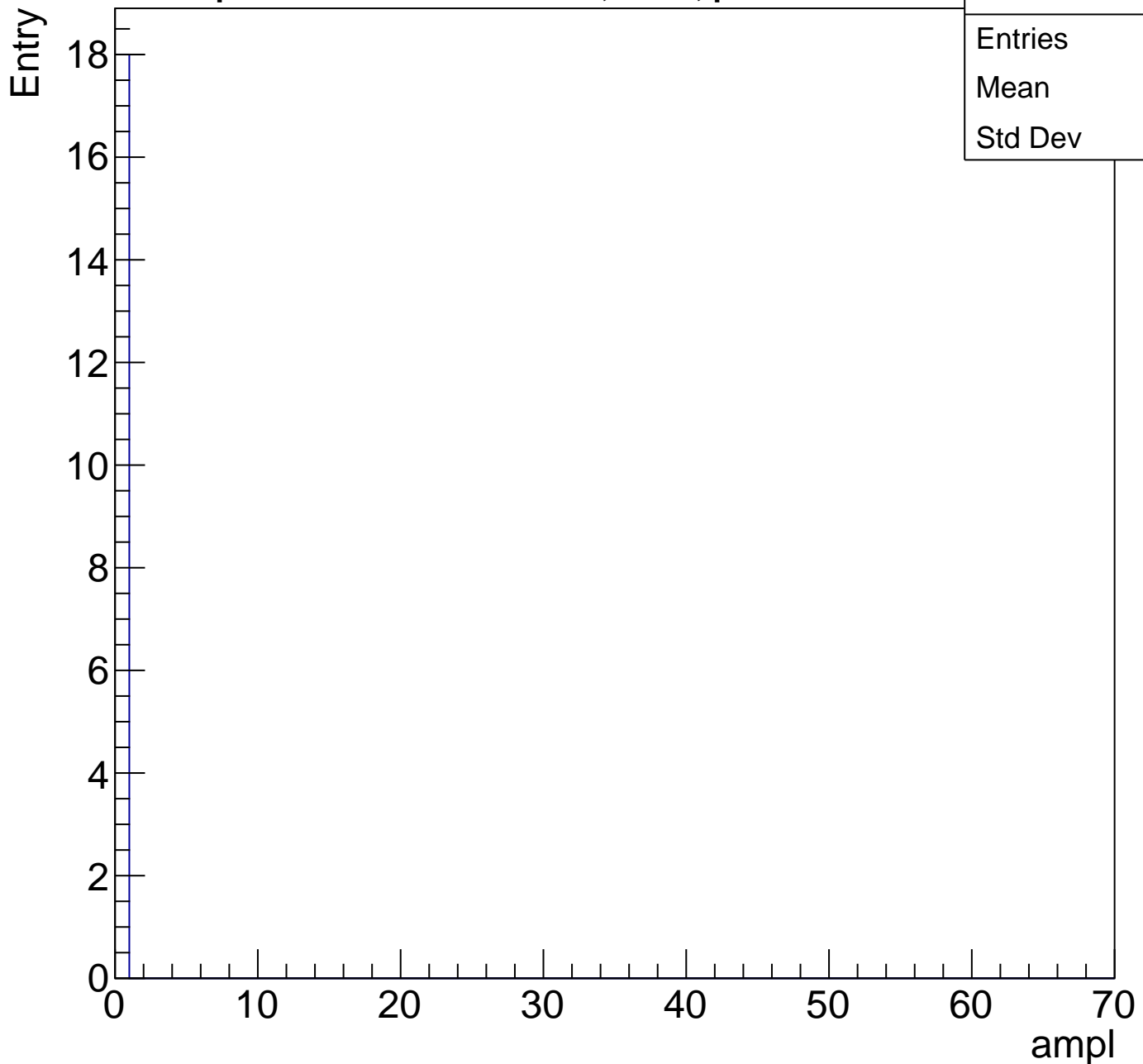
Entry



B1L103S, U10-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0



B1L103S, U10-ch111, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	24.65
Std Dev	11.83

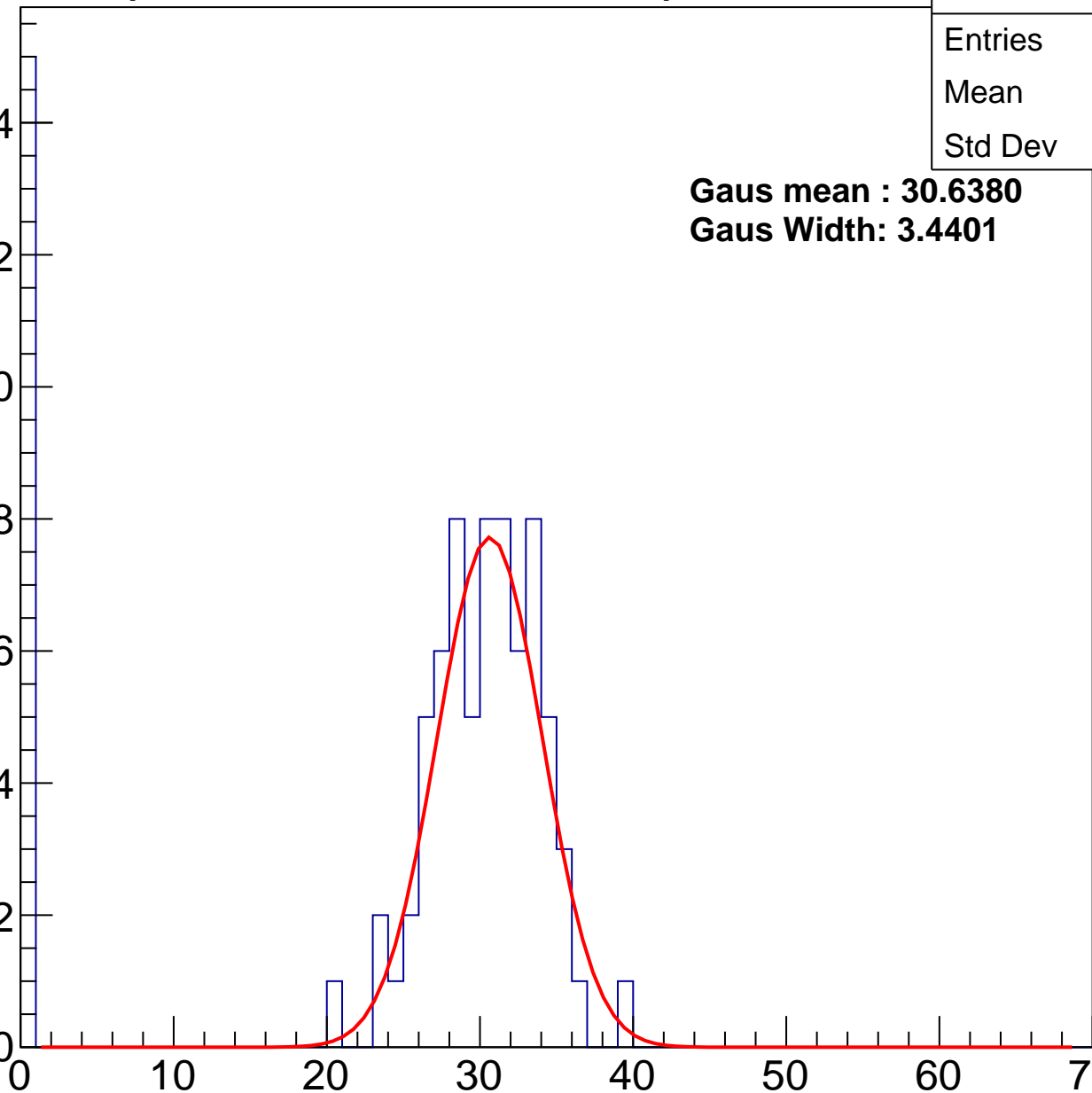
Gaus mean : 30.6380

Gaus Width: 3.4401

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U10-ch111, adc1

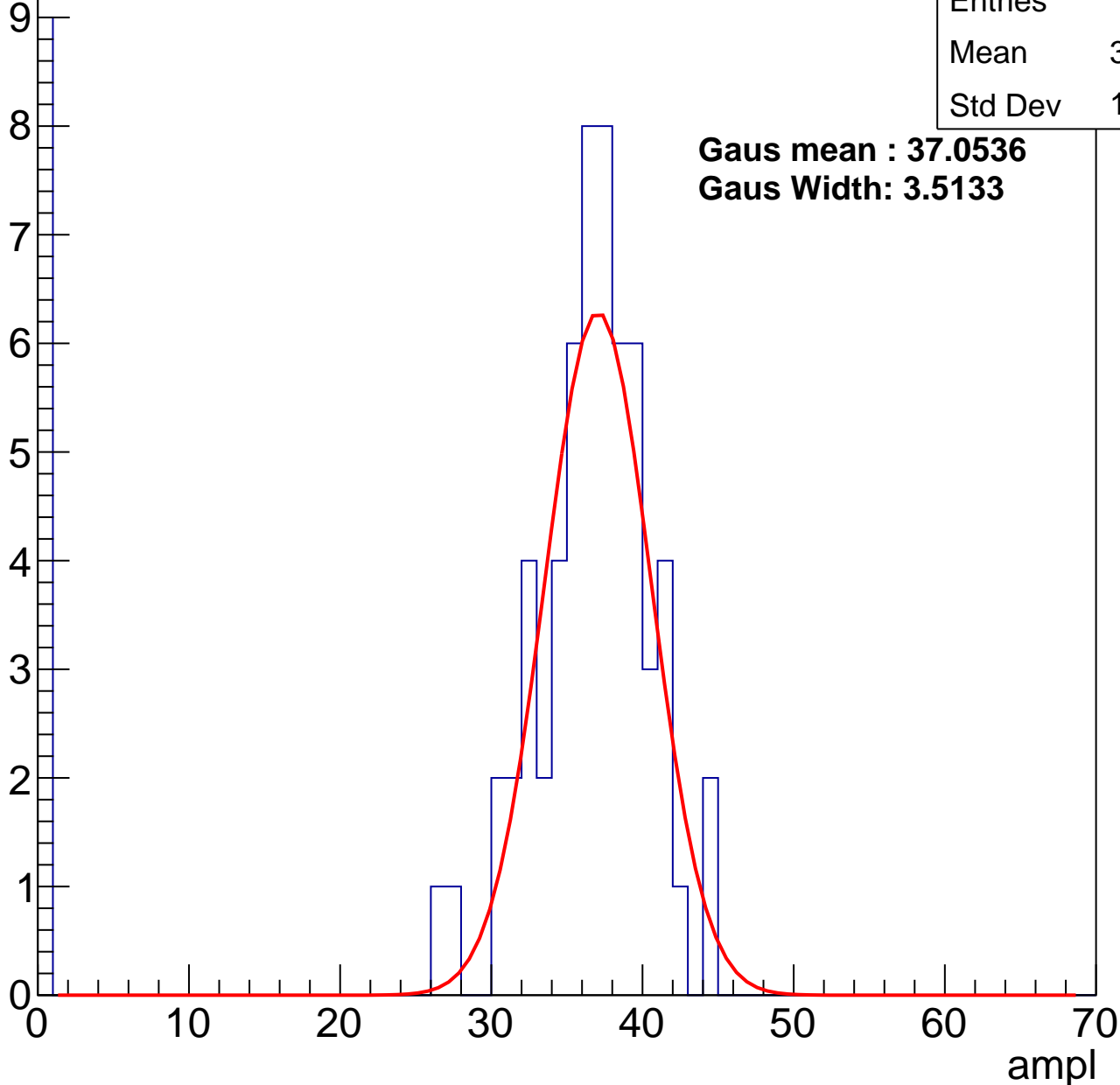
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	31.52
Std Dev	12.67

Gaus mean : 37.0536

Gaus Width: 3.5133



B1L103S, U10-ch111, adc2

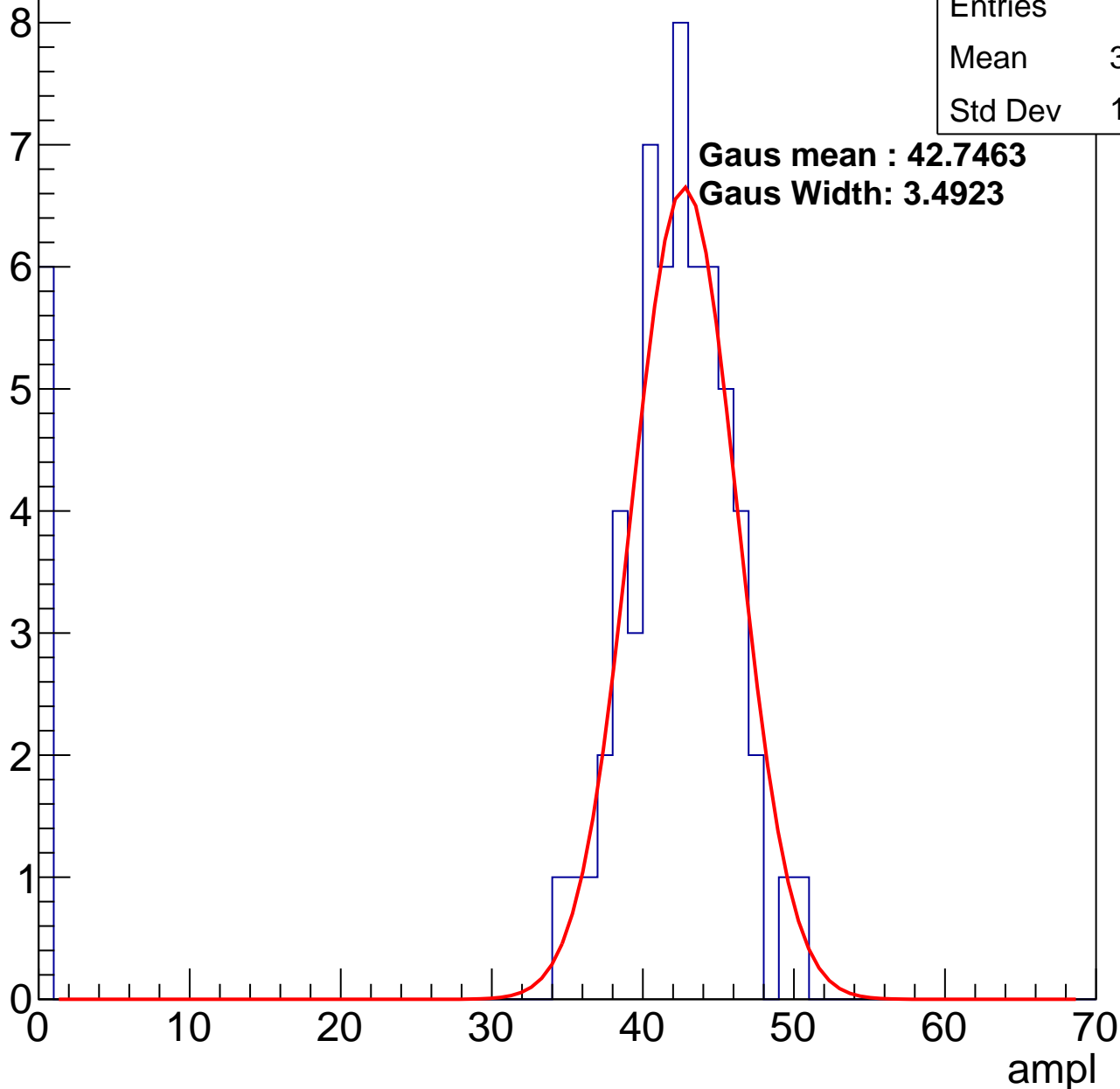
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.03
Std Dev	12.63

Gaus mean : 42.7463

Gaus Width: 3.4923

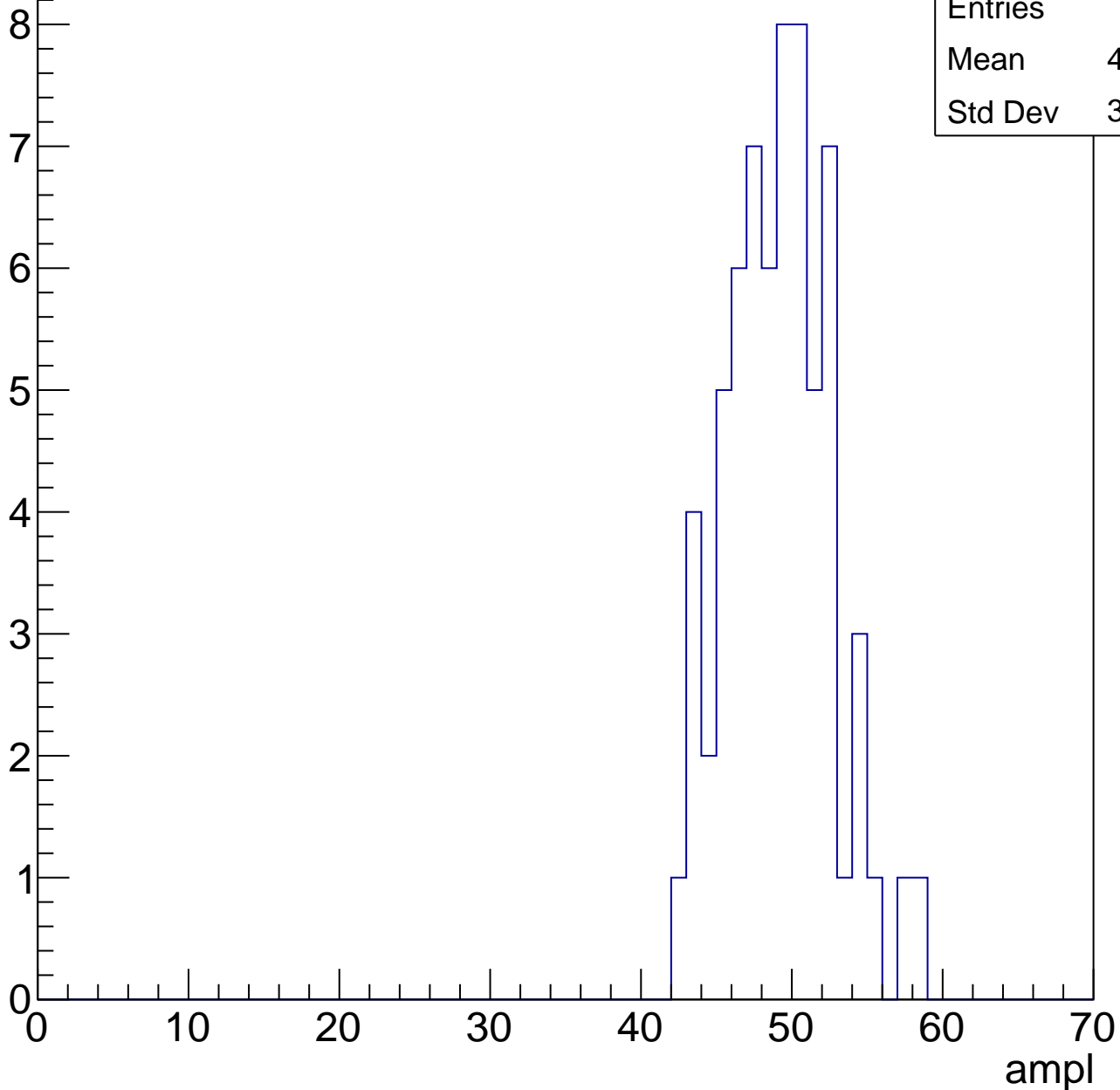


B1L103S, U10-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	48.73
Std Dev	3.414

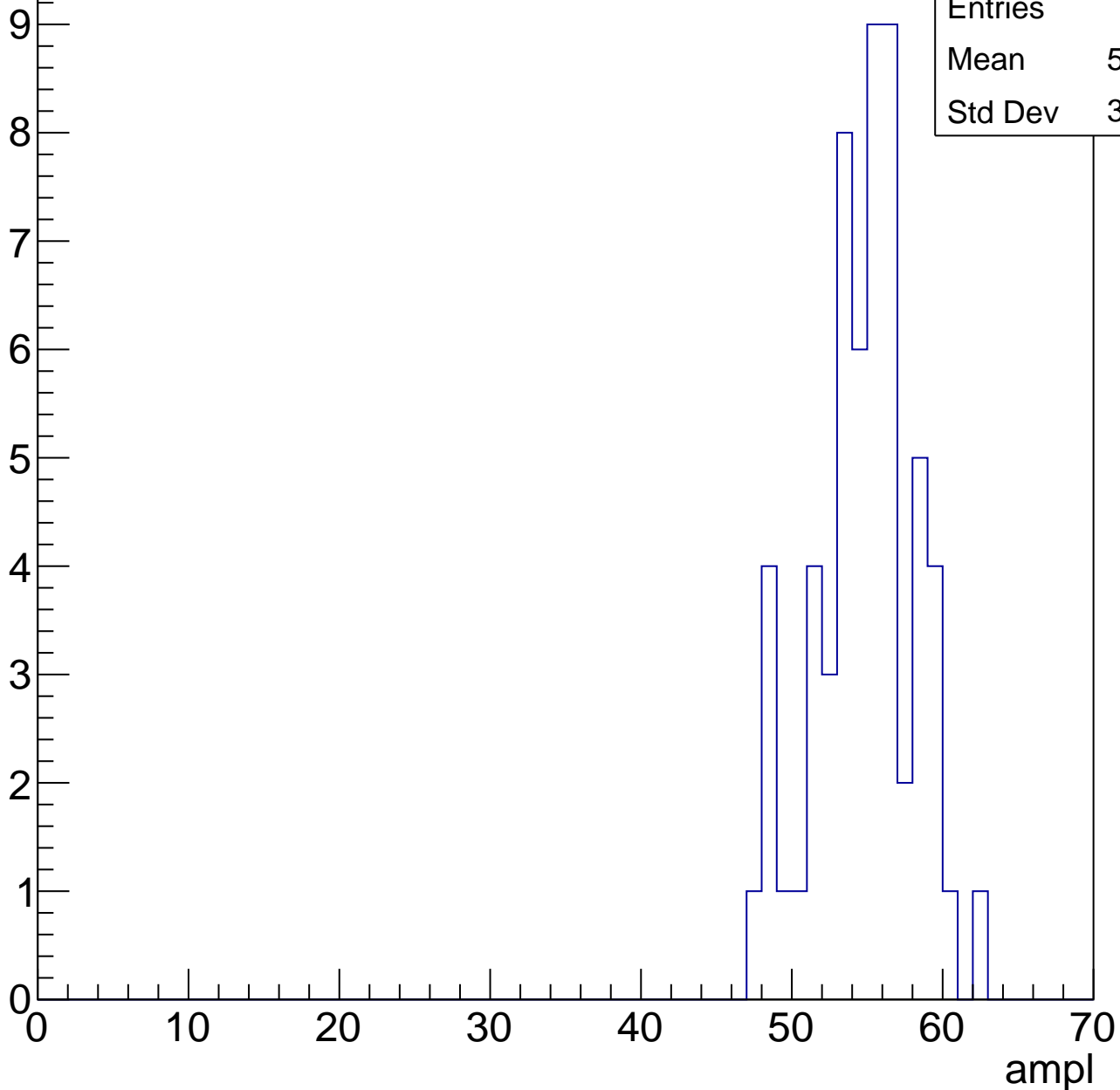


B1L103S, U10-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.36
Std Dev	3.256

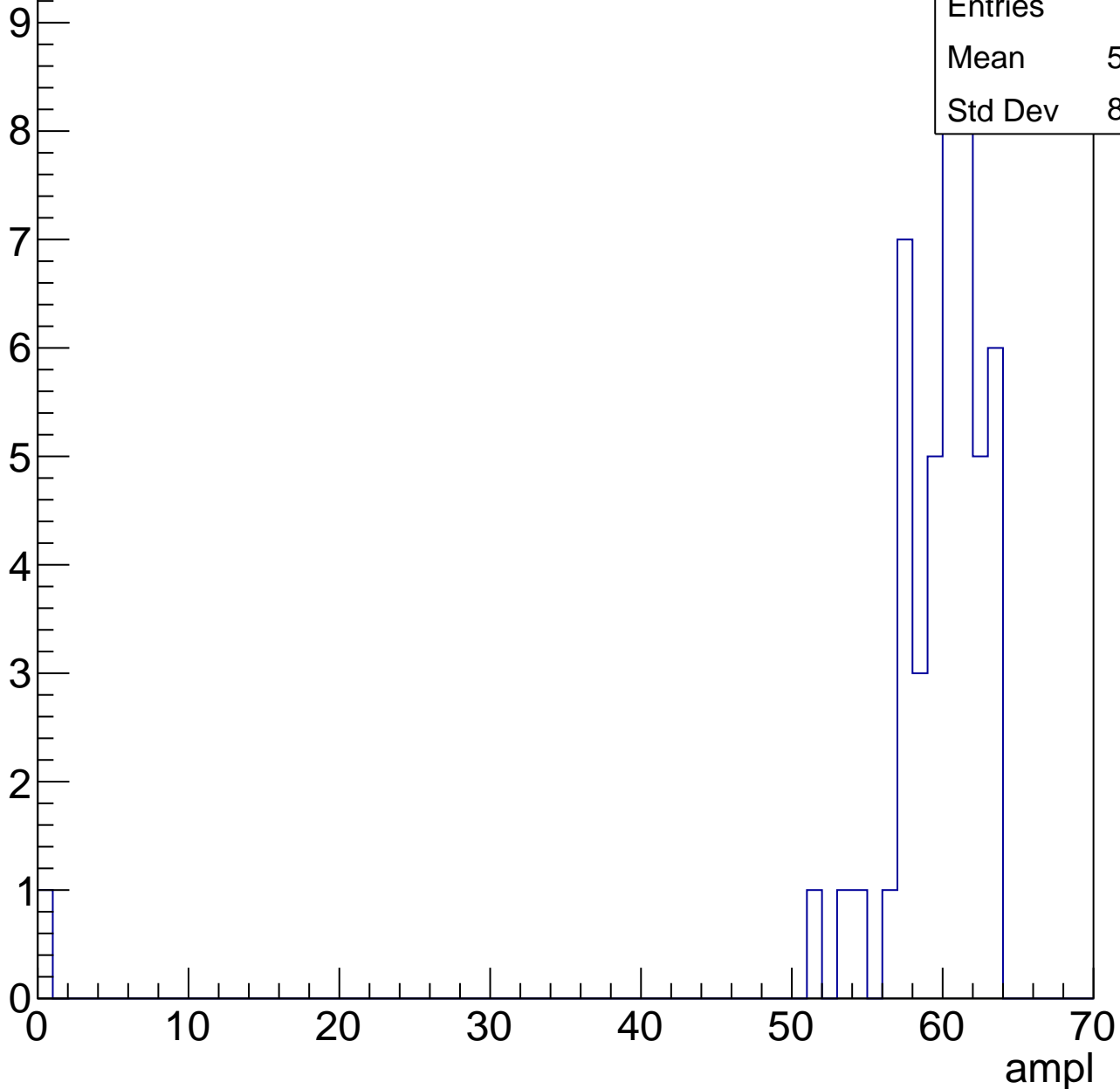


B1L103S, U10-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

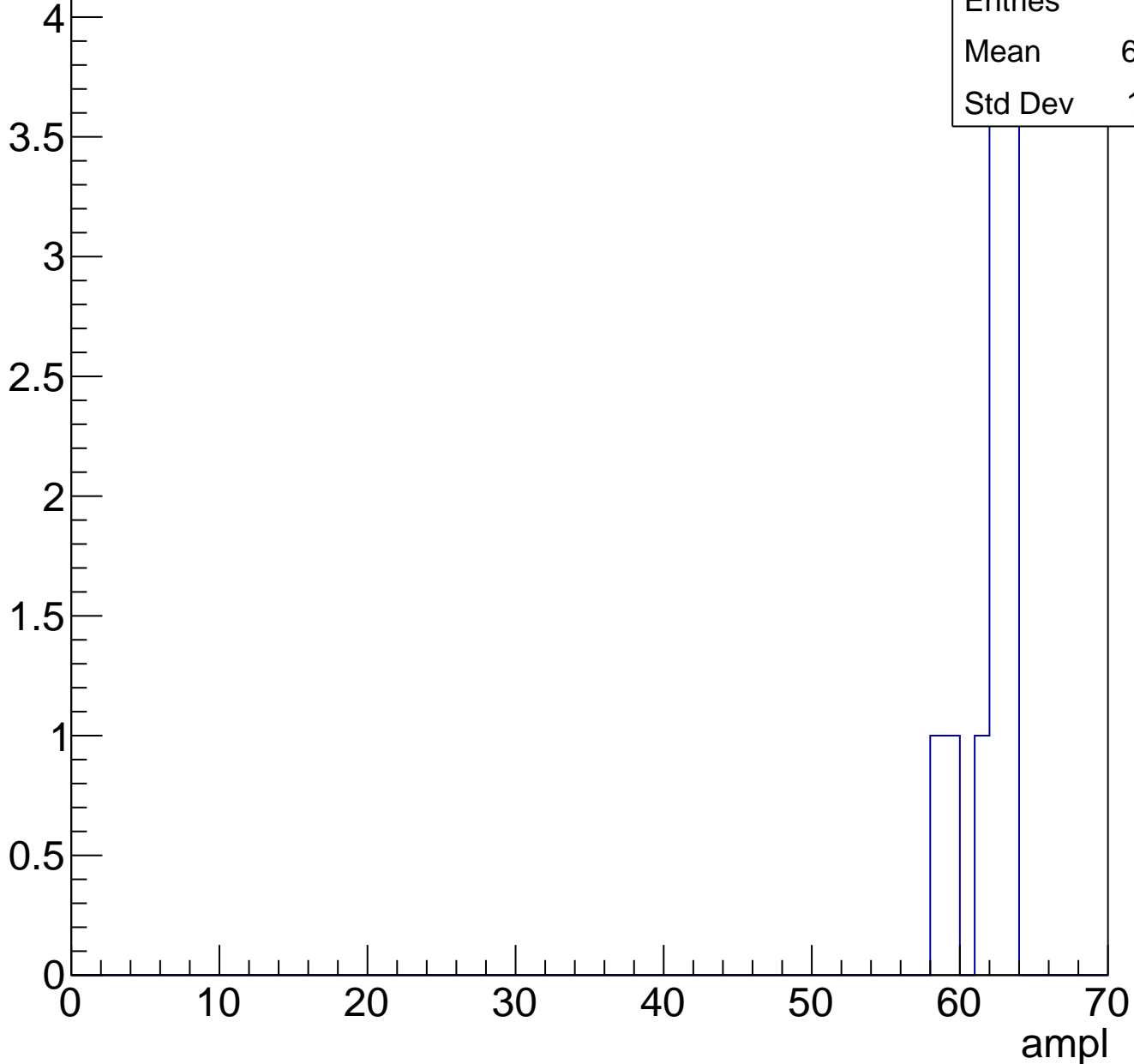
Entries	49
Mean	58.35
Std Dev	8.819



B1L103S, U10-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch112, adc0

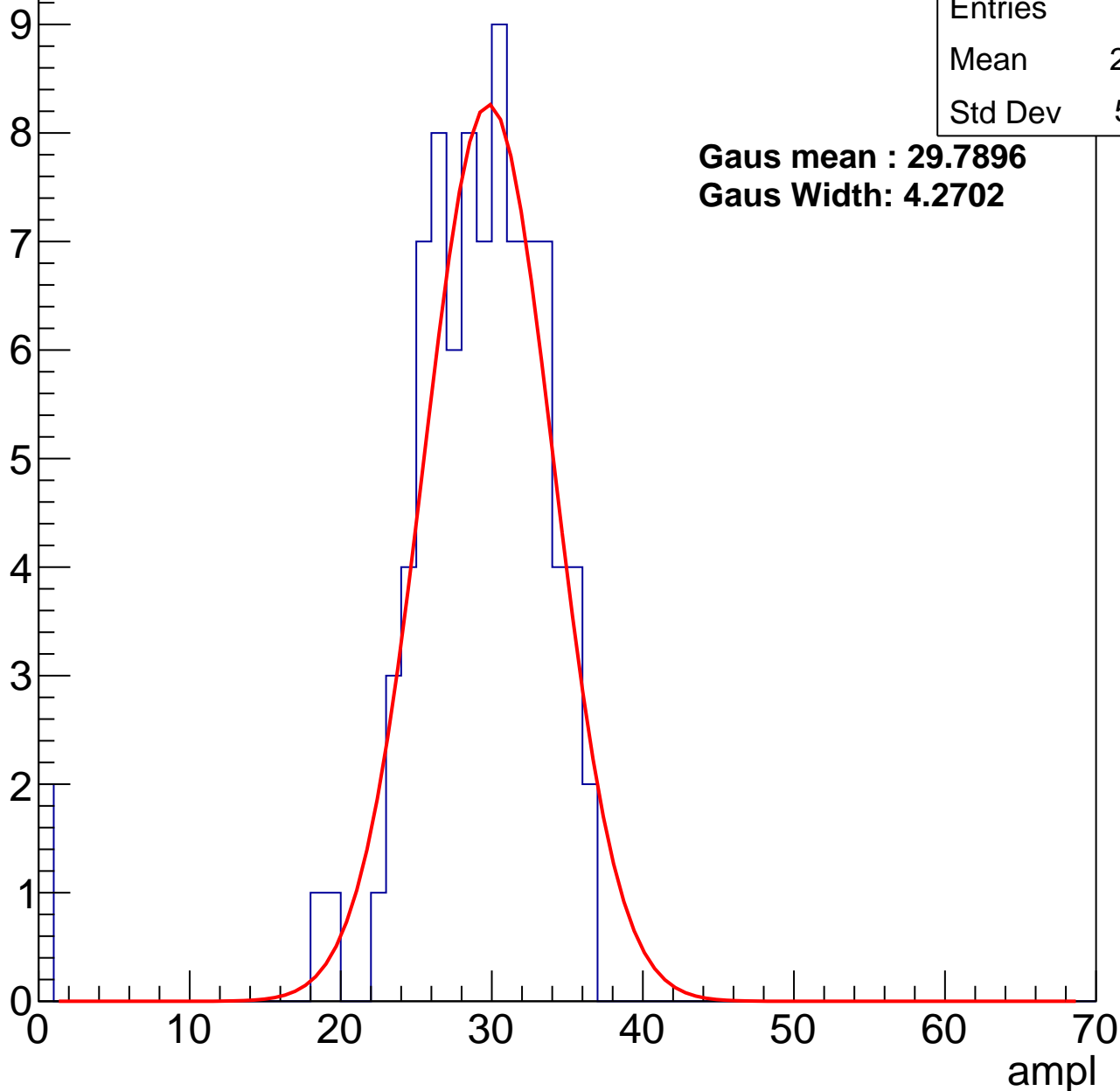
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	28.25
Std Dev	5.721

Gaus mean : 29.7896

Gaus Width: 4.2702



B1L103S, U10-ch112, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	34.53
Std Dev	10.32

Gaus mean : 37.6000

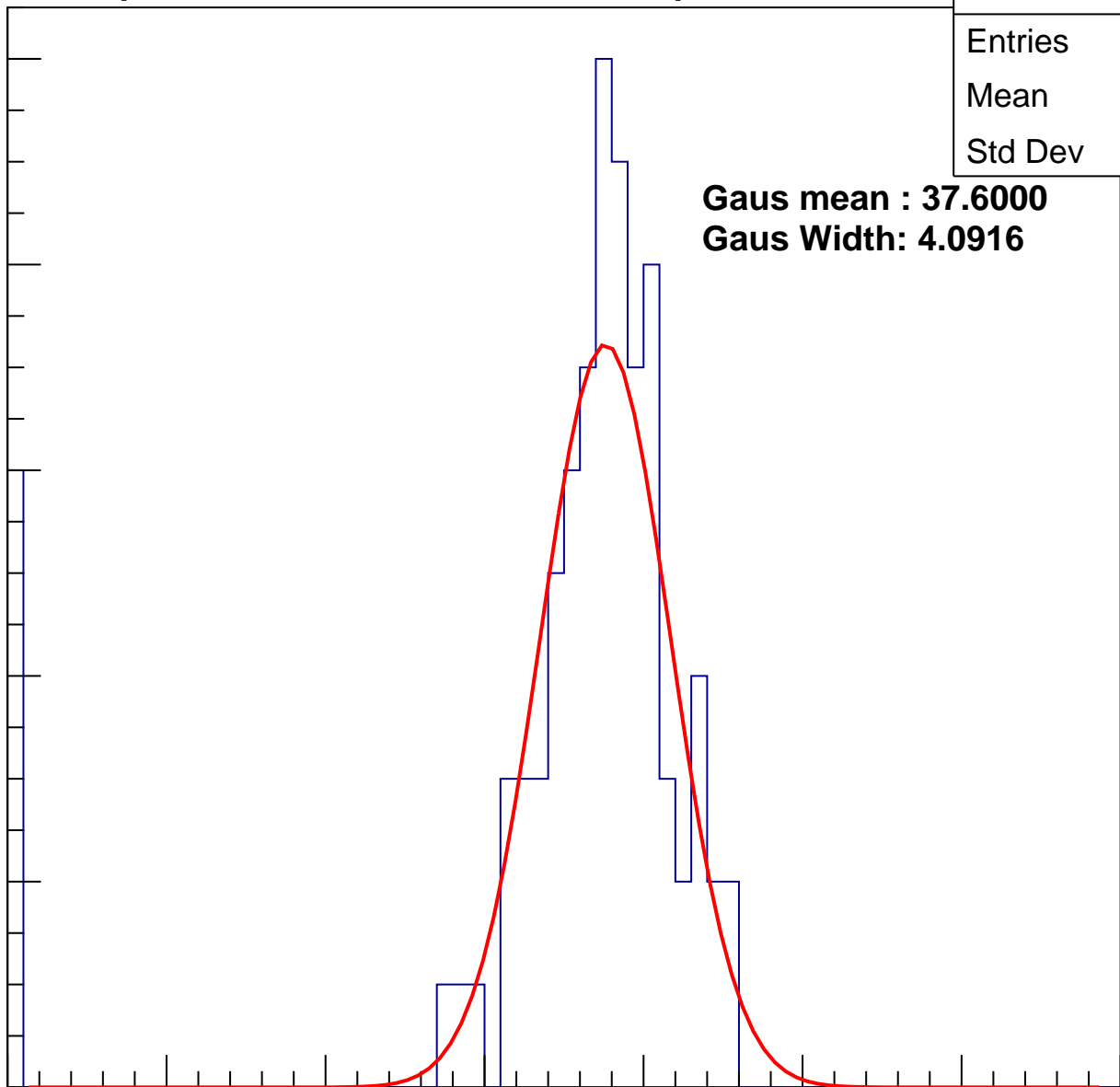
Gaus Width: 4.0916

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch112, adc2

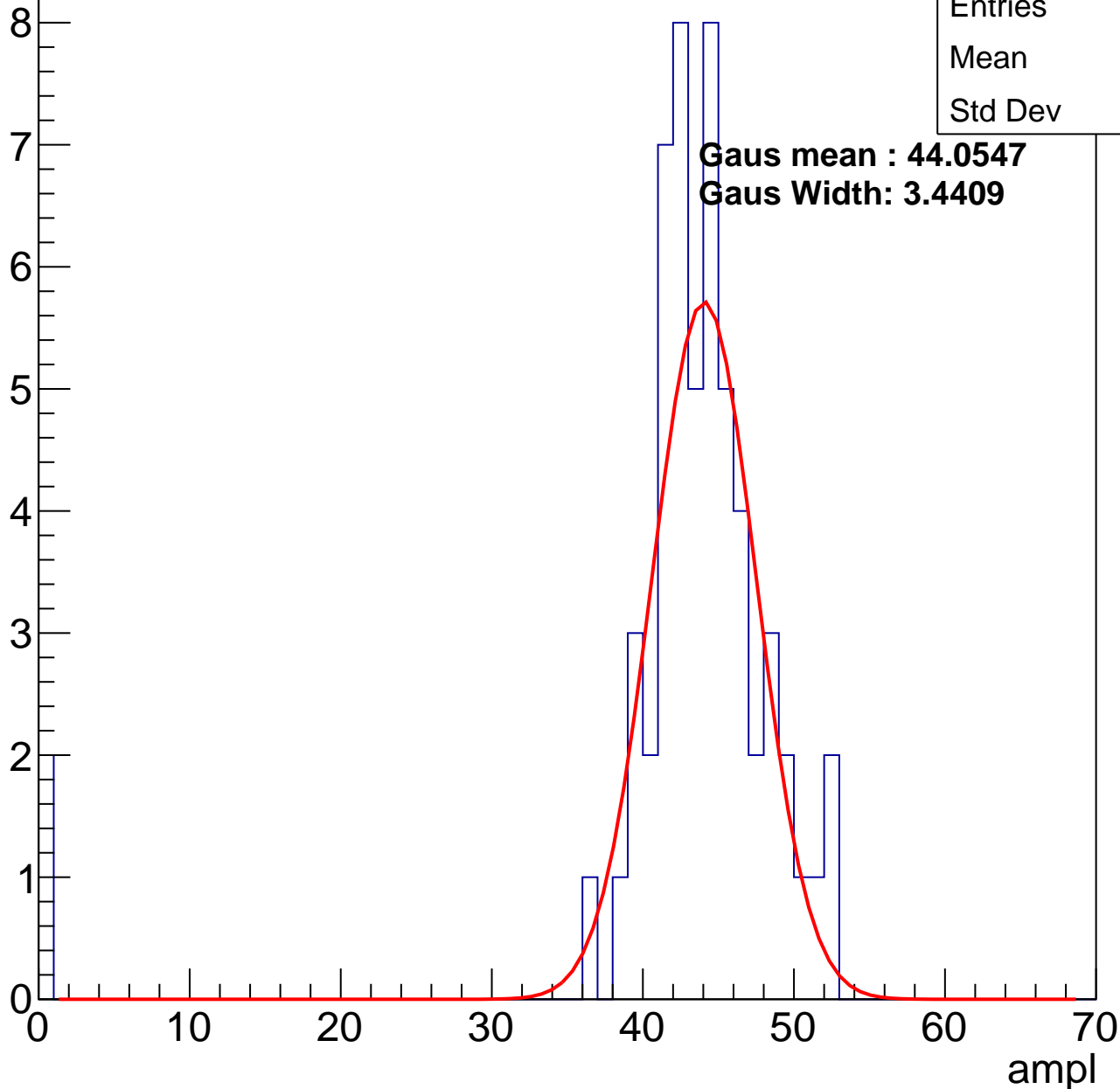
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	42.3
Std Dev	8.75

Gaus mean : 44.0547

Gaus Width: 3.4409

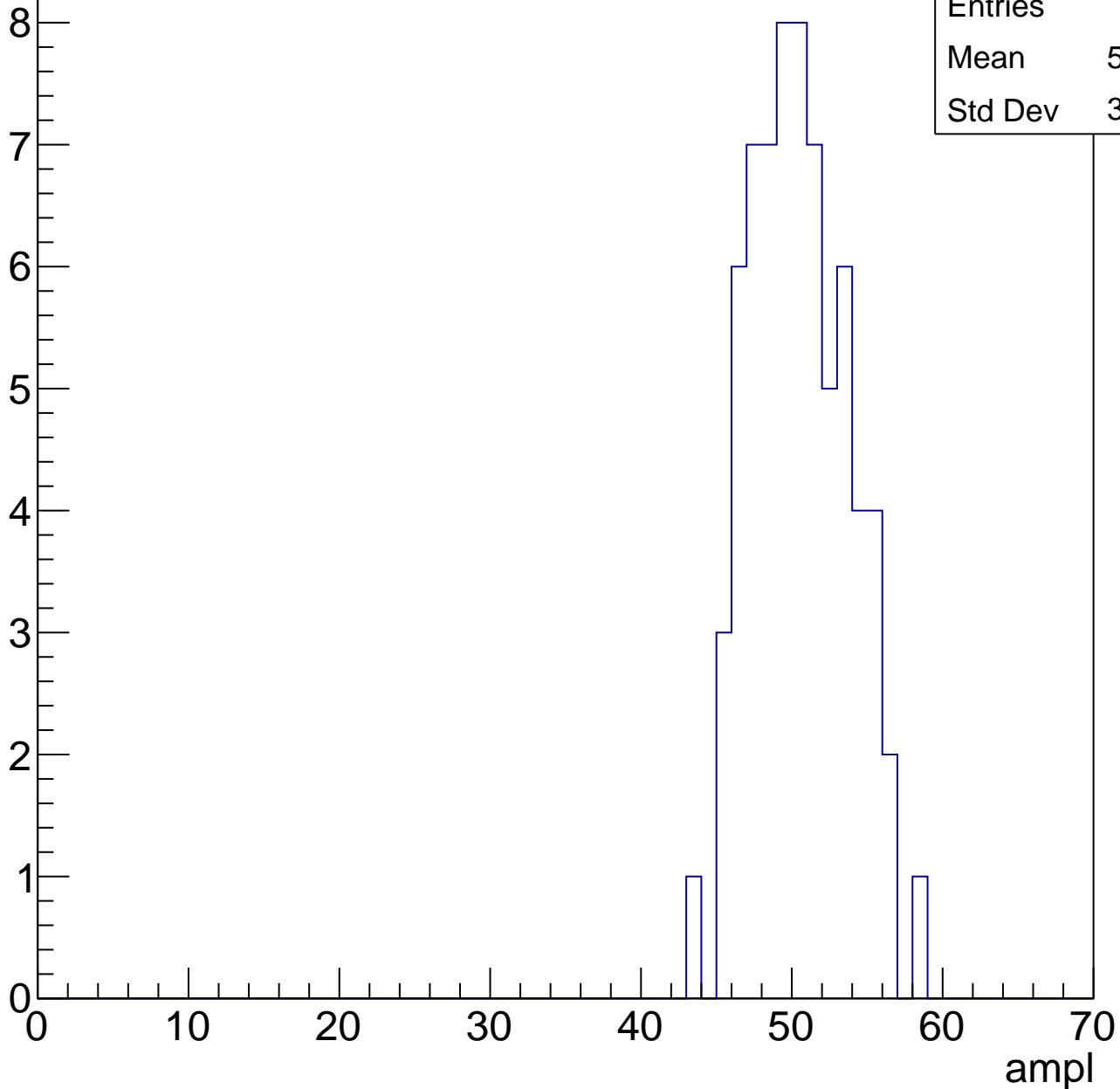


B1L103S, U10-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	50.03
Std Dev	3.185

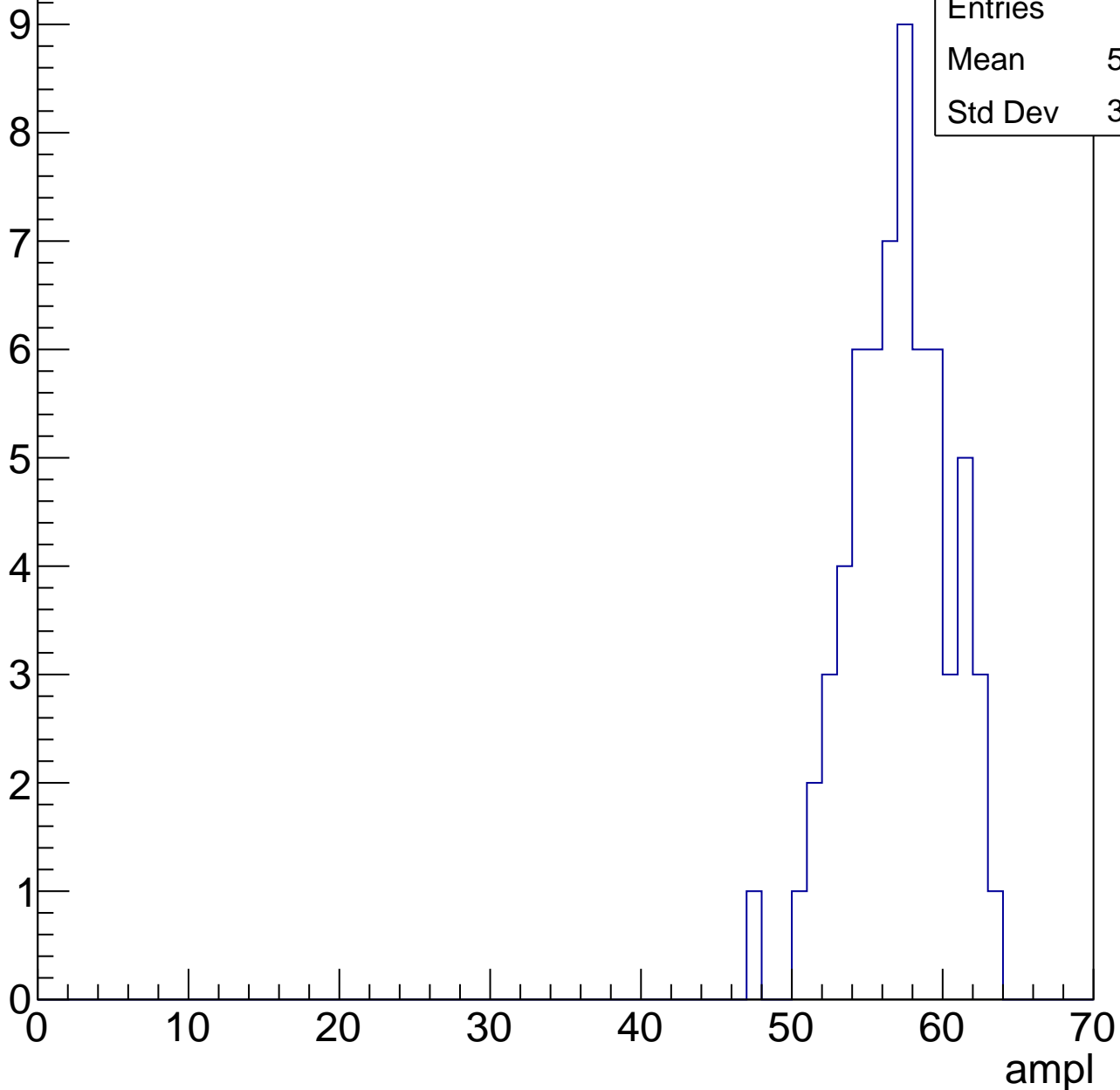


B1L103S, U10-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.54
Std Dev	3.285

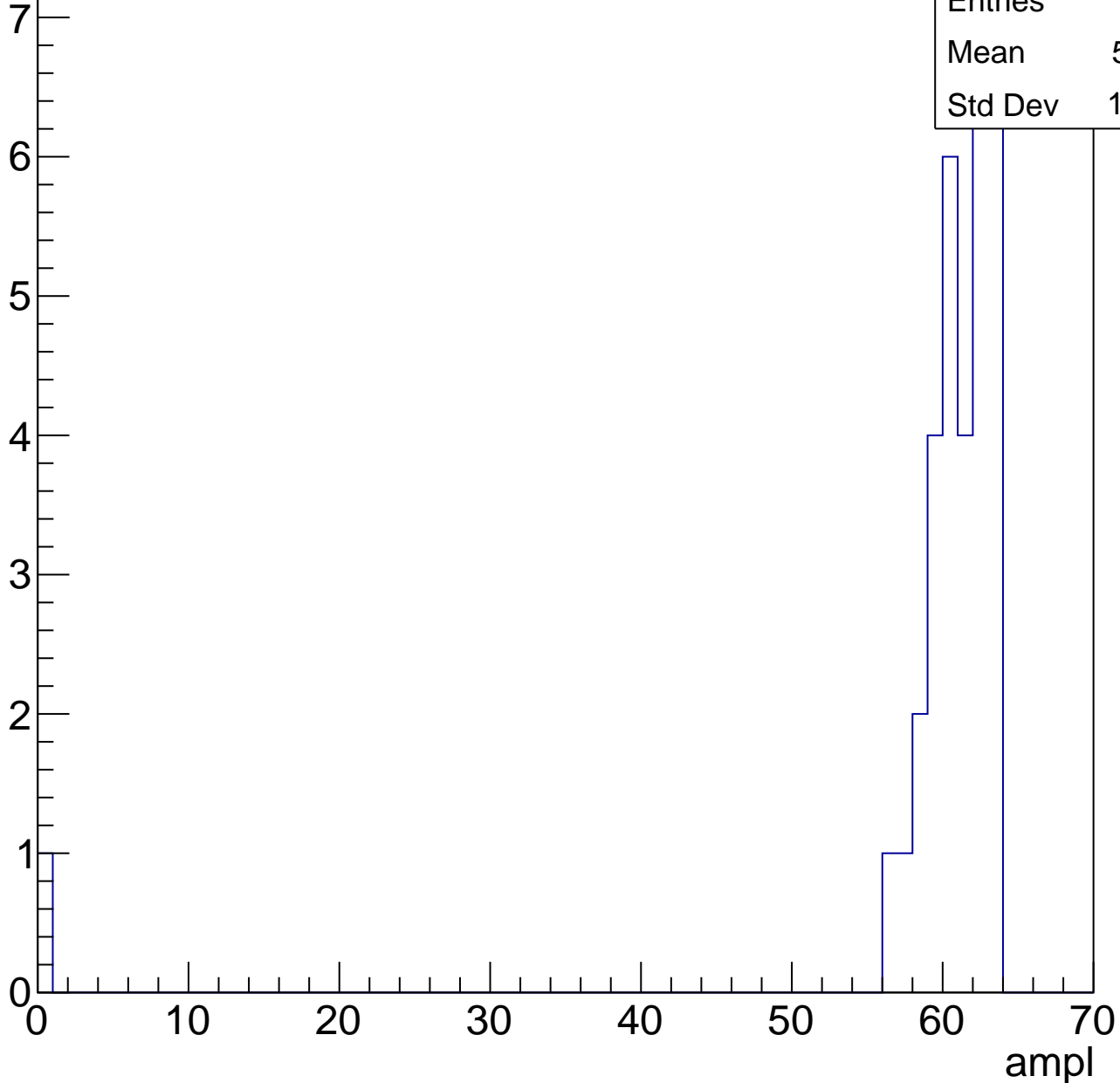


B1L103S, U10-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

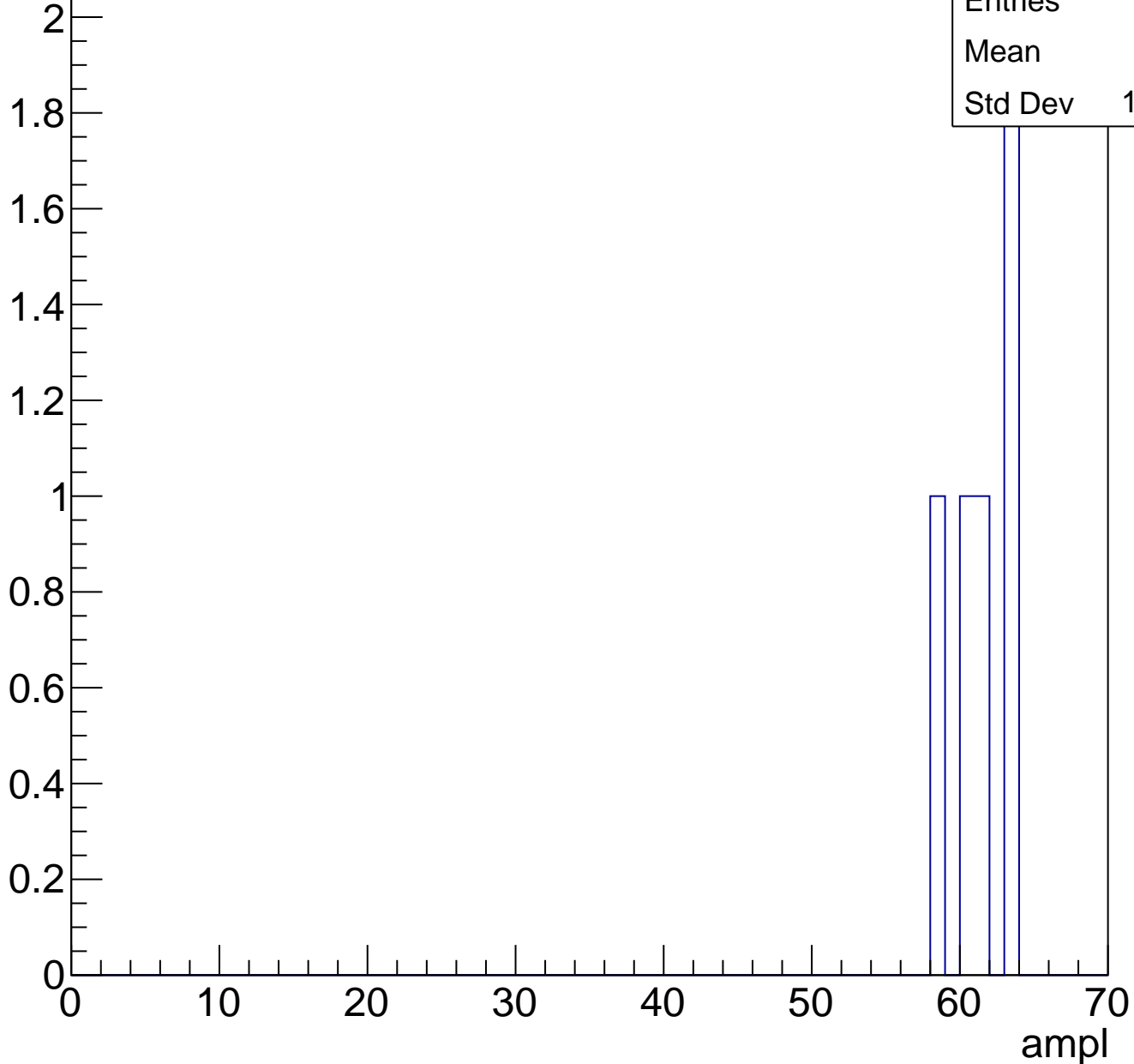
Entries	33
Mean	58.91
Std Dev	10.58



B1L103S, U10-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch113, adc0

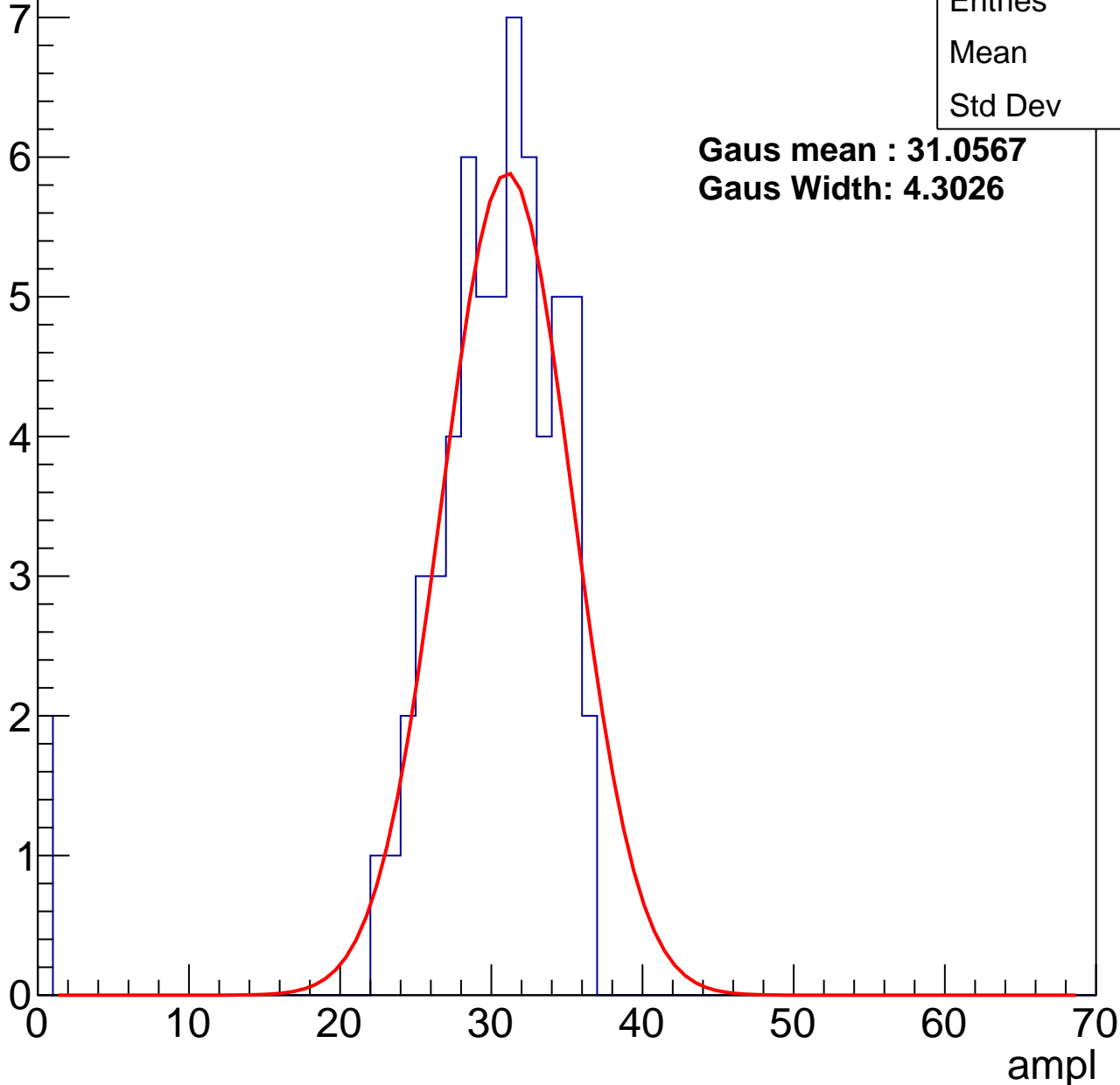
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	29.1
Std Dev	6.36

Gaus mean : 31.0567

Gaus Width: 4.3026



B1L103S, U10-ch113, adc1

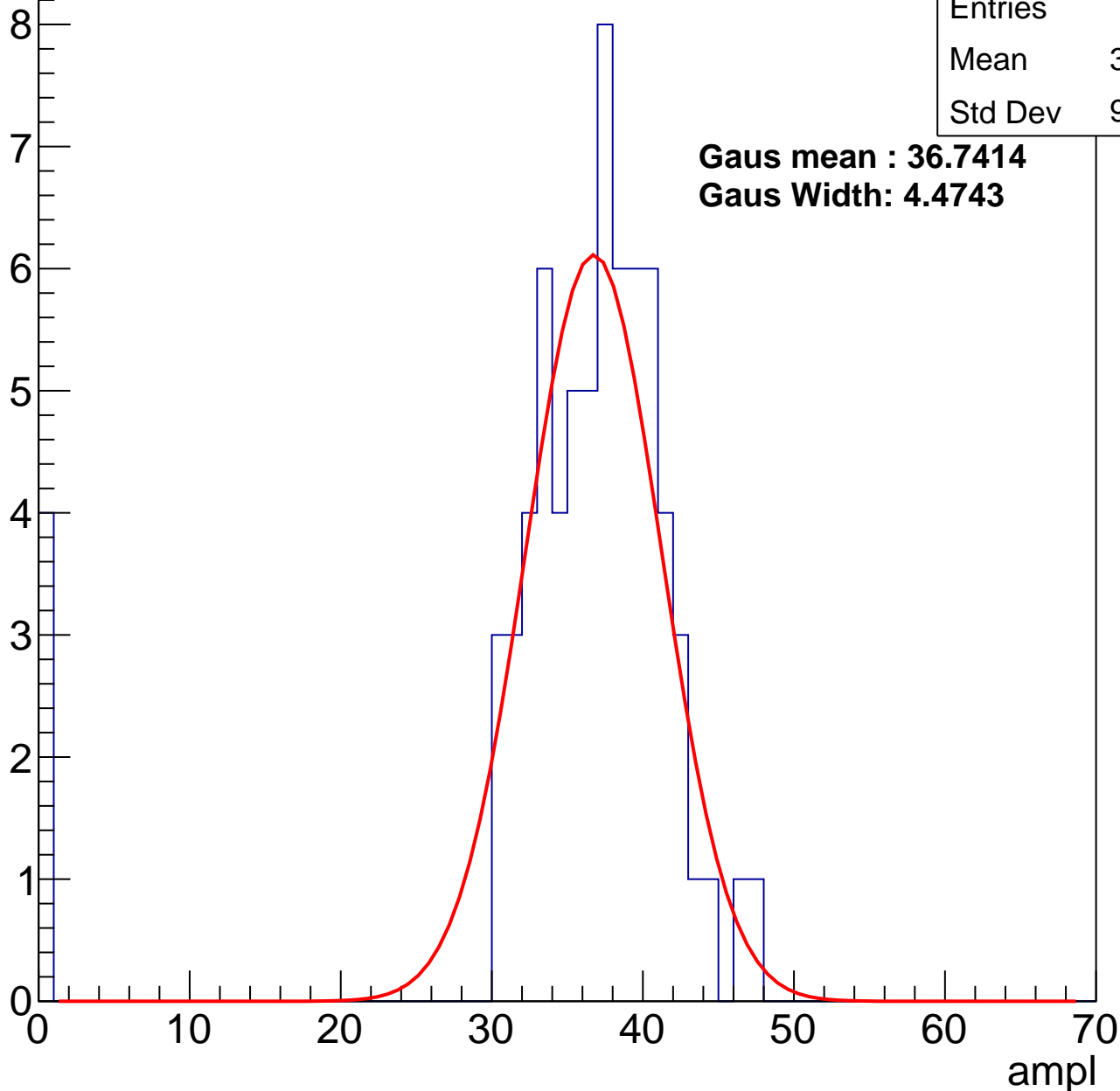
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.76
Std Dev	9.285

Gaus mean : 36.7414

Gaus Width: 4.4743



B1L103S, U10-ch113, adc2

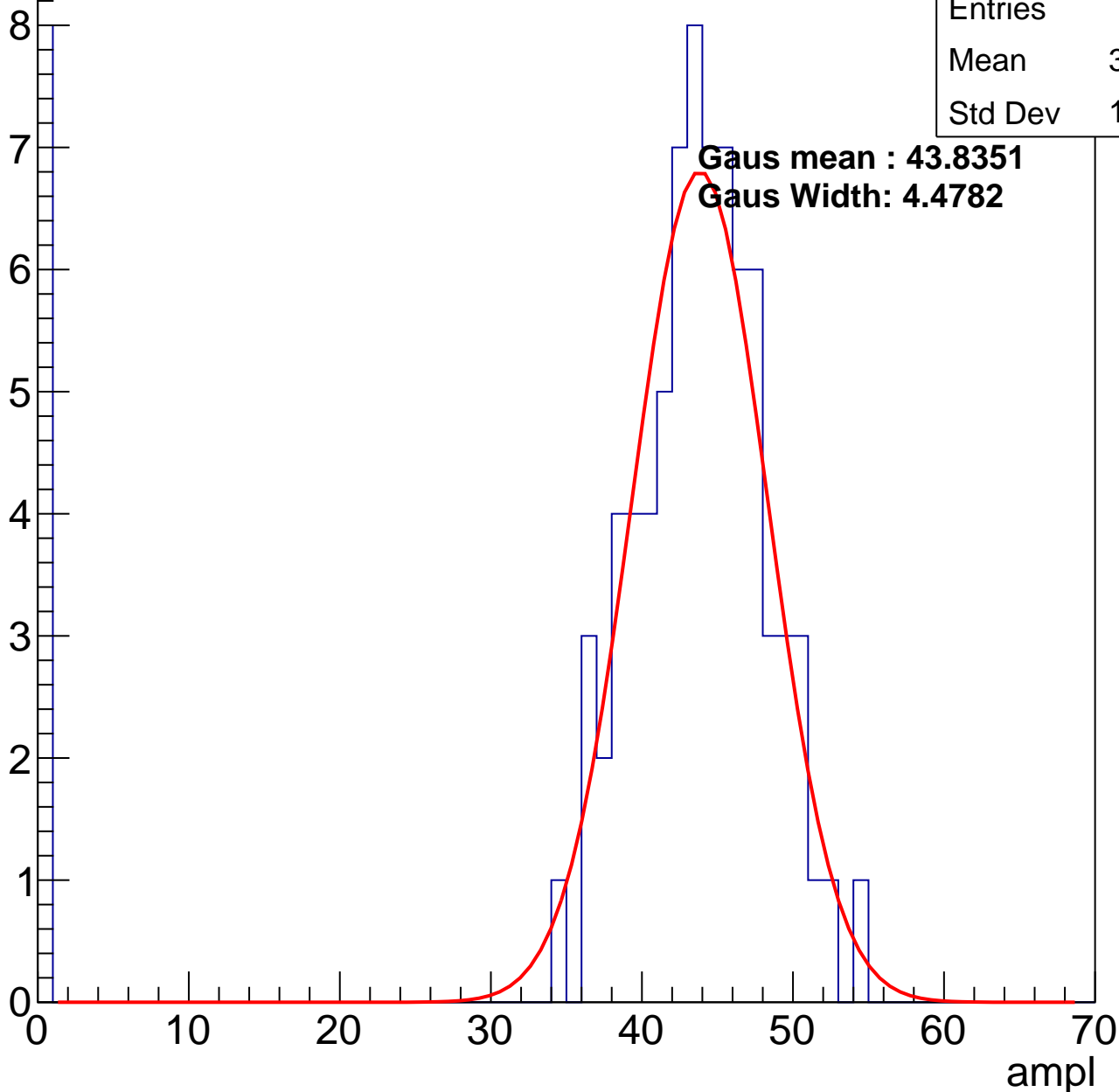
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	39.36
Std Dev	13.36

Gaus mean : 43.8351

Gaus Width: 4.4782

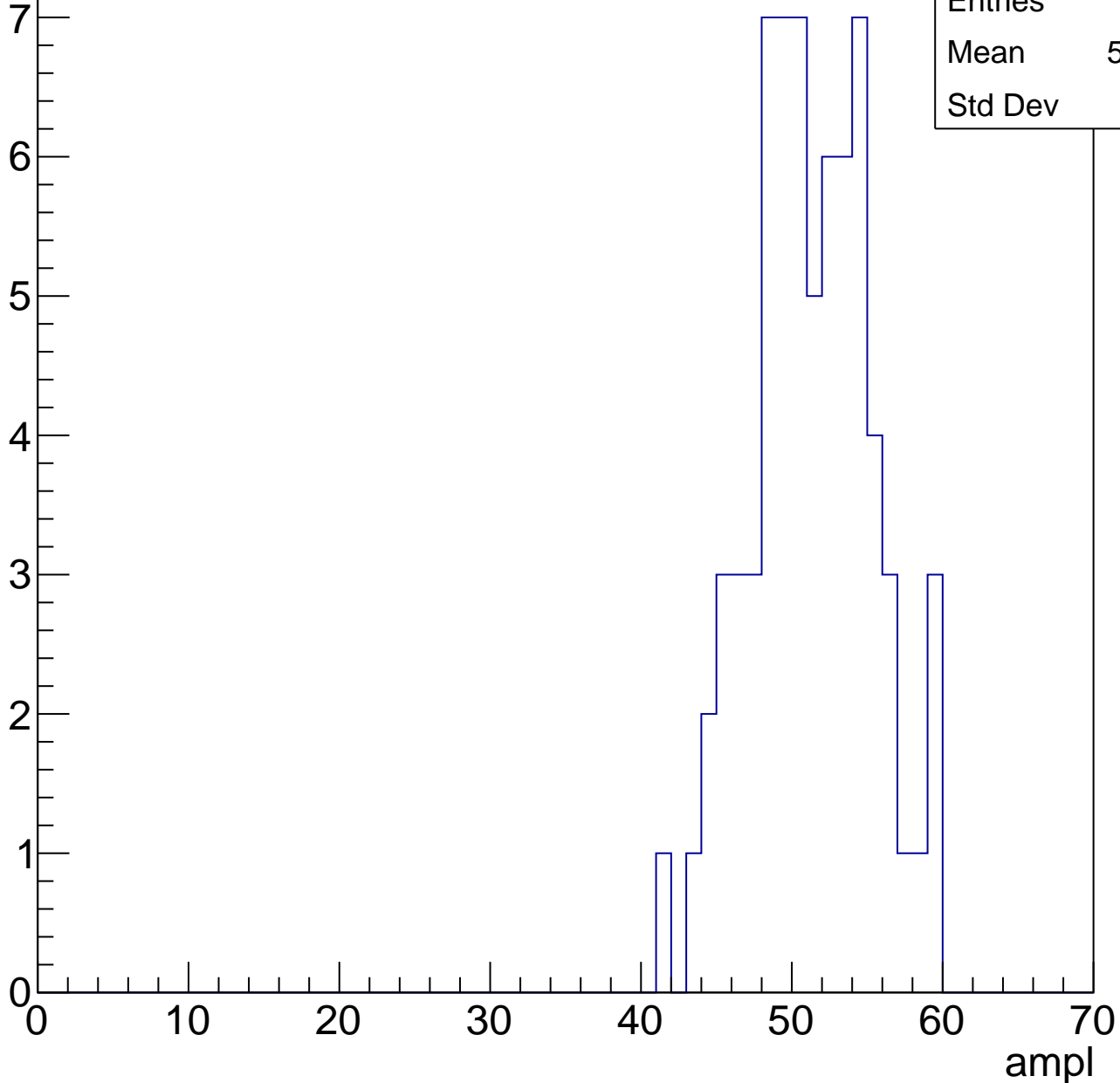


B1L103S, U10-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

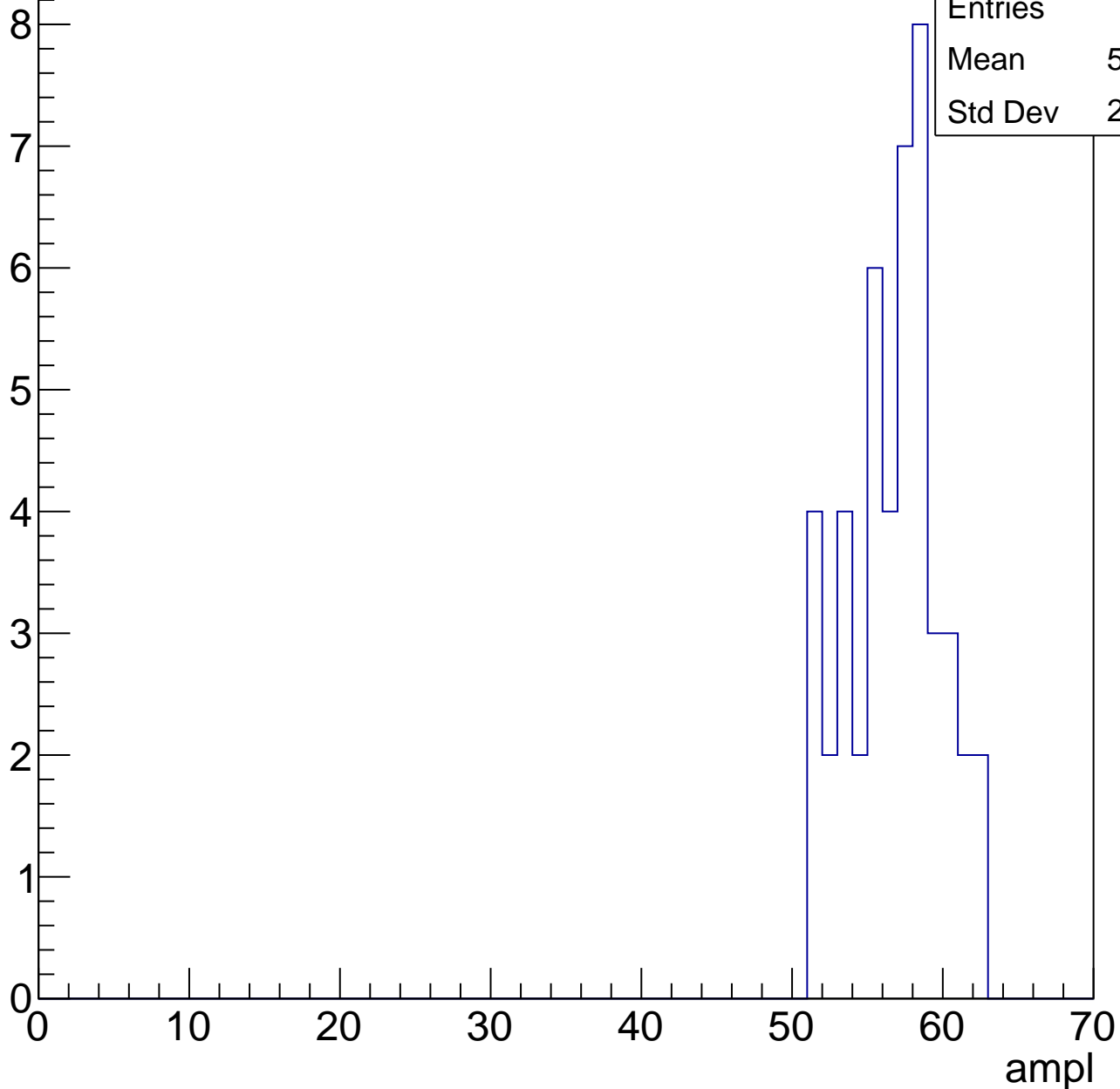
Entries	70
Mean	50.83
Std Dev	3.96



B1L103S, U10-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

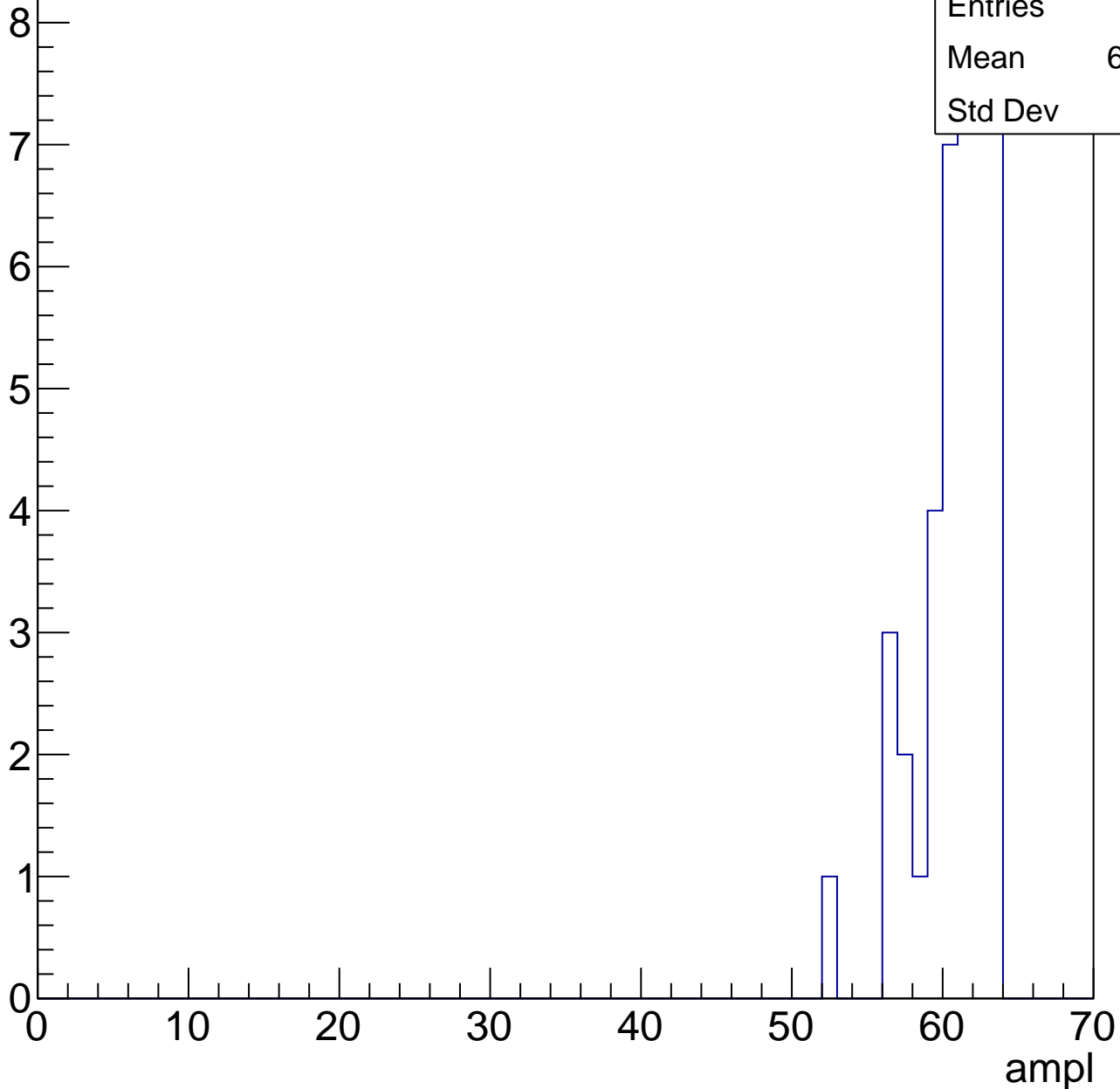


B1L103S, U10-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	60.38
Std Dev	2.41



B1L103S, U10-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

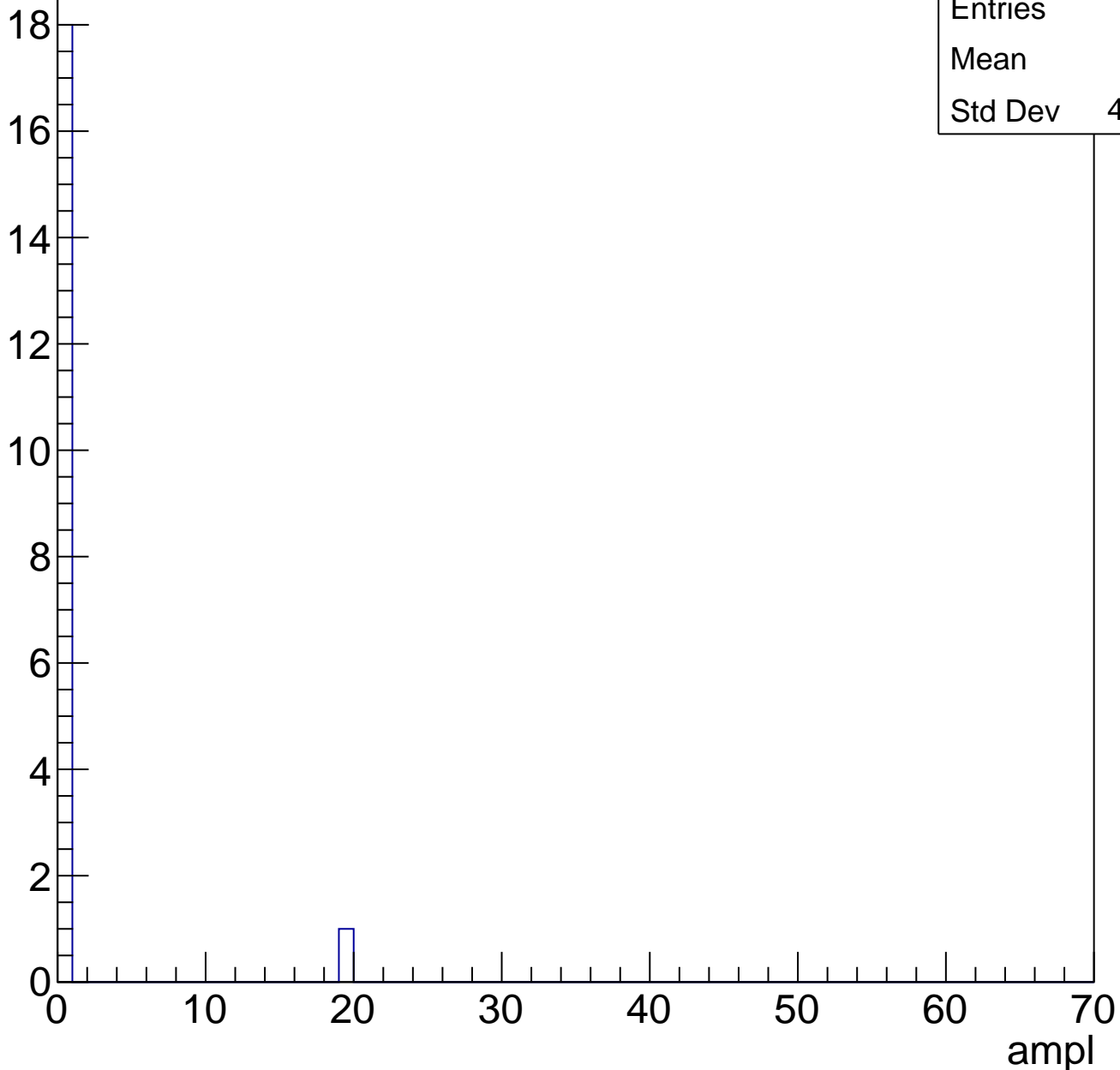
Entry



B1L103S, U10-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1
Std Dev	4.243

B1L103S, U10-ch114, adc0

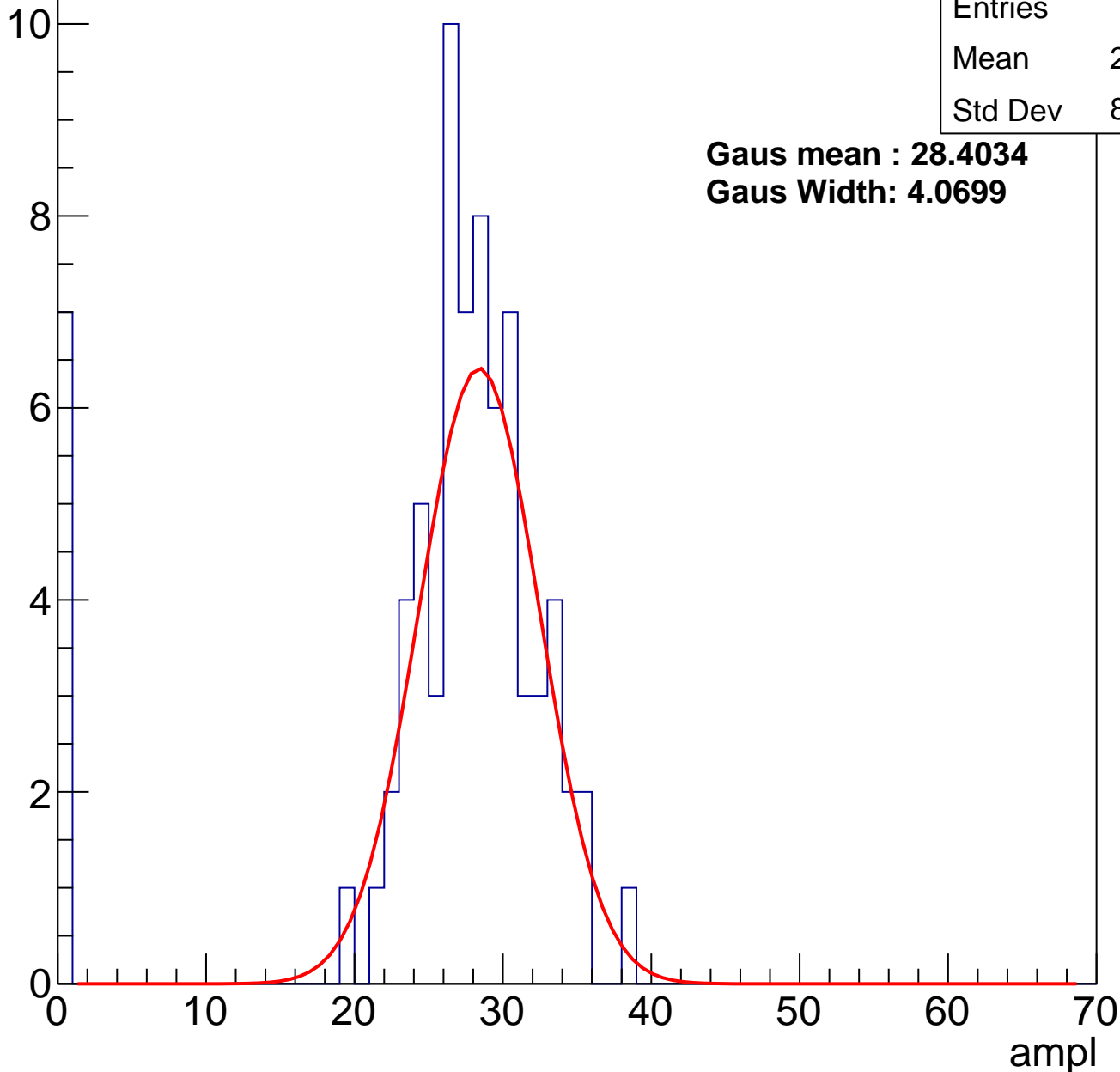
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	25.33
Std Dev	8.797

Gaus mean : 28.4034

Gaus Width: 4.0699

Entry



B1L103S, U10-ch114, adc1

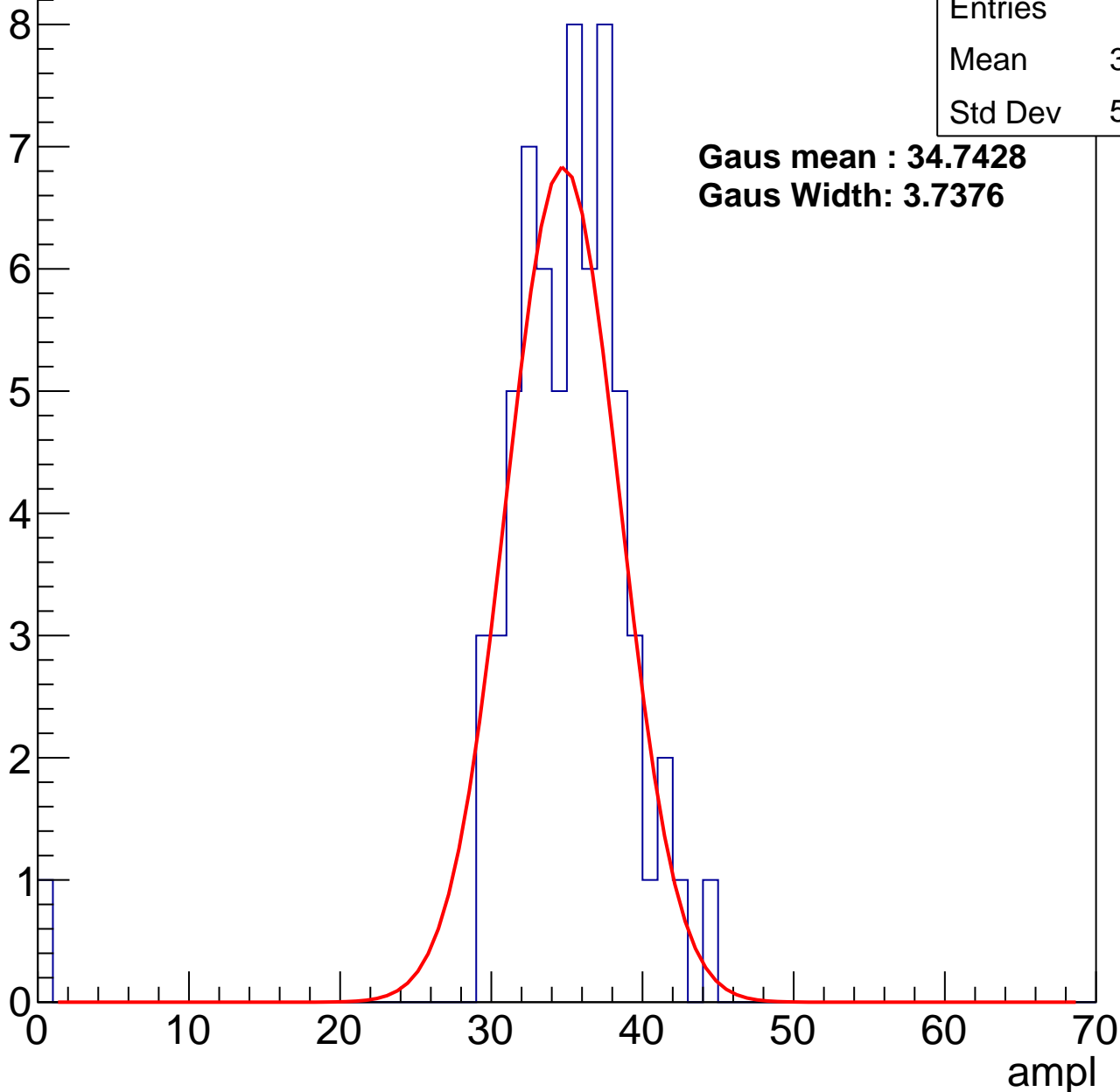
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.32
Std Dev	5.418

Gaus mean : 34.7428

Gaus Width: 3.7376



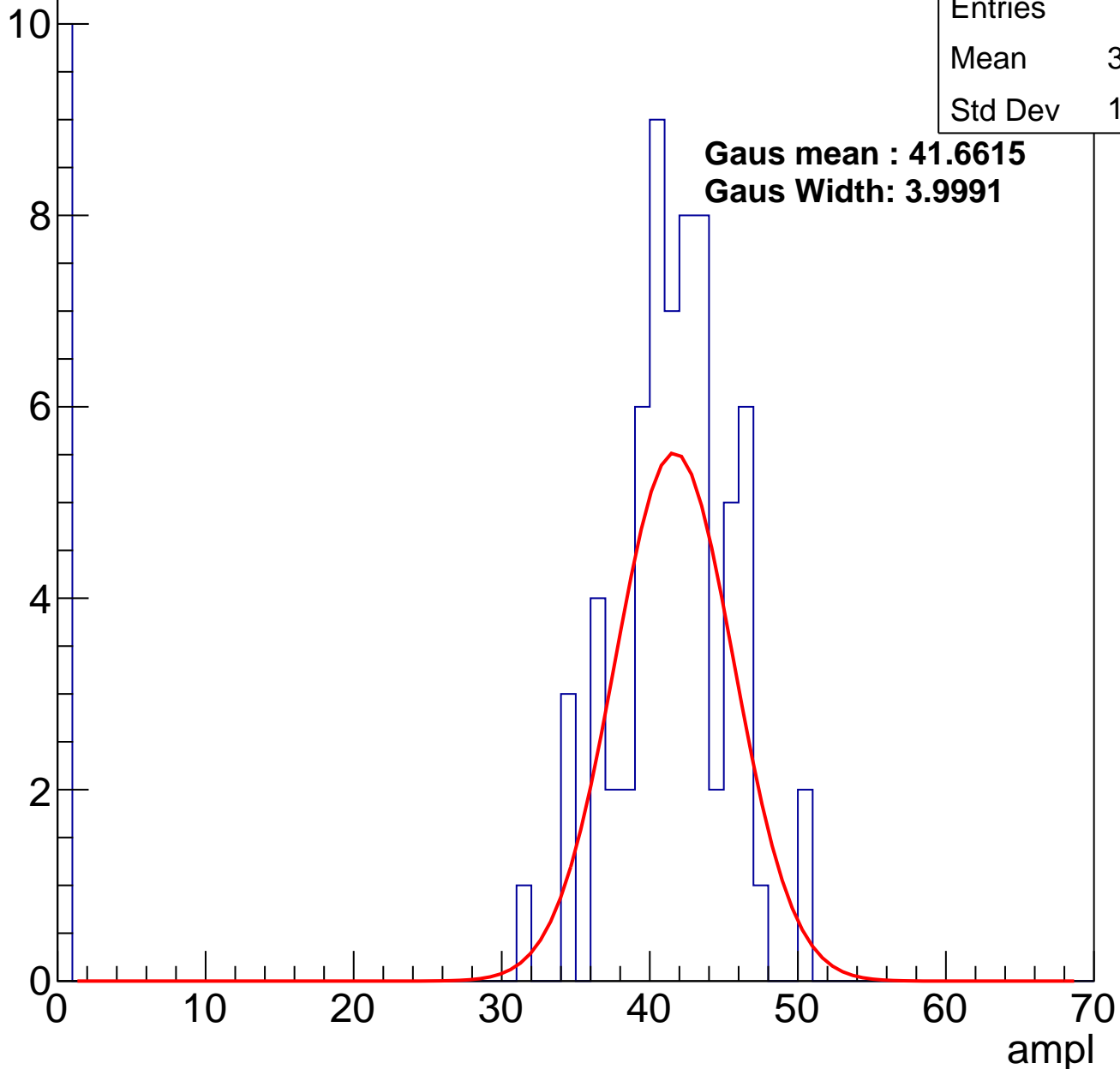
B1L103S, U10-ch114, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.84
Std Dev	14.37

Gaus mean : 41.6615
Gaus Width: 3.9991

Entry

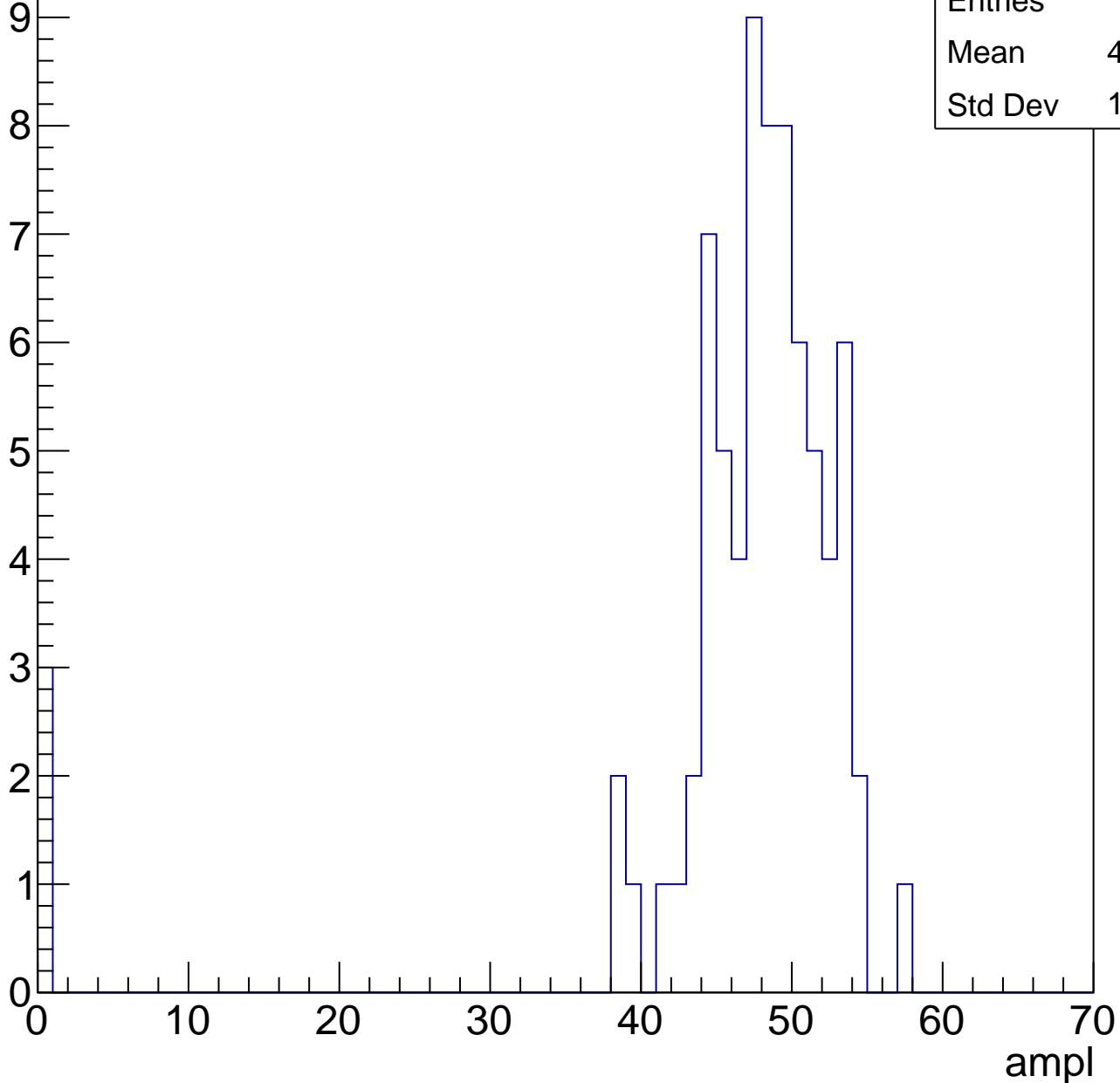


B1L103S, U10-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.95
Std Dev	10.09

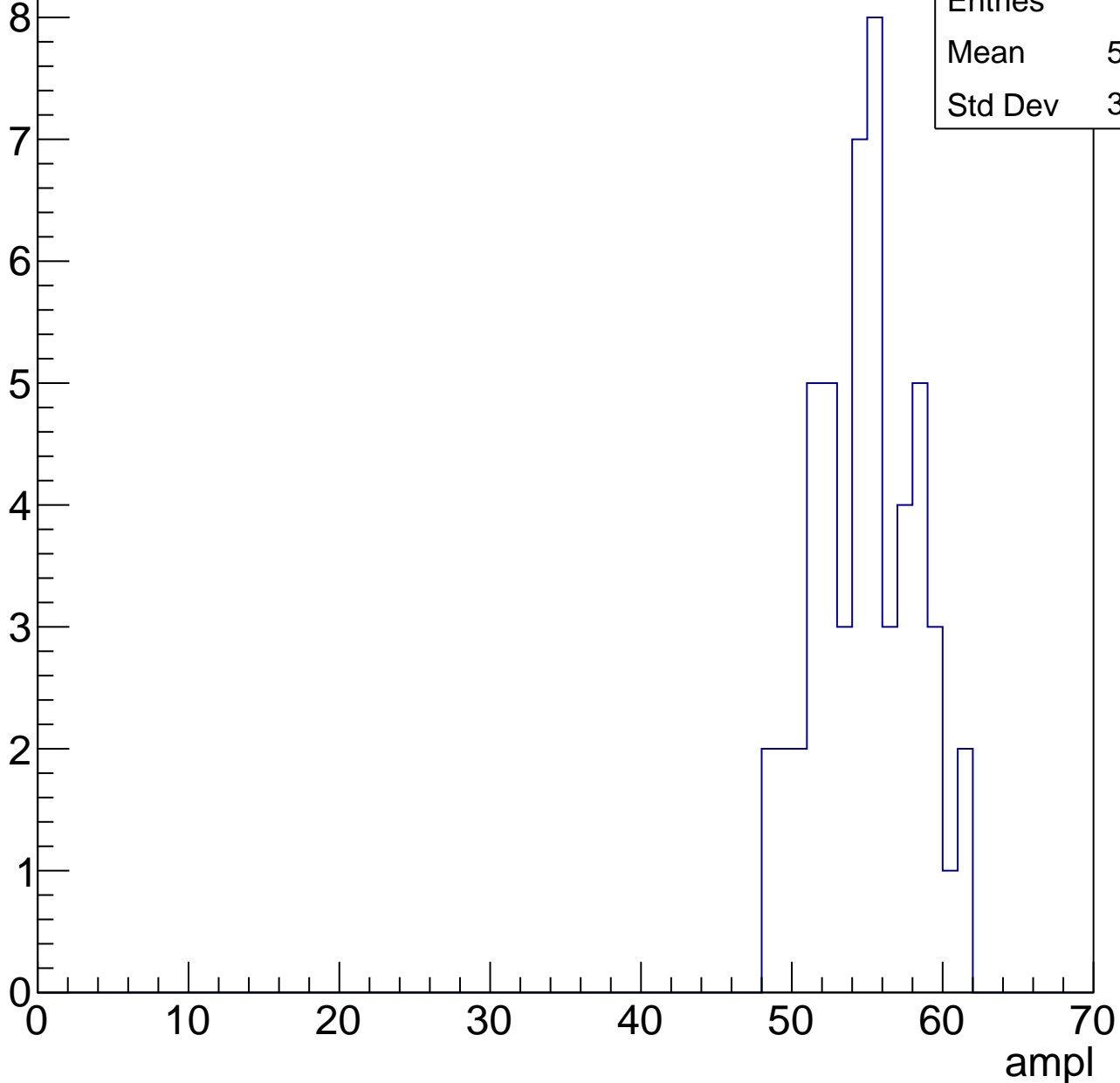


B1L103S, U10-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

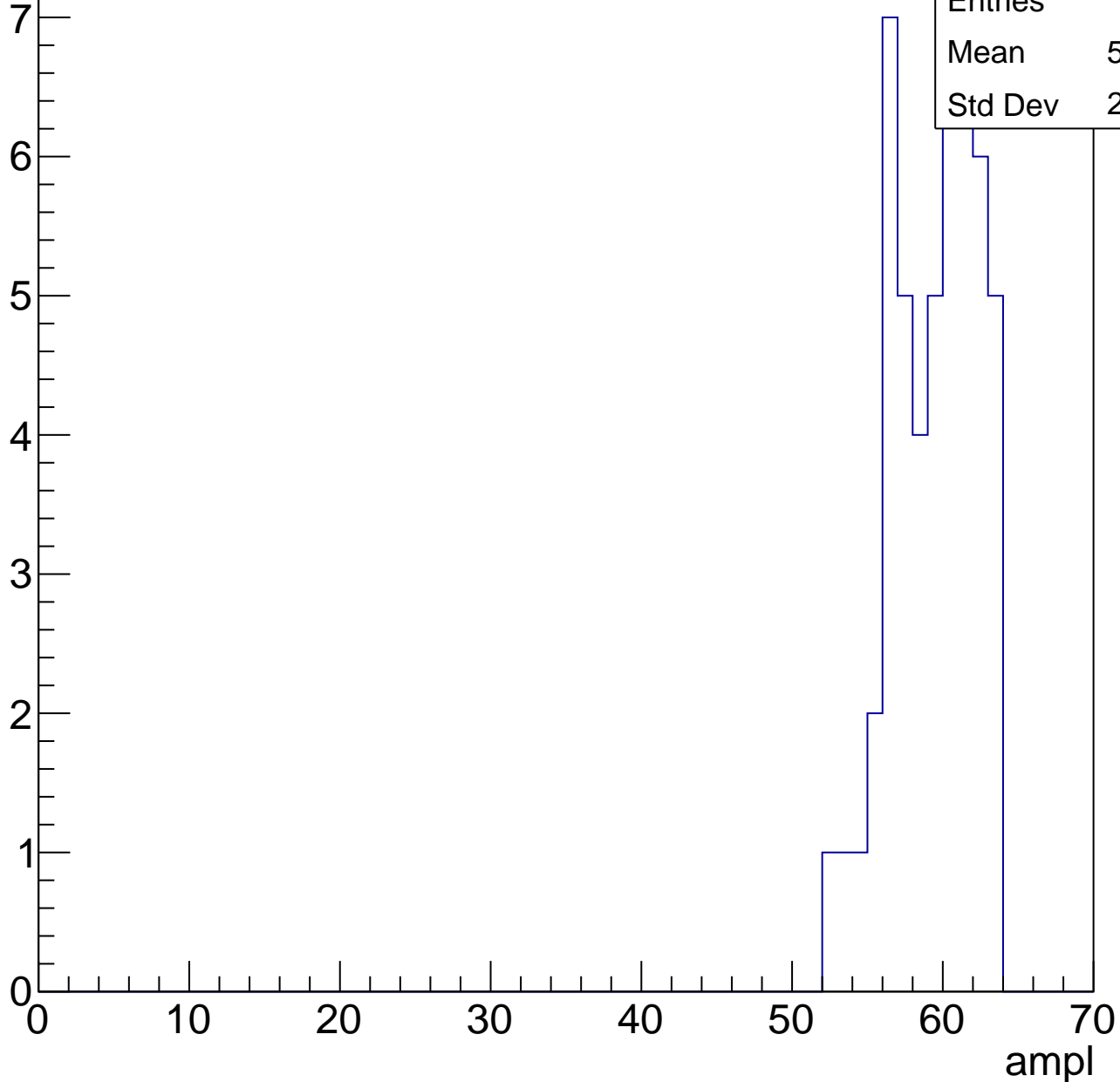
Entries	52
Mean	54.44
Std Dev	3.272



B1L103S, U10-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



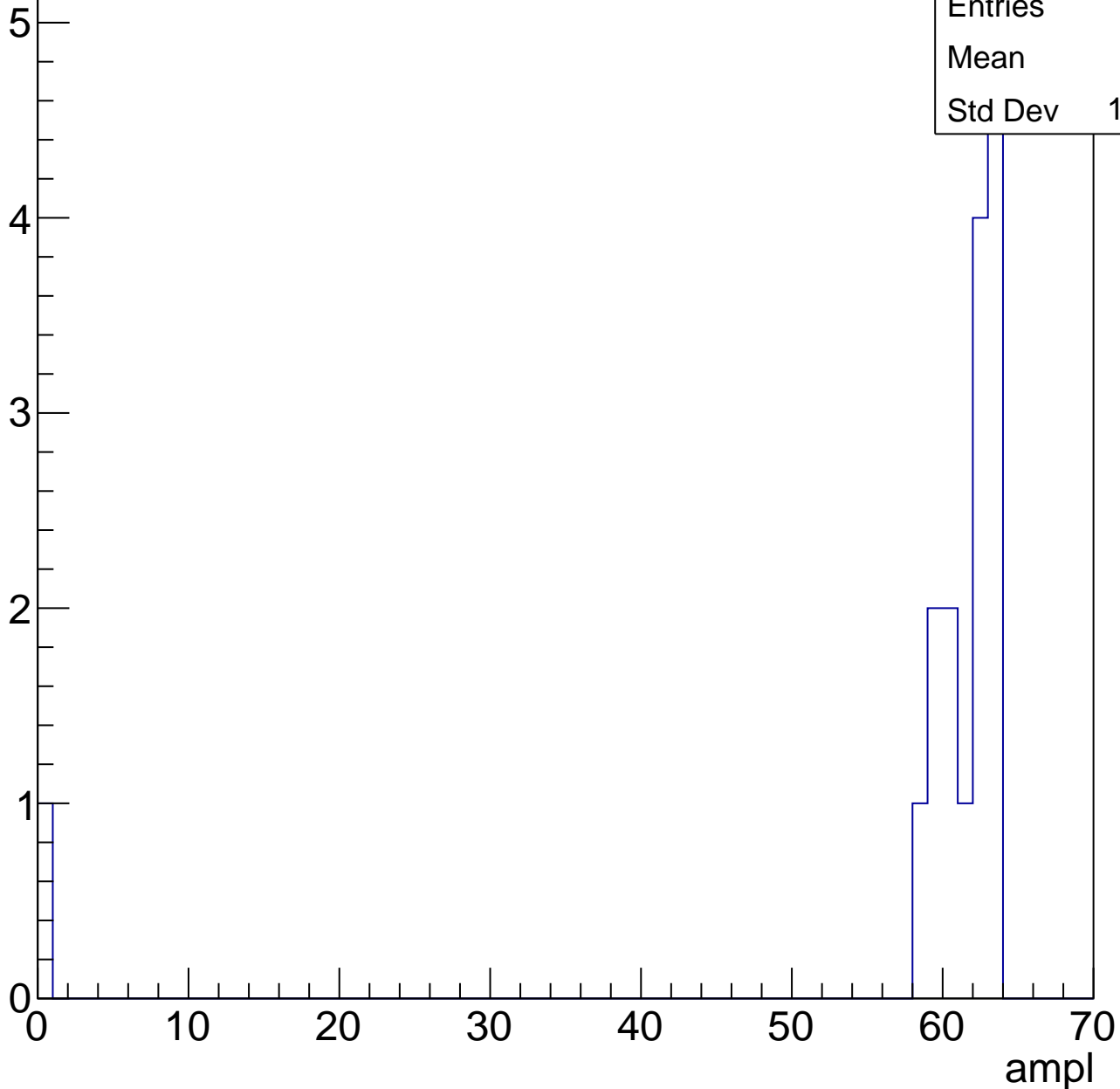
Entries	51
Mean	58.96
Std Dev	2.793

B1L103S, U10-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

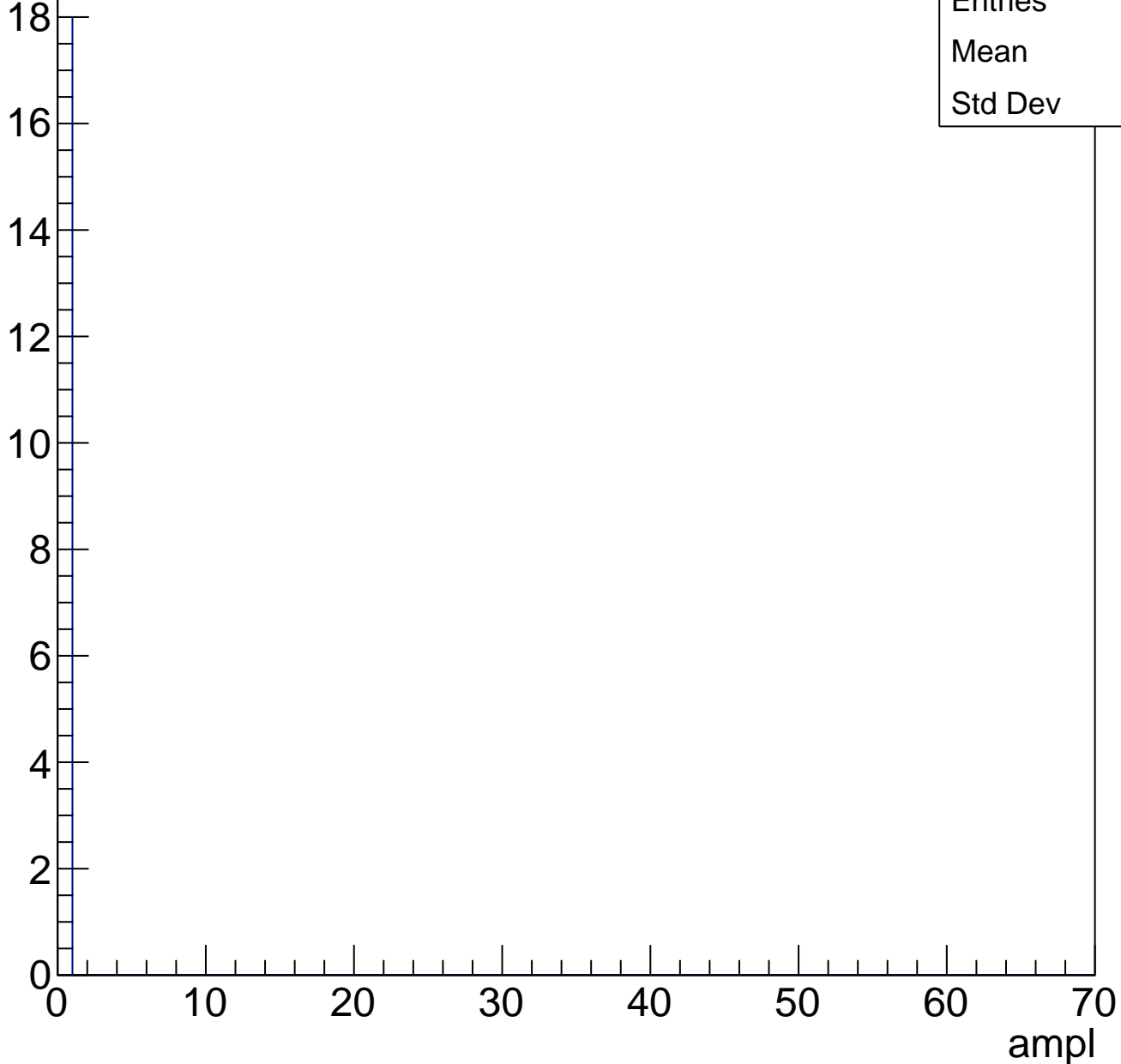
Entries	16
Mean	57.5
Std Dev	14.93



B1L103S, U10-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U10-ch115, adc0

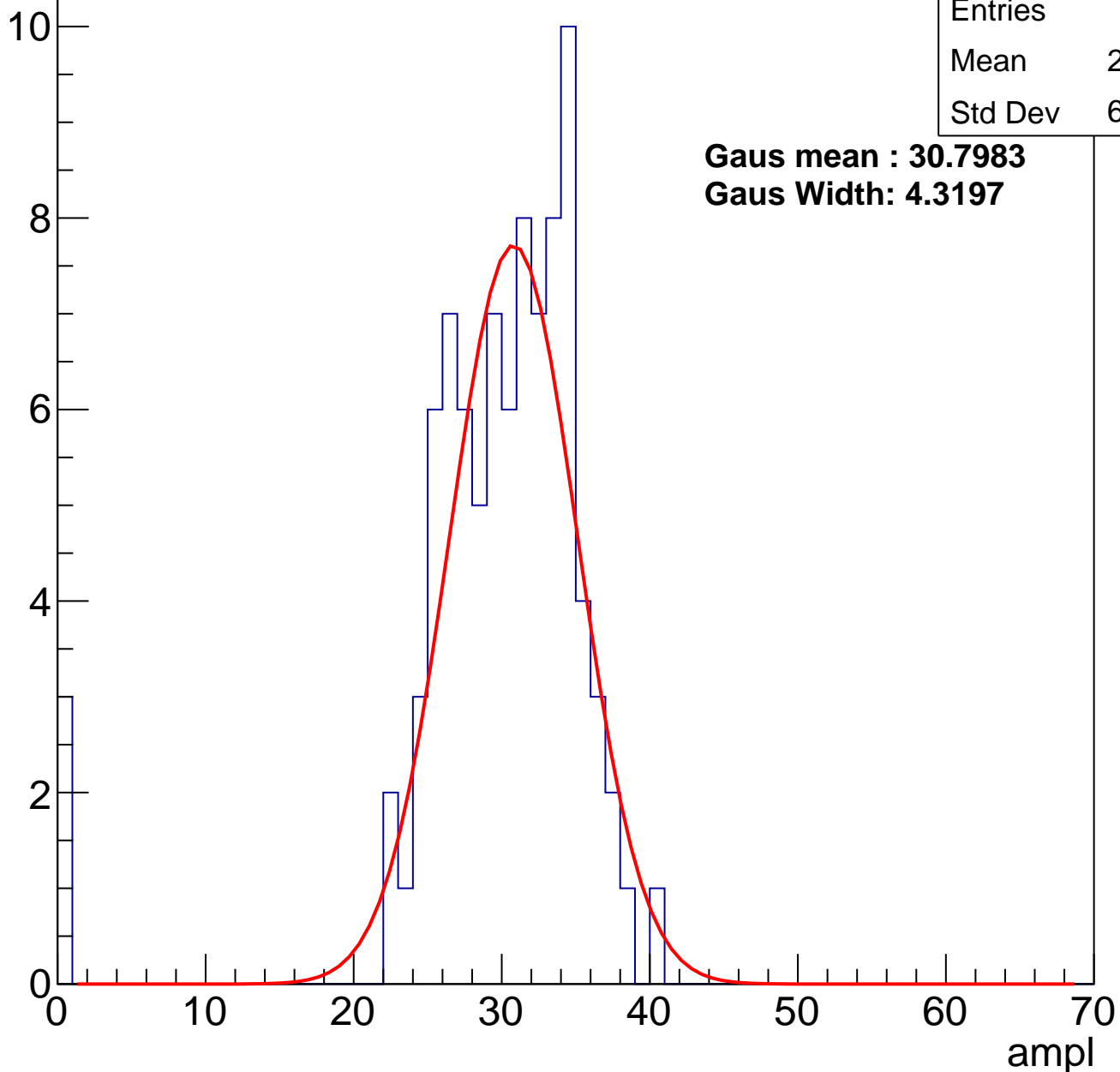
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	29.24
Std Dev	6.679

Gaus mean : 30.7983

Gaus Width: 4.3197

Entry



B1L103S, U10-ch115, adc1

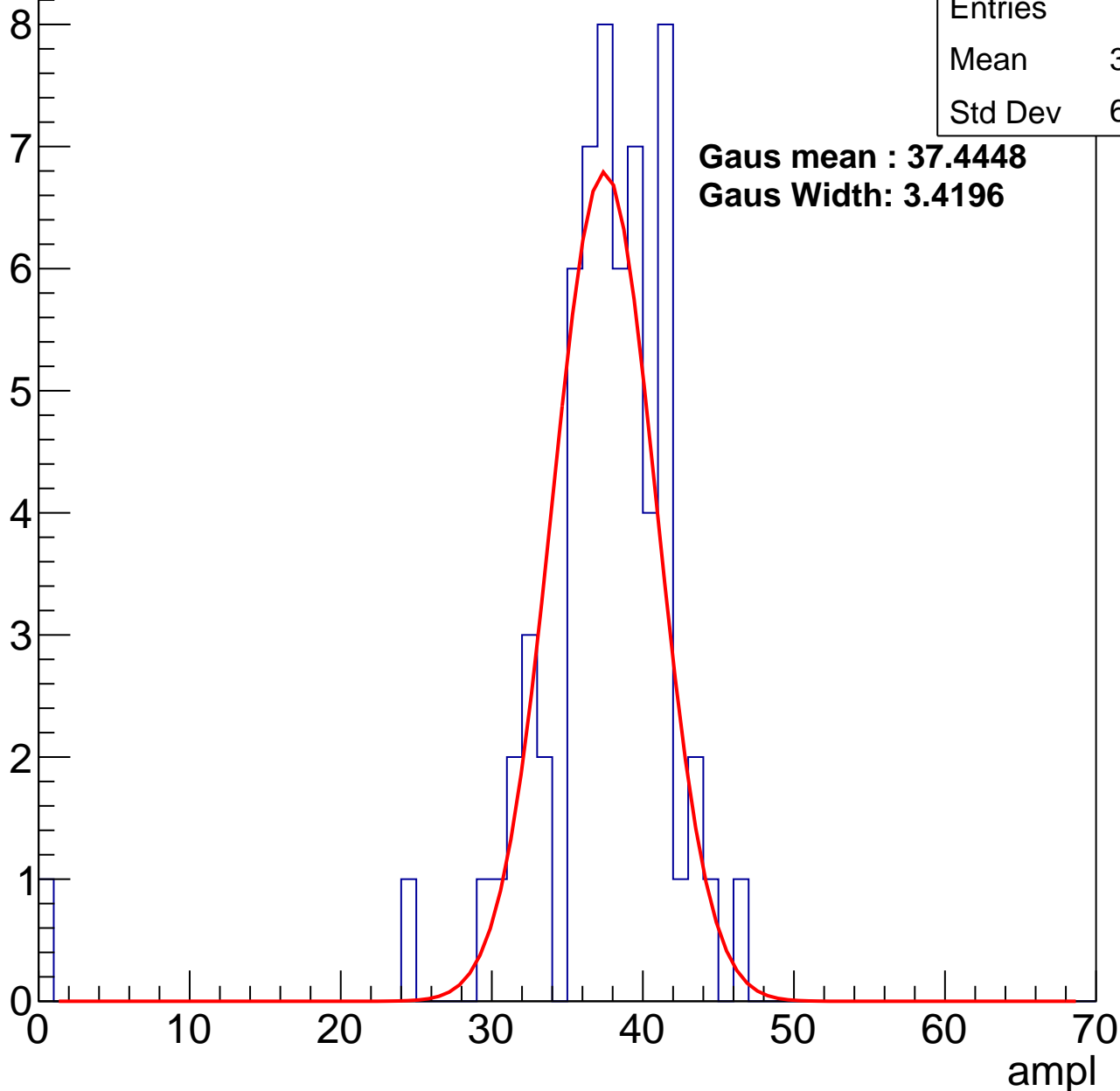
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.65
Std Dev	6.062

Gaus mean : 37.4448

Gaus Width: 3.4196



B1L103S, U10-ch115, adc2

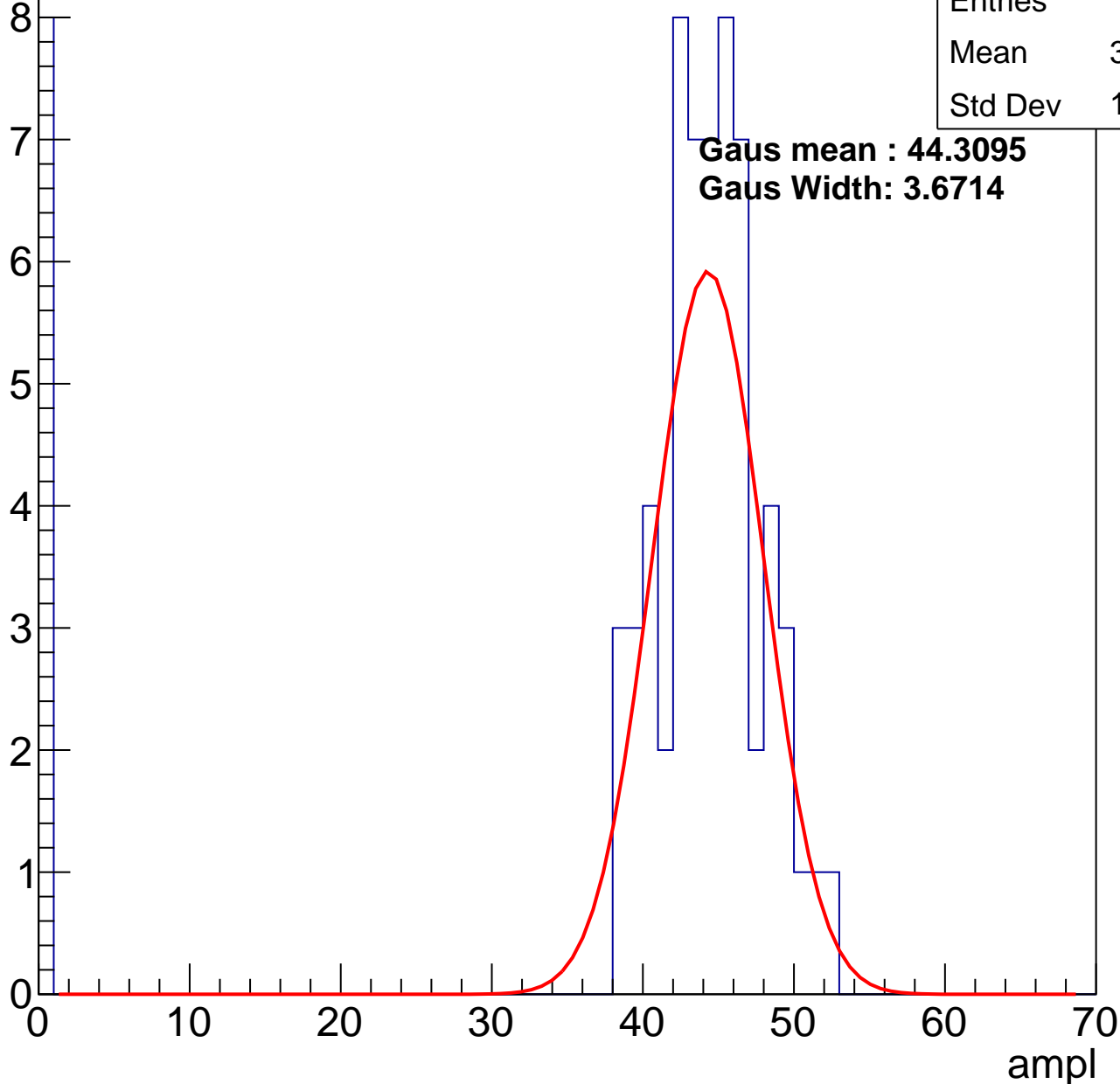
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	38.93
Std Dev	14.43

Gaus mean : 44.3095

Gaus Width: 3.6714

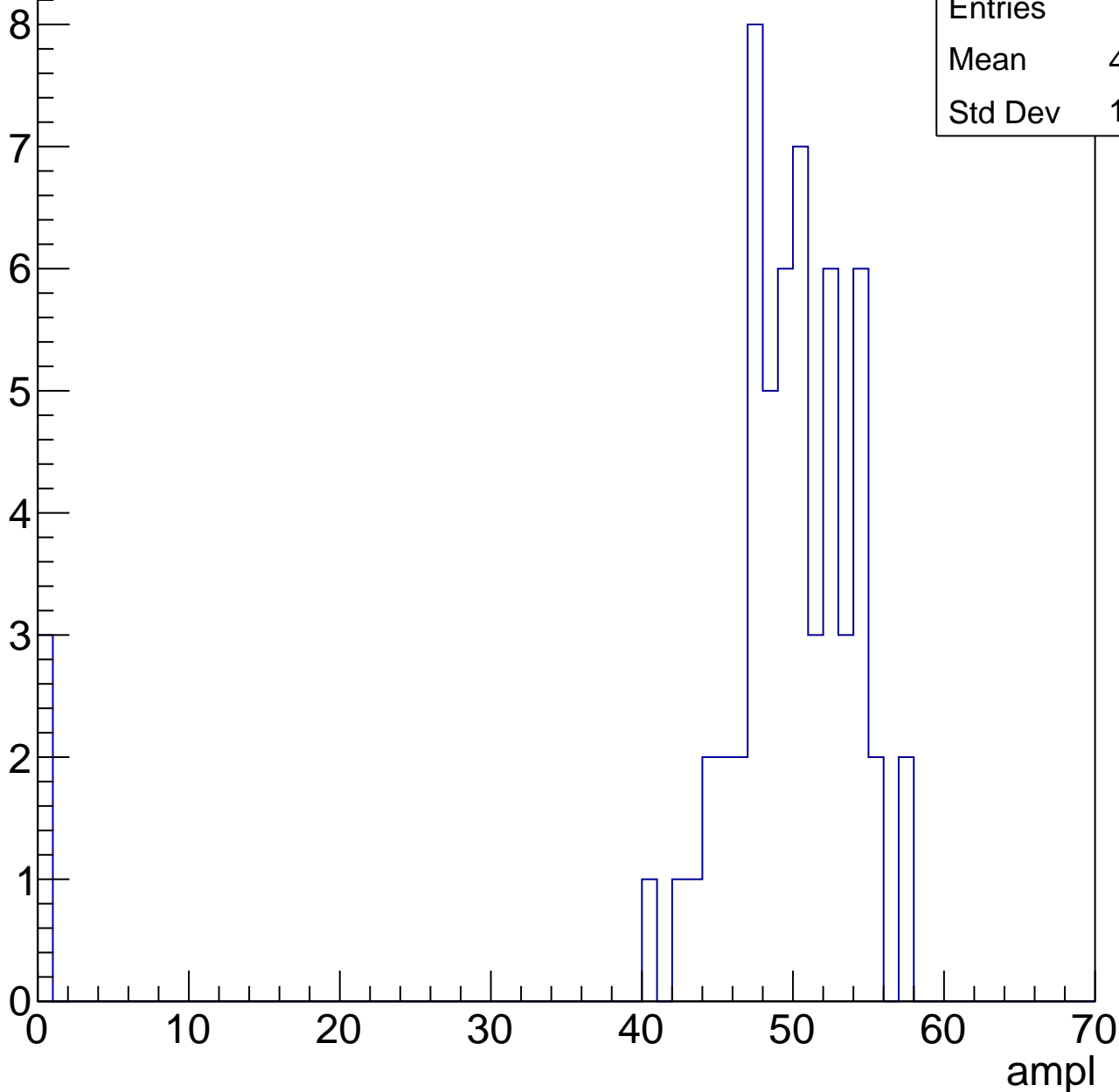


B1L103S, U10-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

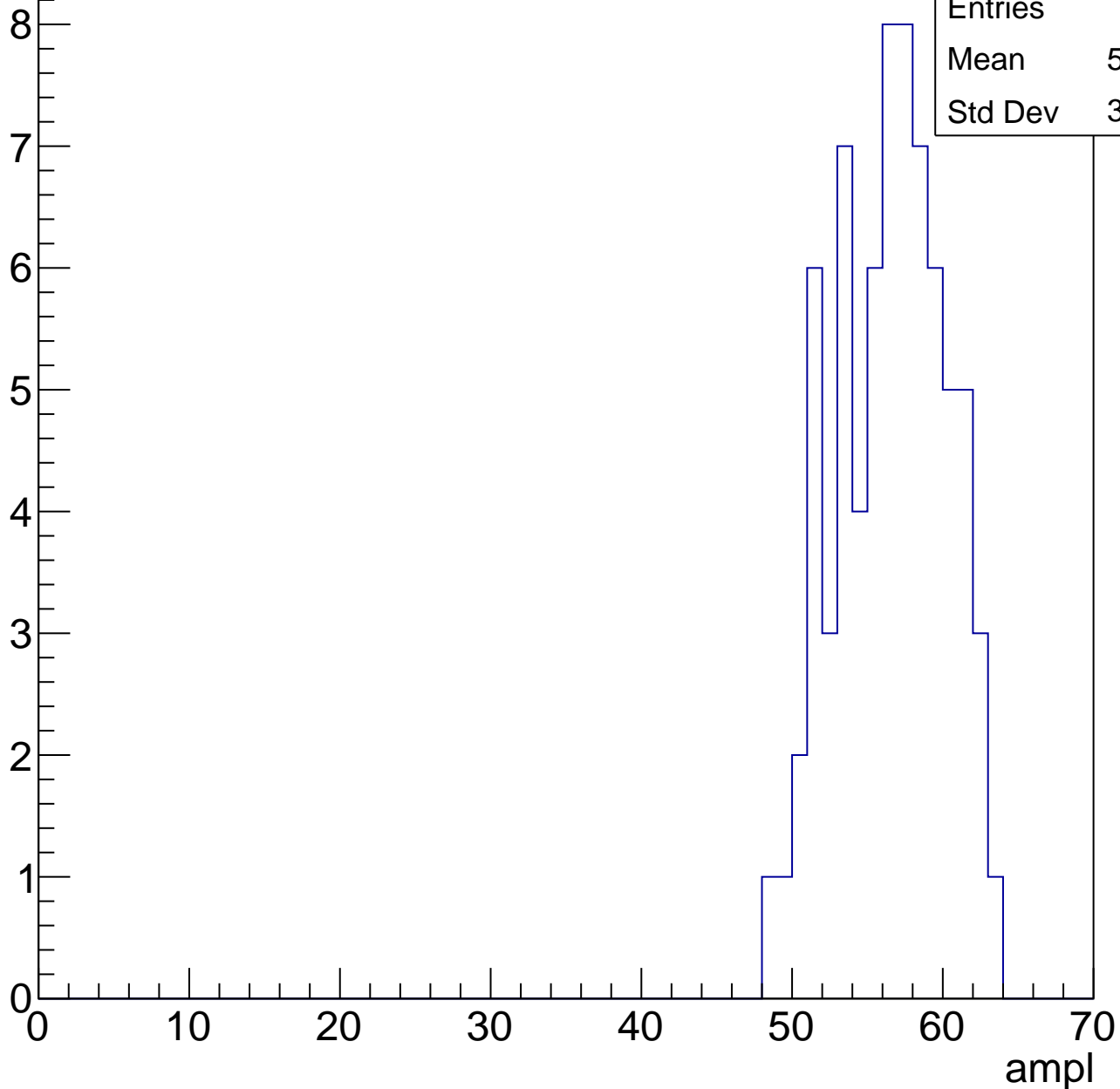
Entries	60
Mean	47.12
Std Dev	11.38



B1L103S, U10-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

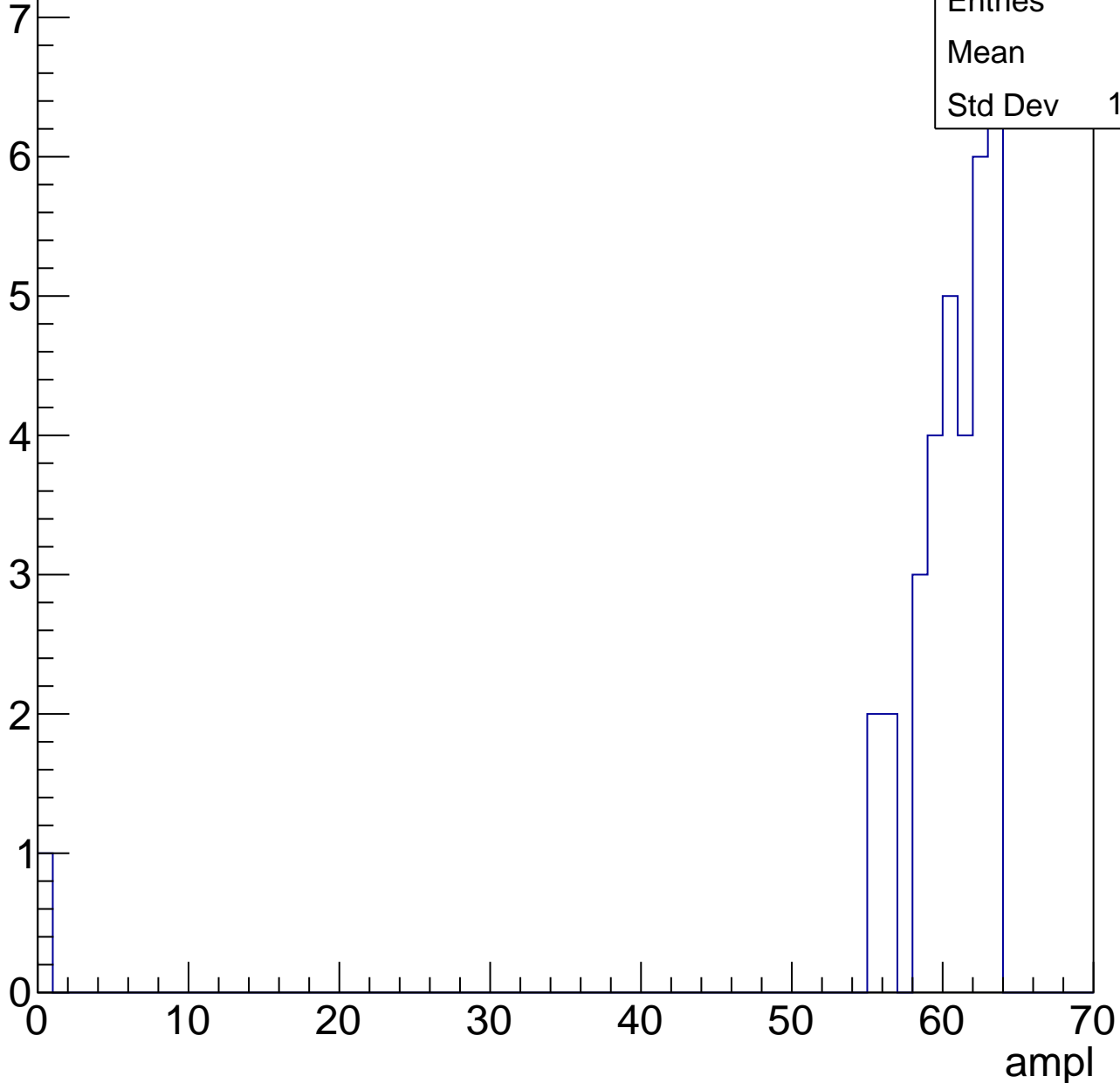


B1L103S, U10-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	58.5
Std Dev	10.45



B1L103S, U10-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

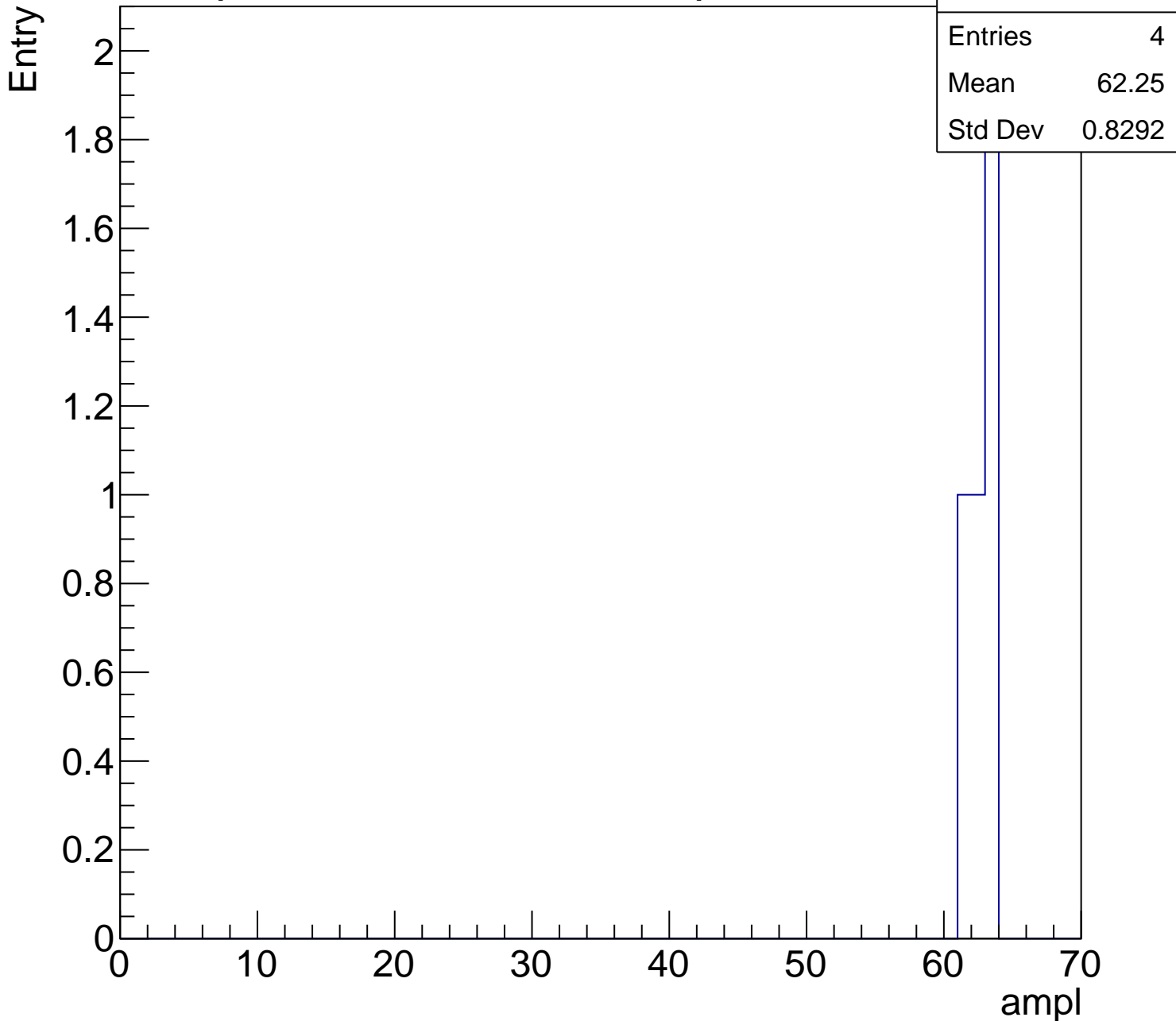
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

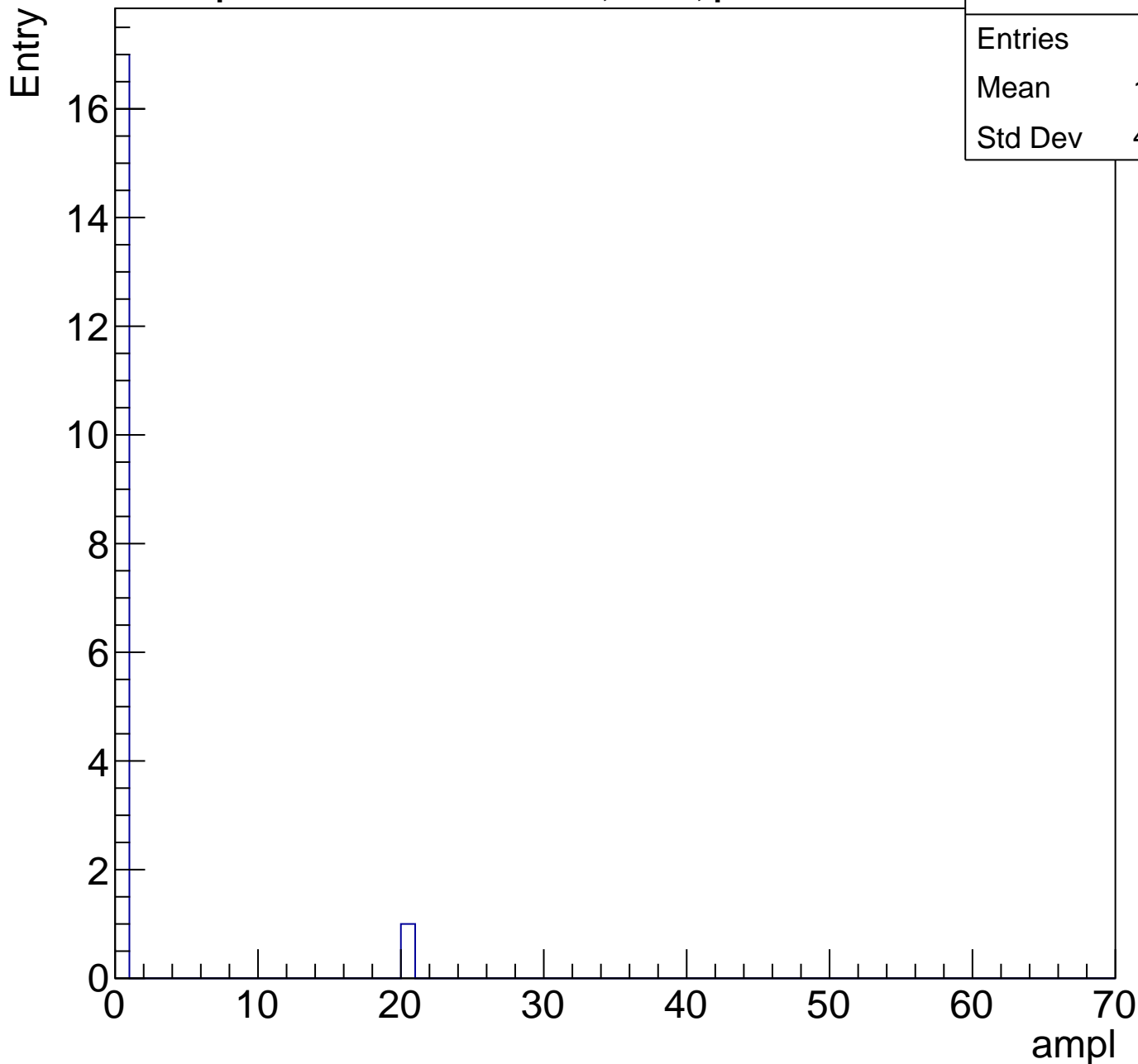
ampl



B1L103S, U10-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.111
Std Dev	4.581



B1L103S, U10-ch116, adc0

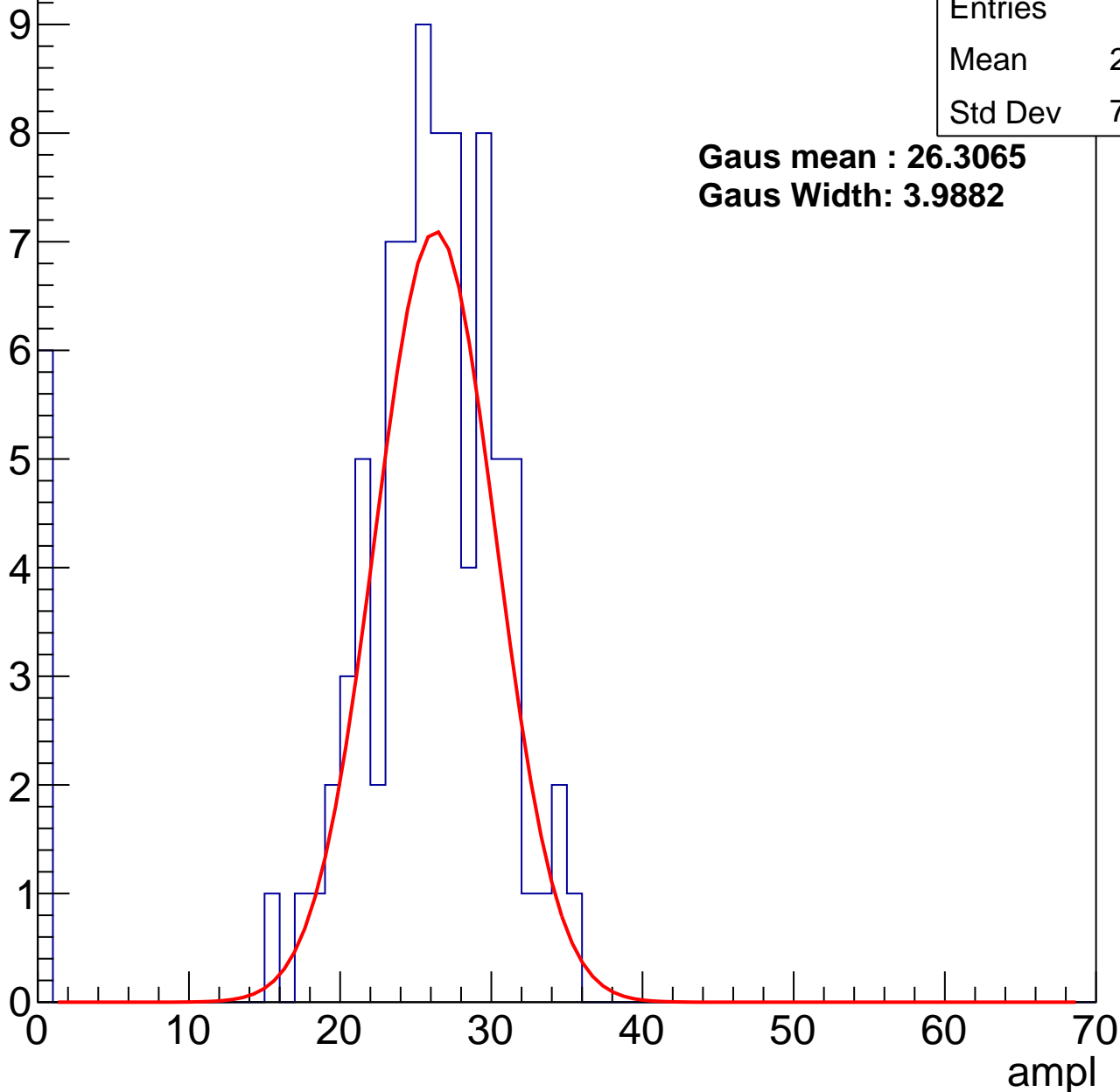
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	24.05
Std Dev	7.617

Gaus mean : 26.3065

Gaus Width: 3.9882



B1L103S, U10-ch116, adc1

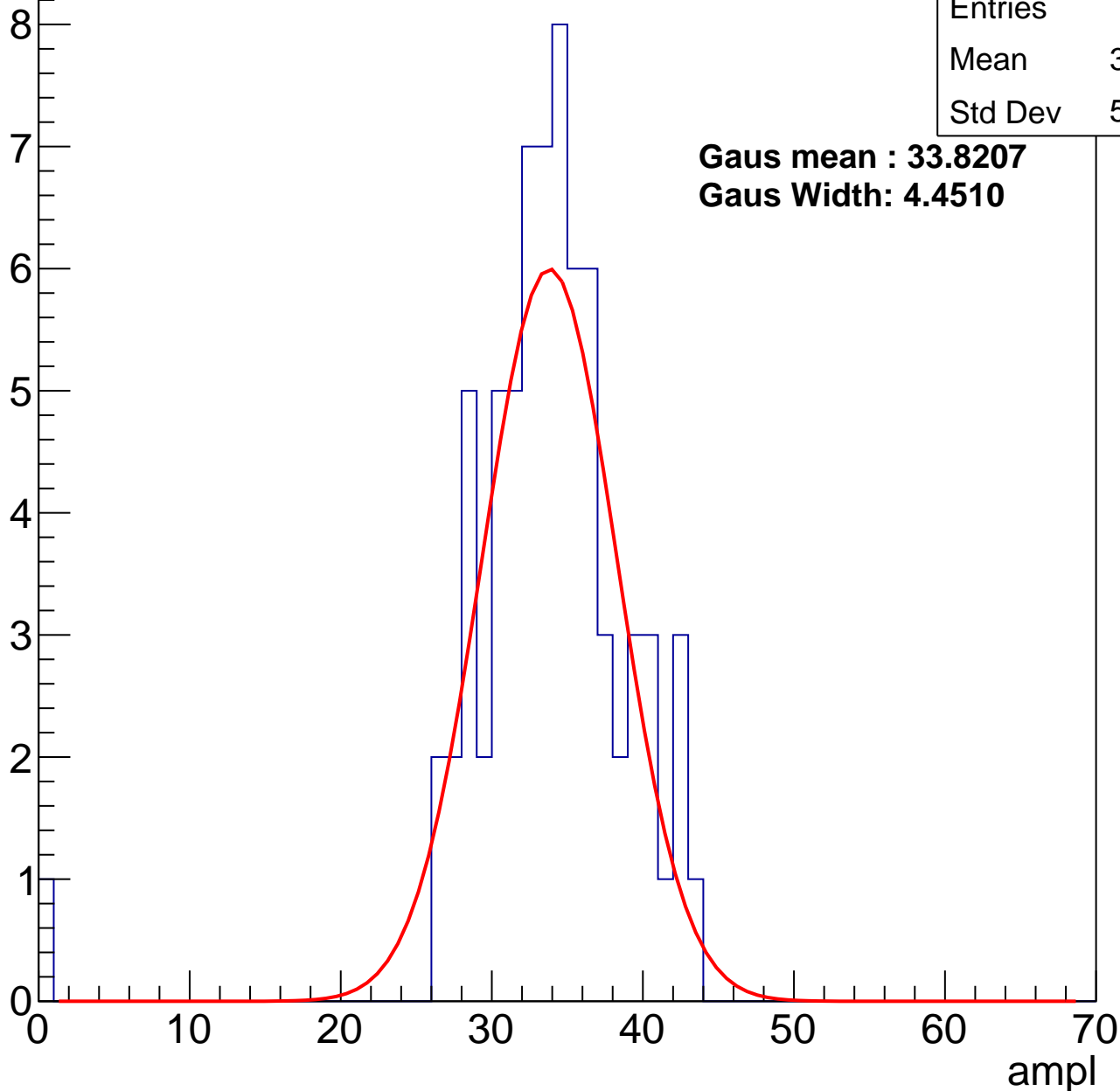
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	33.28
Std Dev	5.687

Gaus mean : 33.8207

Gaus Width: 4.4510



B1L103S, U10-ch116, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	33.16
Std Dev	15.15

Gaus mean : 40.0951

Gaus Width: 4.9413

Entry

12

10

8

6

4

2

0

0

10

20

30

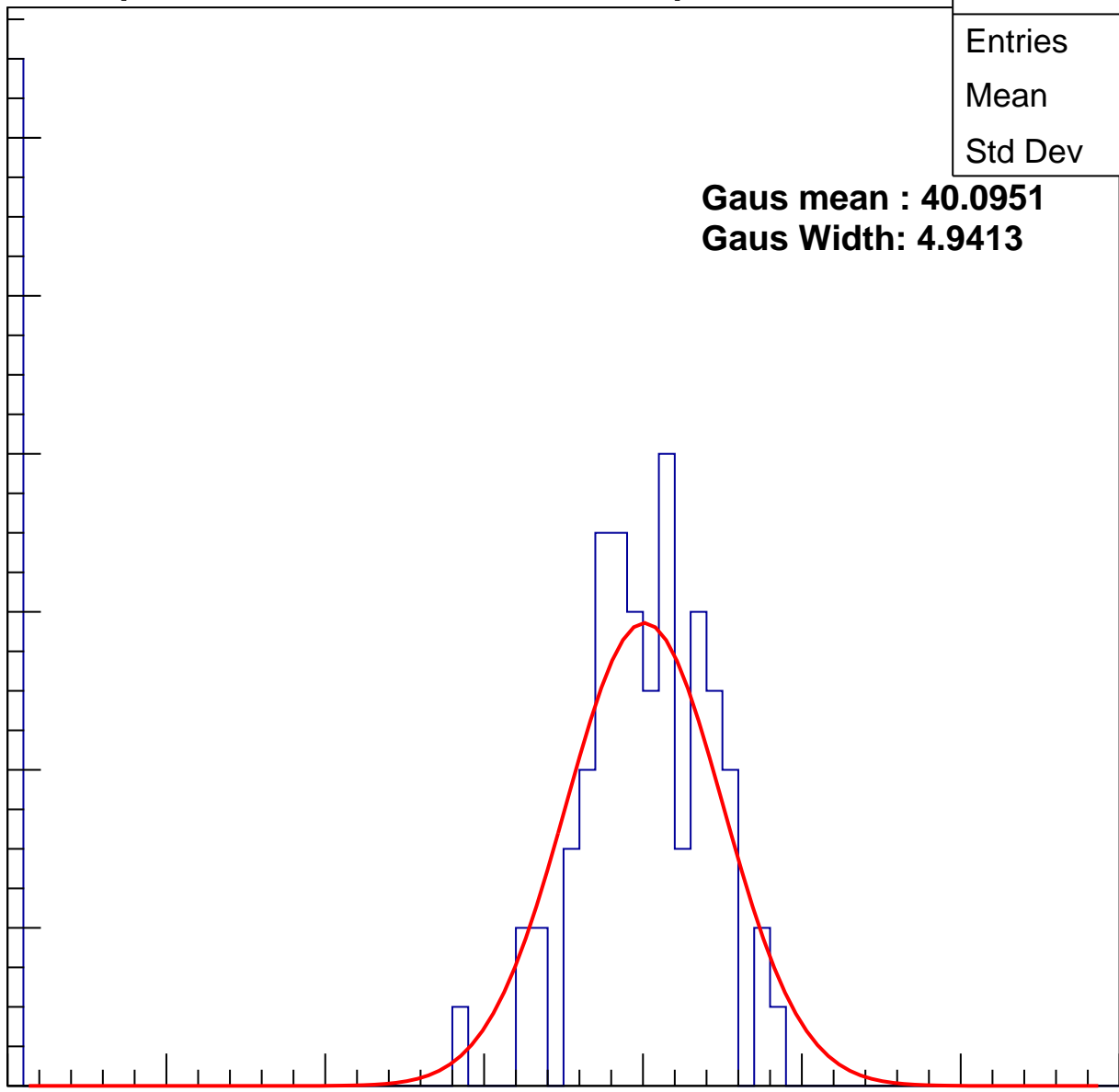
40

50

60

70

ampl

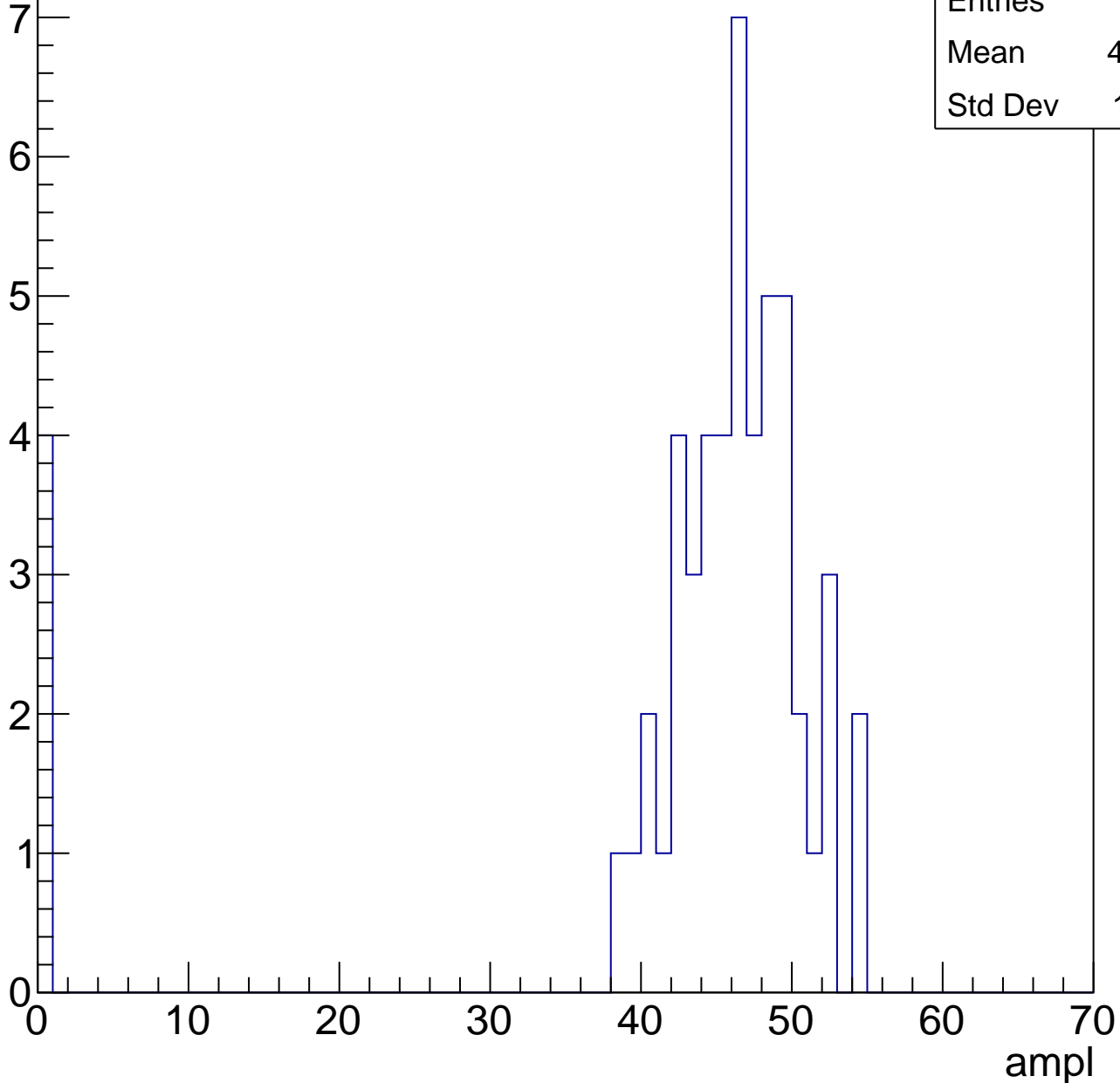


B1L103S, U10-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	42.66
Std Dev	12.71

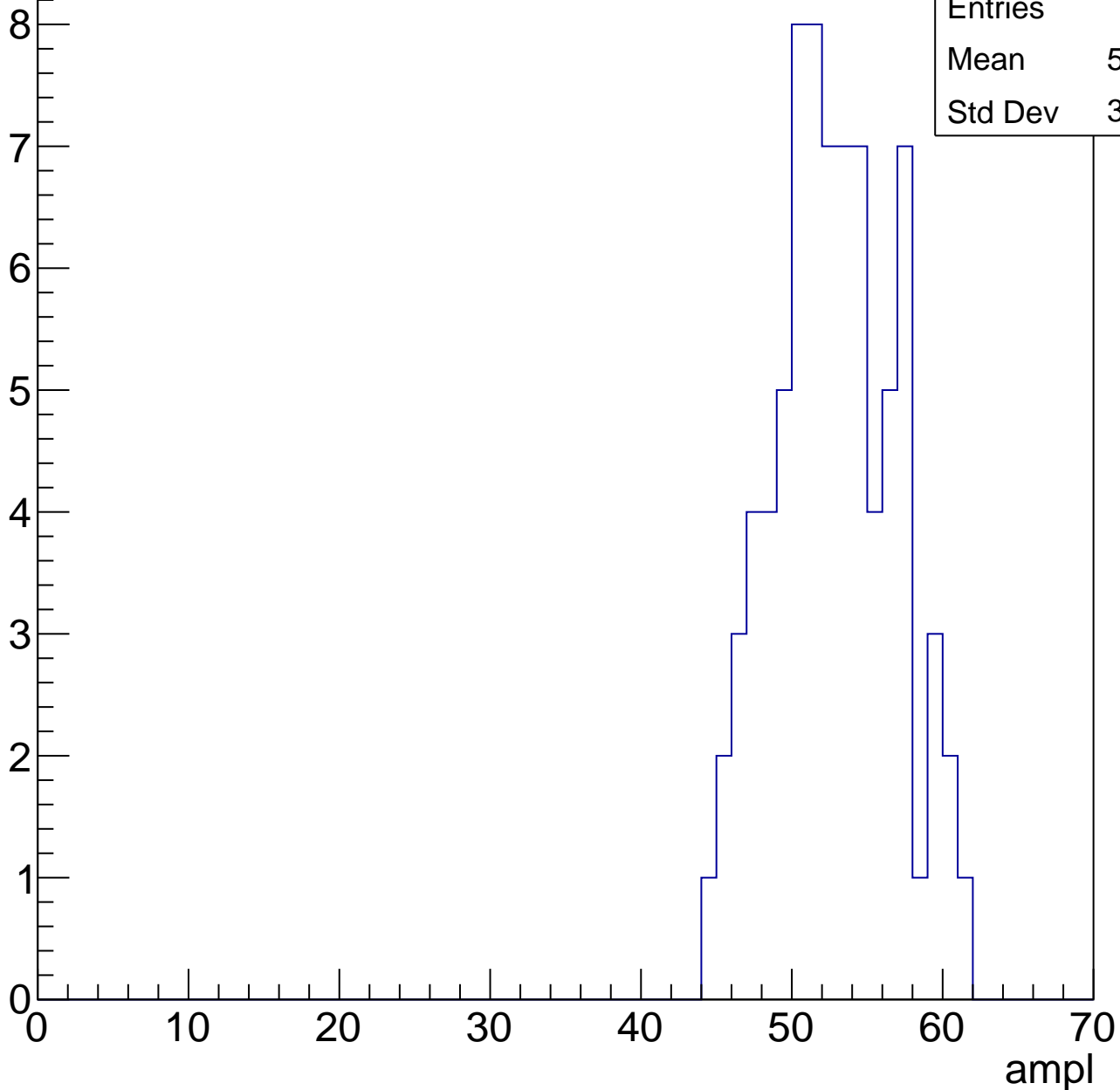


B1L103S, U10-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	52.32
Std Dev	3.944

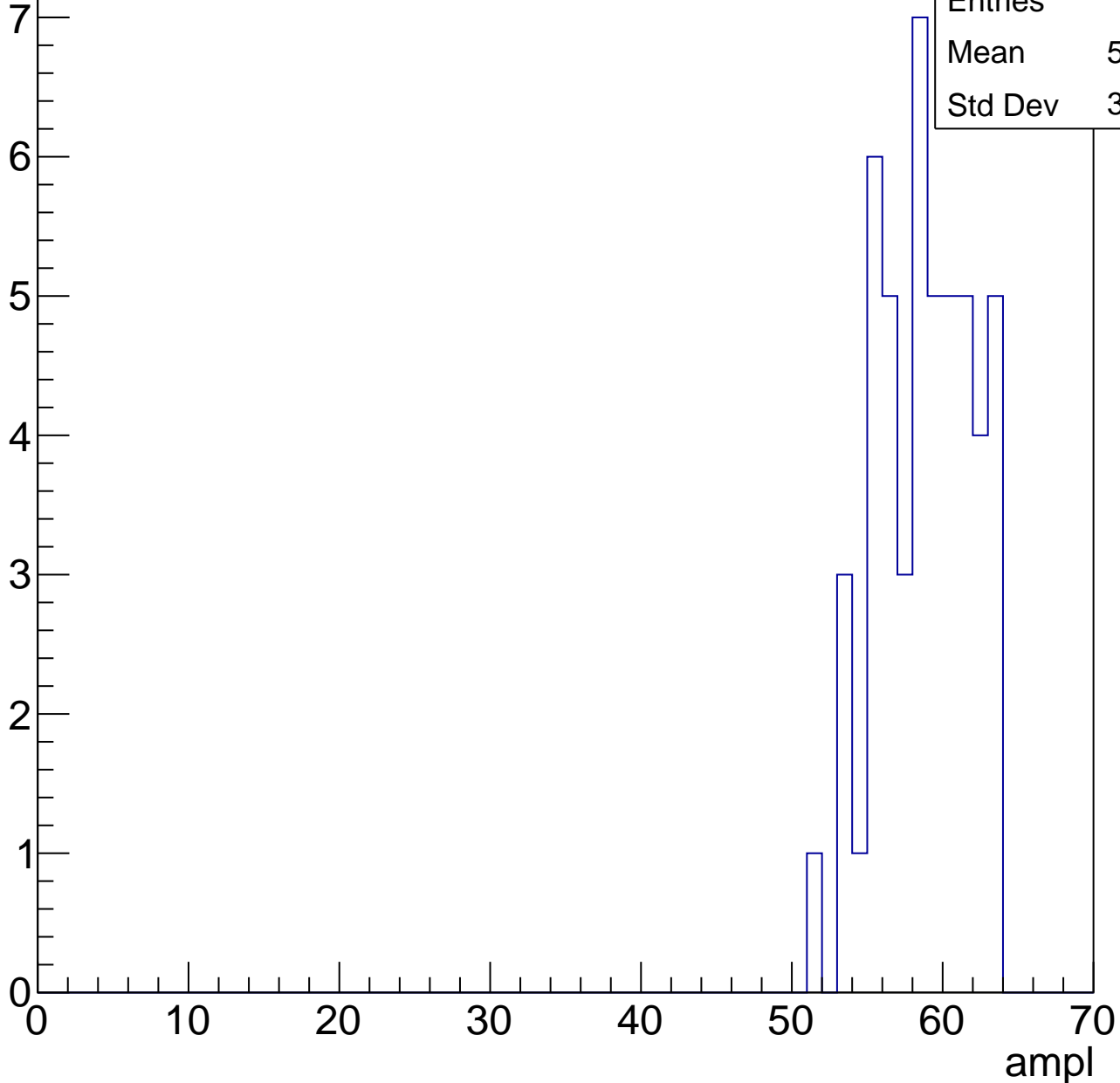


B1L103S, U10-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.28
Std Dev	3.073

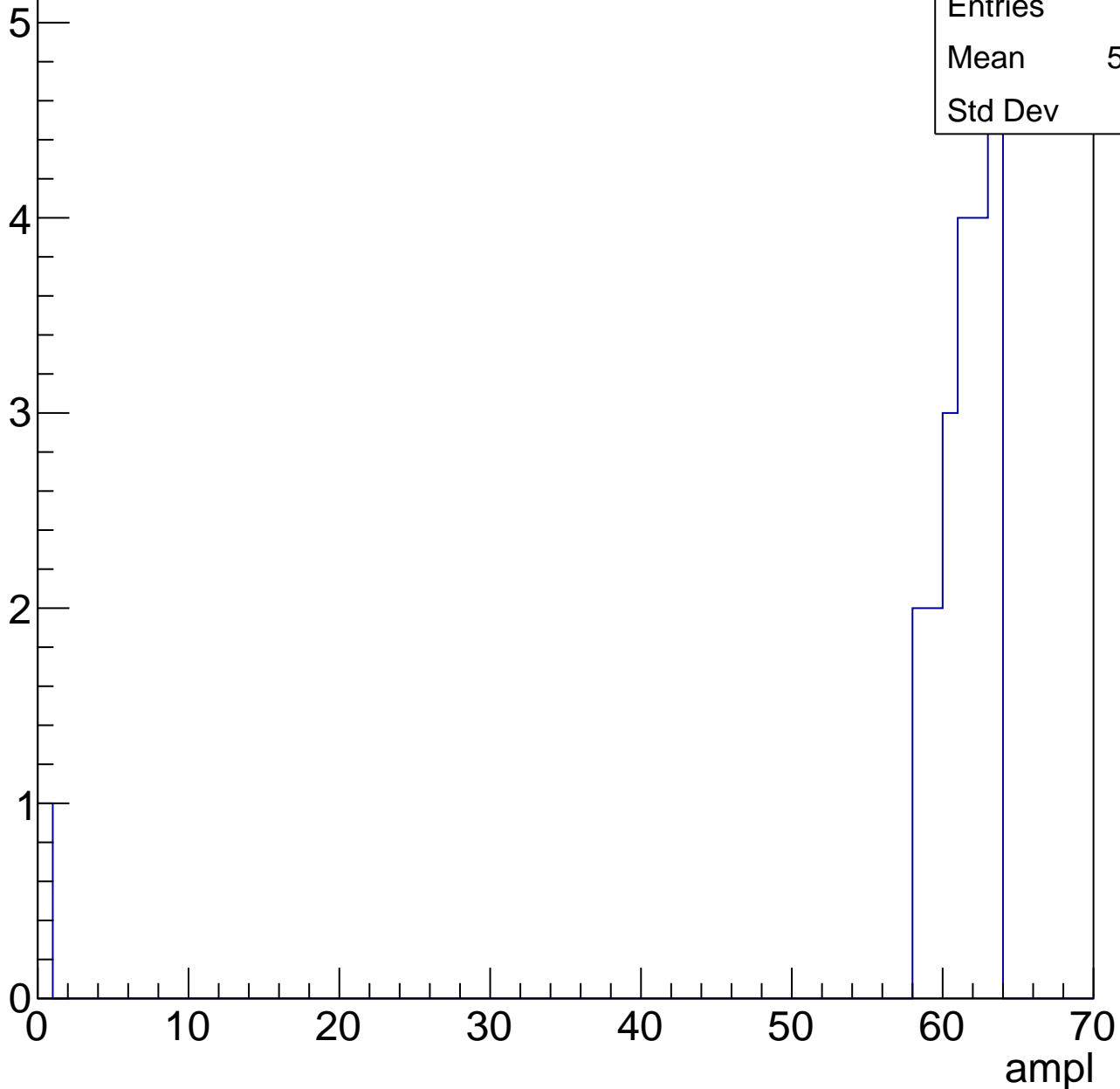


B1L103S, U10-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.14
Std Dev	13.1

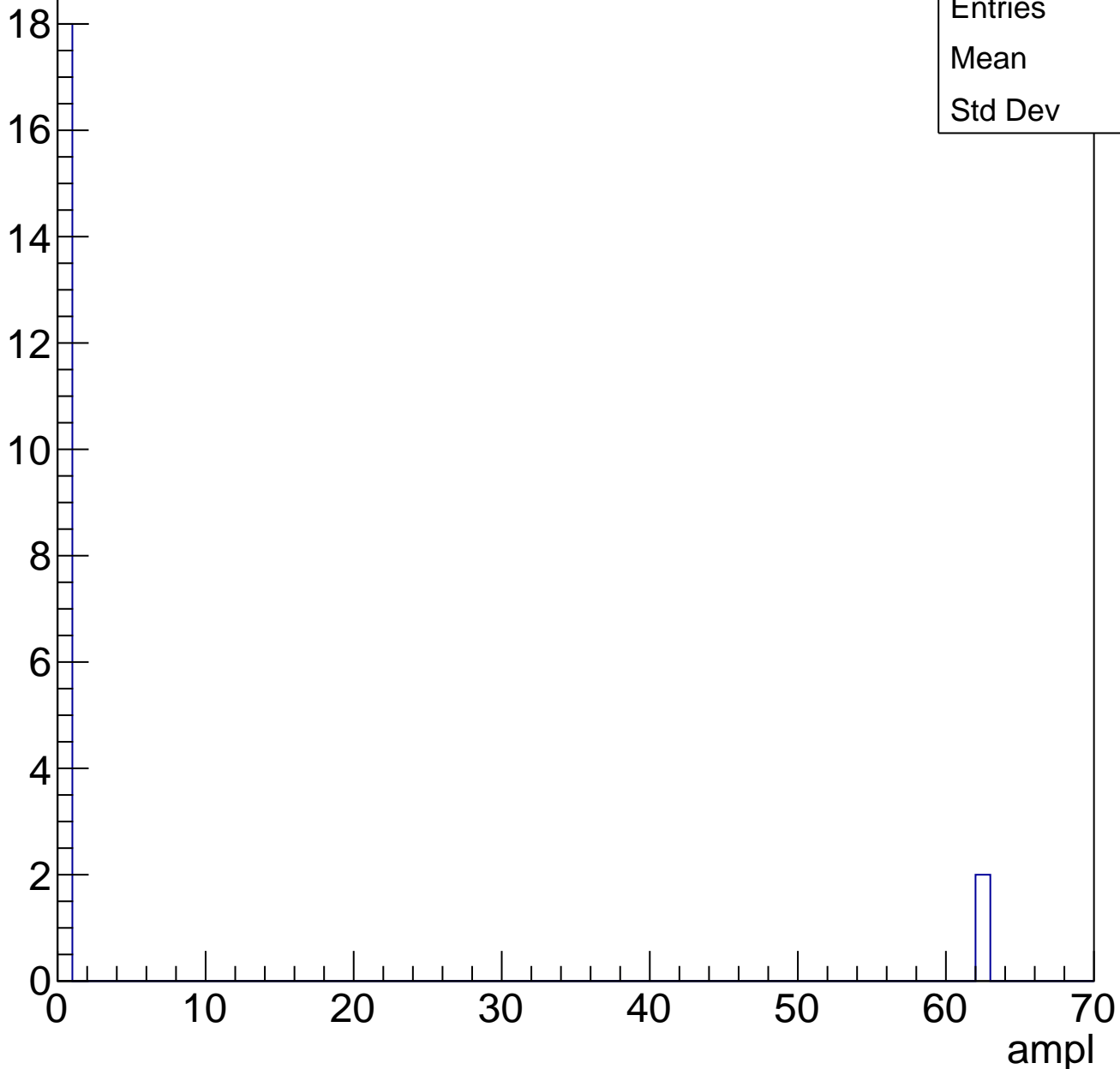


B1L103S, U10-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.2
Std Dev	18.6

Entry



B1L103S, U10-ch117, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.11
Std Dev	10.86

Gaus mean : 28.9955

Gaus Width: 3.2660

Entry

12

10

8

6

4

2

0

0

10

20

30

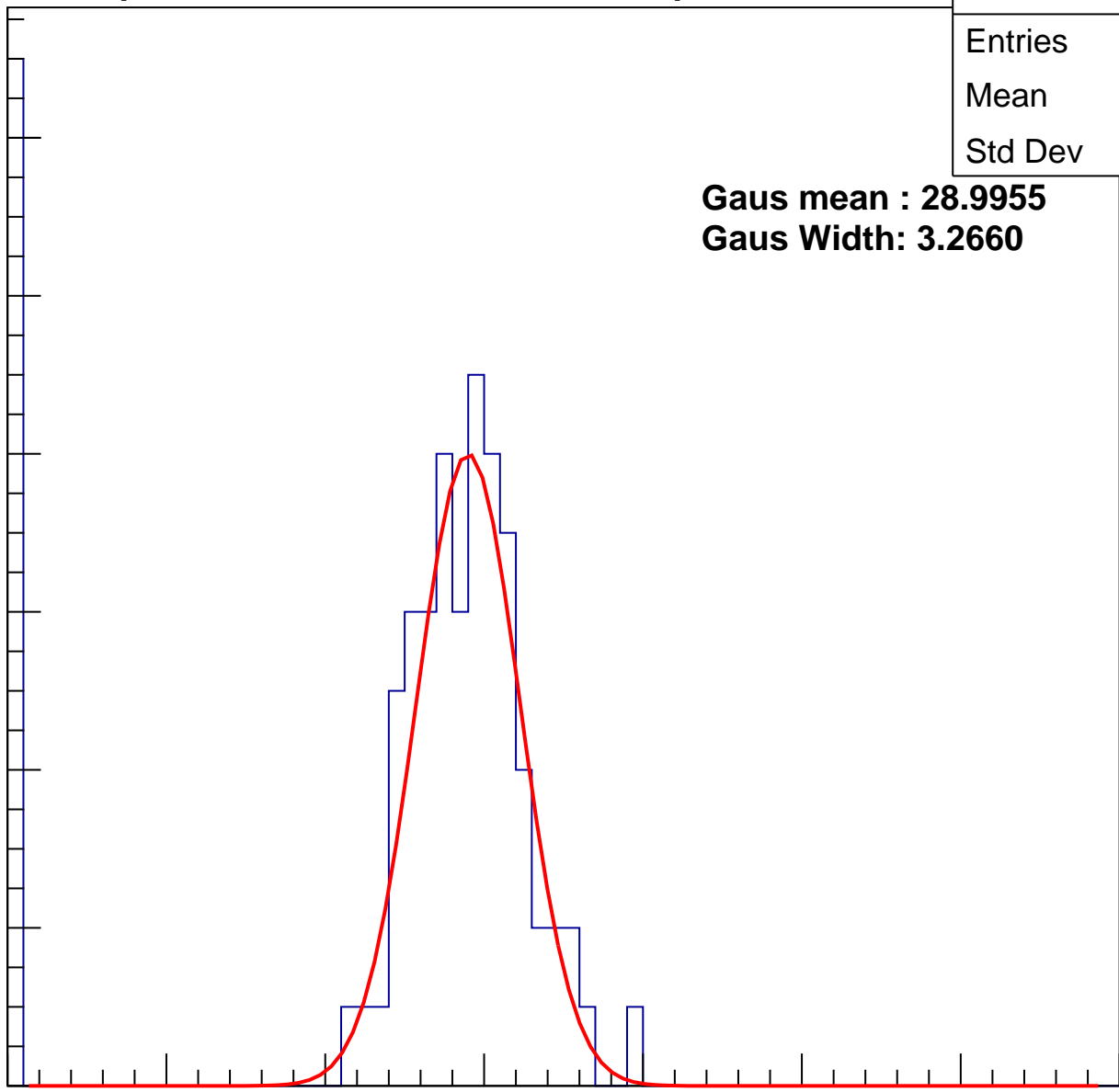
40

50

60

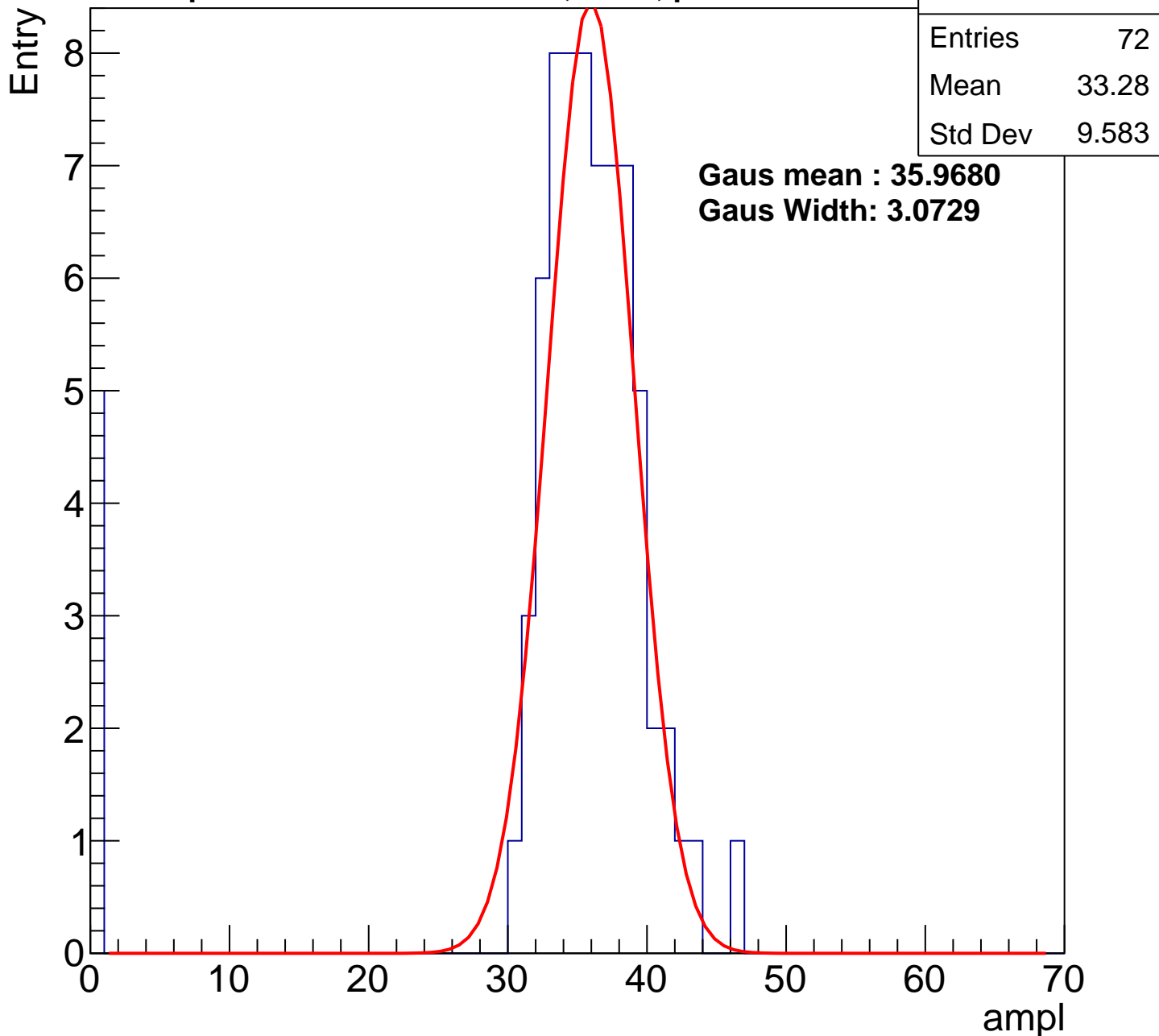
70

ampl



B1L103S, U10-ch117, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch117, adc2

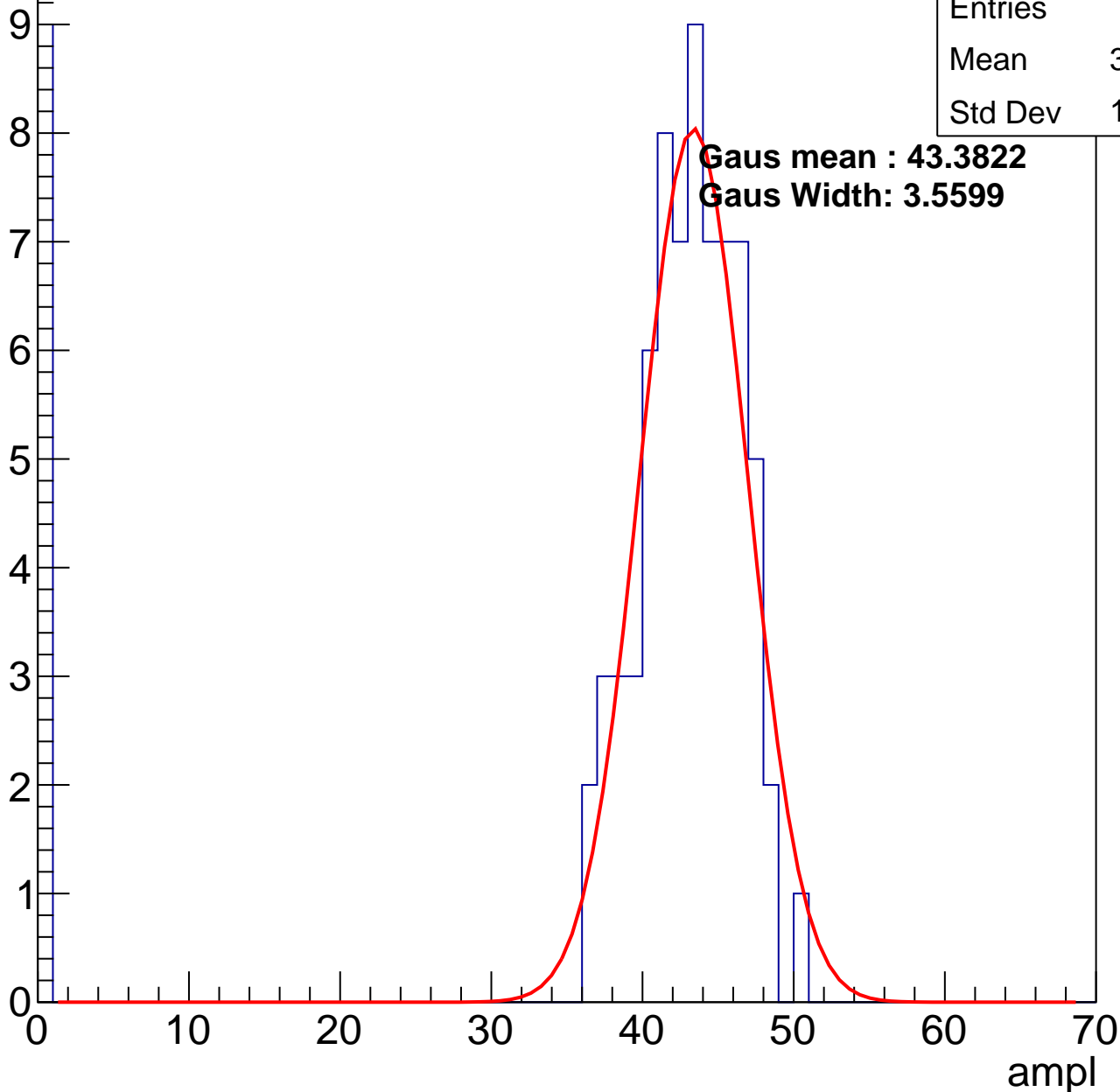
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	37.84
Std Dev	13.89

Gaus mean : 43.3822

Gaus Width: 3.5599

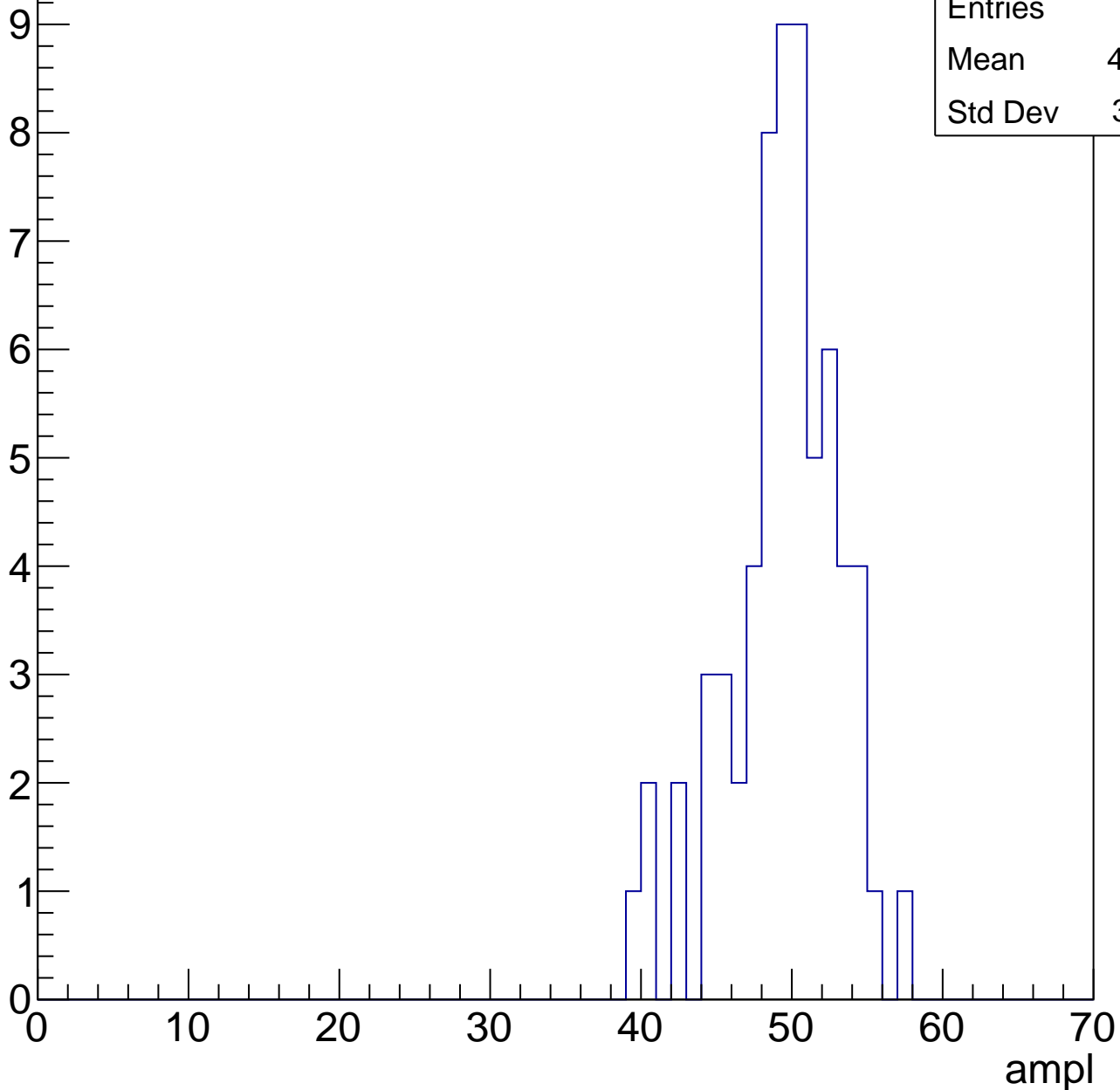


B1L103S, U10-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.94
Std Dev	3.691

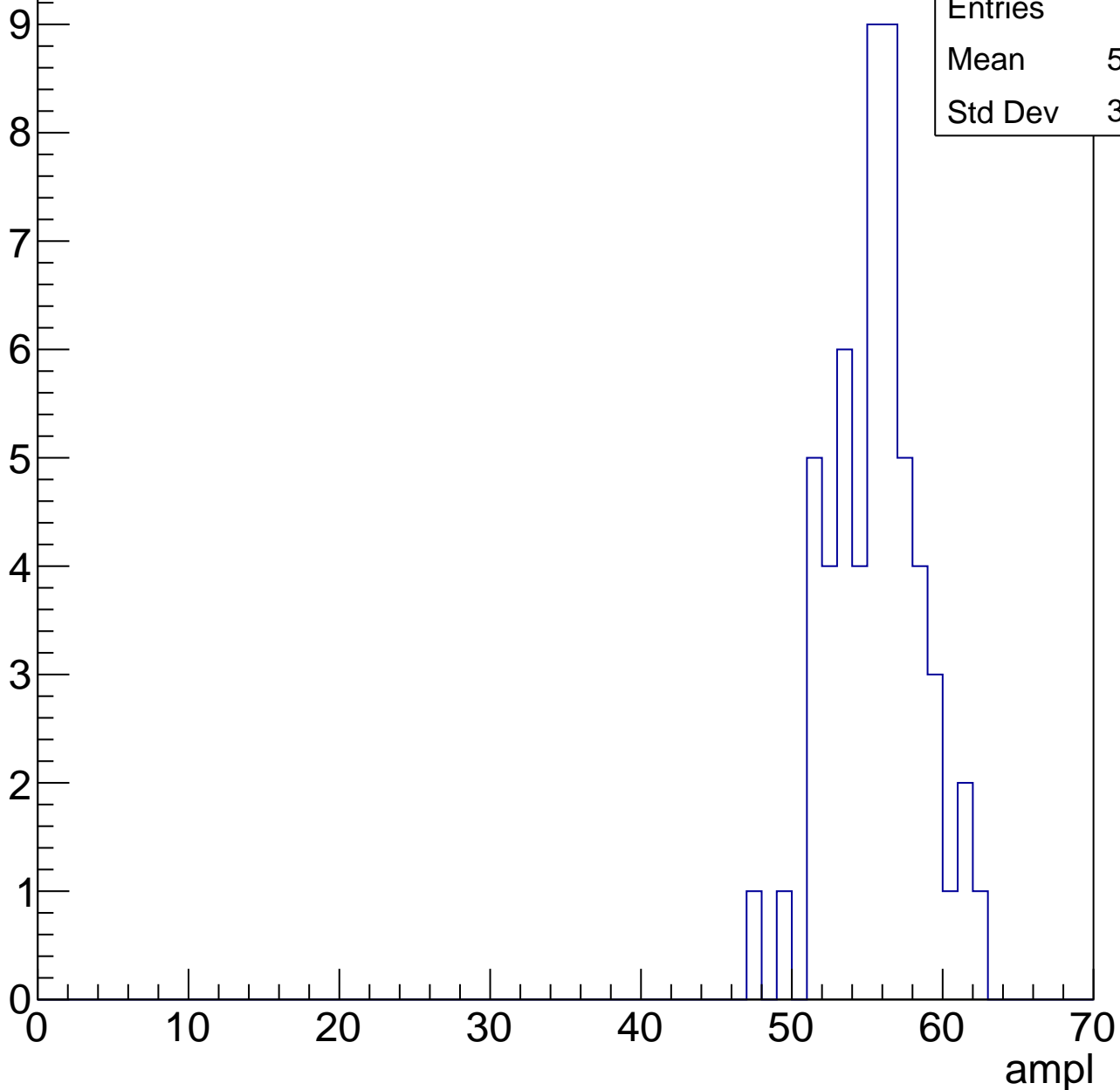


B1L103S, U10-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

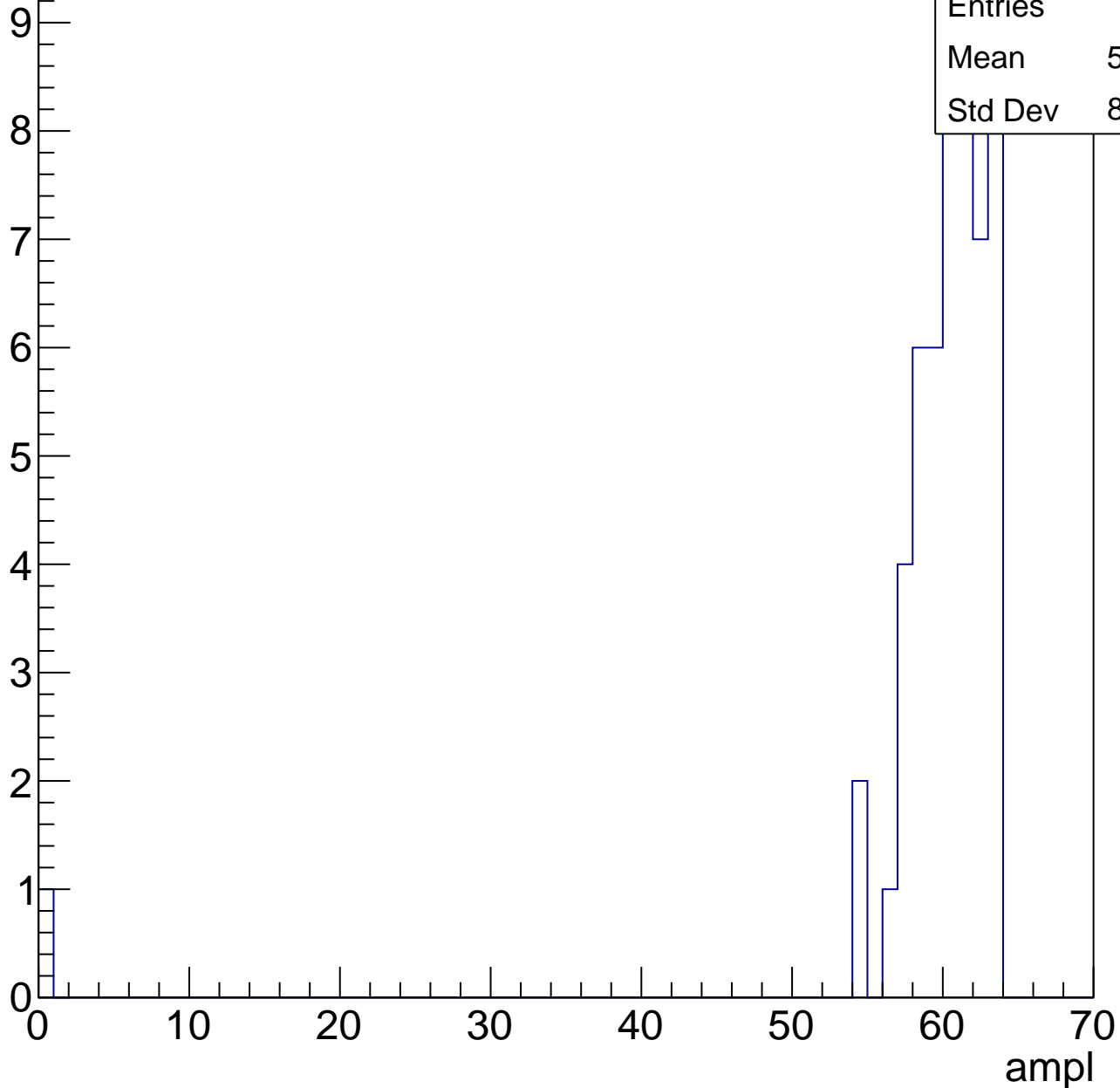
Entries	55
Mean	55.09
Std Dev	3.023



B1L103S, U10-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

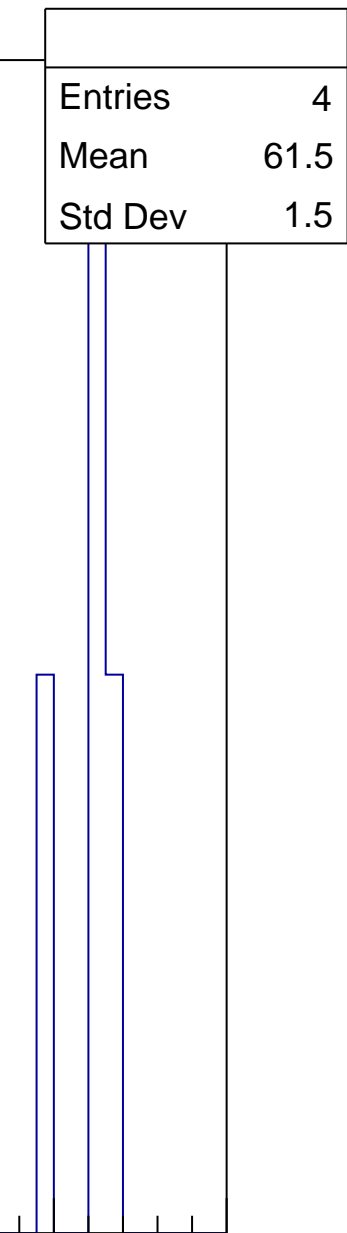
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.5
Std Dev	1.5

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch118, adc0

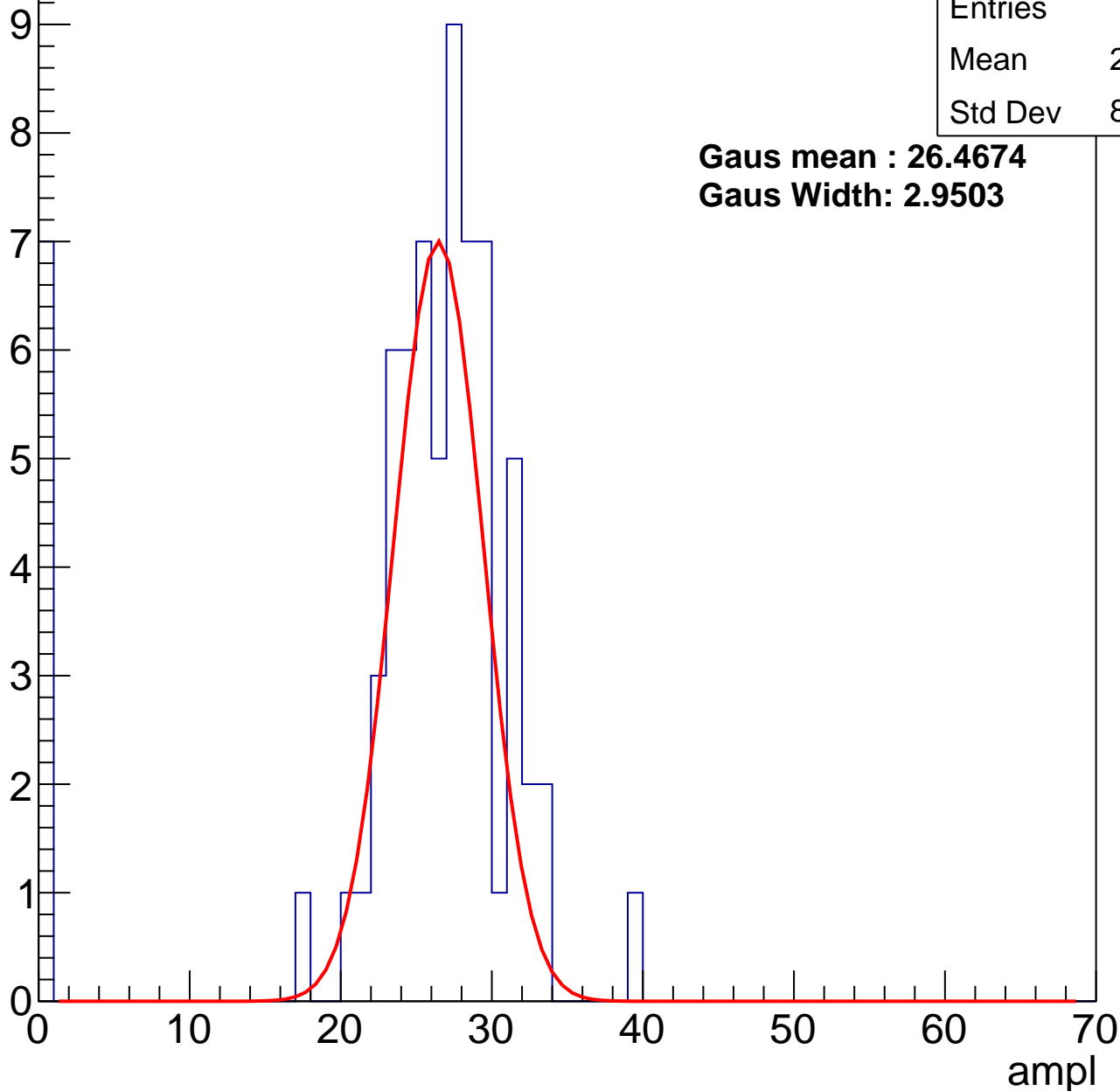
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	24.04
Std Dev	8.652

Gaus mean : 26.4674

Gaus Width: 2.9503



B1L103S, U10-ch118, adc1

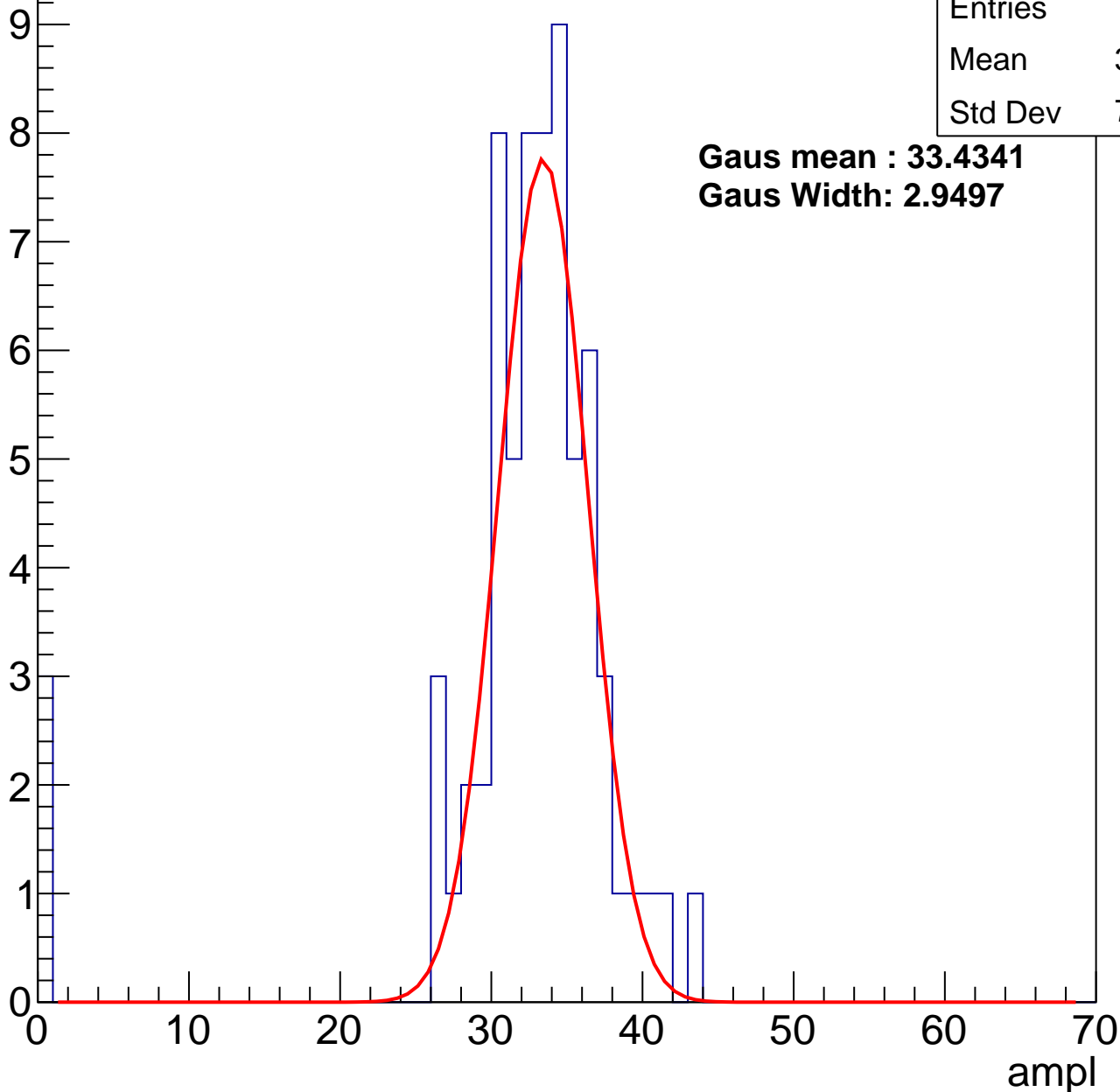
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	31.51
Std Dev	7.561

Gaus mean : 33.4341

Gaus Width: 2.9497



B1L103S, U10-ch118, adc2

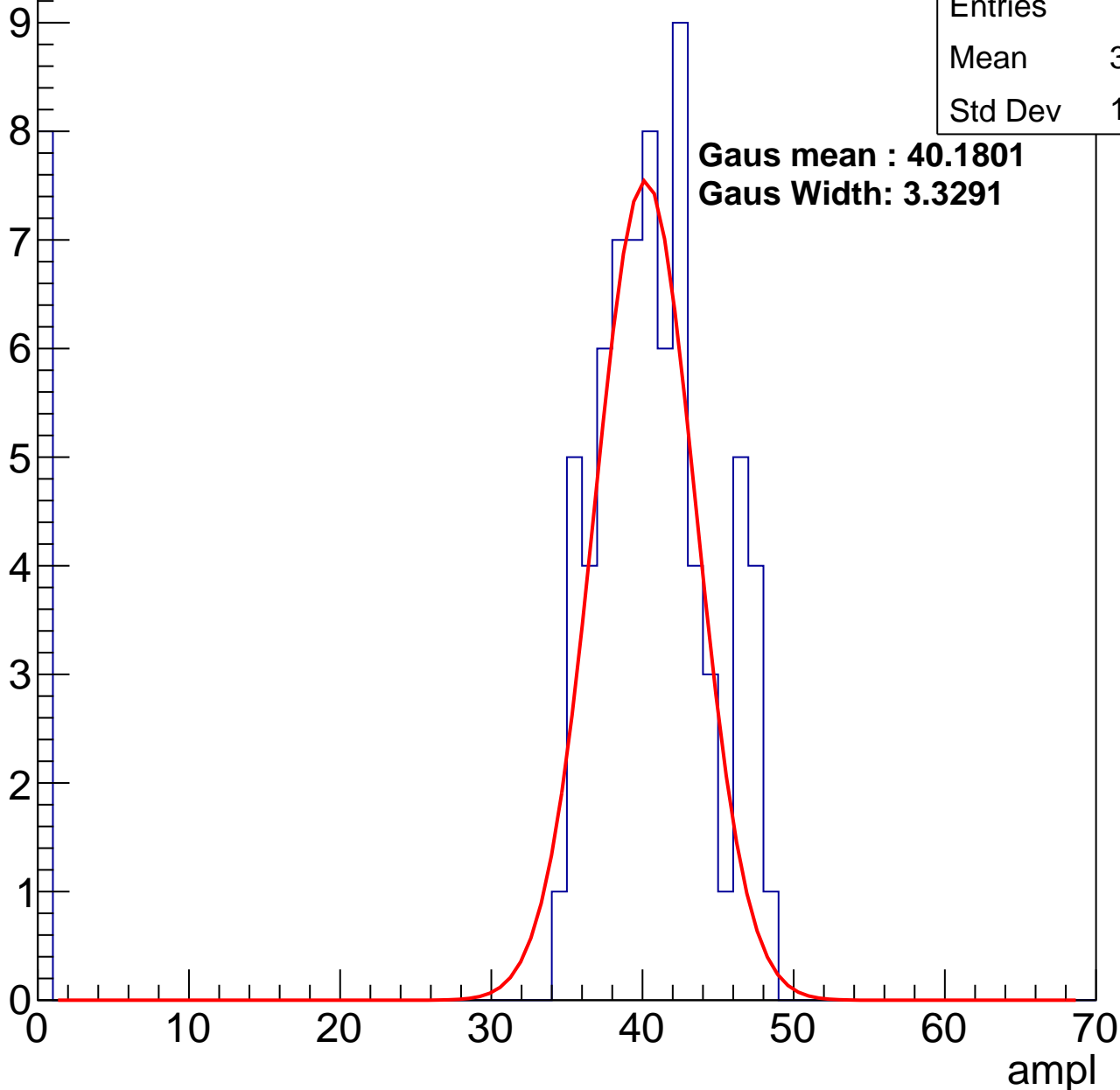
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	36.37
Std Dev	12.66

Gaus mean : 40.1801

Gaus Width: 3.3291

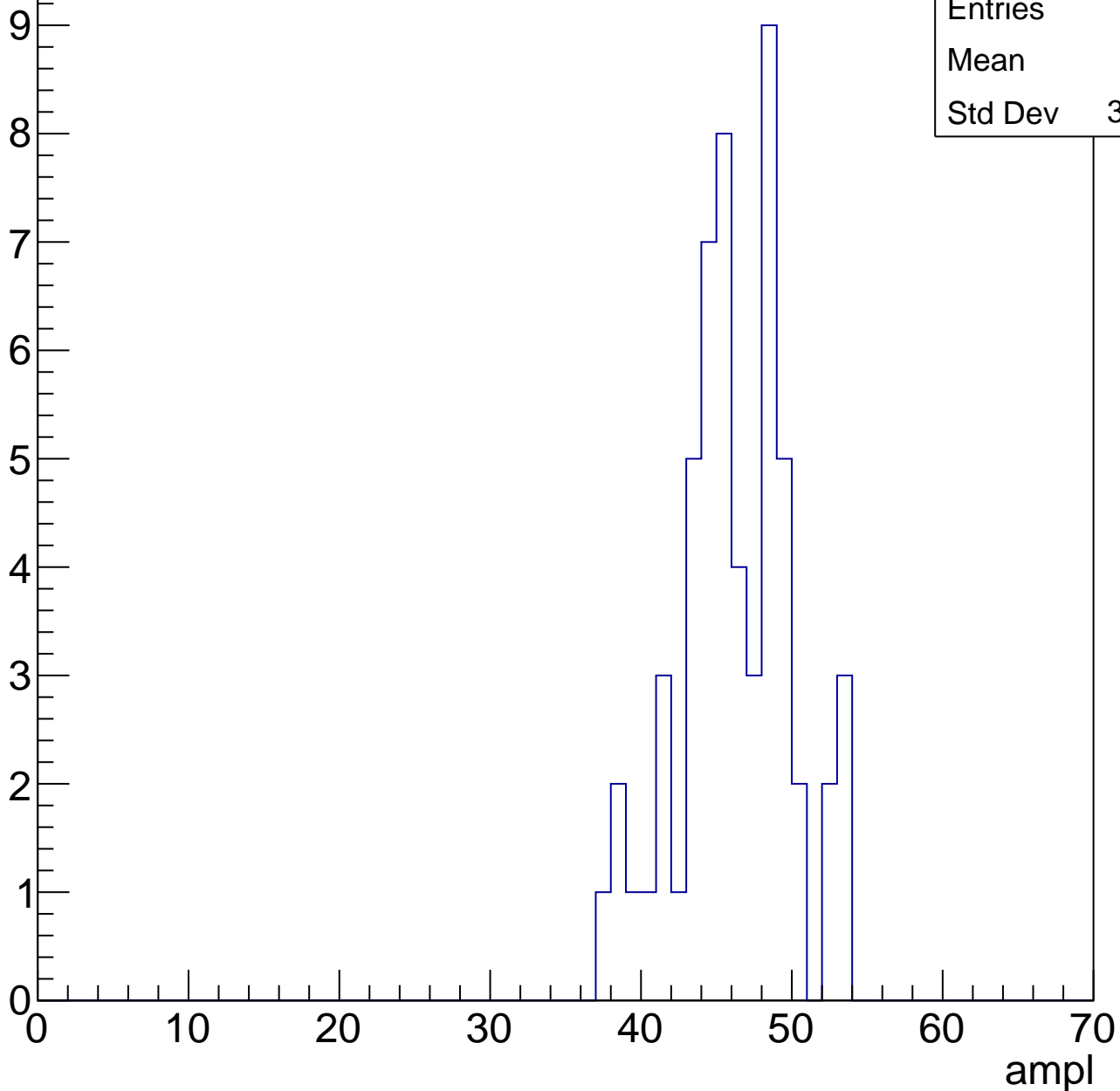


B1L103S, U10-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	45.7
Std Dev	3.737

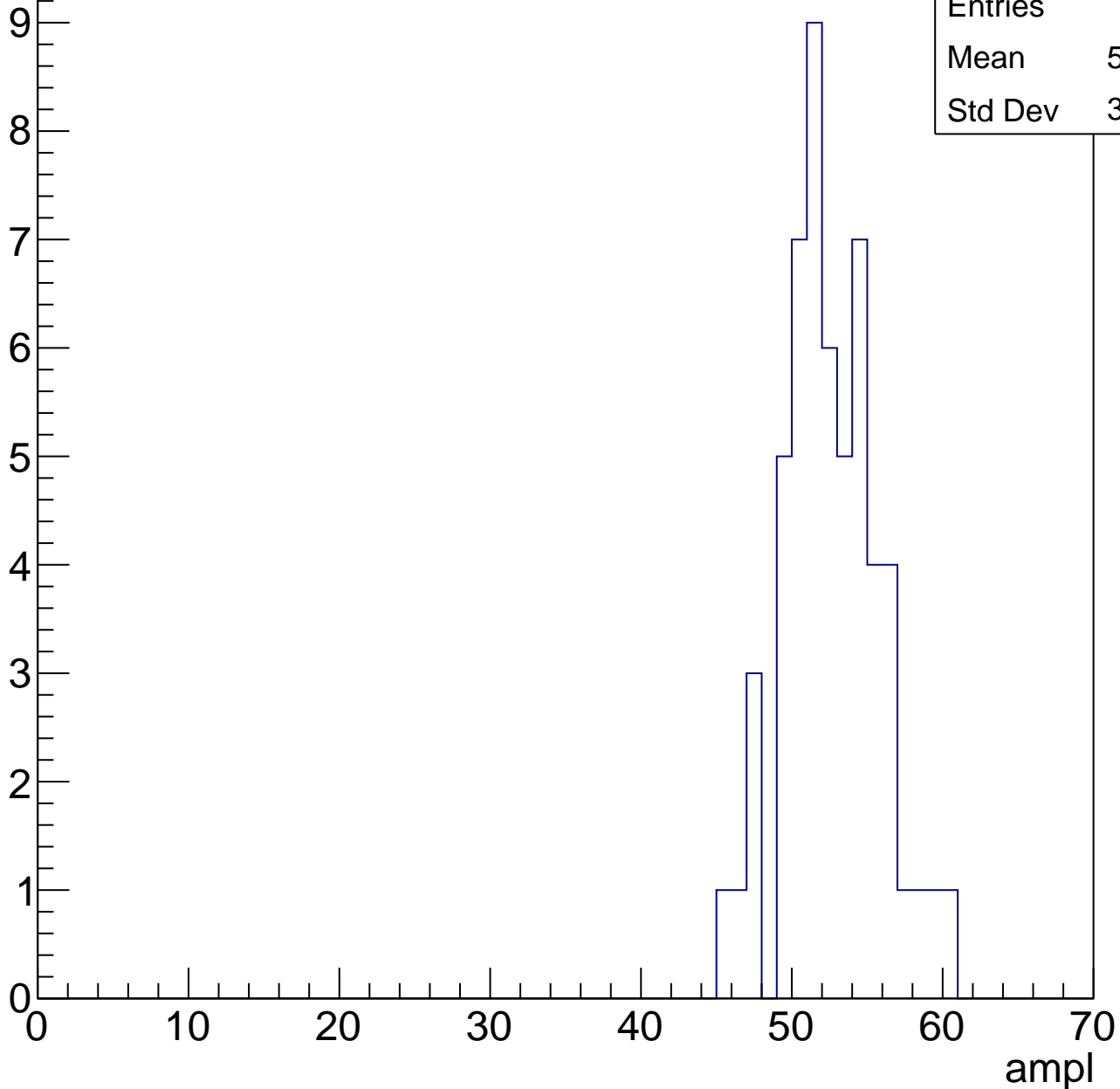


B1L103S, U10-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

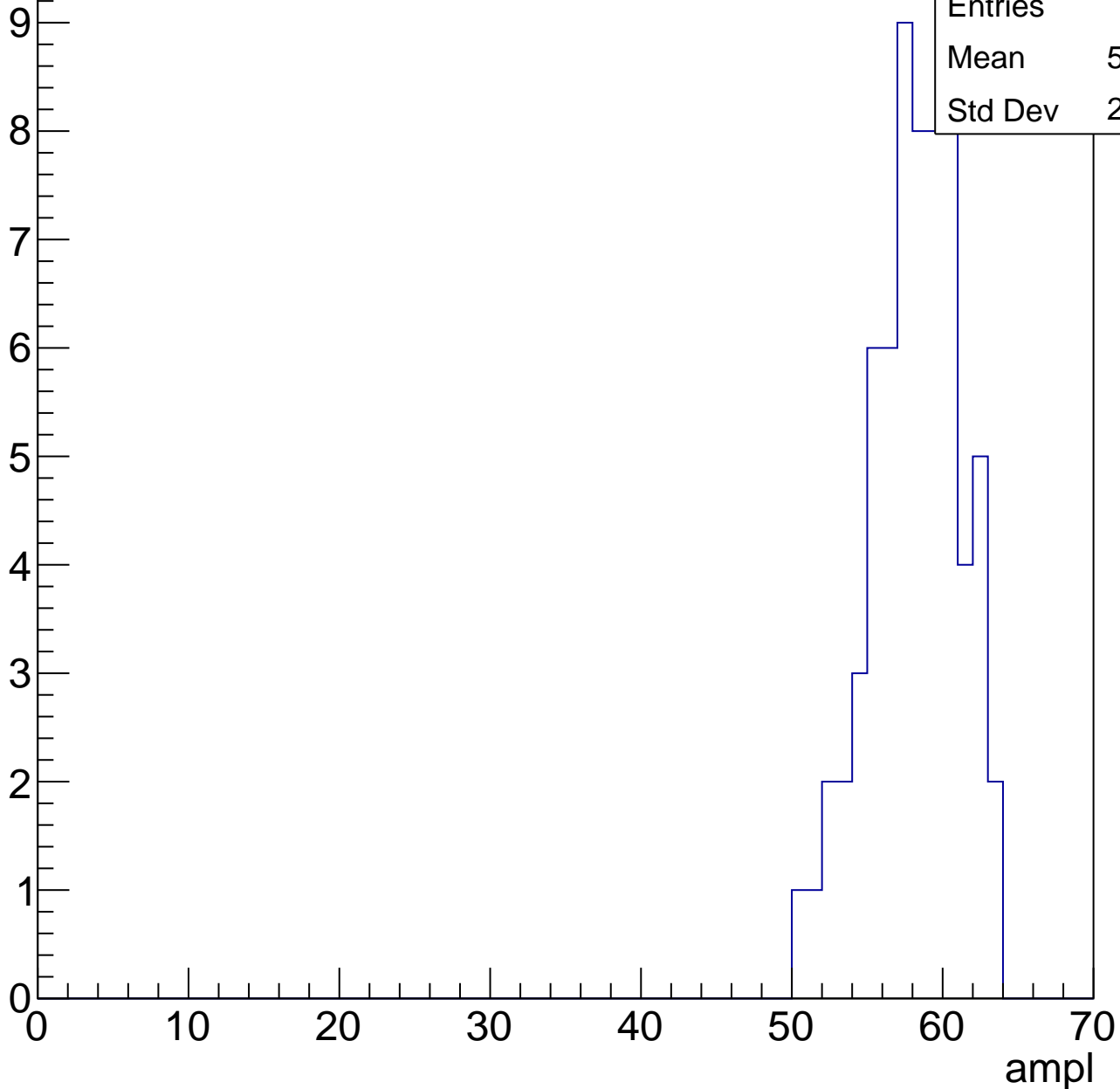
Entries	56
Mean	52.12
Std Dev	3.129



B1L103S, U10-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

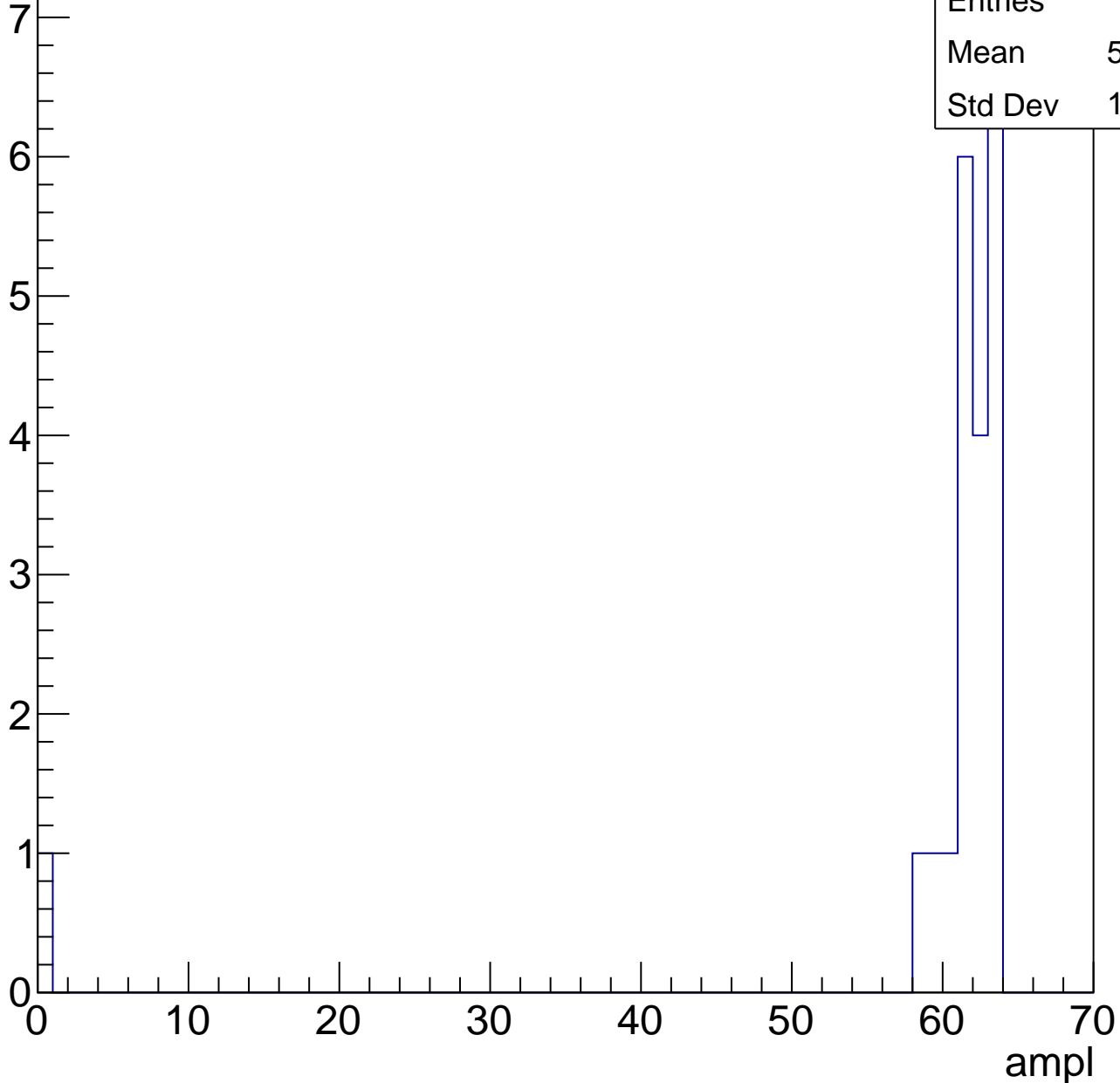


B1L103S, U10-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.67
Std Dev	13.19

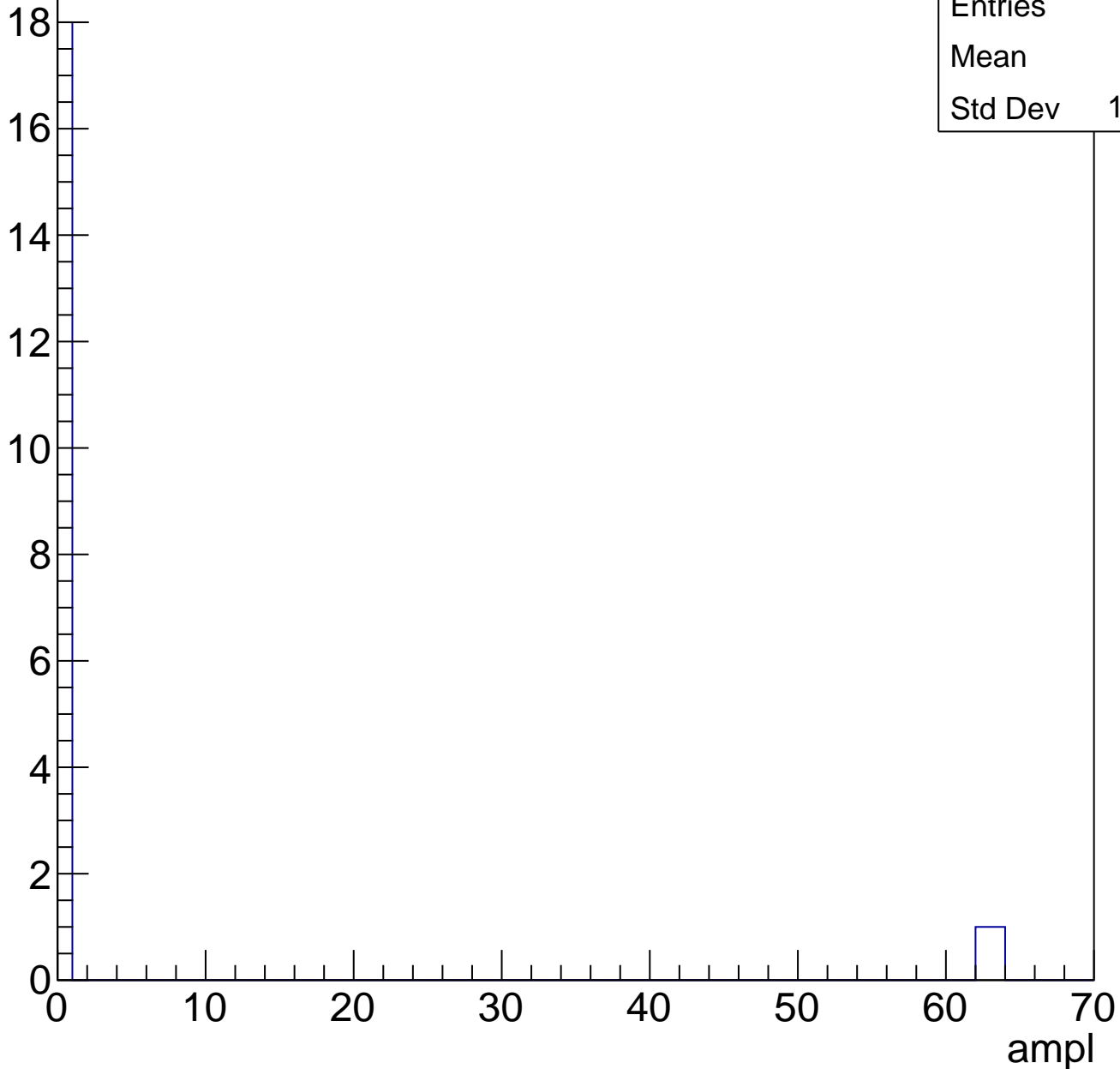


B1L103S, U10-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.25
Std Dev	18.75

Entry



B1L103S, U10-ch119, adc0

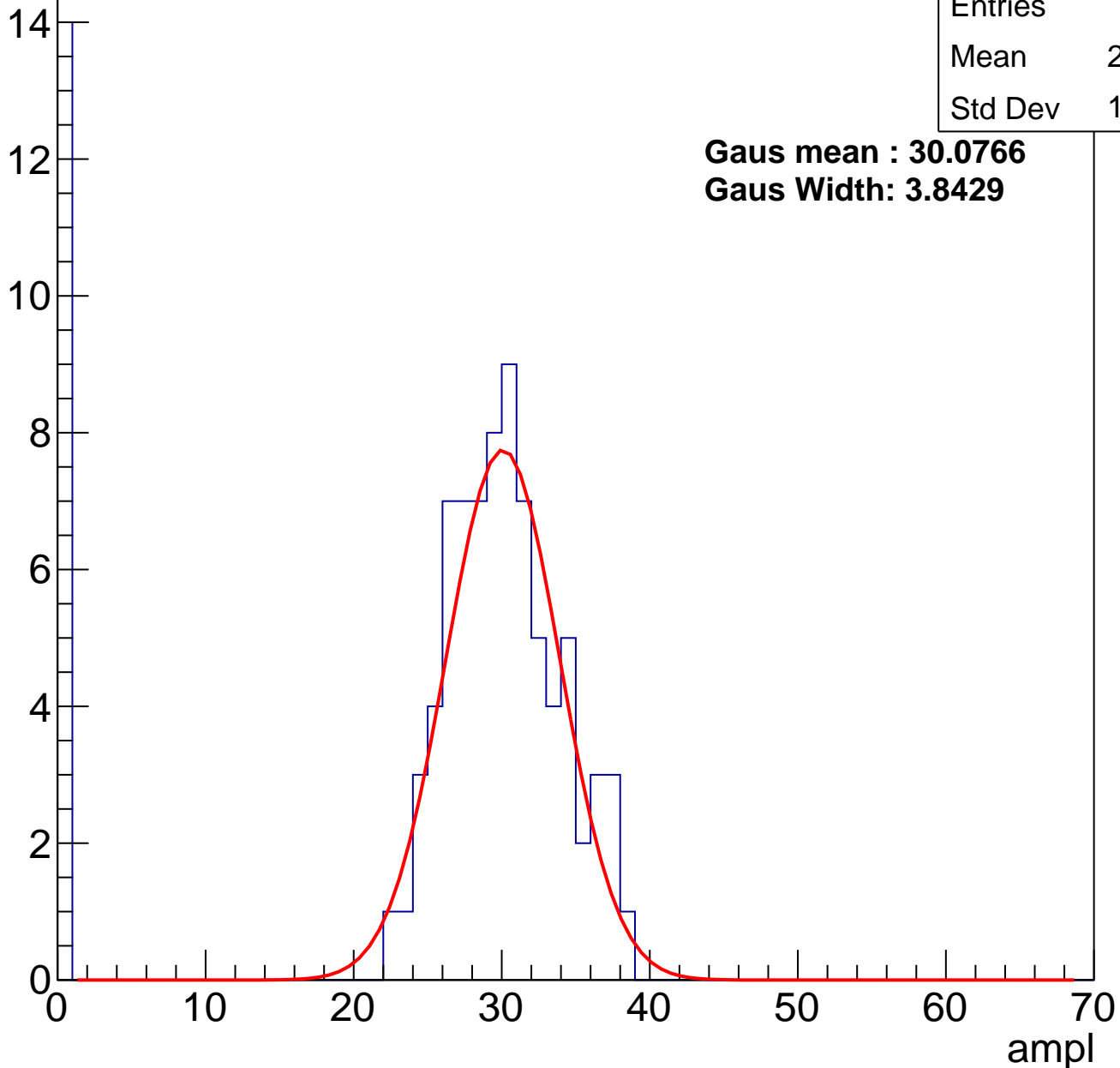
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	25.19
Std Dev	11.26

Gaus mean : 30.0766

Gaus Width: 3.8429

Entry



B1L103S, U10-ch119, adc1

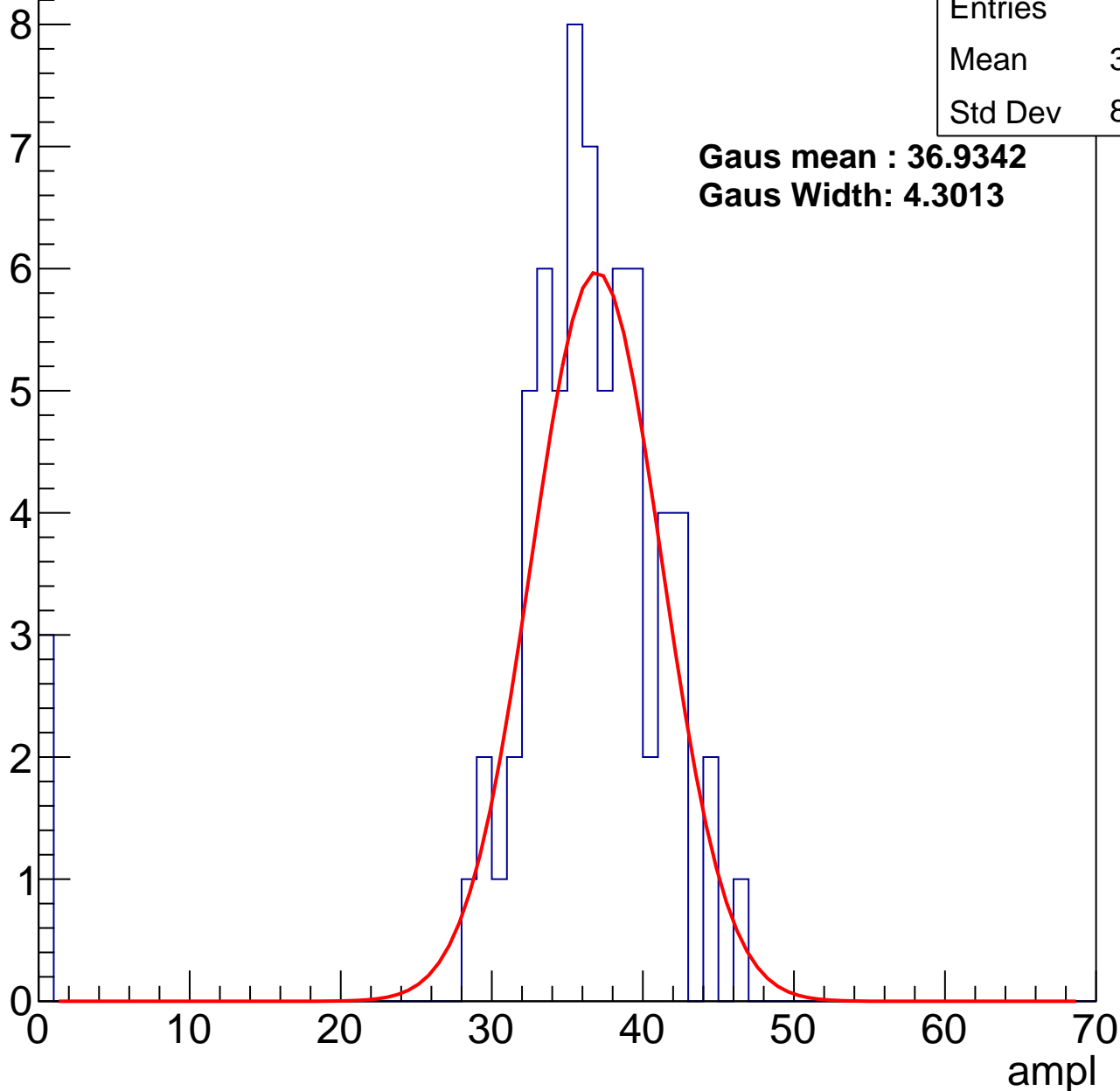
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.73
Std Dev	8.256

Gaus mean : 36.9342

Gaus Width: 4.3013



B1L103S, U10-ch119, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	37.84
Std Dev	14.61

Gaus mean : 43.4157

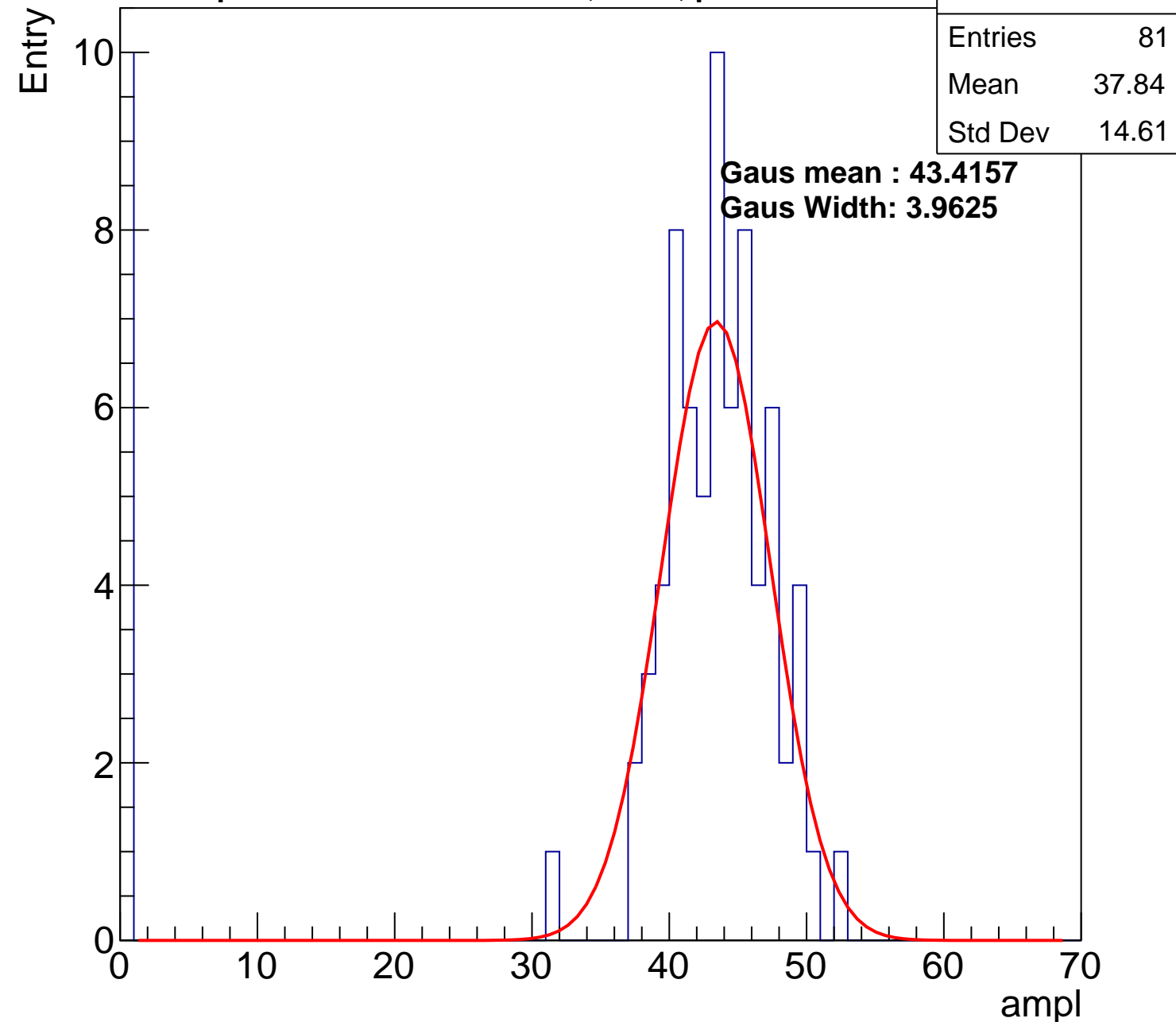
Gaus Width: 3.9625

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

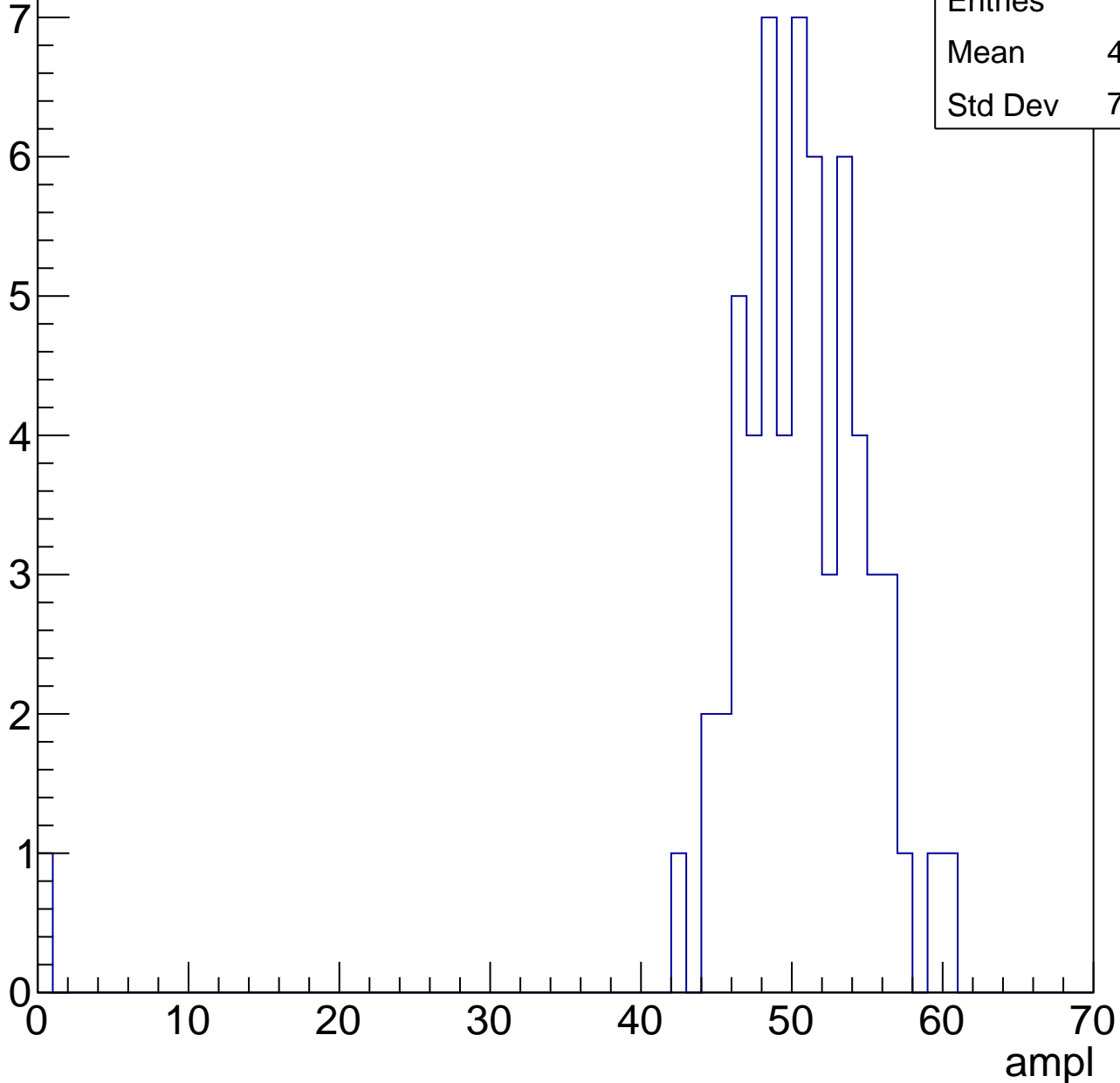


B1L103S, U10-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.59
Std Dev	7.436

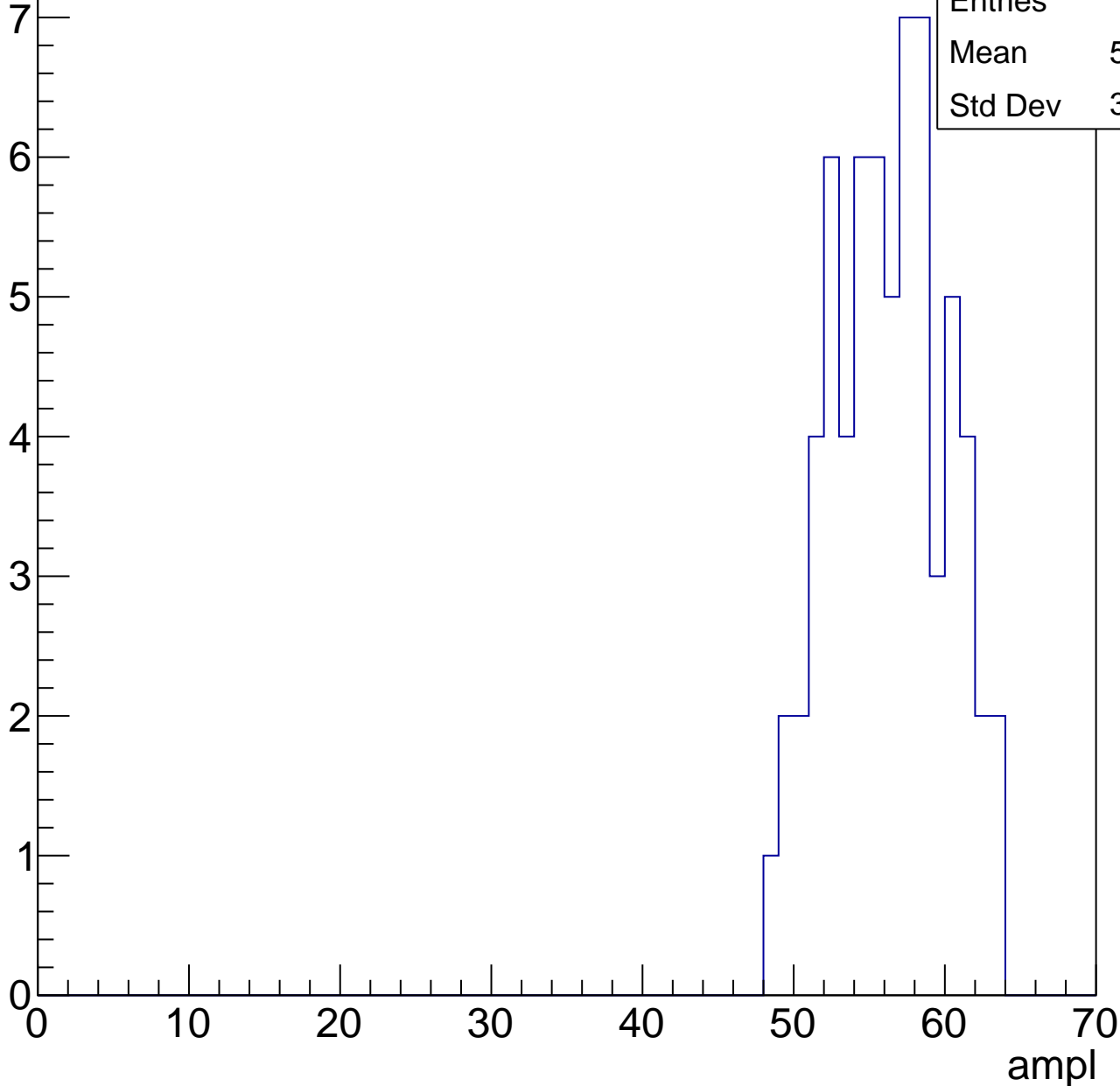


B1L103S, U10-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	55.82
Std Dev	3.705

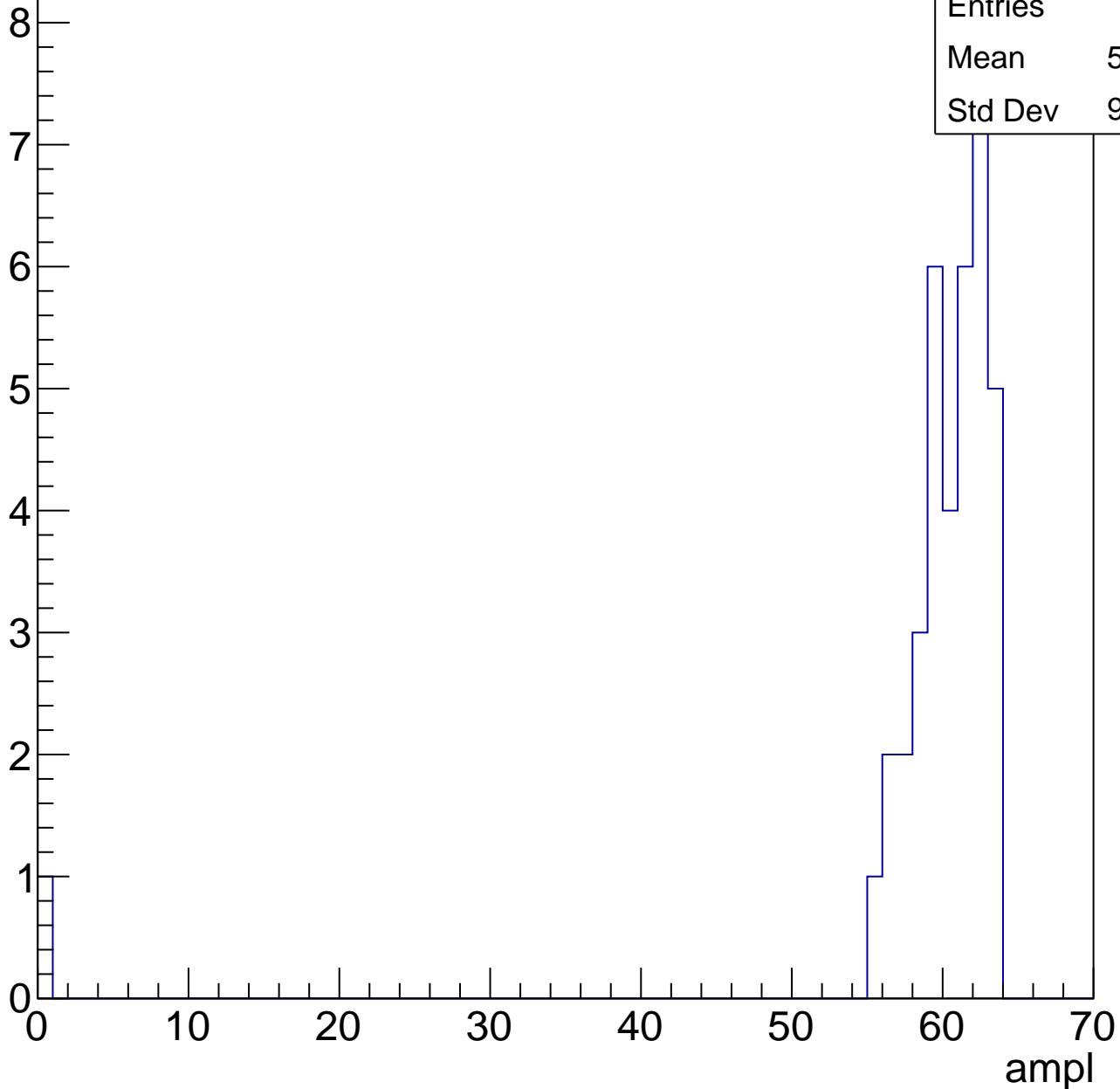


B1L103S, U10-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.58
Std Dev	9.867



B1L103S, U10-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

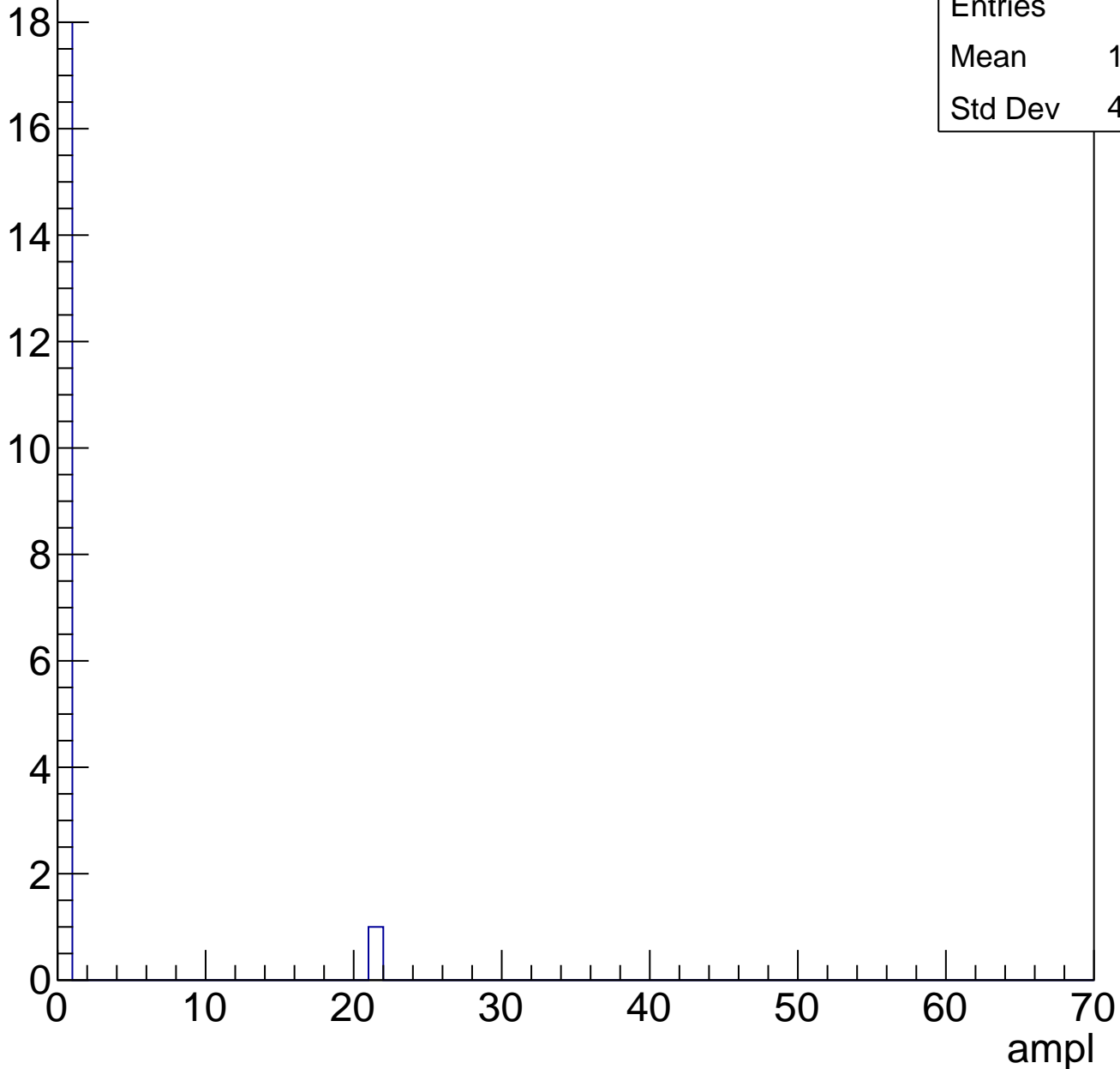


B1L103S, U10-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U10-ch120, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	23.46
Std Dev	10.06

Gaus mean : 27.4317

Gaus Width: 3.7904

Entry

12

10

8

6

4

2

0

0

10

20

30

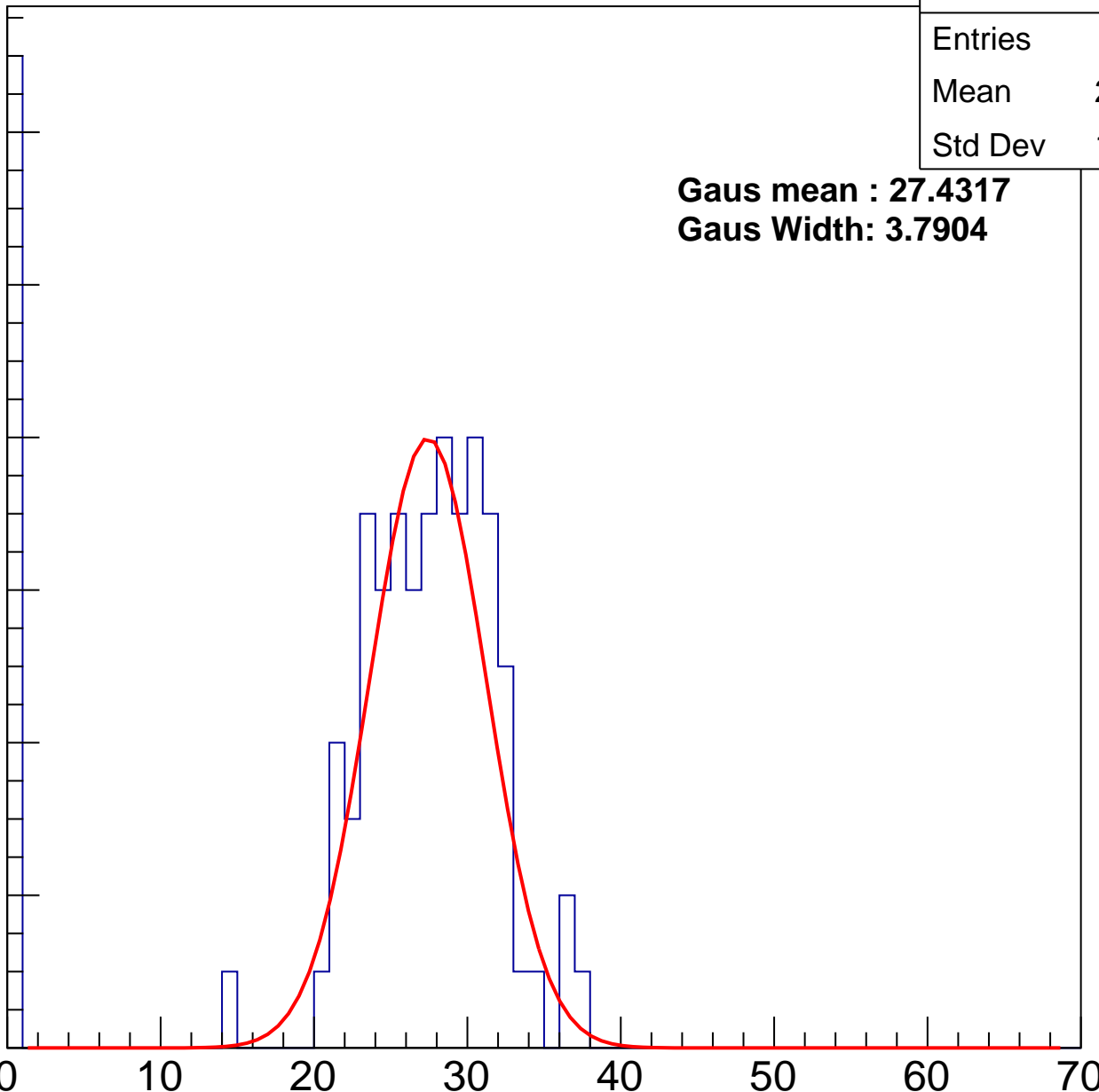
40

50

60

70

ampl



B1L103S, U10-ch120, adc1

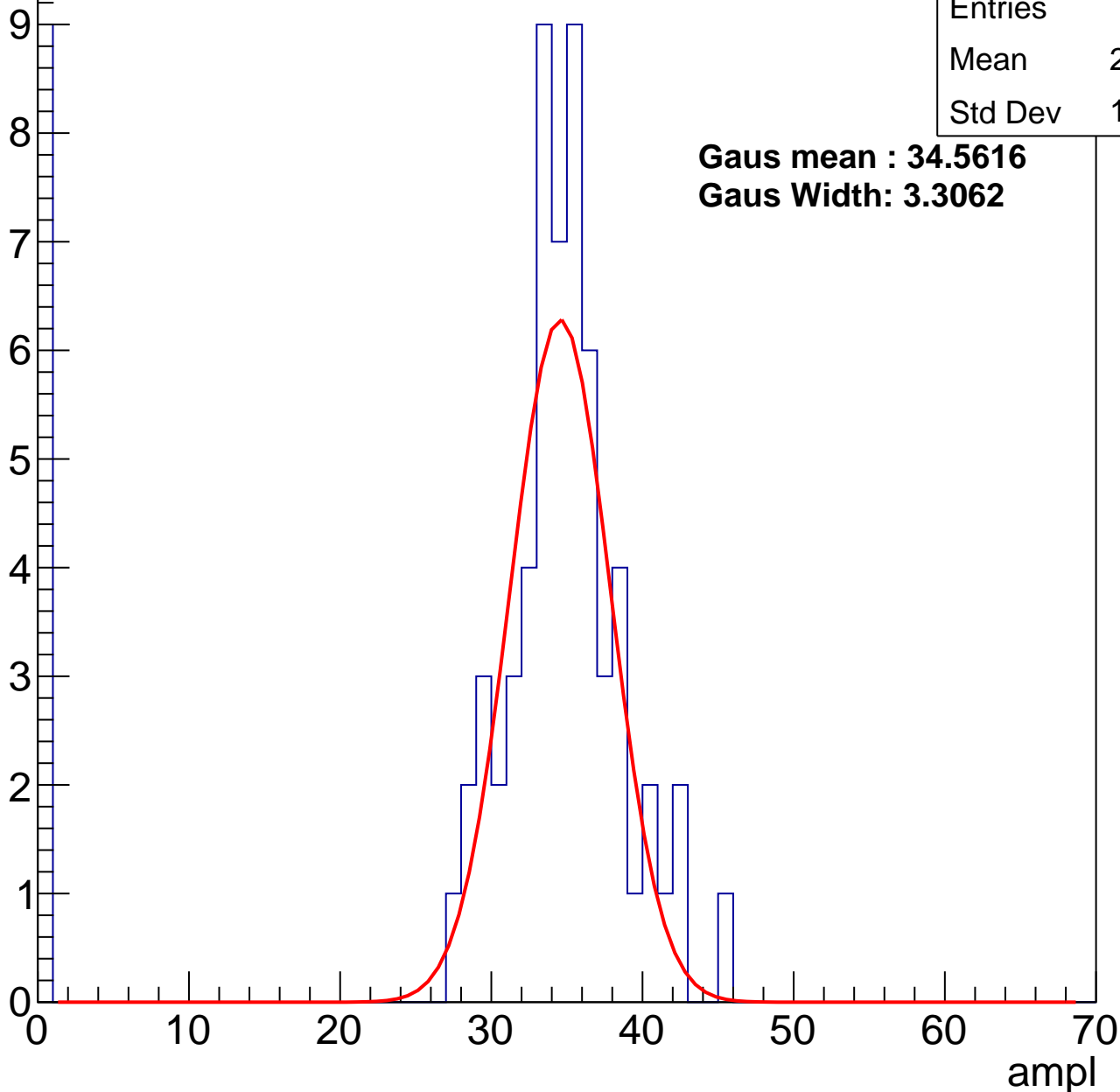
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	29.99
Std Dev	12.09

Gaus mean : 34.5616

Gaus Width: 3.3062



B1L103S, U10-ch120, adc2

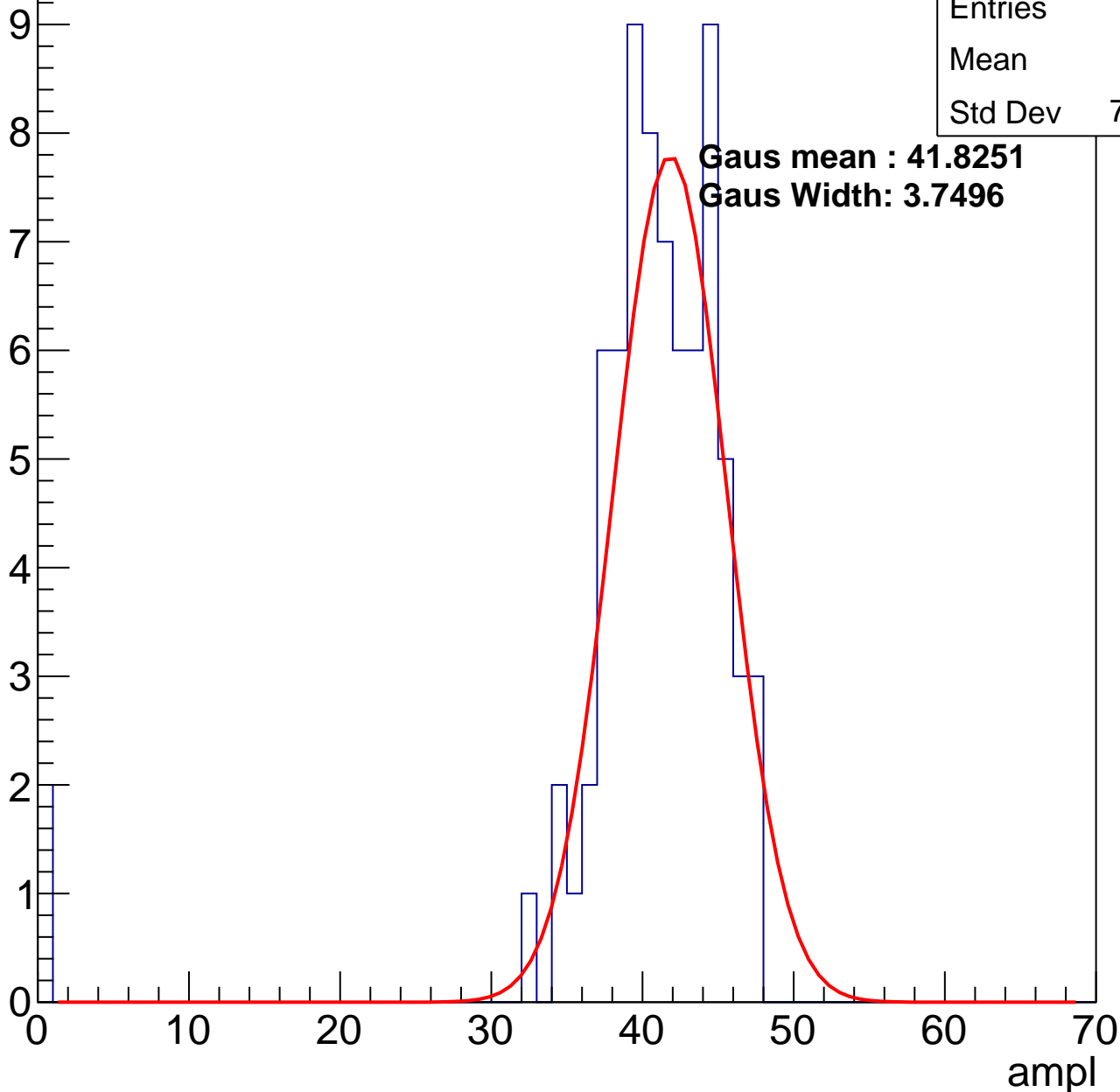
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	39.8
Std Dev	7.332

Gaus mean : 41.8251

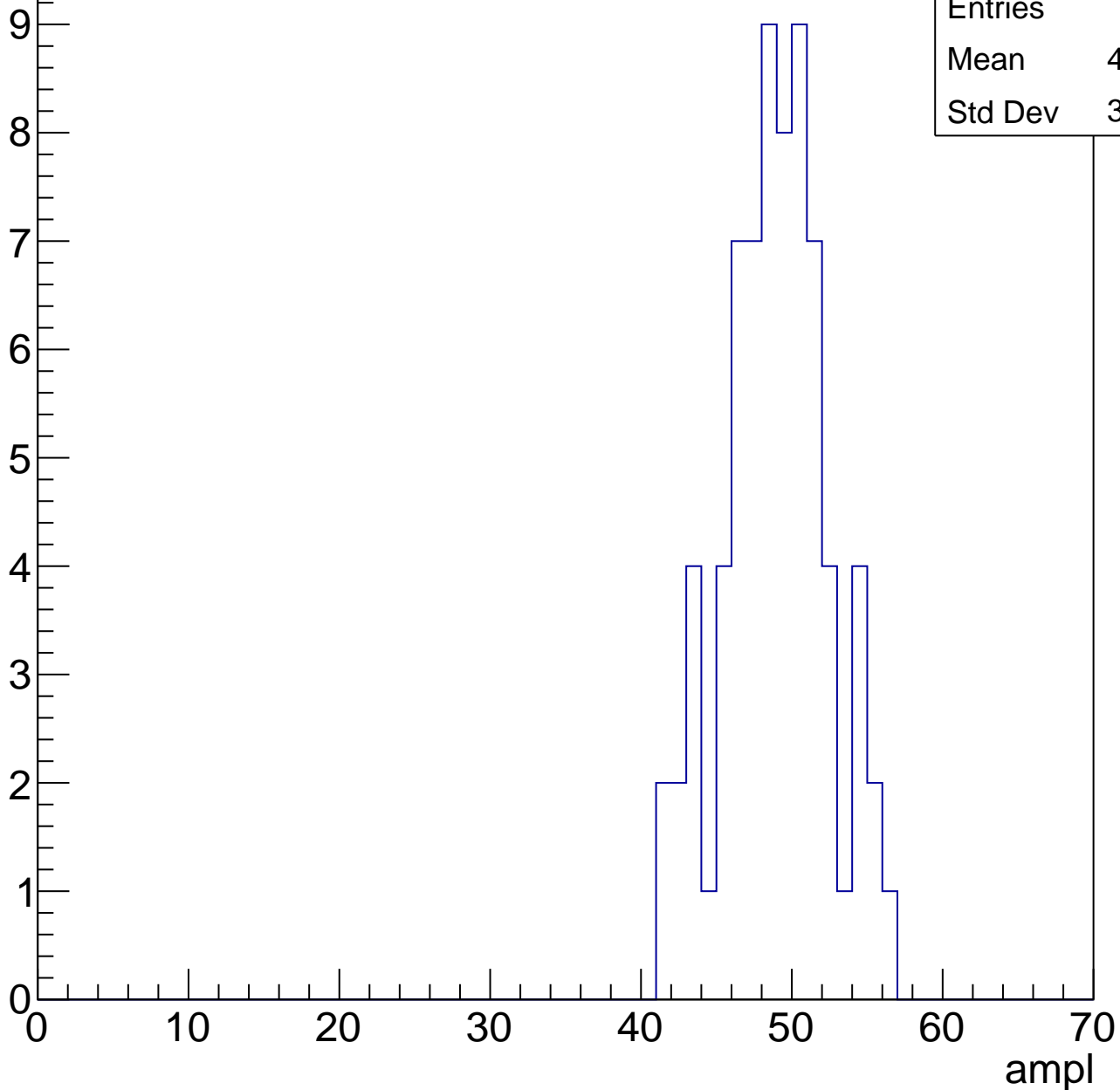
Gaus Width: 3.7496



B1L103S, U10-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

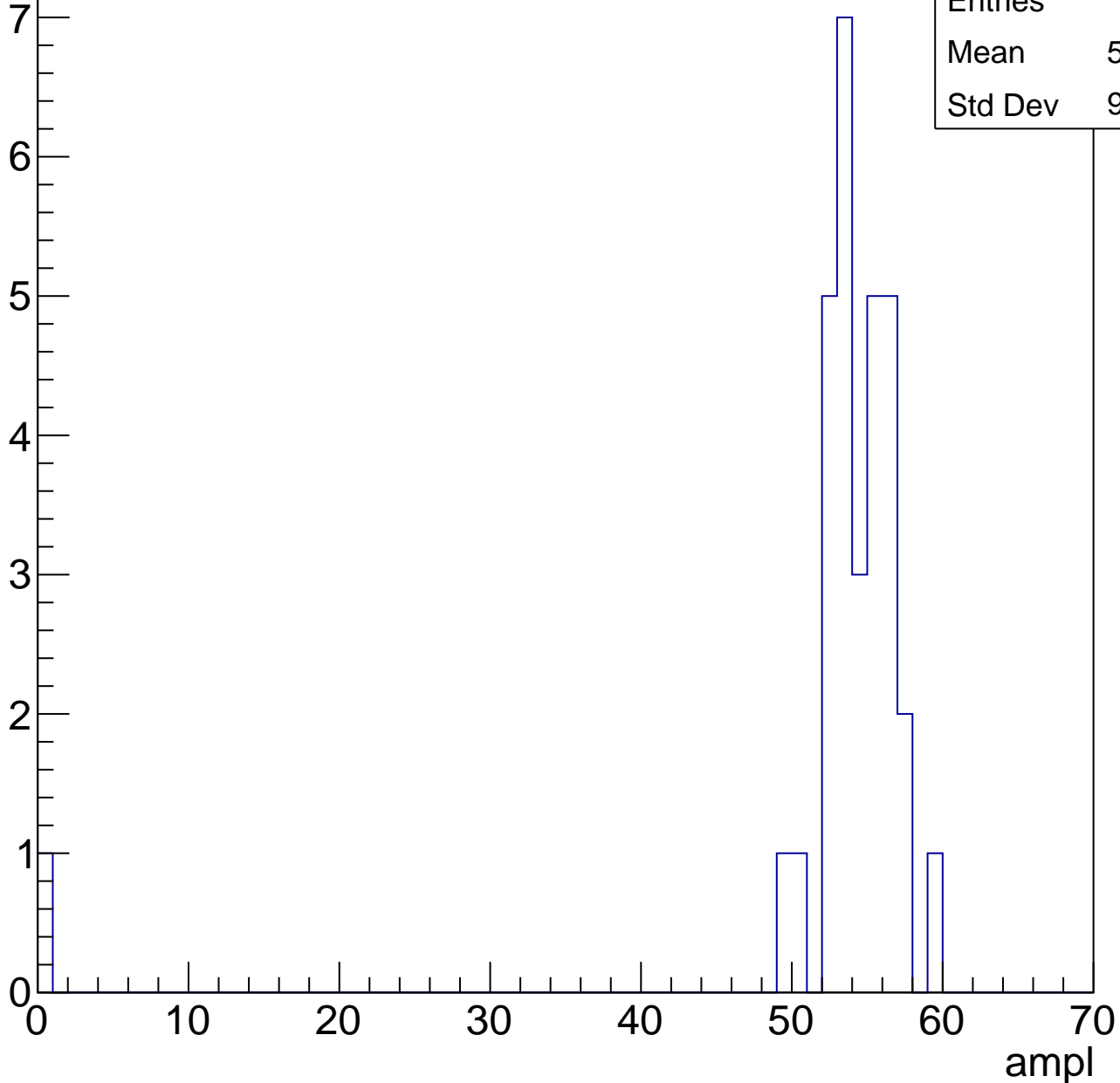


B1L103S, U10-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	52.26
Std Dev	9.768

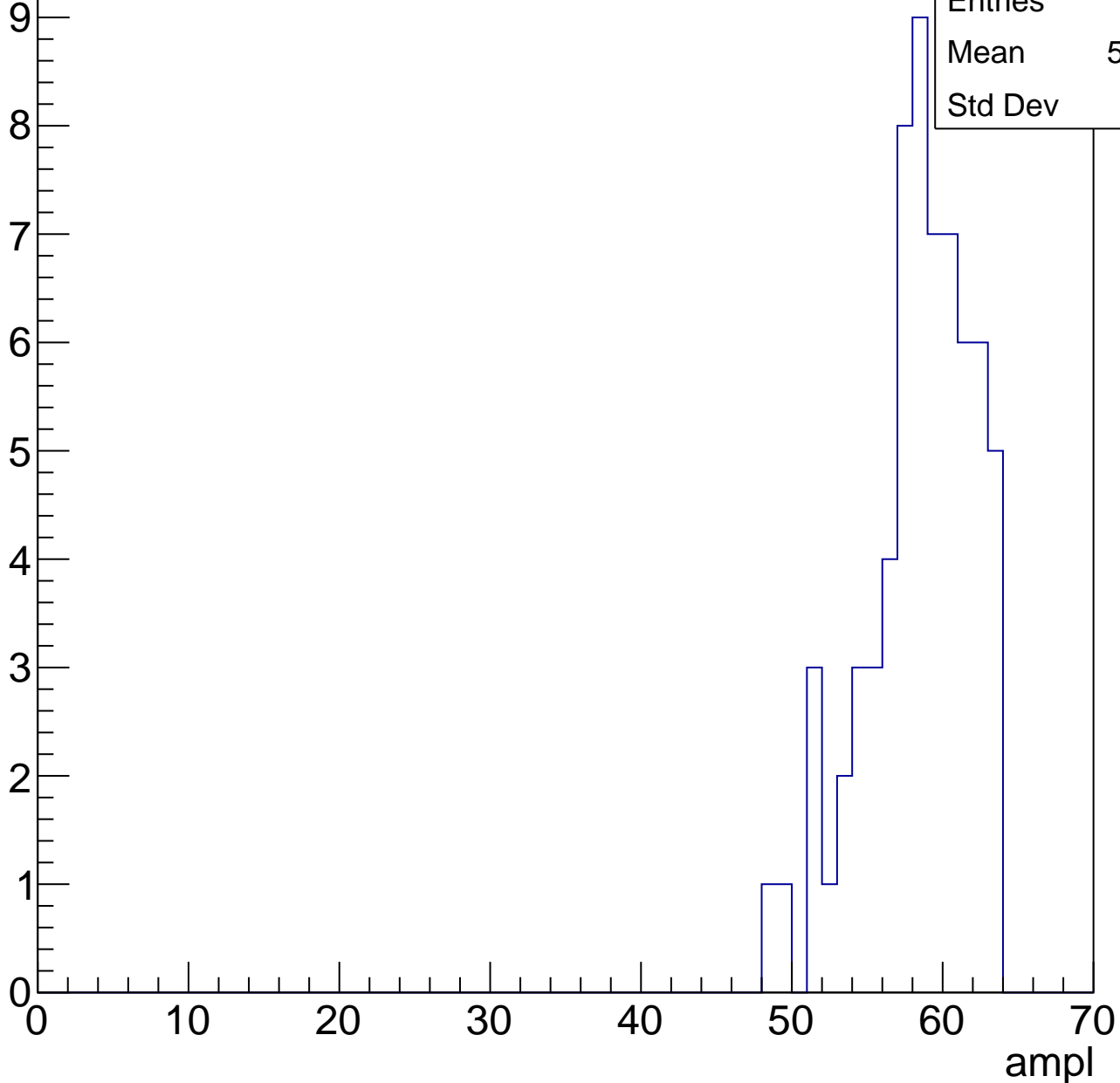


B1L103S, U10-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	57.92
Std Dev	3.53



B1L103S, U10-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

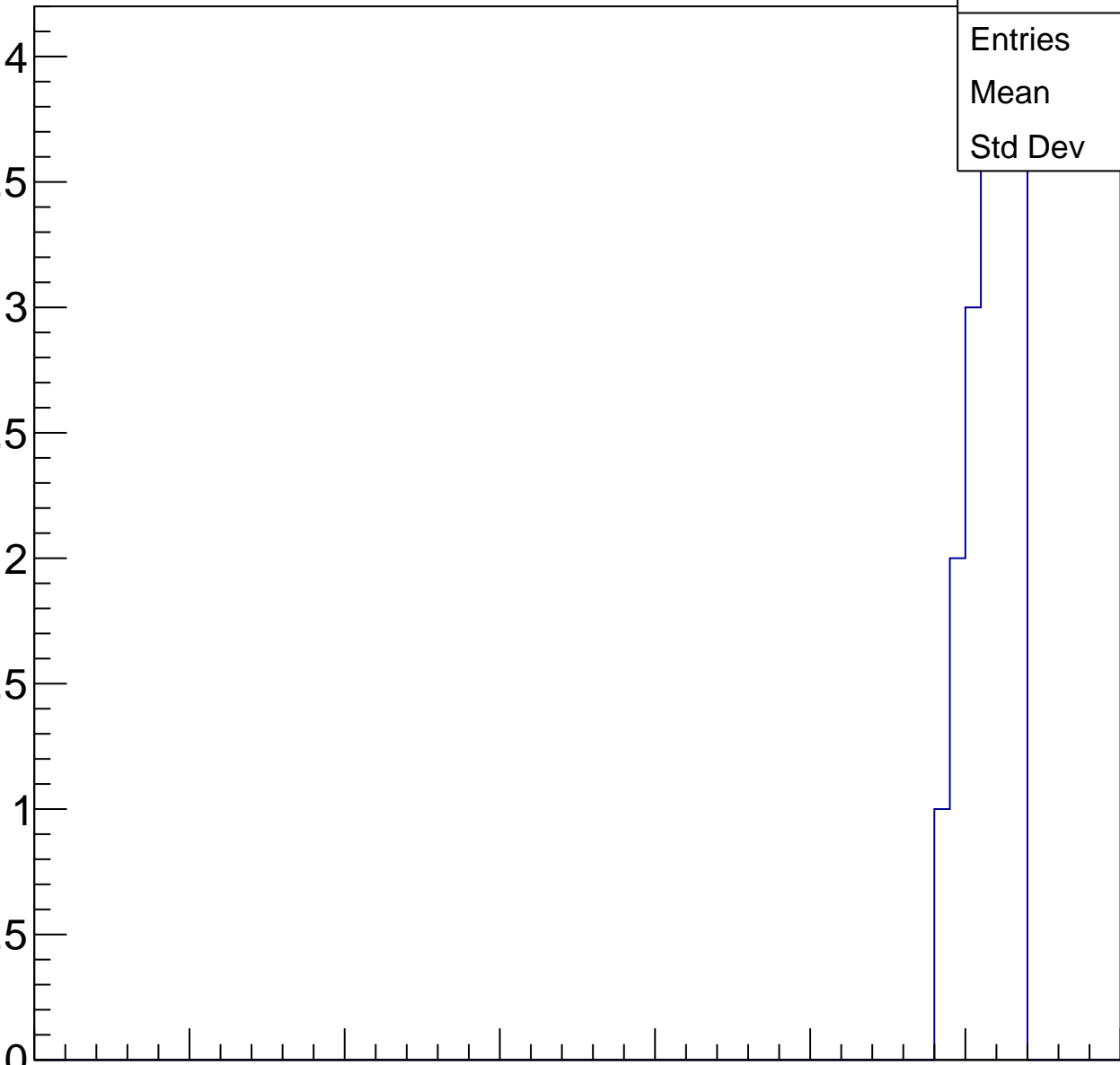
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	18
Mean	61.11
Std Dev	1.487

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U10-ch121, adc0

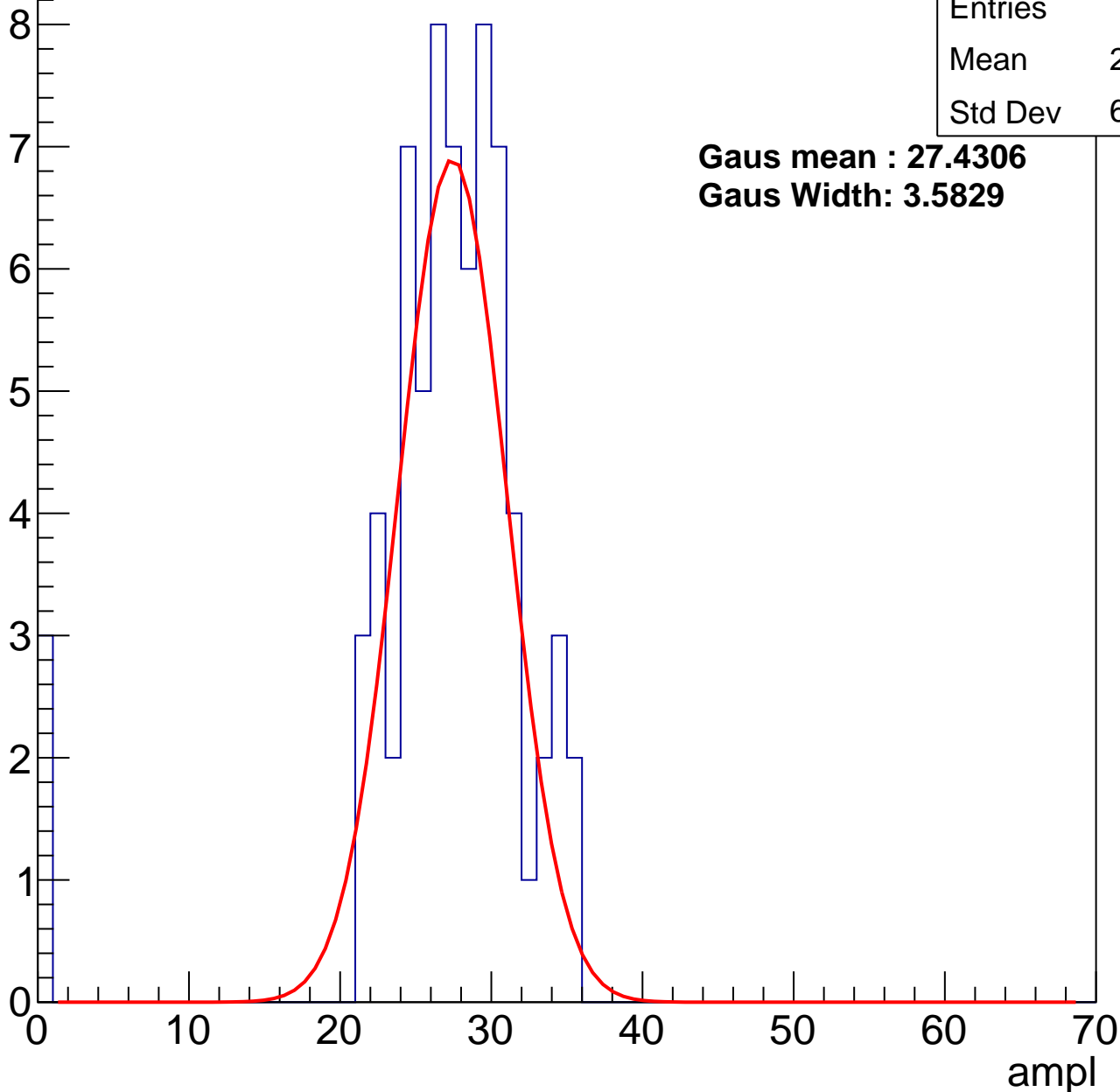
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	26.26
Std Dev	6.472

Gaus mean : 27.4306

Gaus Width: 3.5829



B1L103S, U10-ch121, adc1

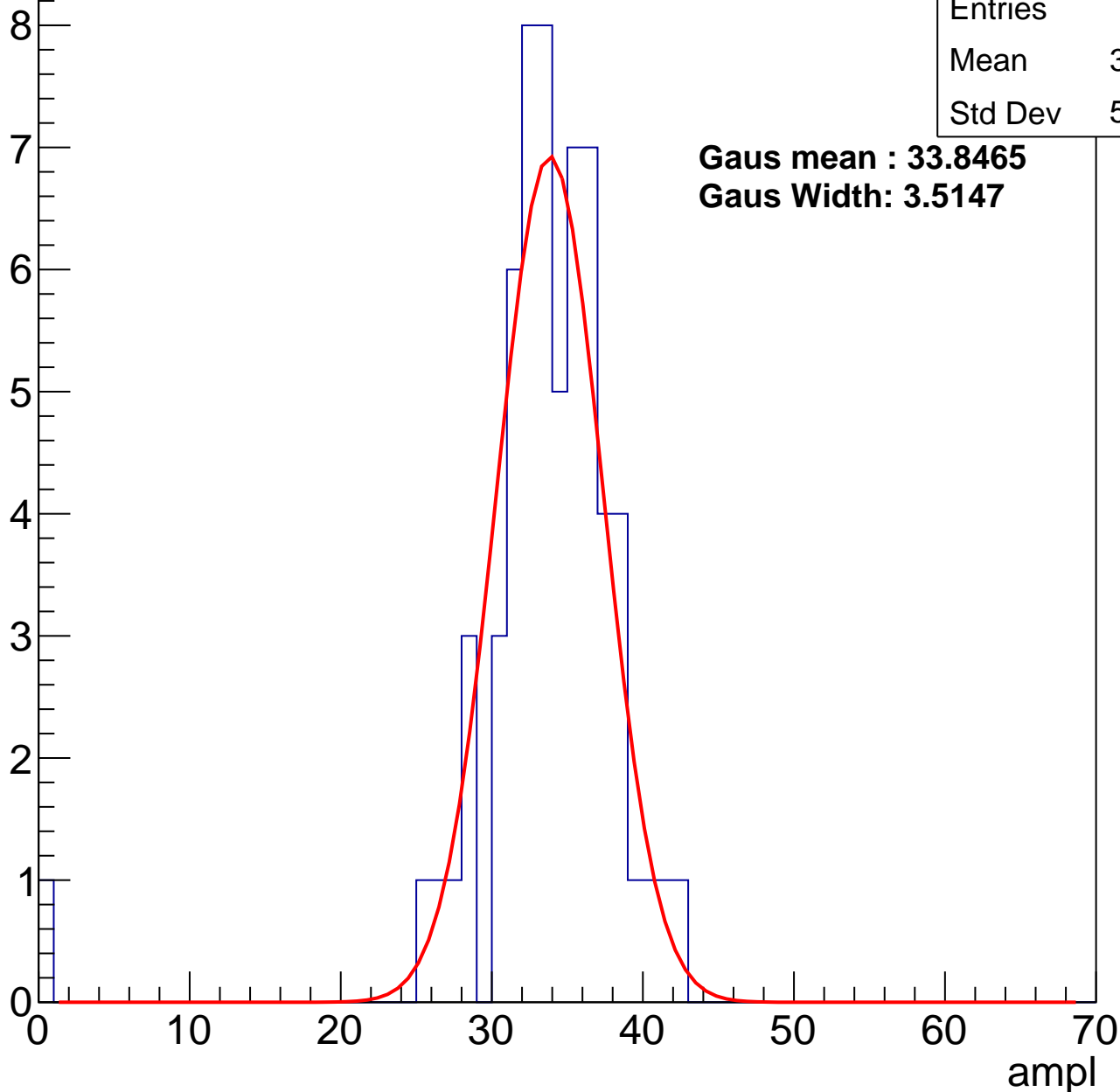
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.13
Std Dev	5.435

Gaus mean : 33.8465

Gaus Width: 3.5147



B1L103S, U10-ch121, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	37.2
Std Dev	12.4

Gaus mean : 41.2042

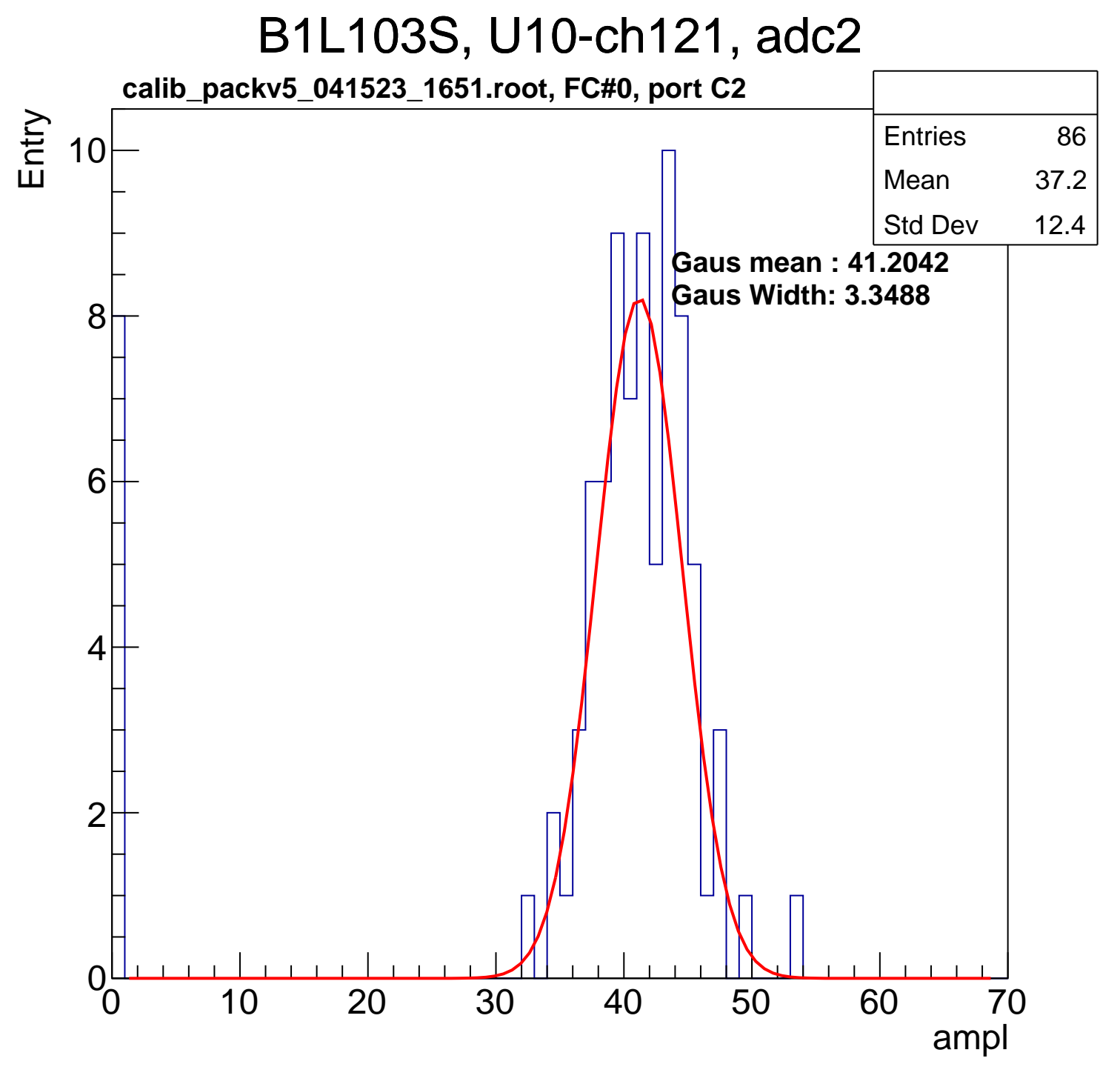
Gaus Width: 3.3488

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

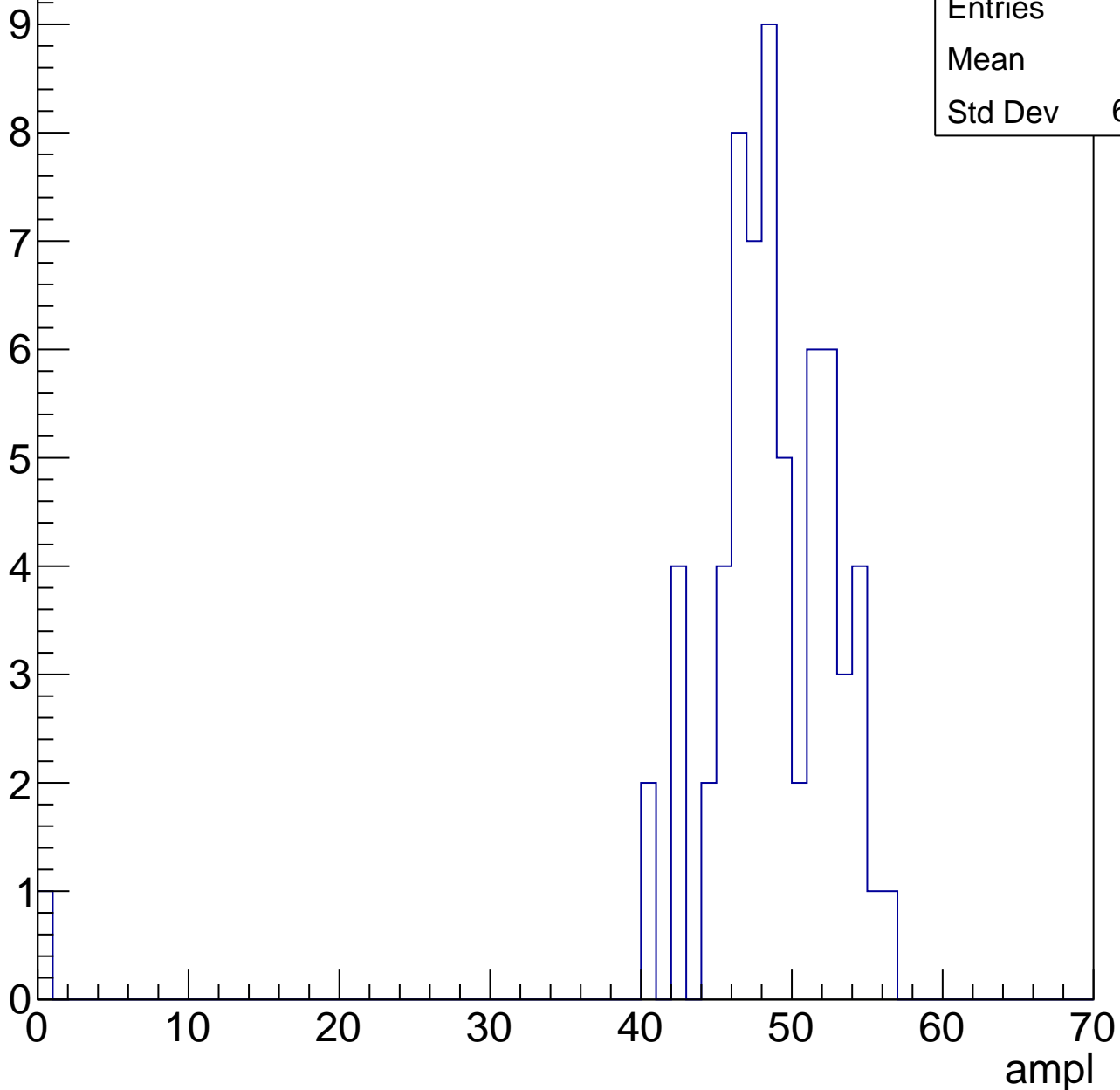


B1L103S, U10-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	47.6
Std Dev	6.981

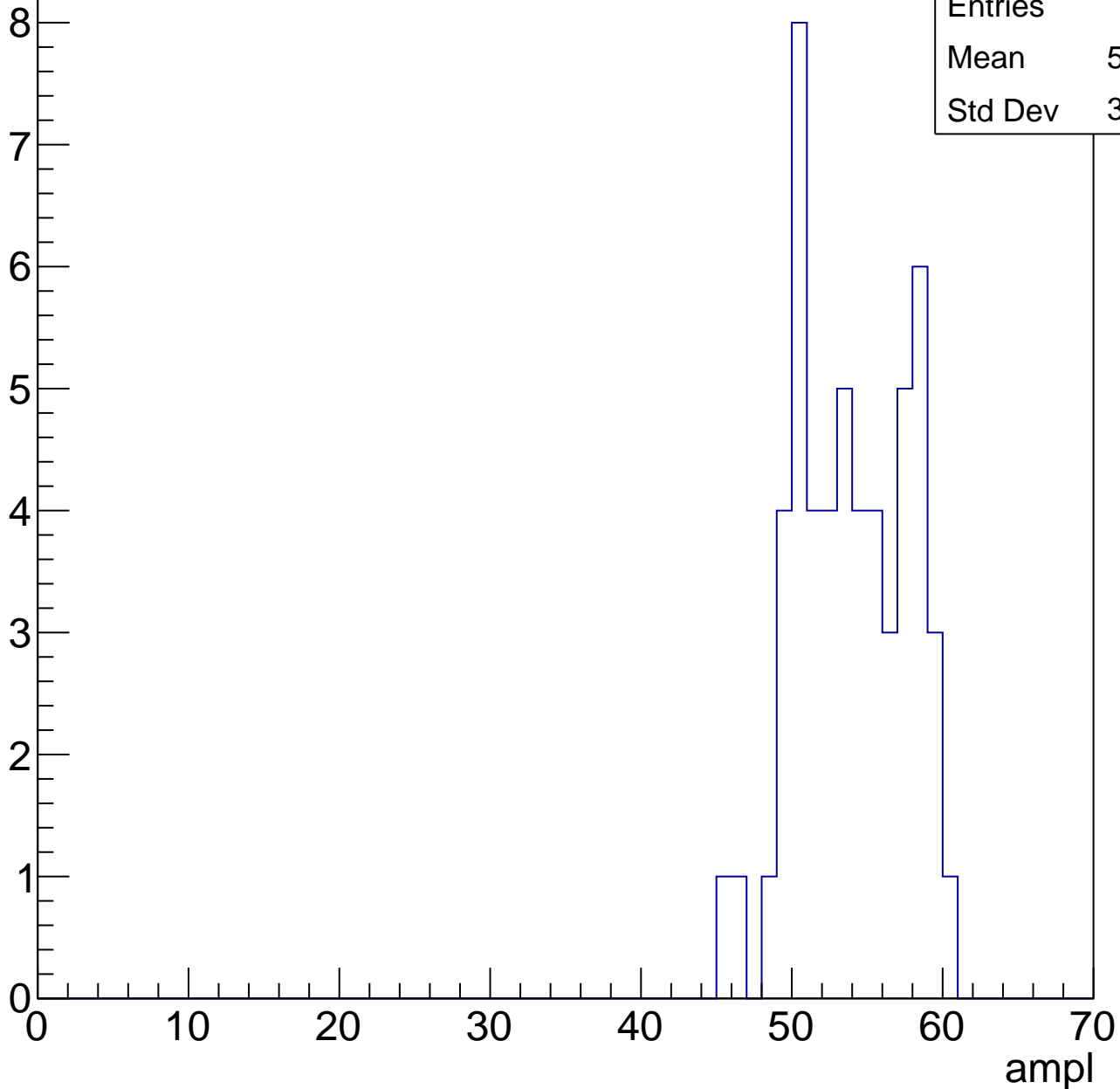


B1L103S, U10-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.44
Std Dev	3.645

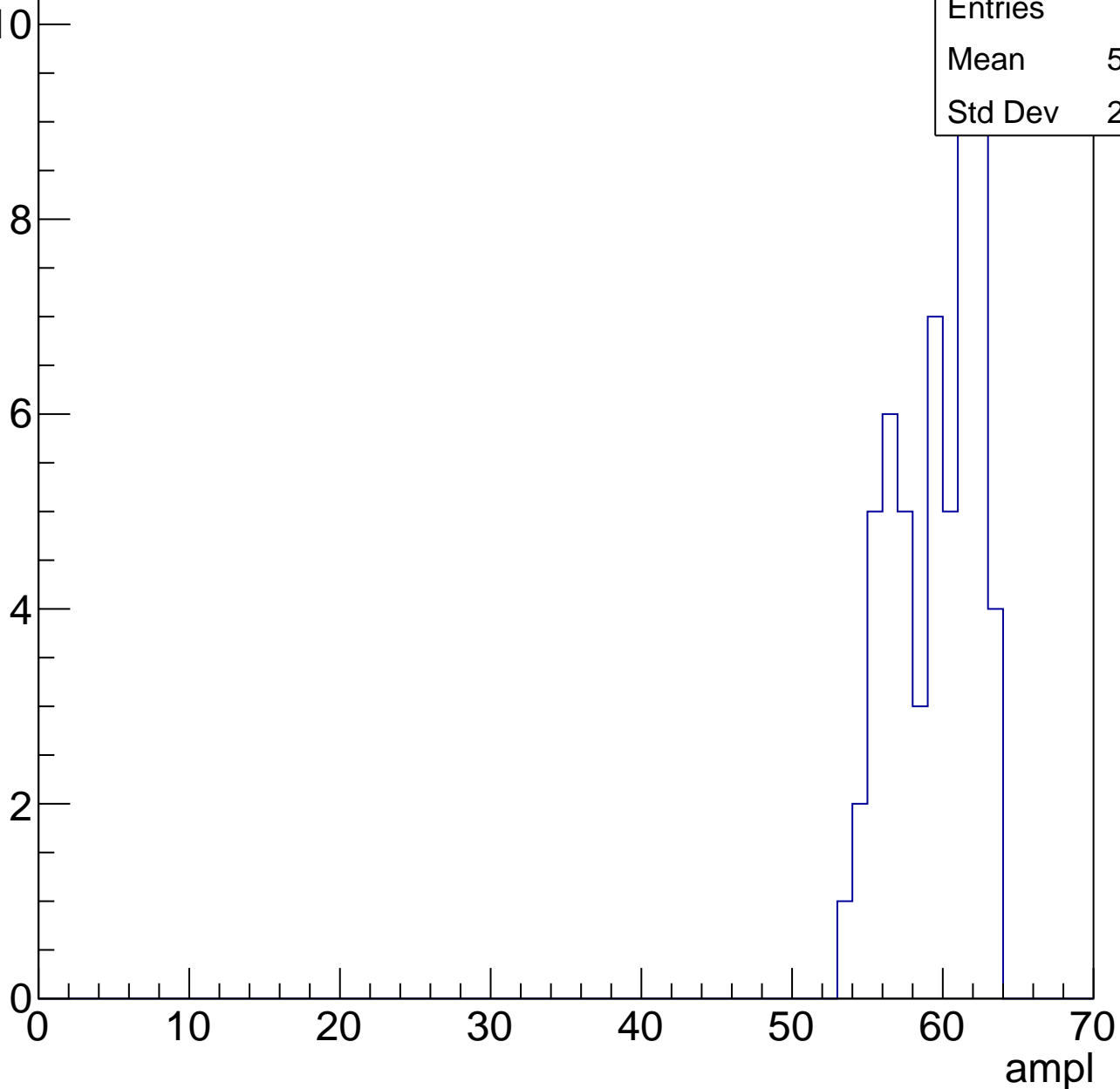


B1L103S, U10-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	59.04
Std Dev	2.772

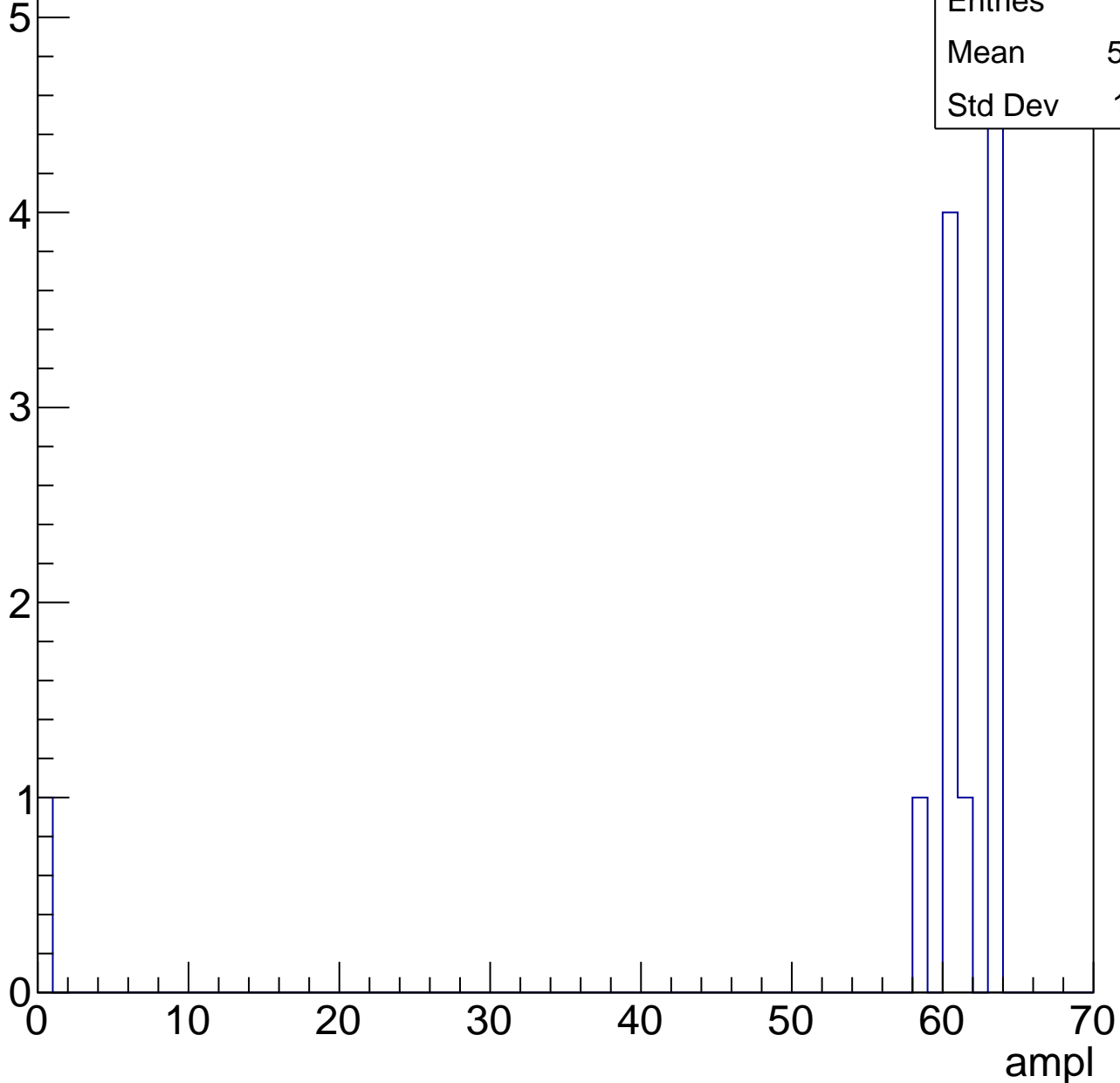


B1L103S, U10-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

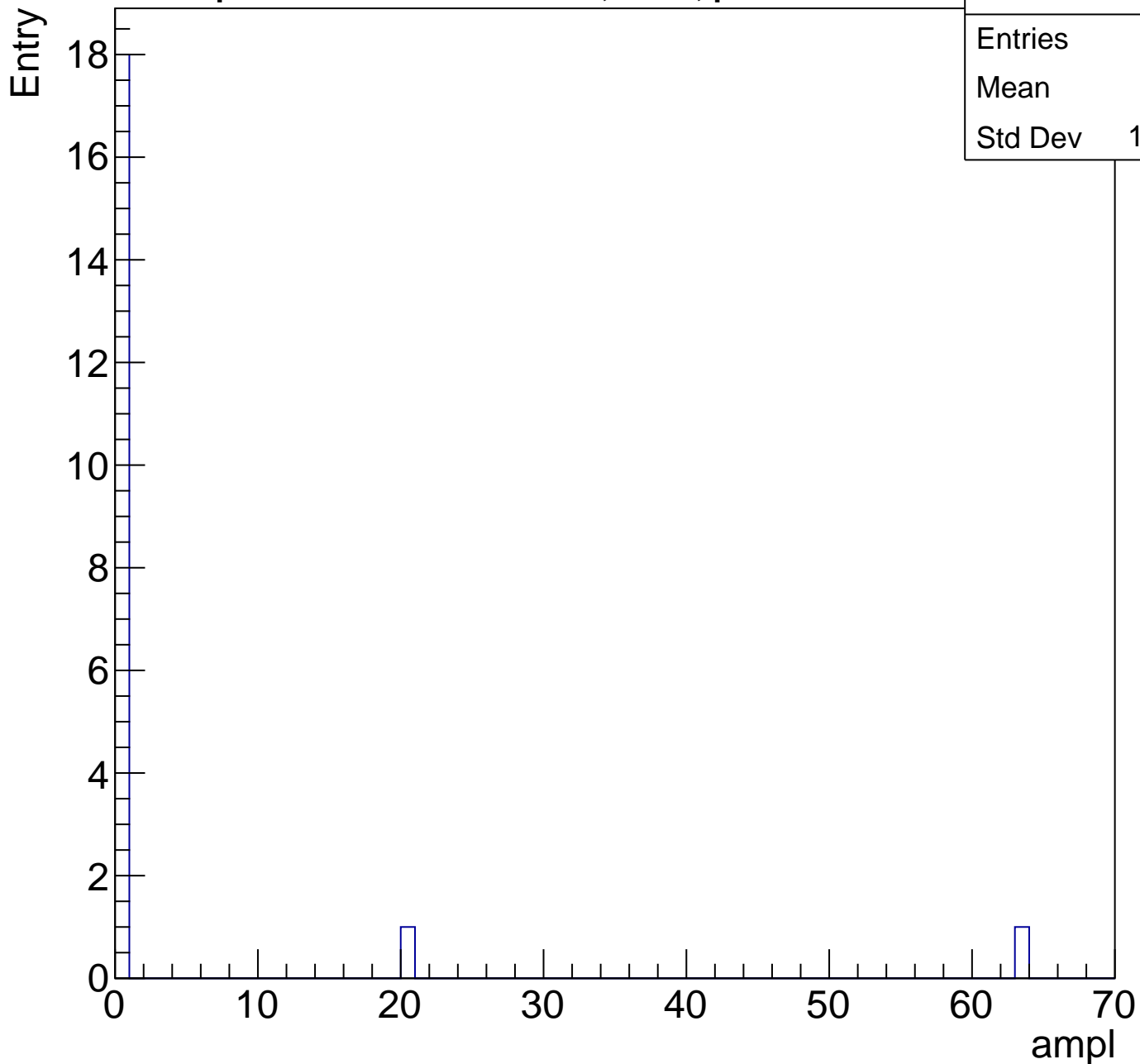
Entries	12
Mean	56.17
Std Dev	17.01



B1L103S, U10-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.19



B1L103S, U10-ch122, adc0

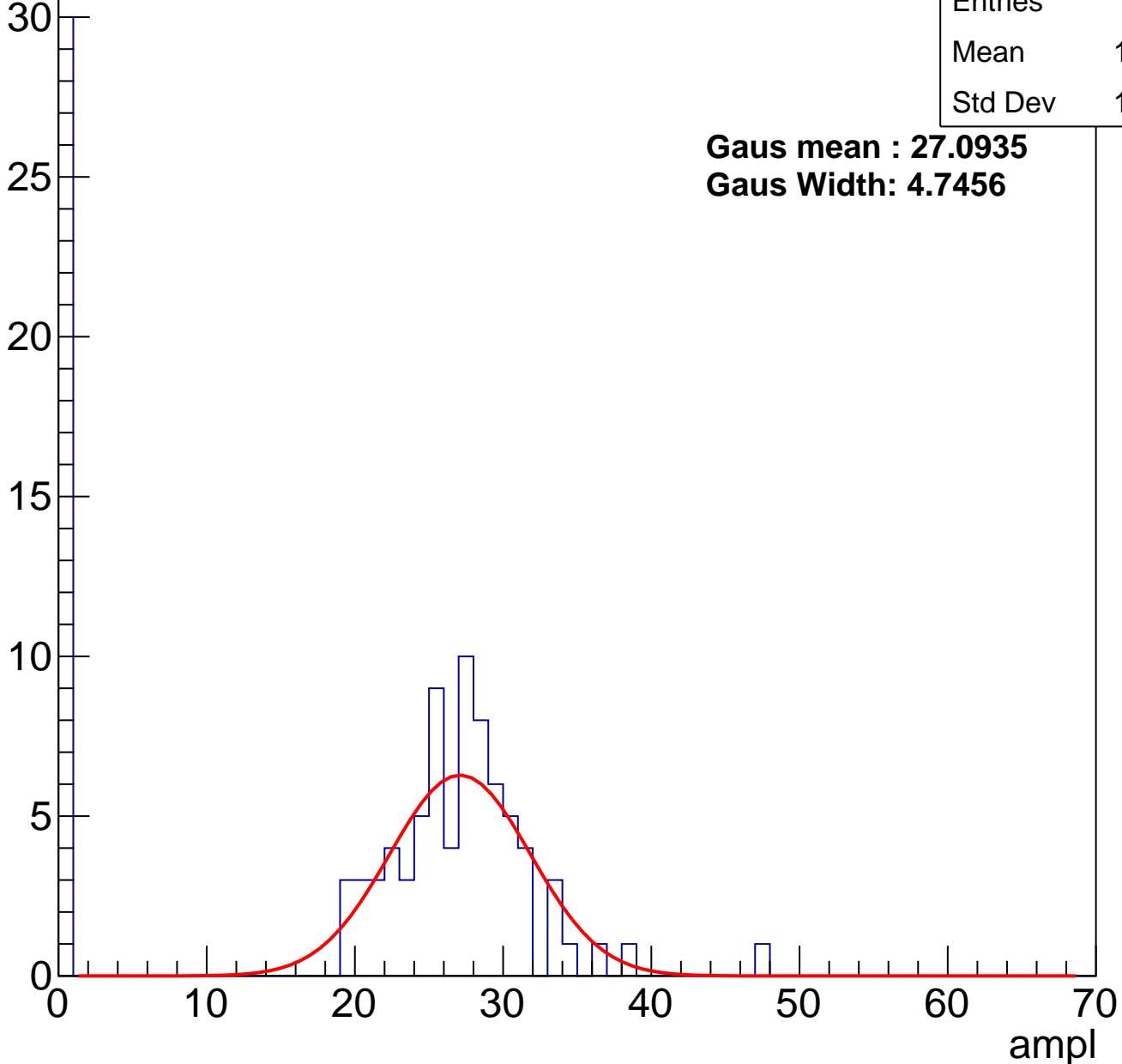
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	19.06
Std Dev	12.74

Gaus mean : 27.0935

Gaus Width: 4.7456

Entry



B1L103S, U10-ch122, adc1

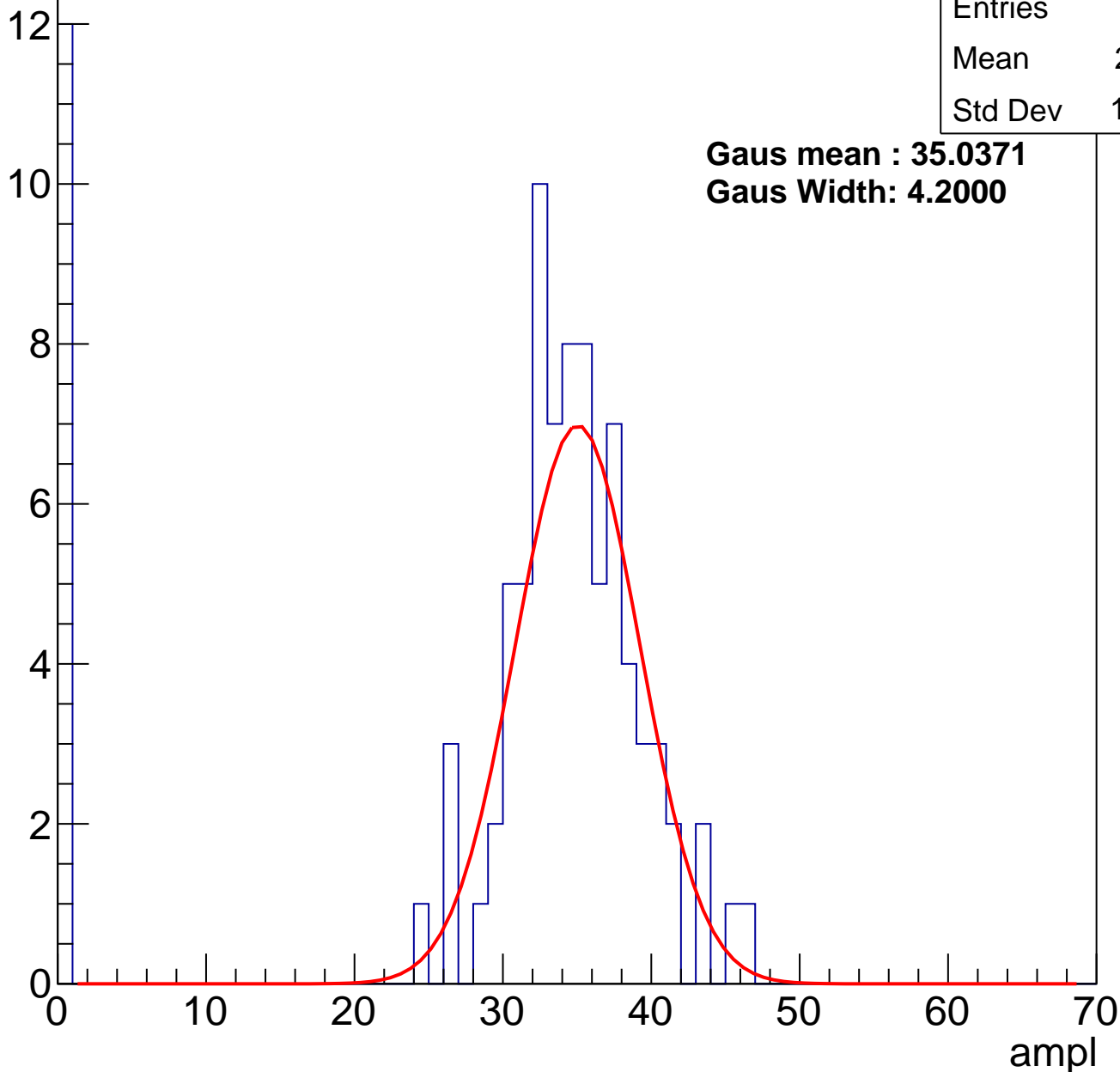
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	29.81
Std Dev	12.34

Gaus mean : 35.0371

Gaus Width: 4.2000

Entry



B1L103S, U10-ch122, adc2

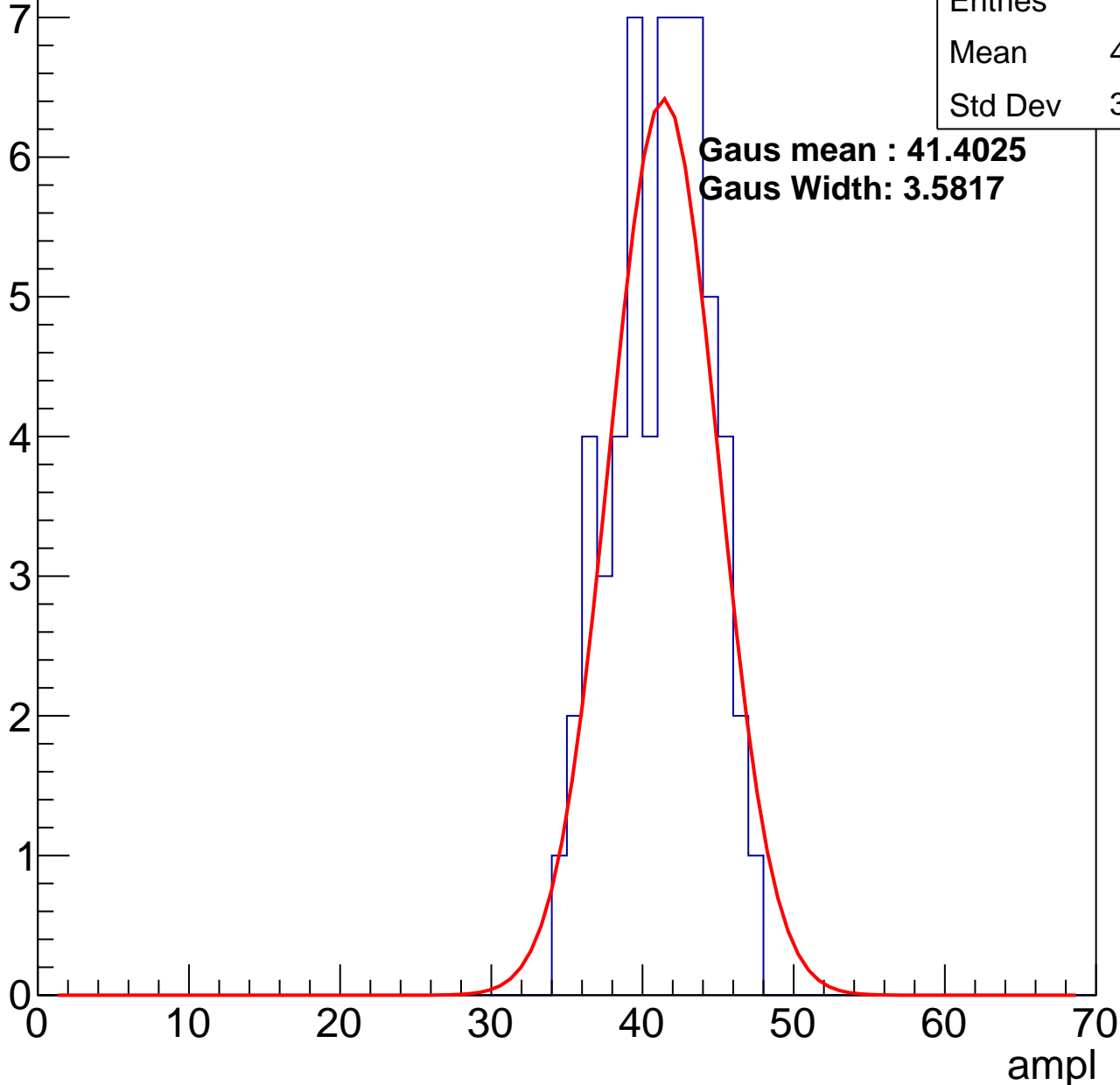
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.78
Std Dev	3.119

Gaus mean : 41.4025

Gaus Width: 3.5817

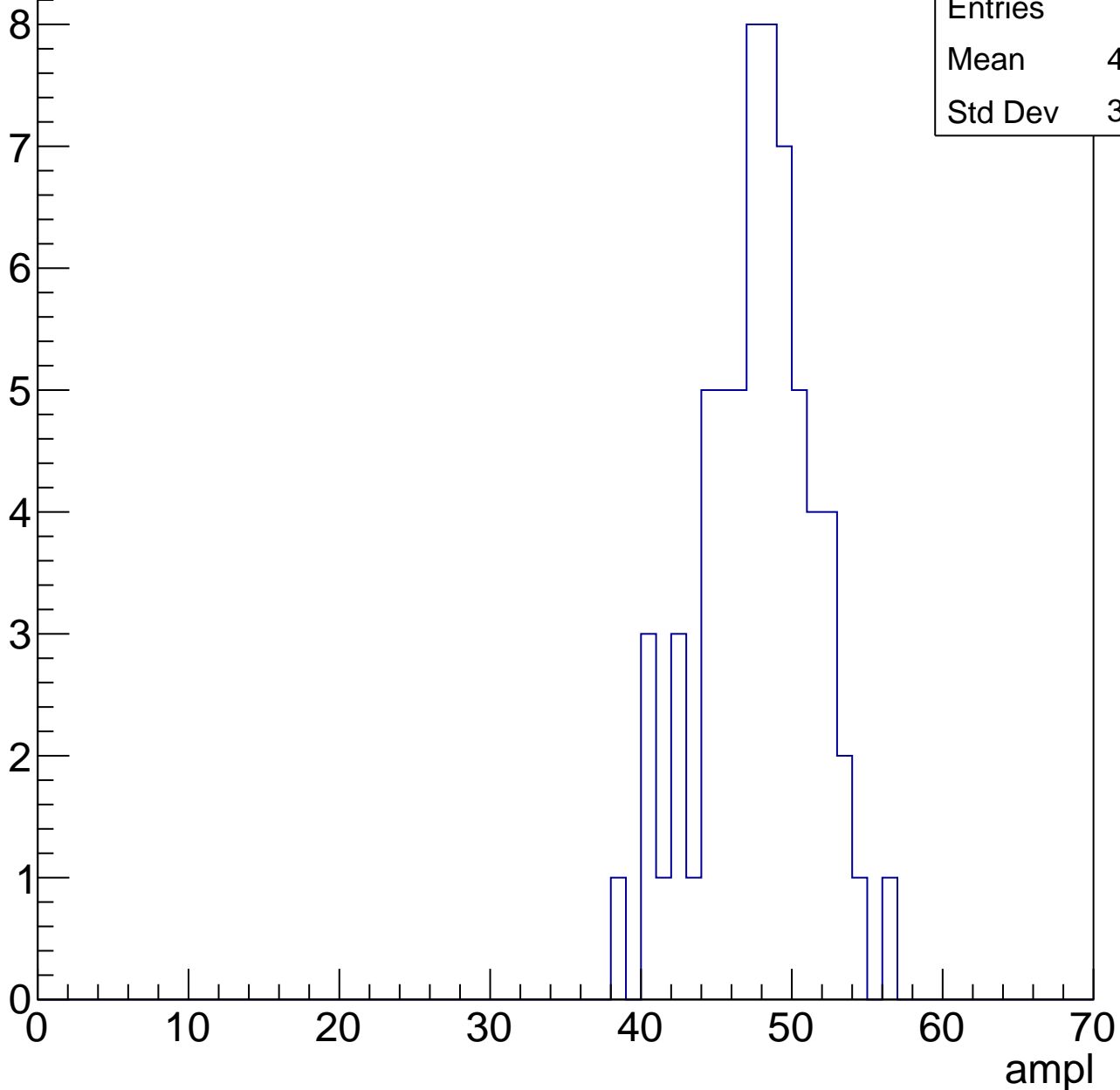


B1L103S, U10-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	47.25
Std Dev	3.683

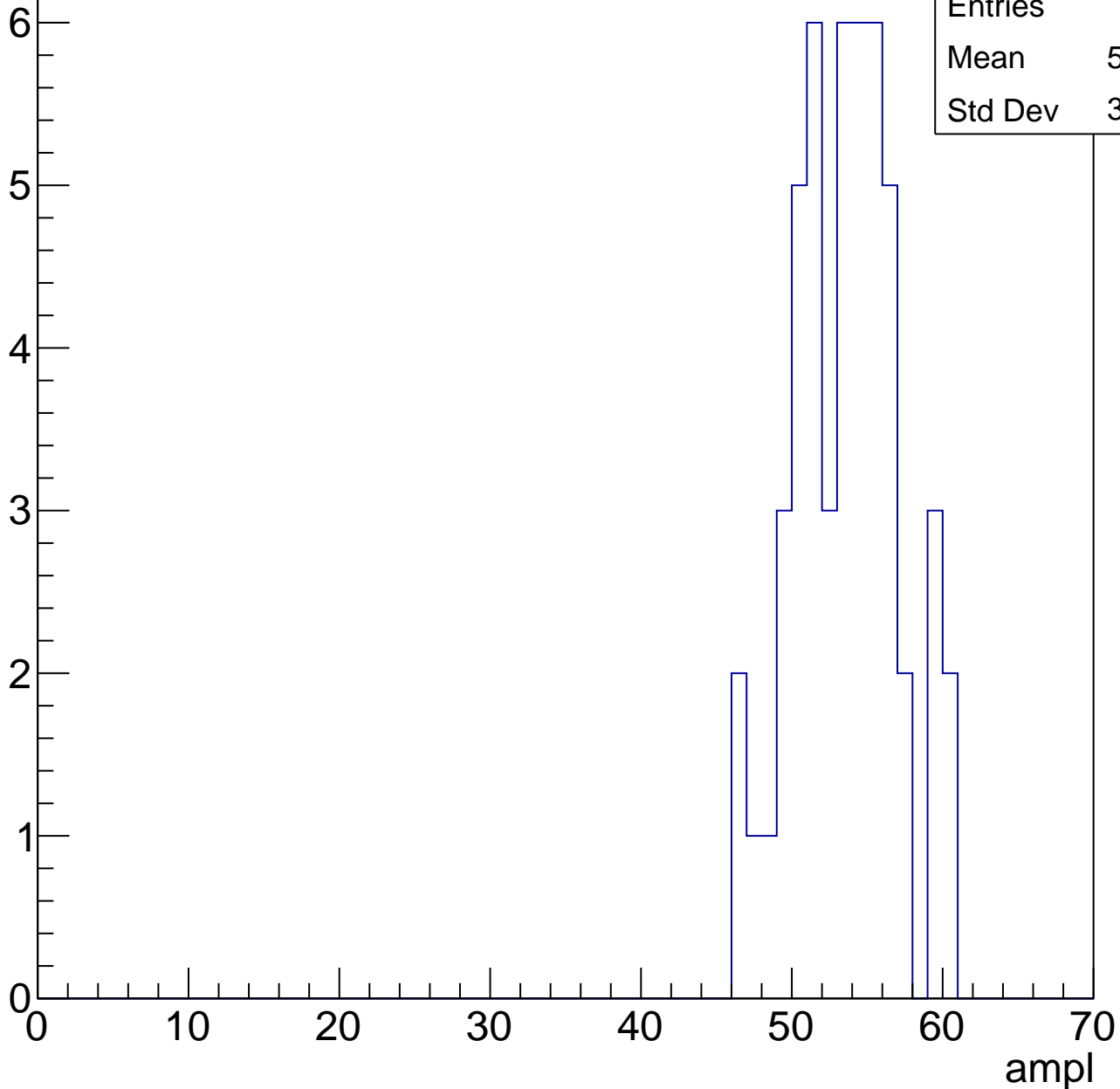


B1L103S, U10-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

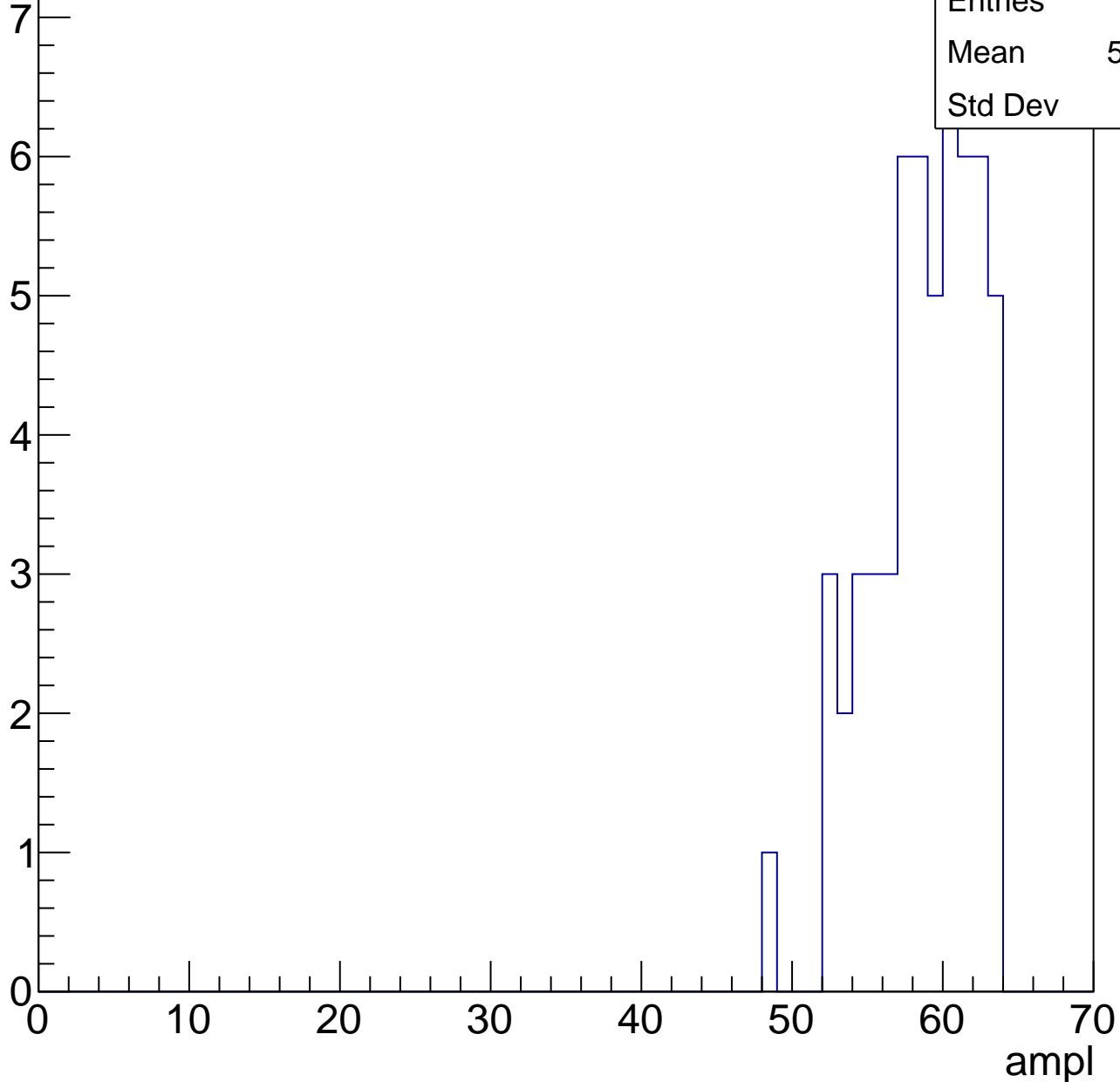
Entries	51
Mean	53.12
Std Dev	3.405



B1L103S, U10-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

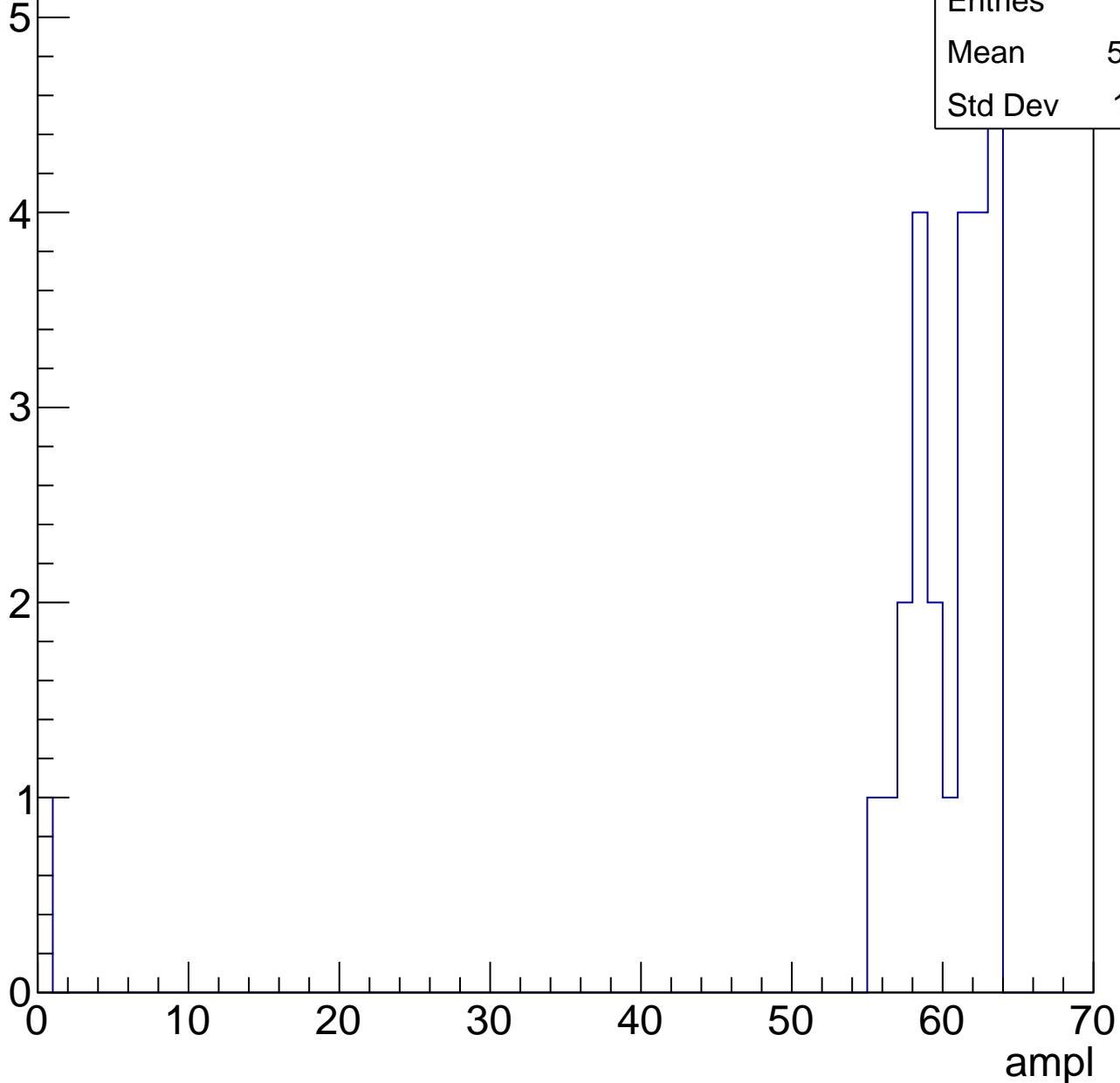


B1L103S, U10-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	57.68
Std Dev	12.01



B1L103S, U10-ch122, adc7

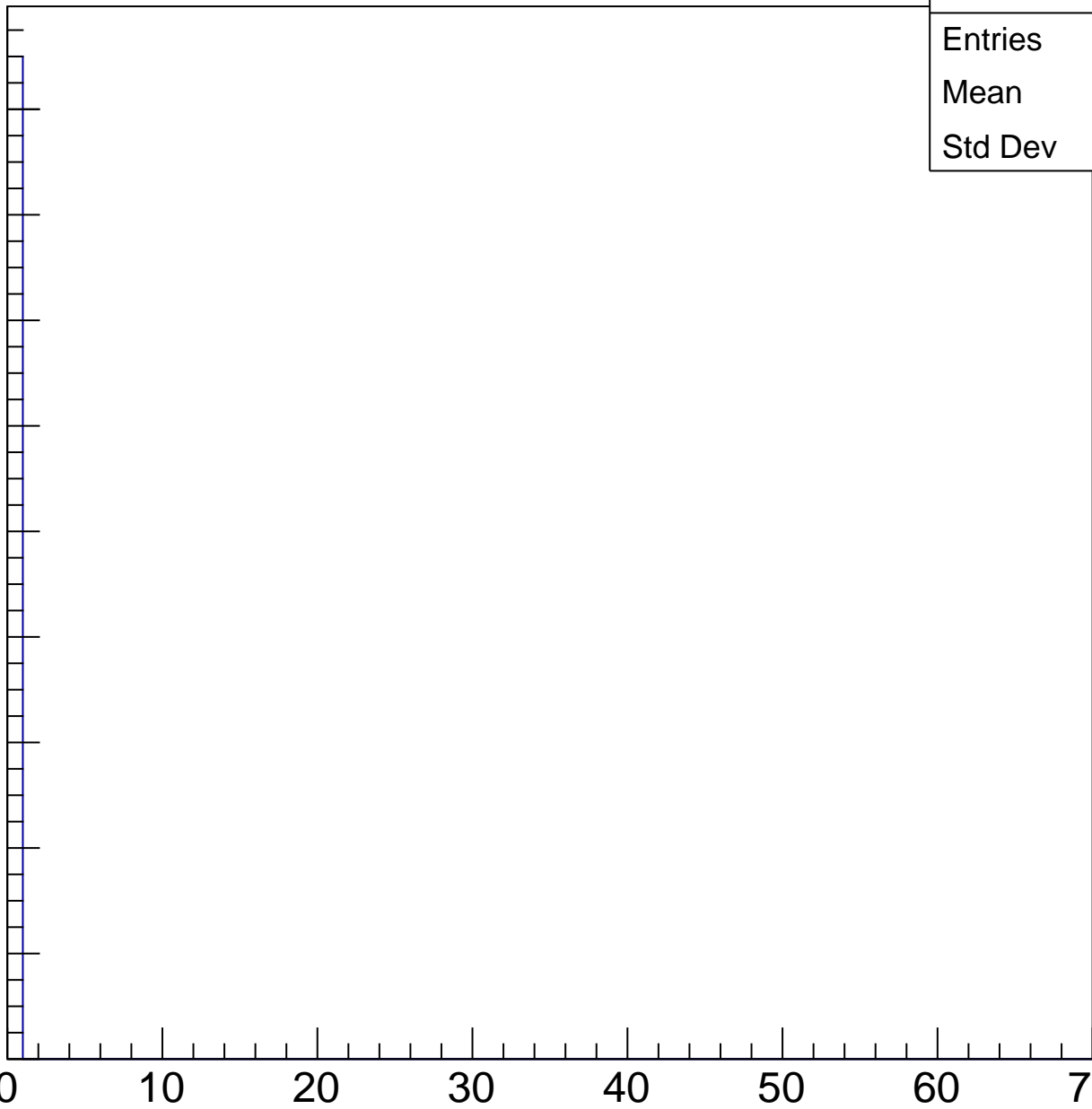
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl



B1L103S, U10-ch123, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.98
Std Dev	11.67

Gaus mean : 31.1268

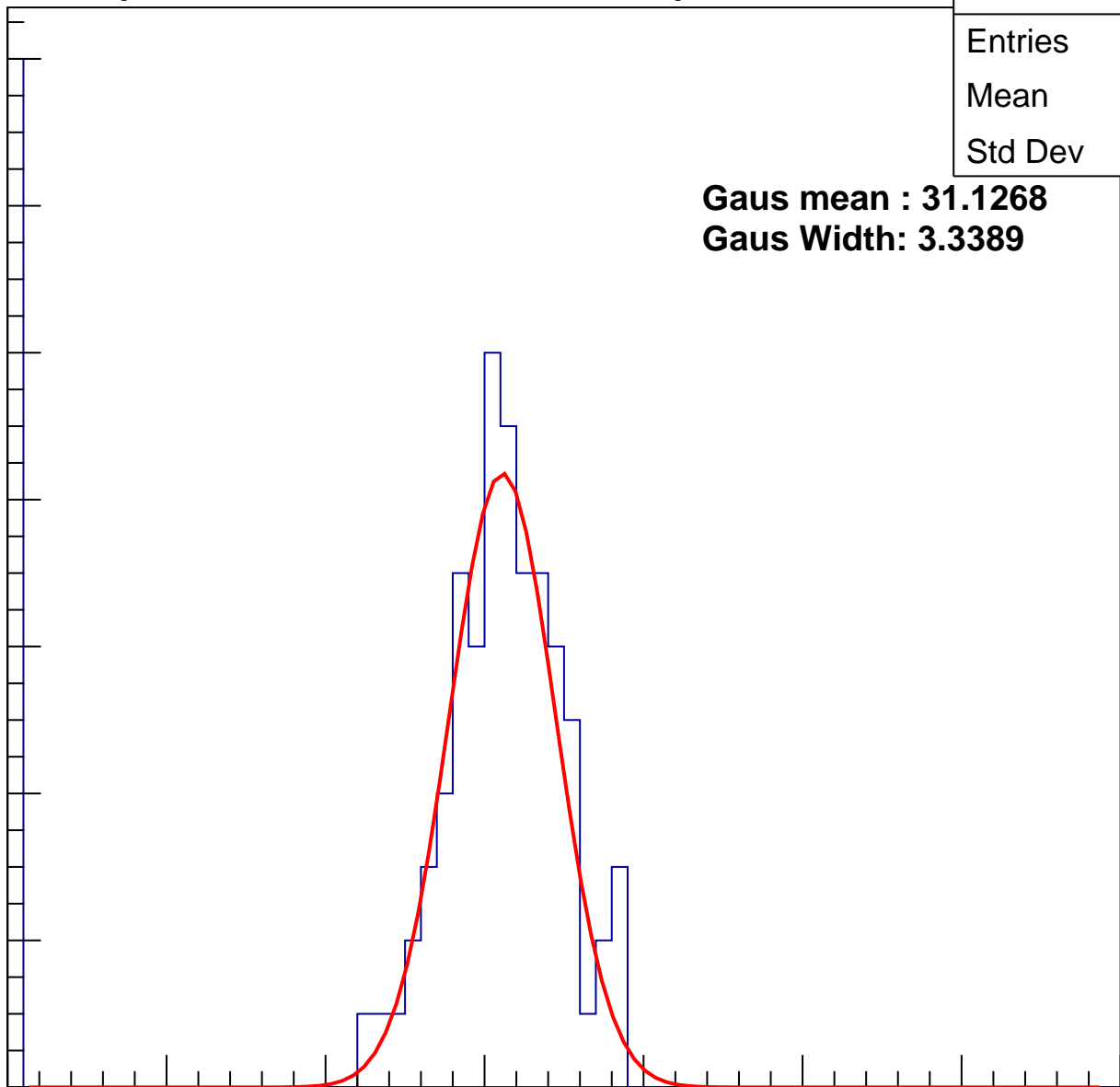
Gaus Width: 3.3389

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch123, adc1

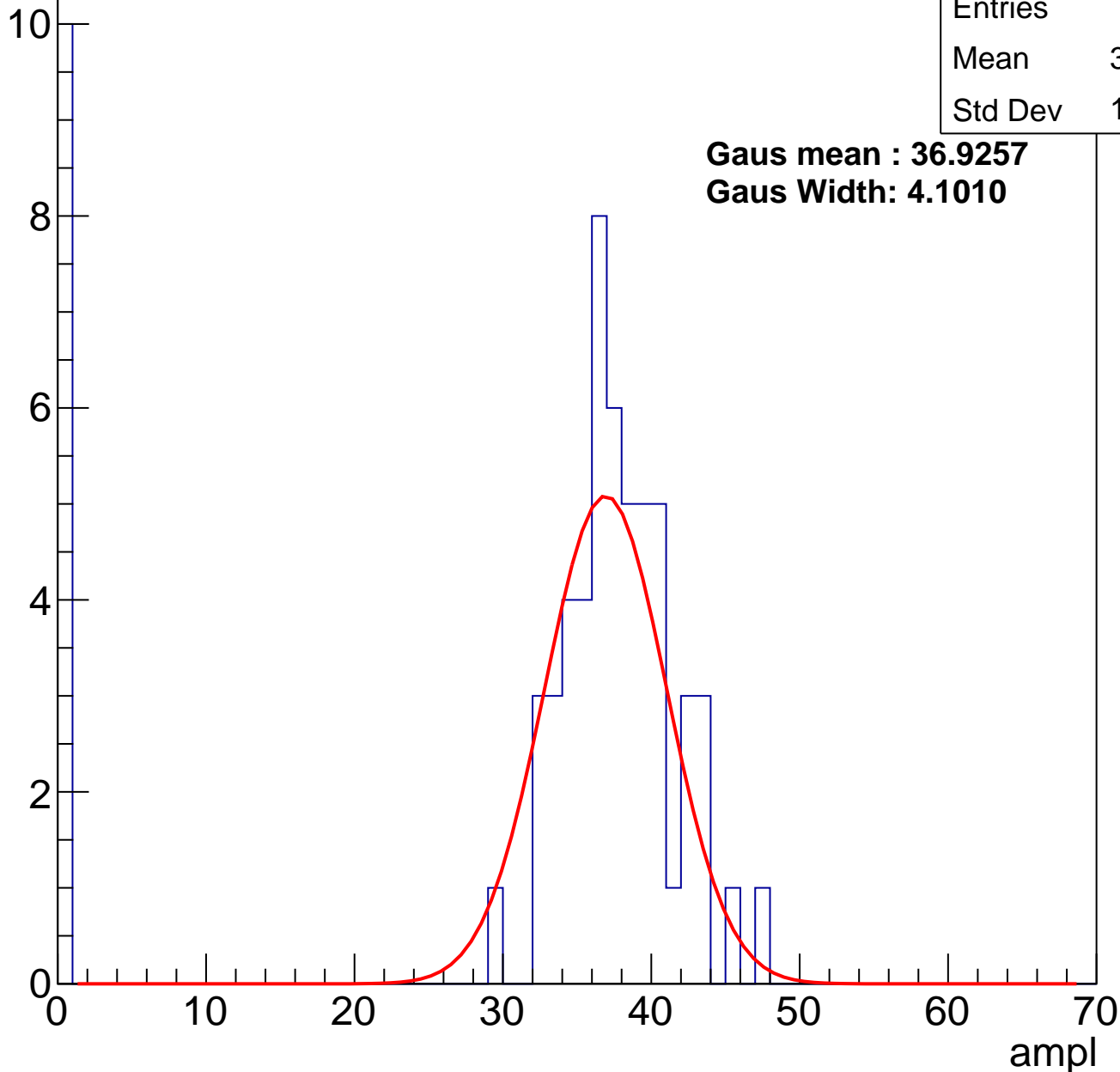
calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	31.48
Std Dev	14.06

Gaus mean : 36.9257

Gaus Width: 4.1010

Entry



B1L103S, U10-ch123, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	41.24
Std Dev	10.17

Gaus mean : 43.6730

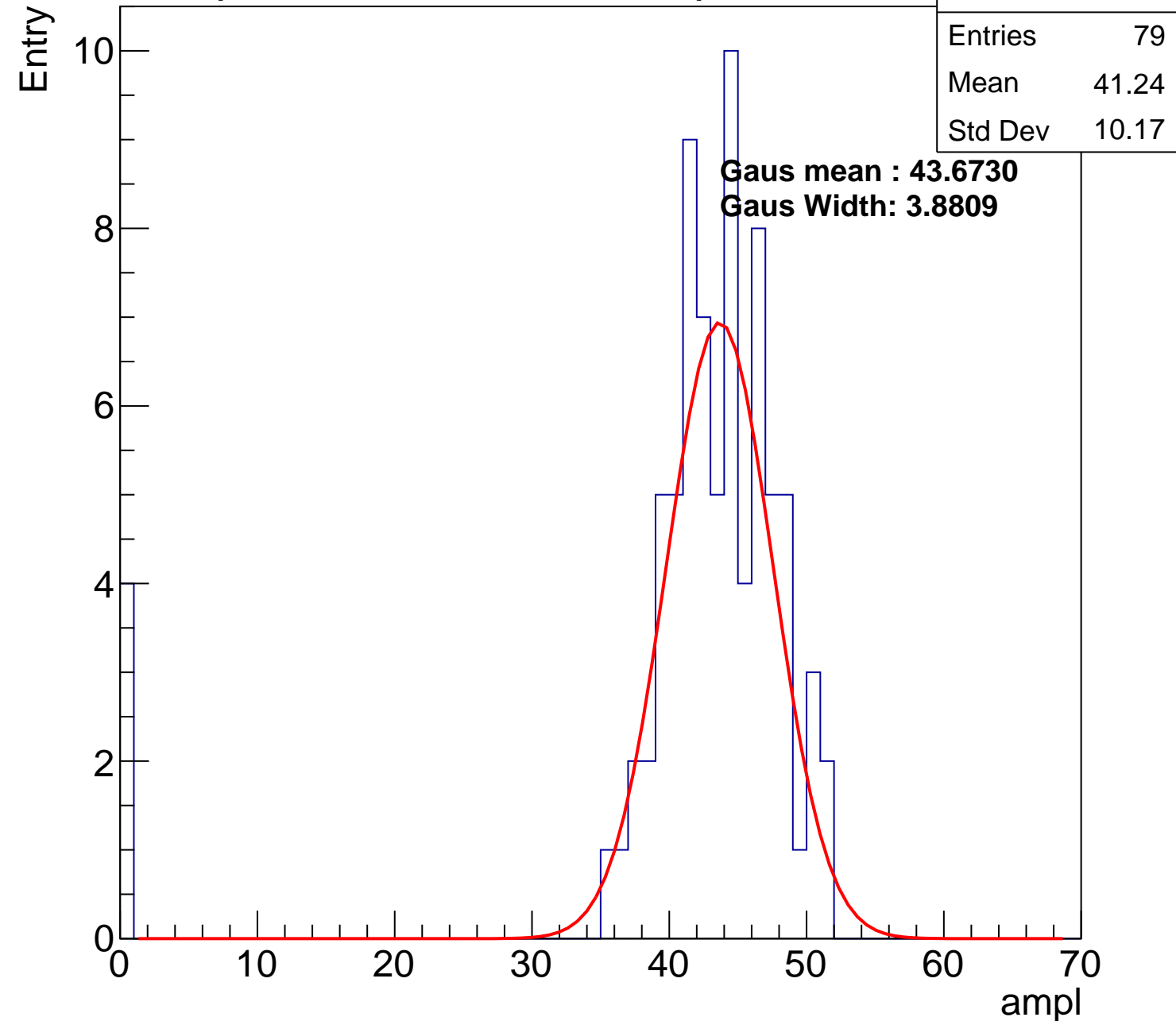
Gaus Width: 3.8809

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

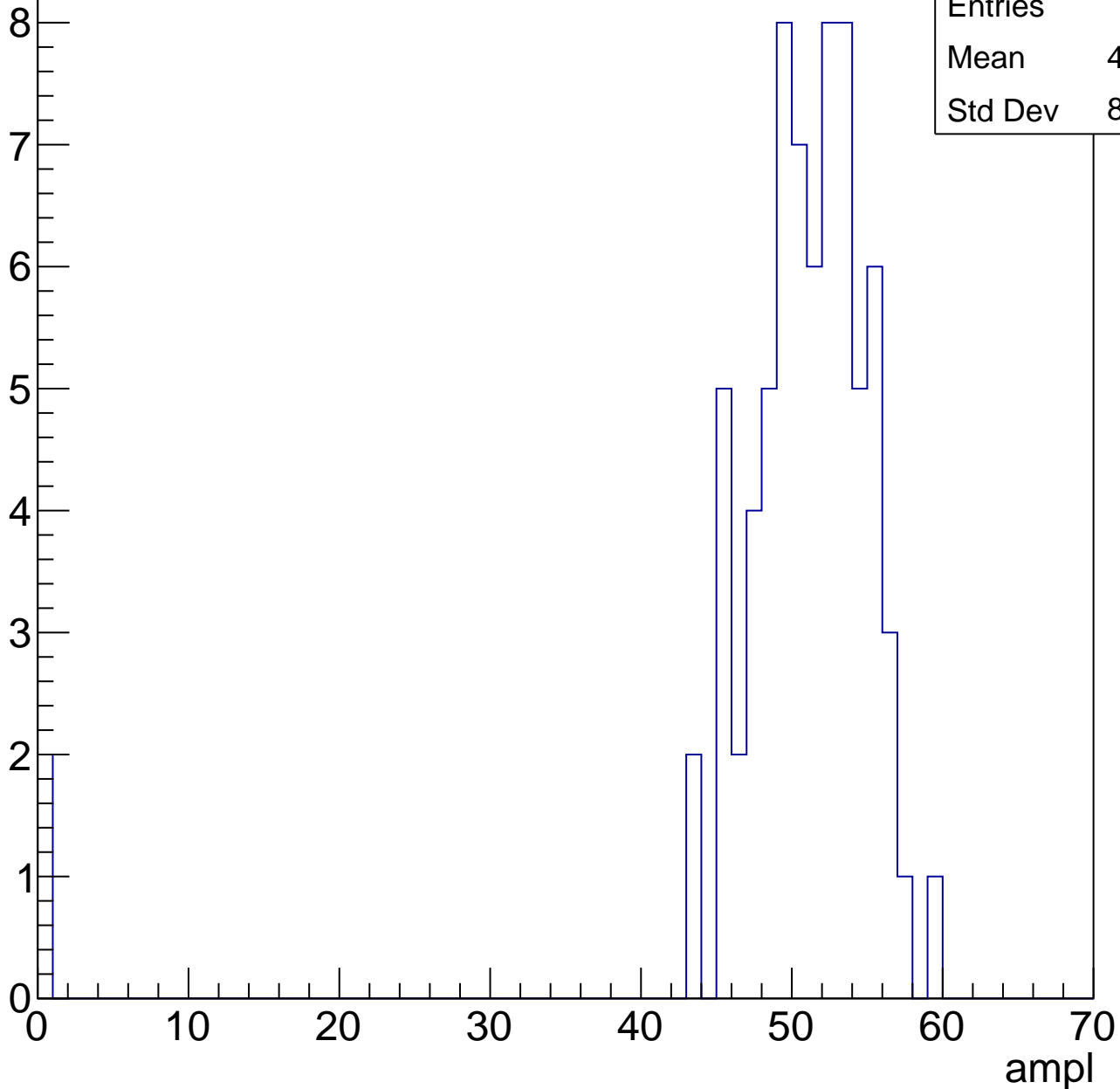


B1L103S, U10-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	49.36
Std Dev	8.965

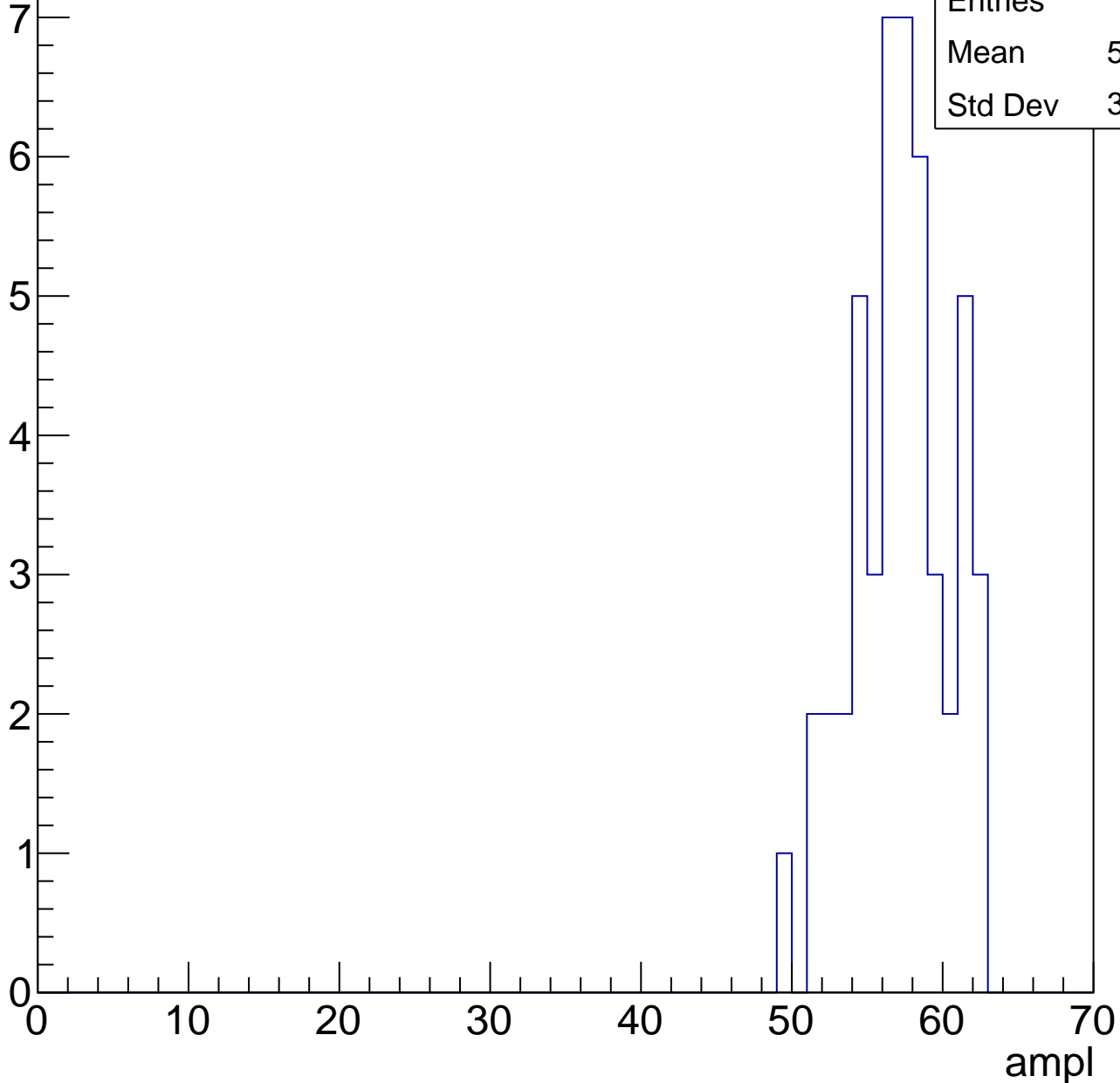


B1L103S, U10-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	56.73
Std Dev	3.127

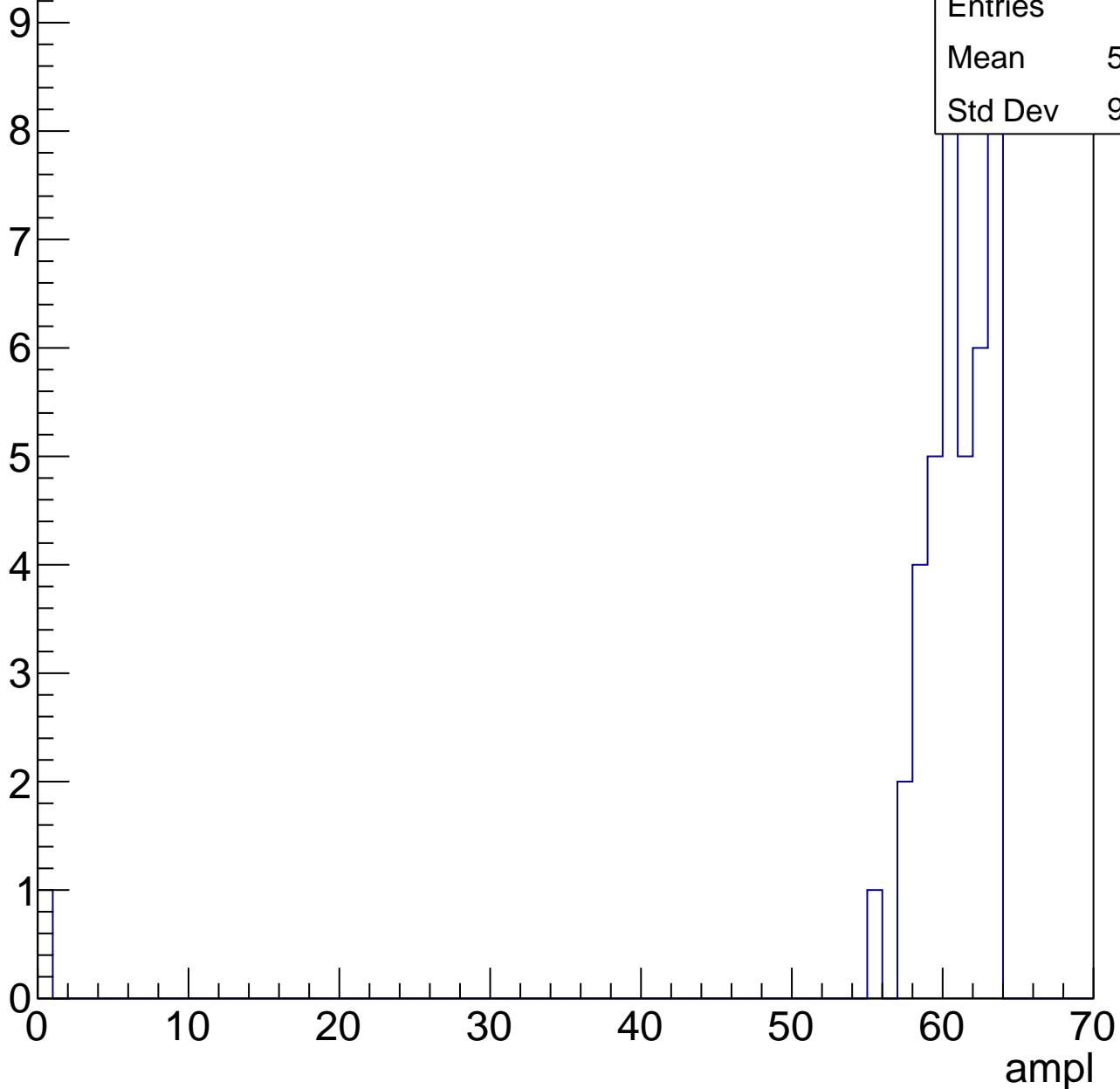


B1L103S, U10-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	59.02
Std Dev	9.544



B1L103S, U10-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U10-ch124, adc0

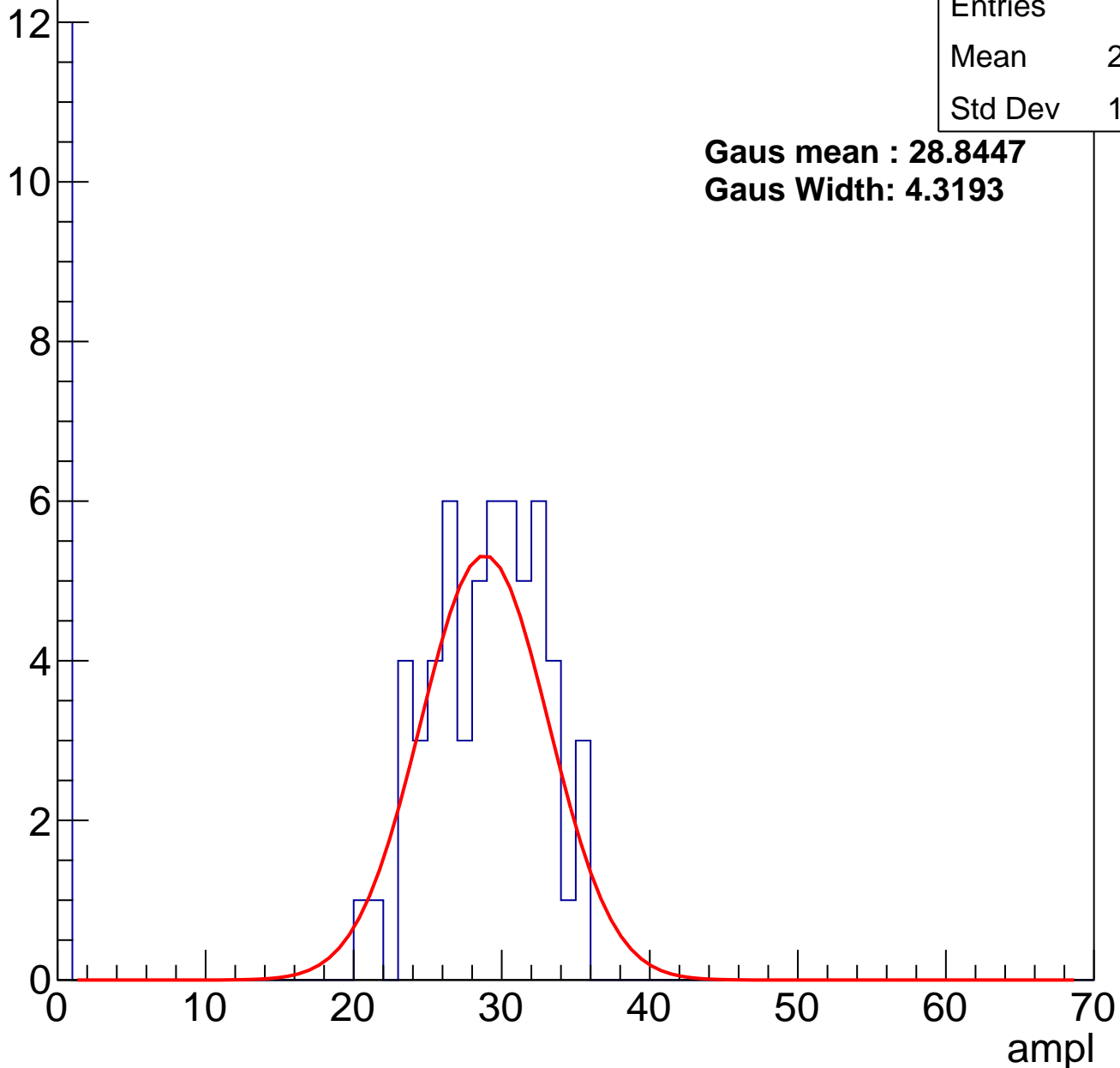
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	23.63
Std Dev	11.24

Gaus mean : 28.8447

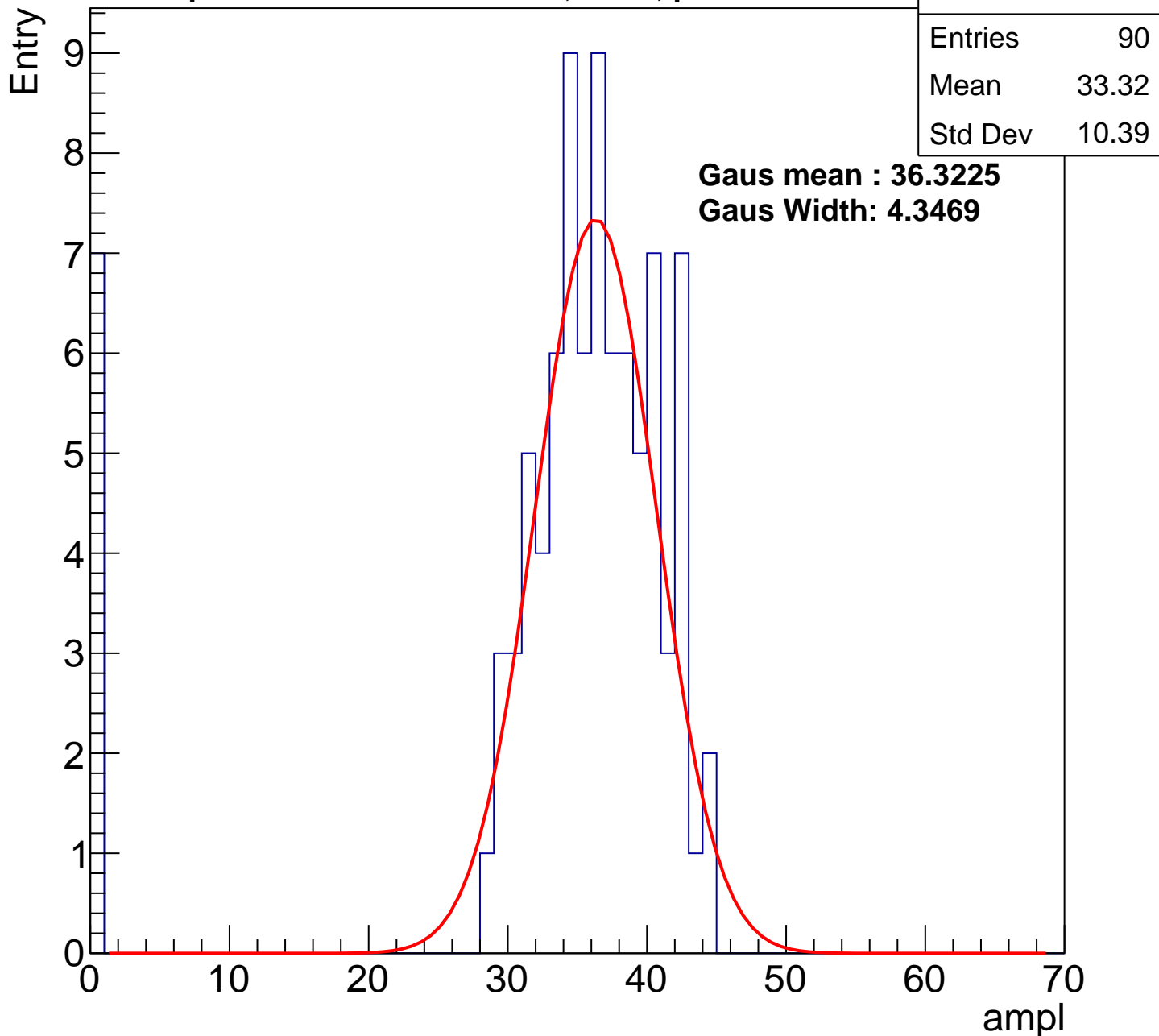
Gaus Width: 4.3193

Entry



B1L103S, U10-ch124, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U10-ch124, adc2

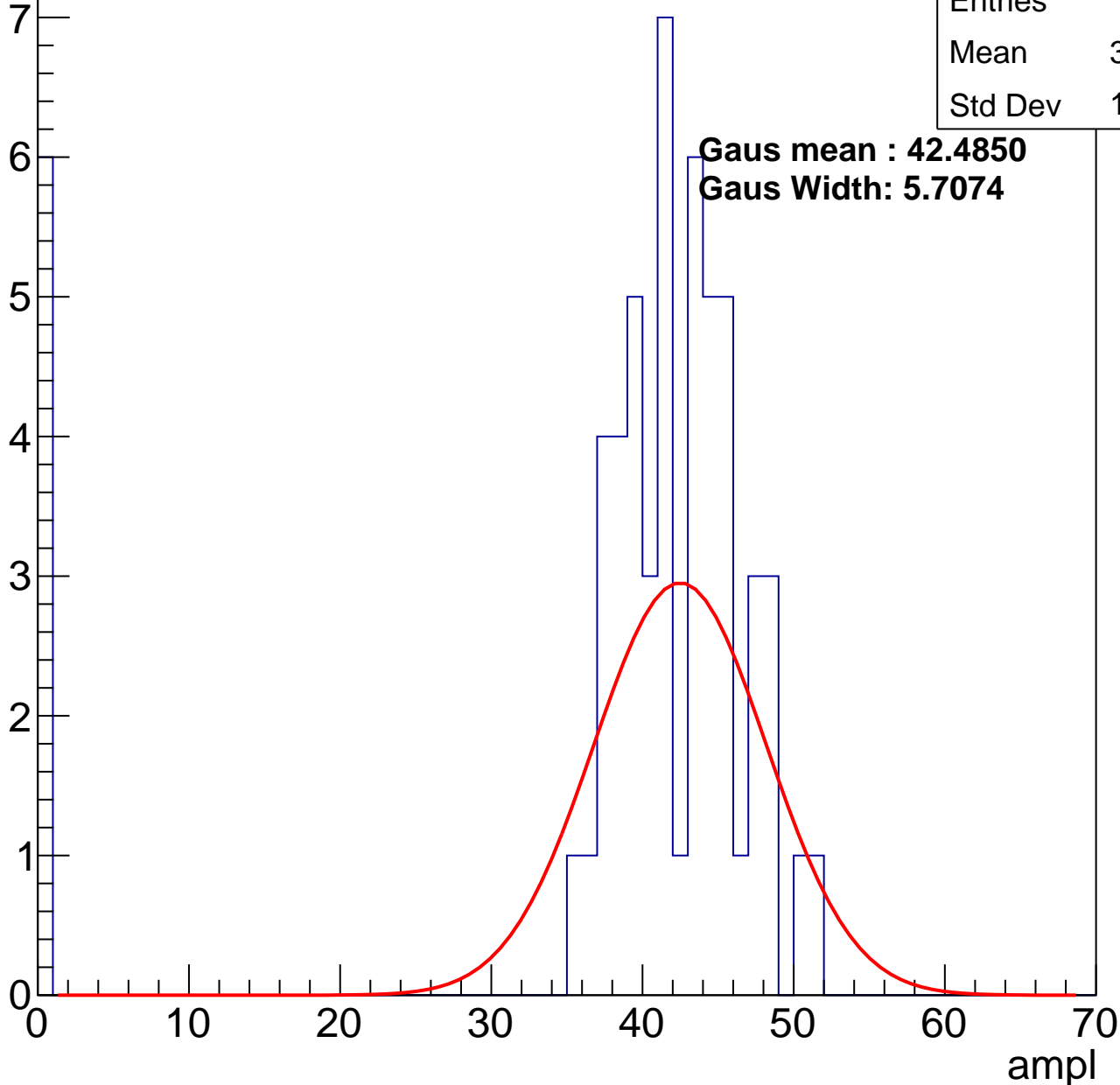
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	37.72
Std Dev	13.42

Gaus mean : 42.4850

Gaus Width: 5.7074

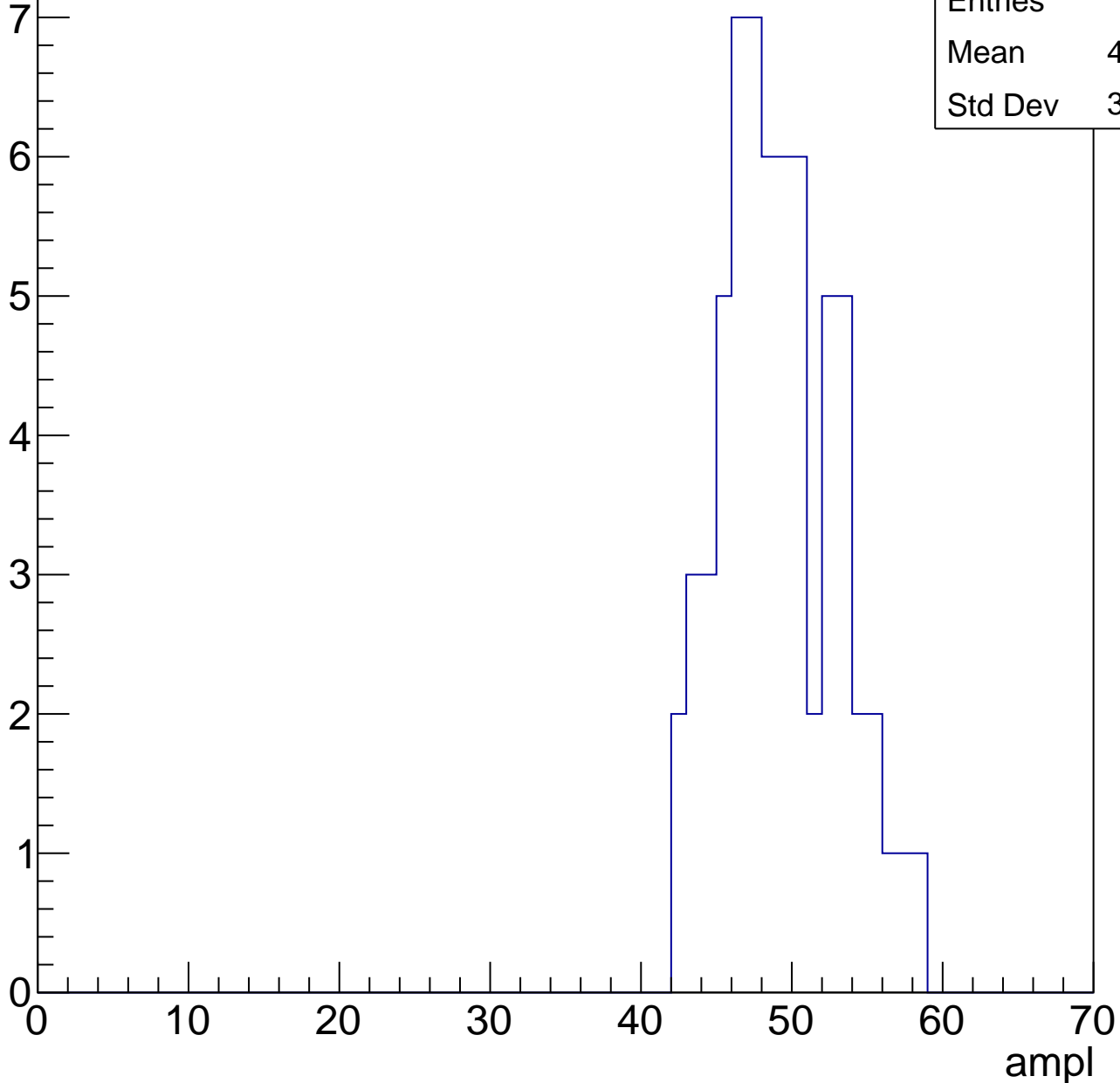


B1L103S, U10-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.73
Std Dev	3.768

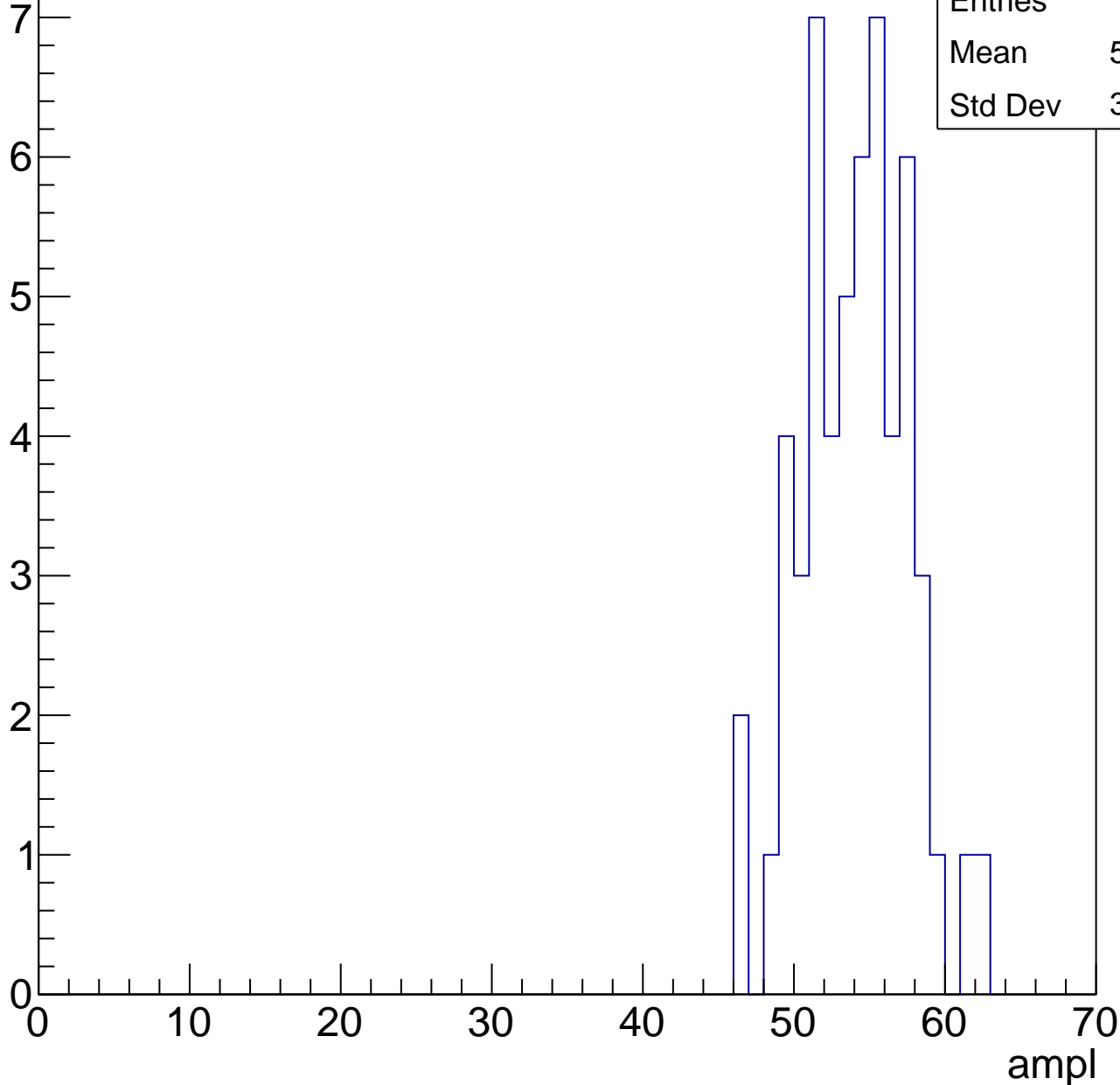


B1L103S, U10-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.58
Std Dev	3.436

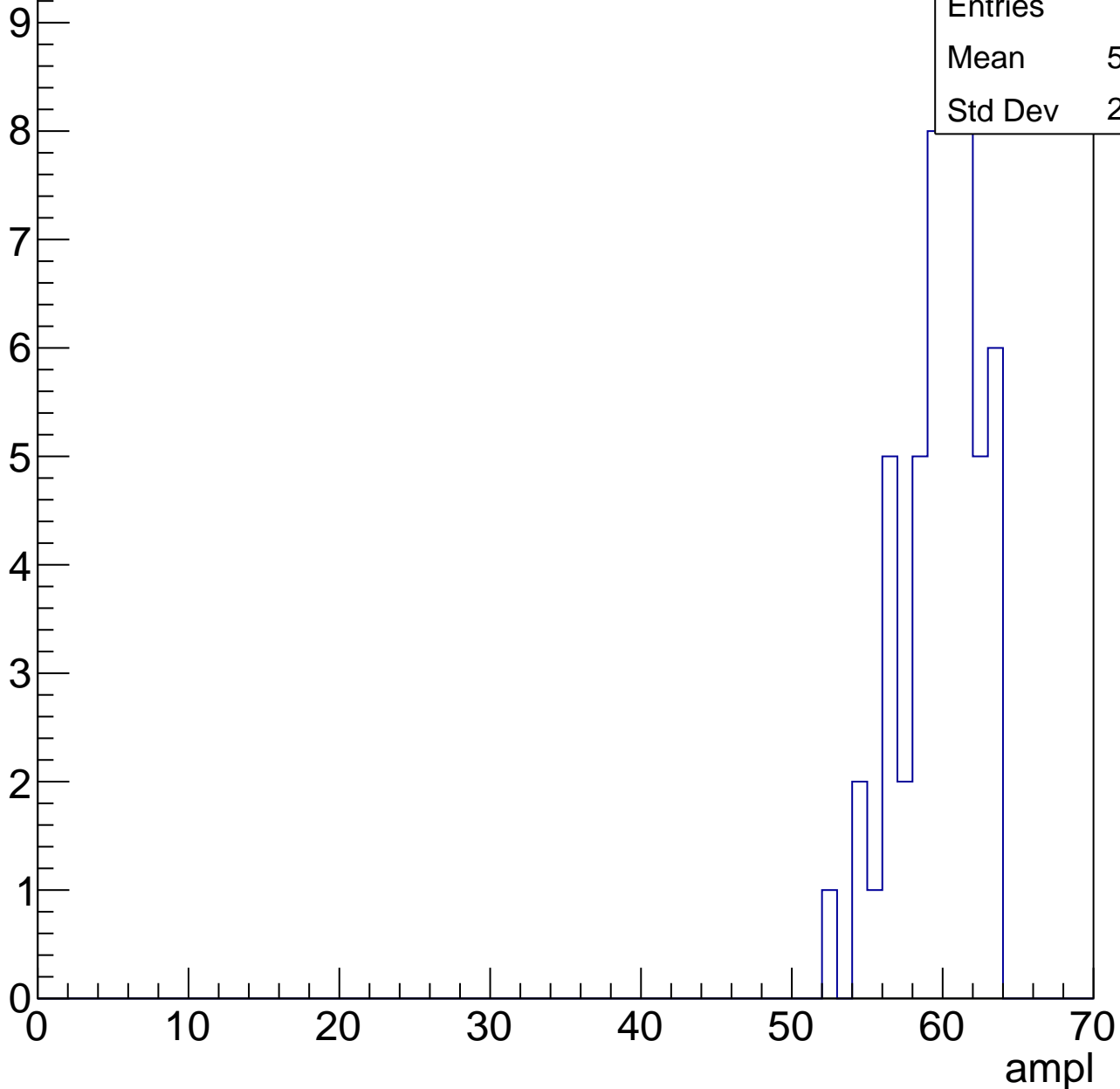


B1L103S, U10-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

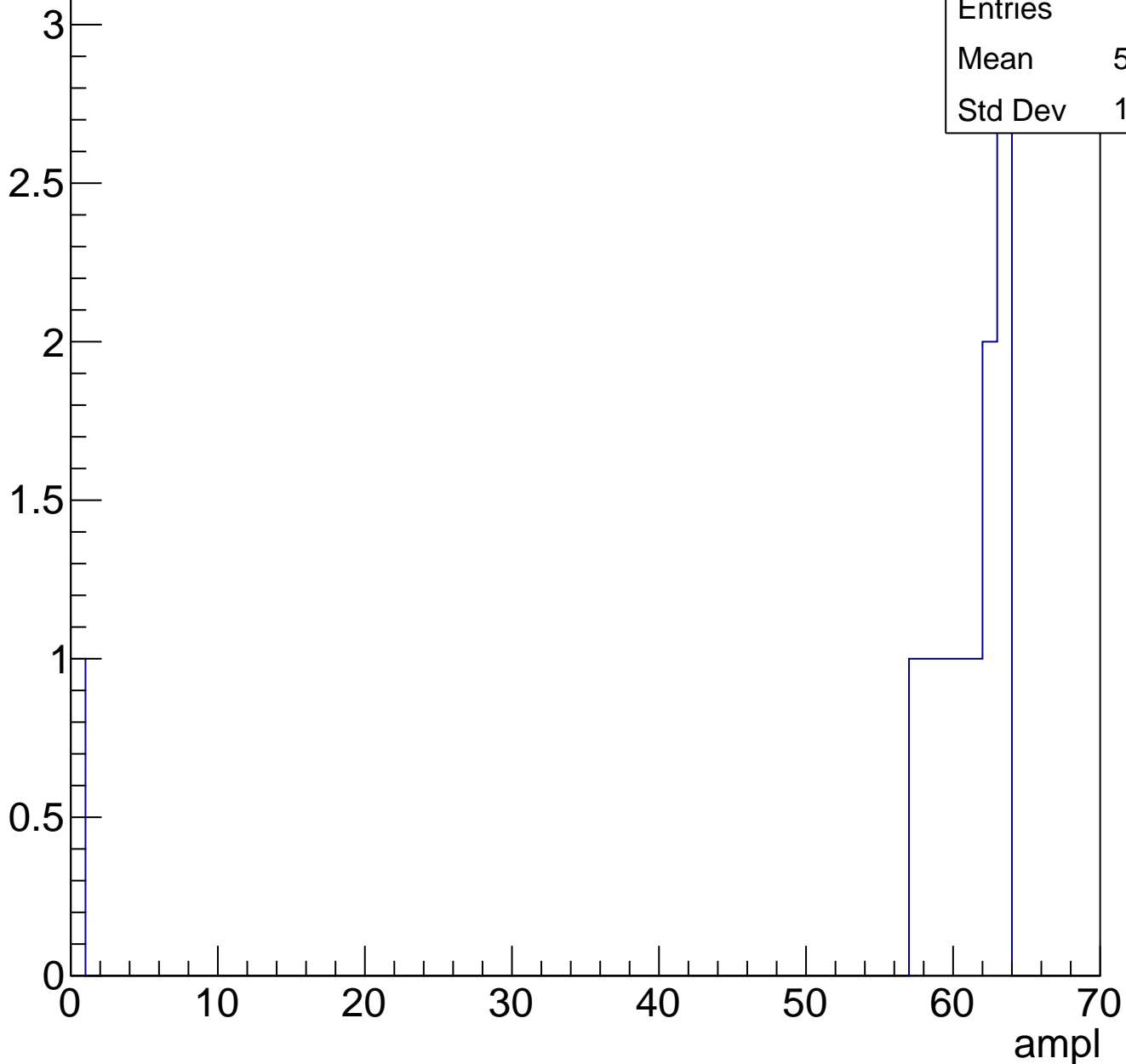
Entries	52
Mean	59.37
Std Dev	2.587



B1L103S, U10-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

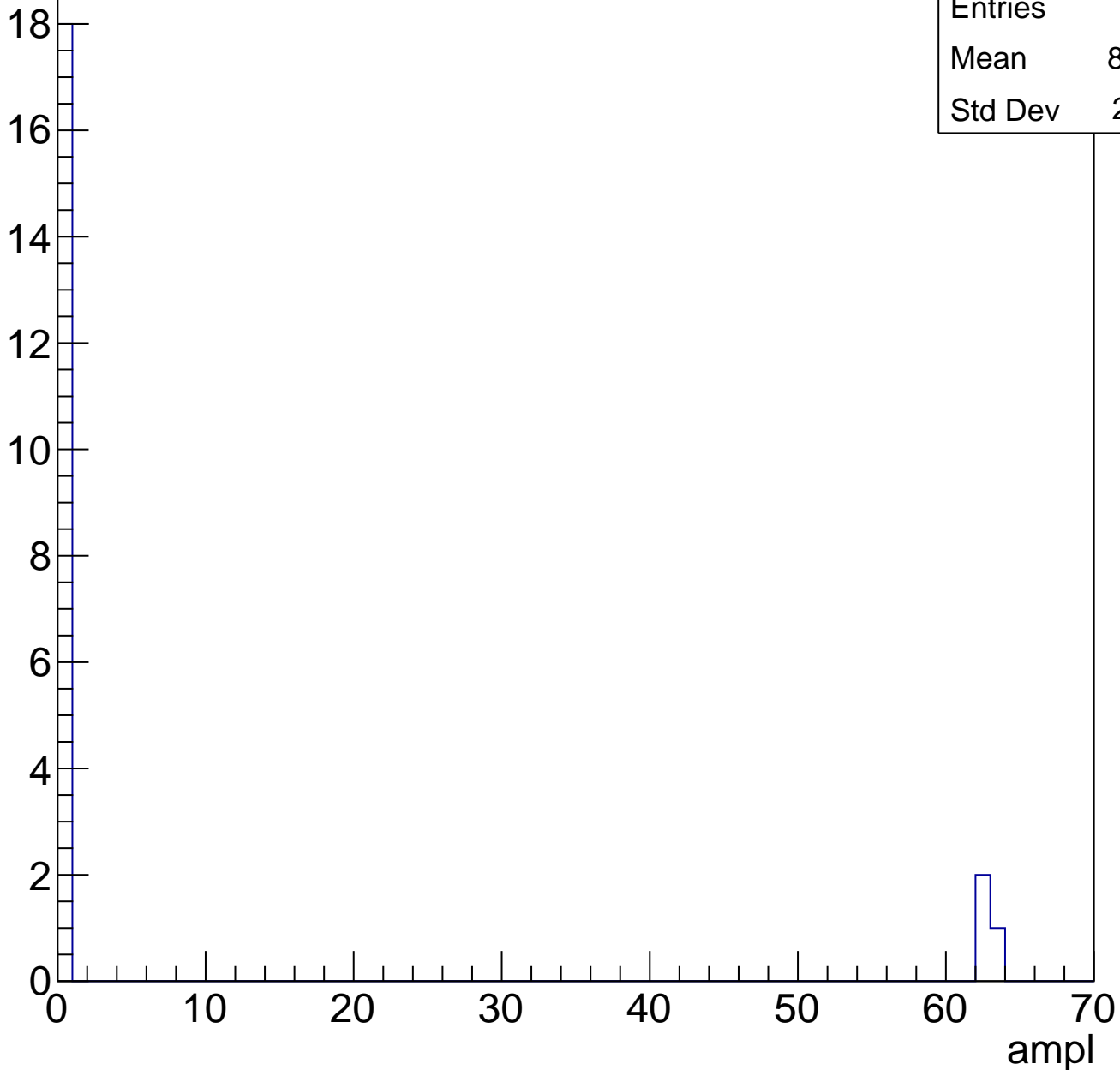


B1L103S, U10-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.905
Std Dev	21.81

Entry



B1L103S, U10-ch125, adc0

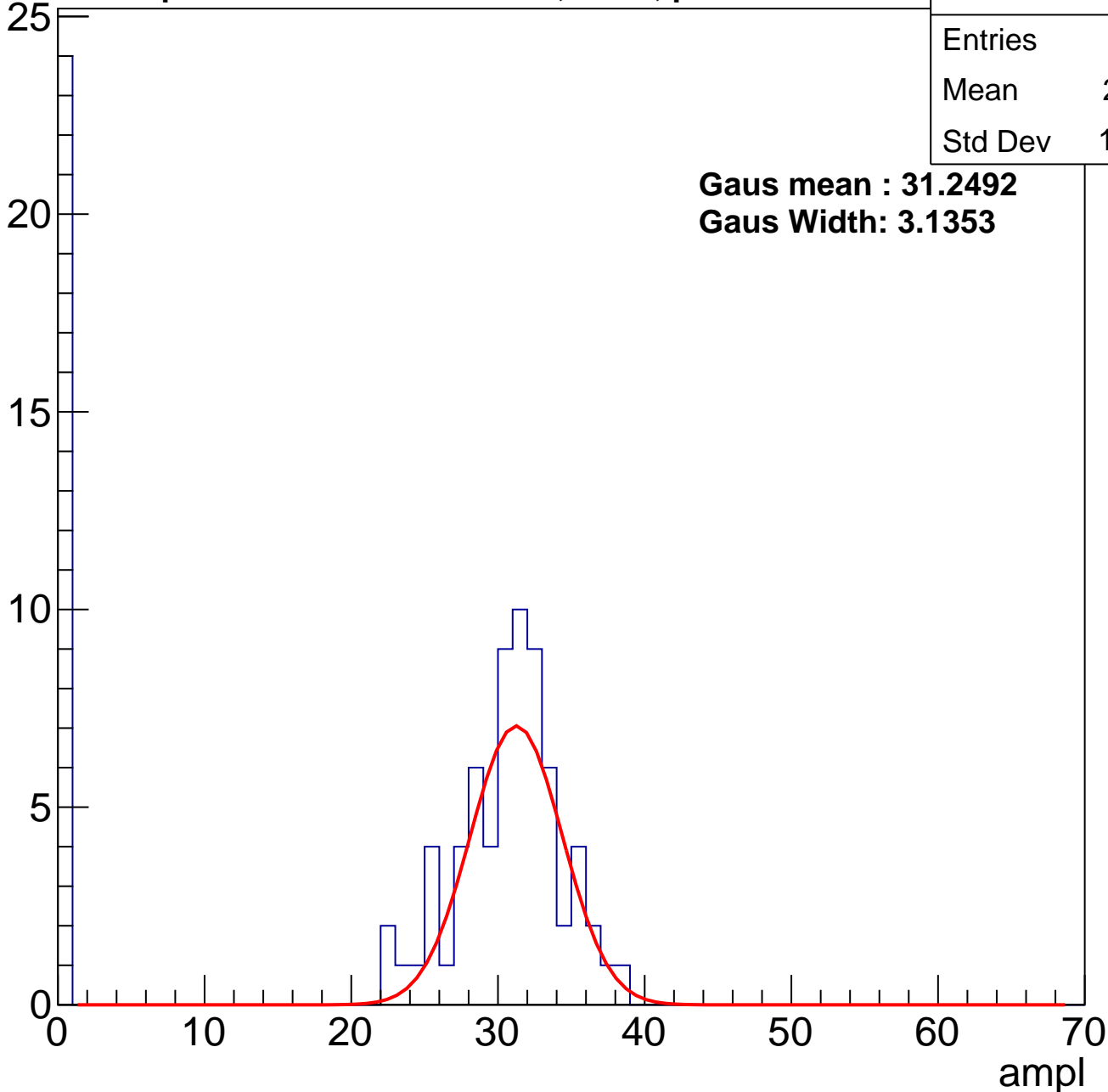
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.31
Std Dev	13.68

Gaus mean : 31.2492

Gaus Width: 3.1353

Entry



B1L103S, U10-ch125, adc1

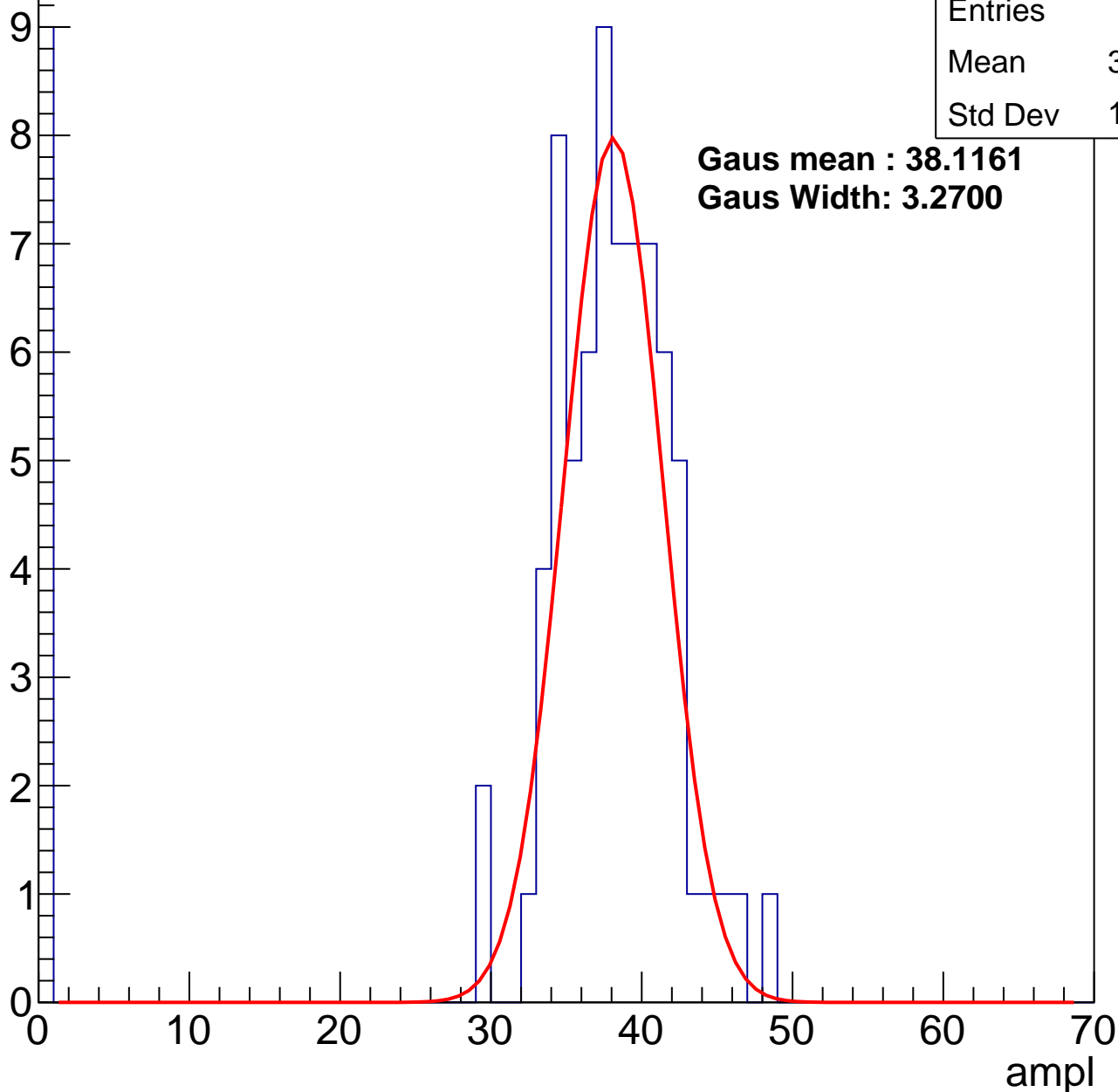
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	33.57
Std Dev	12.35

Gaus mean : 38.1161

Gaus Width: 3.2700



B1L103S, U10-ch125, adc2

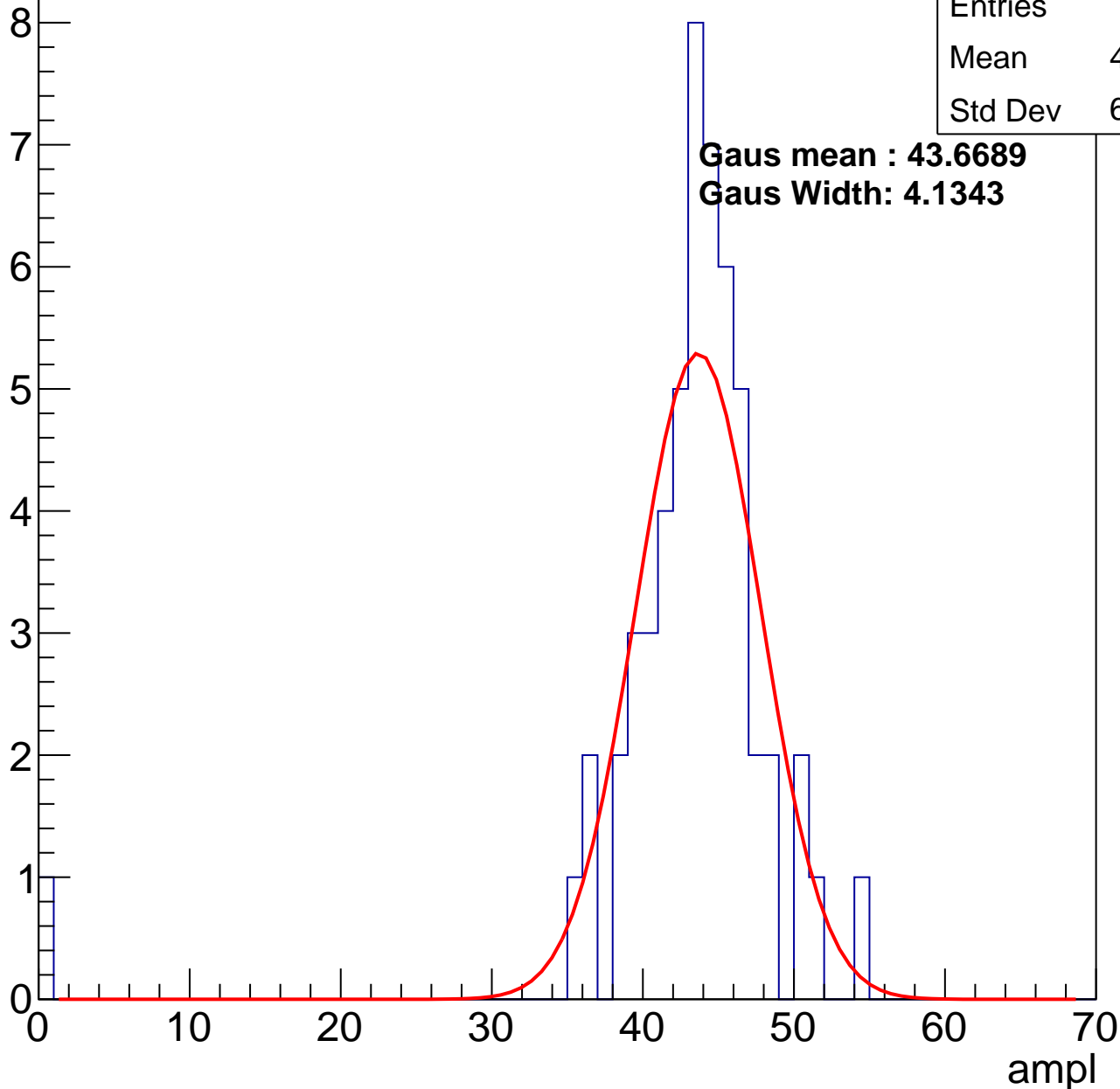
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	42.56
Std Dev	6.859

Gaus mean : 43.6689

Gaus Width: 4.1343

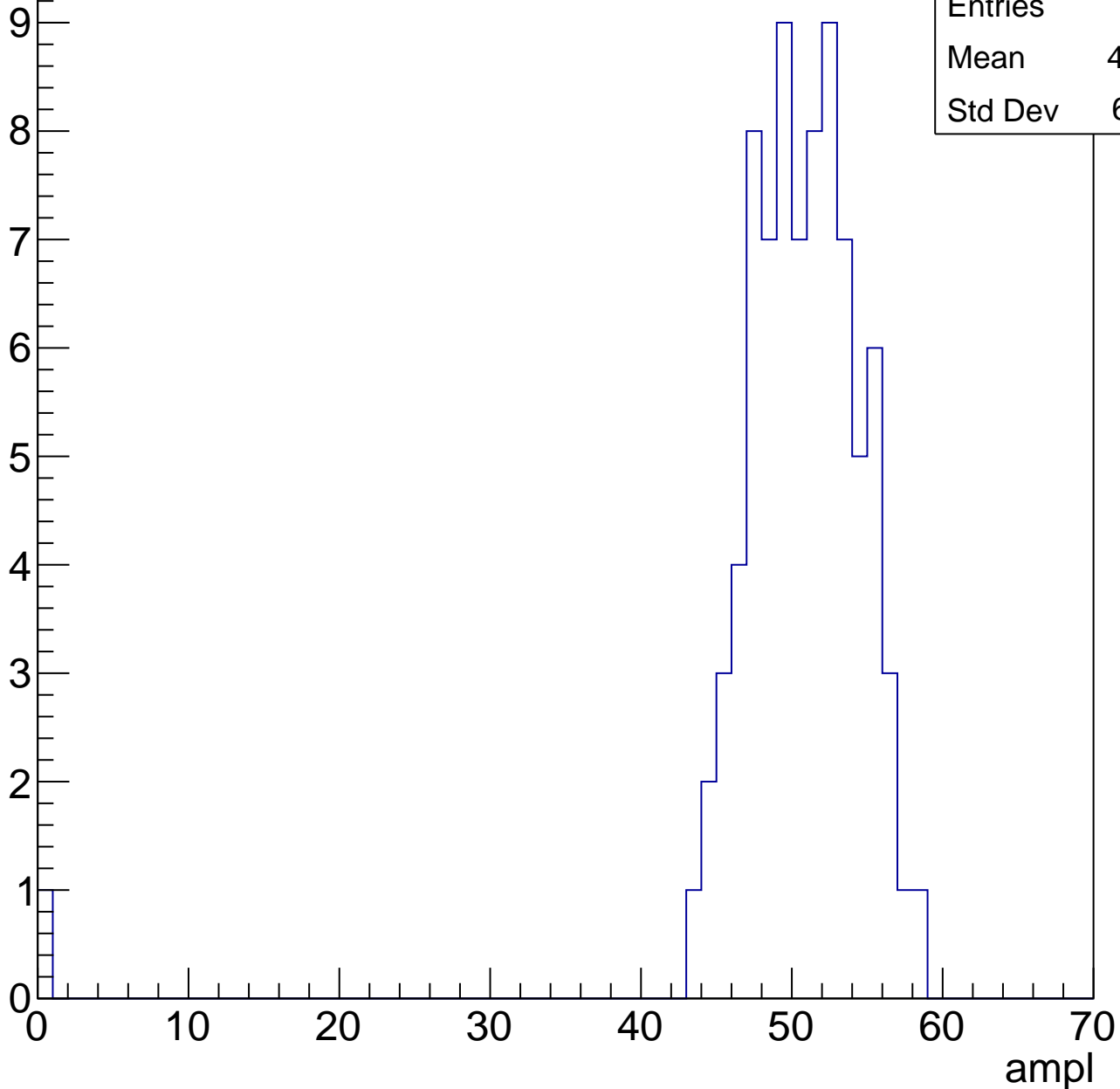


B1L103S, U10-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

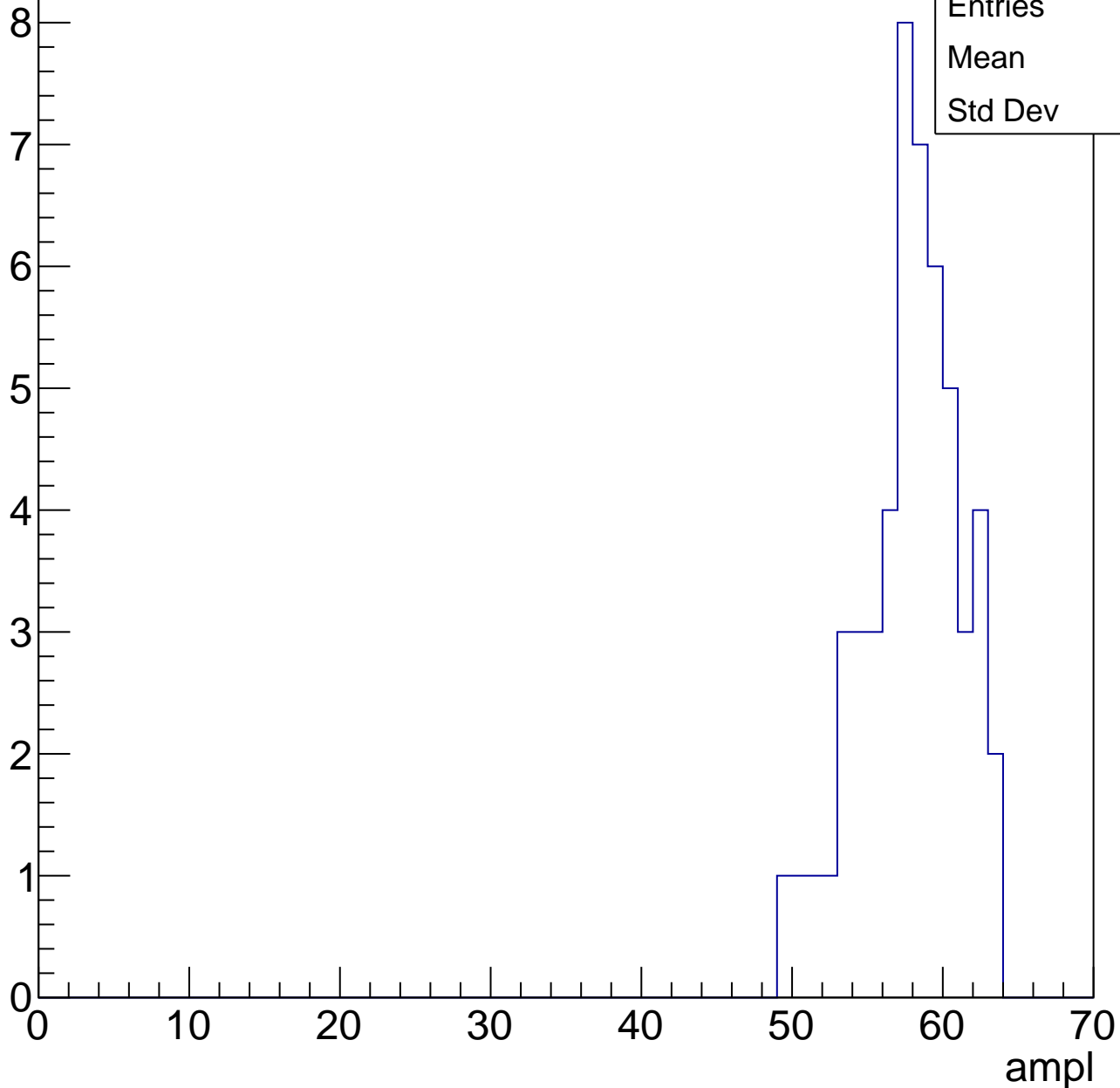
Entries	82
Mean	49.79
Std Dev	6.461



B1L103S, U10-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

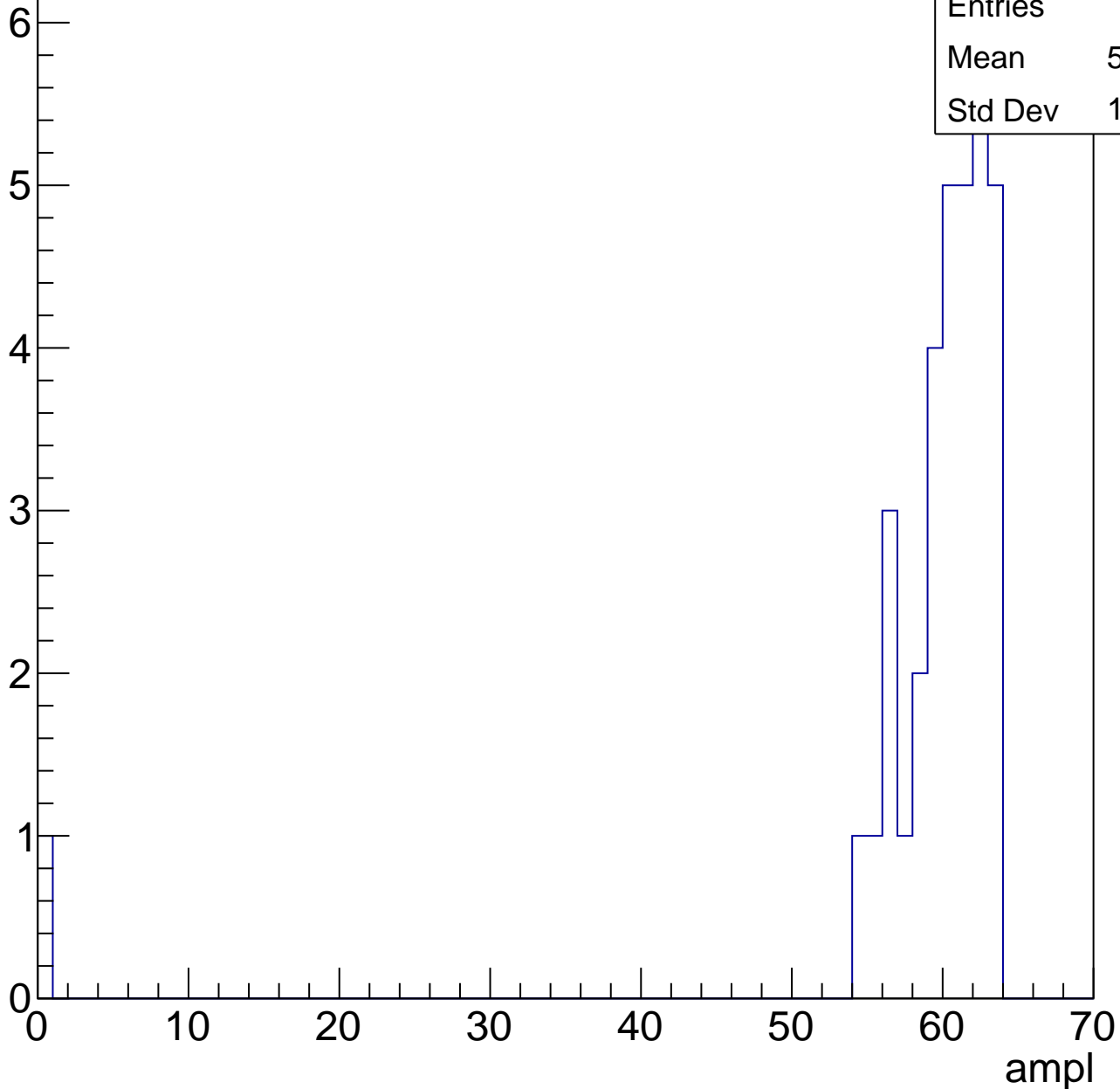


B1L103S, U10-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	58.18
Std Dev	10.42



B1L103S, U10-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U10-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U10-ch126, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	21.68
Std Dev	10.53

Gaus mean : 27.3763

Gaus Width: 3.5327

Entry

10

8

6

4

2

0

0

10

20

30

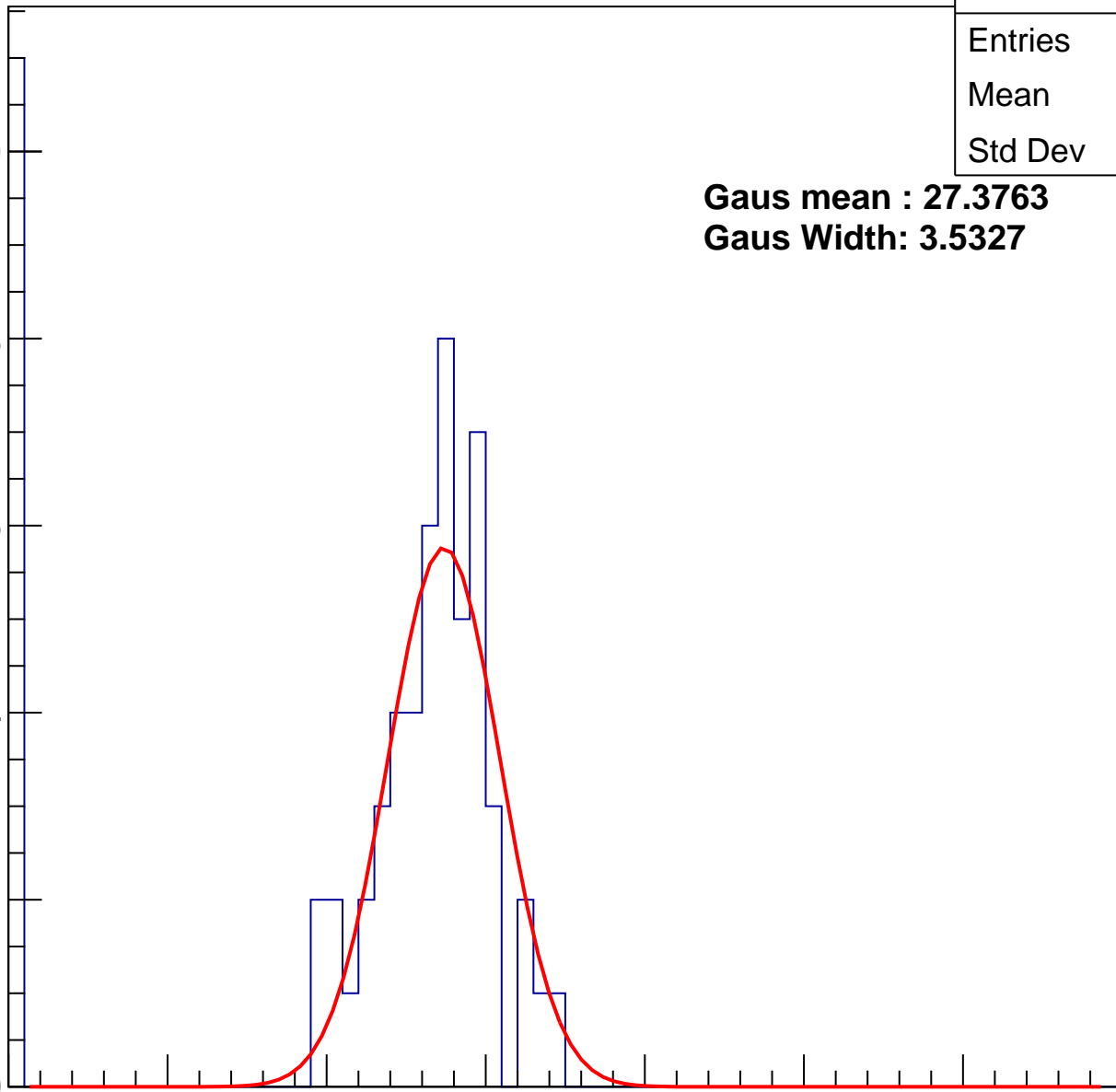
40

50

60

70

ampl



B1L103S, U10-ch126, adc1

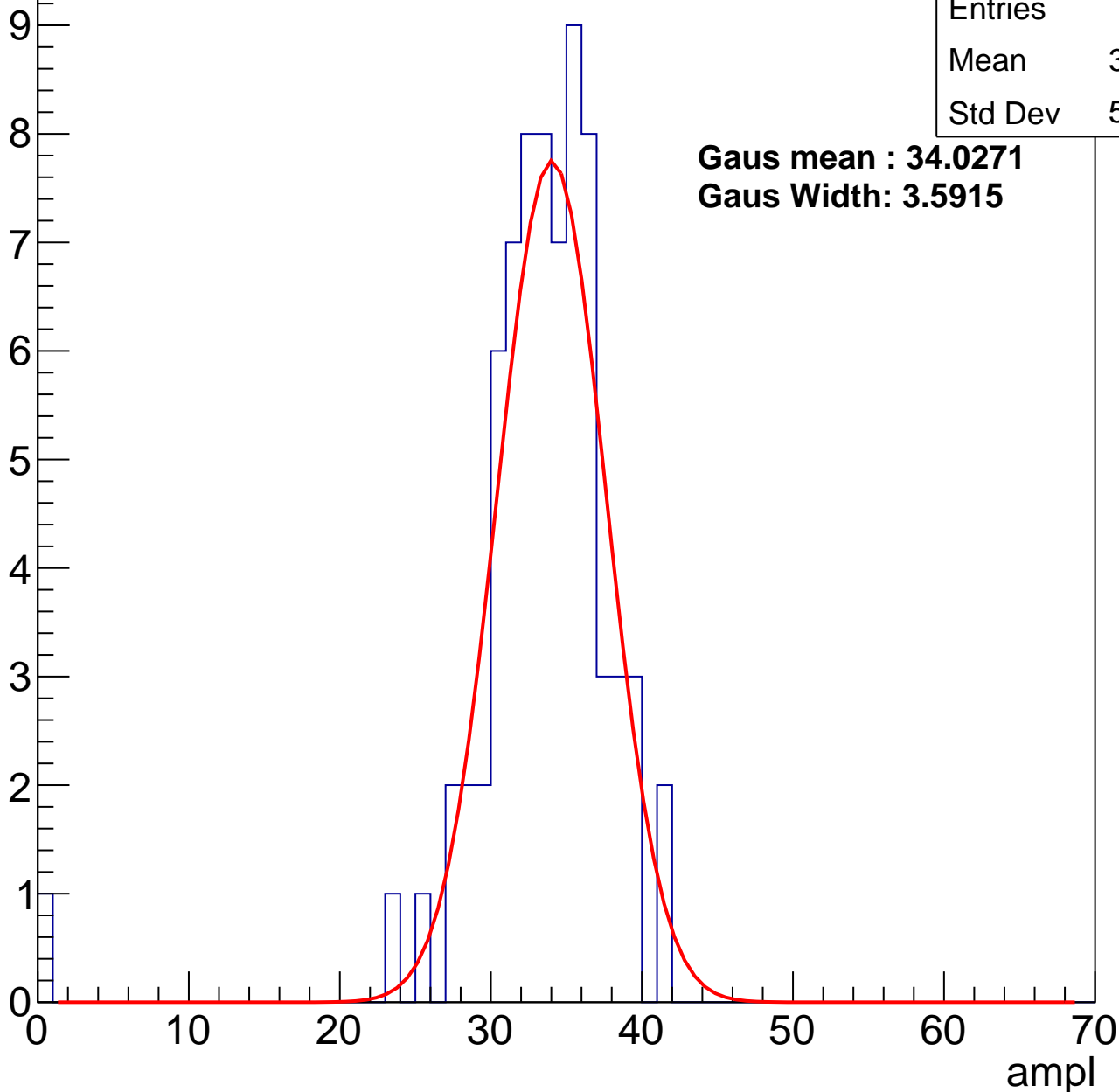
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.85
Std Dev	5.194

Gaus mean : 34.0271

Gaus Width: 3.5915



B1L103S, U10-ch126, adc2

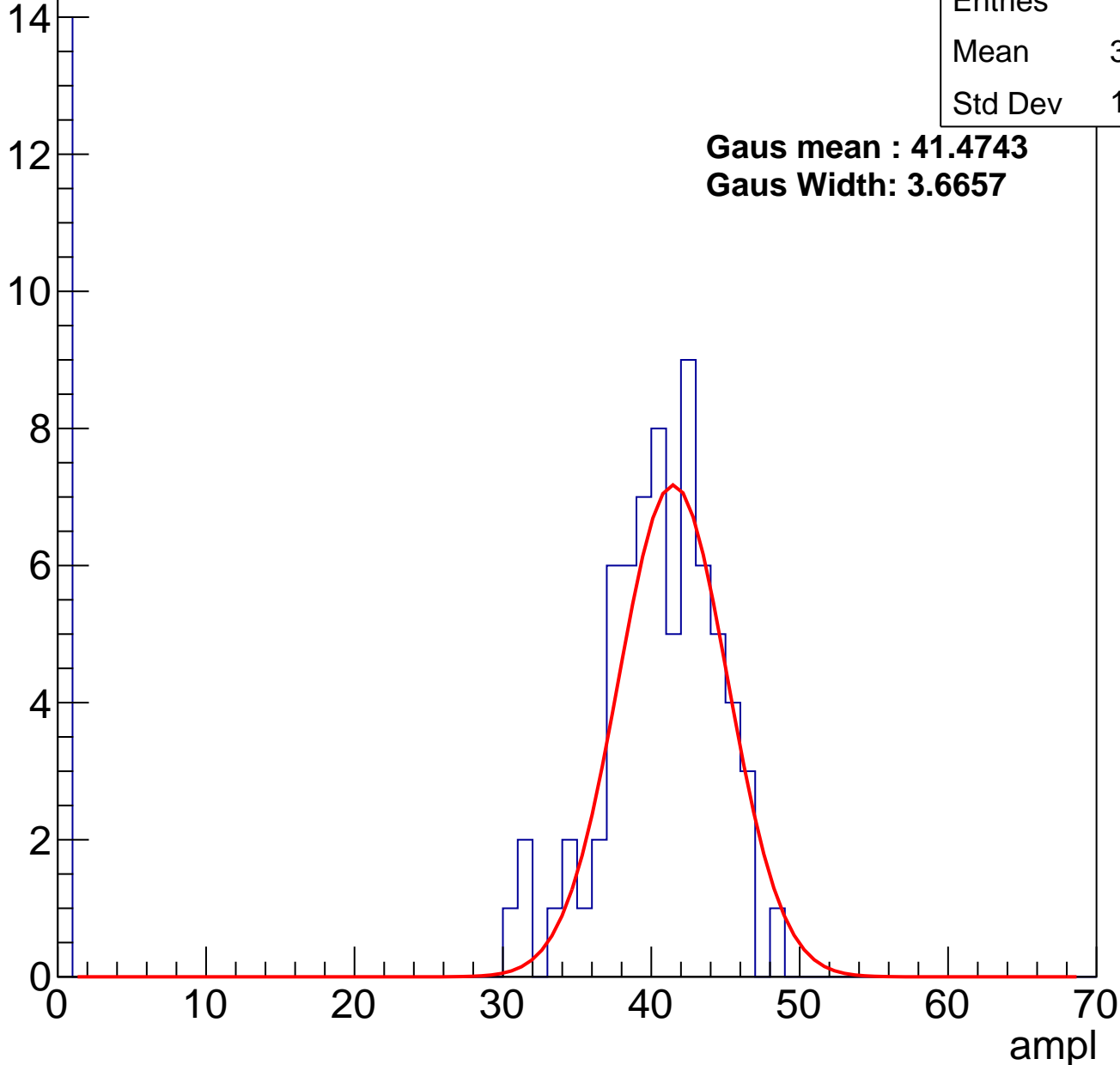
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	33.37
Std Dev	15.42

Gaus mean : 41.4743

Gaus Width: 3.6657

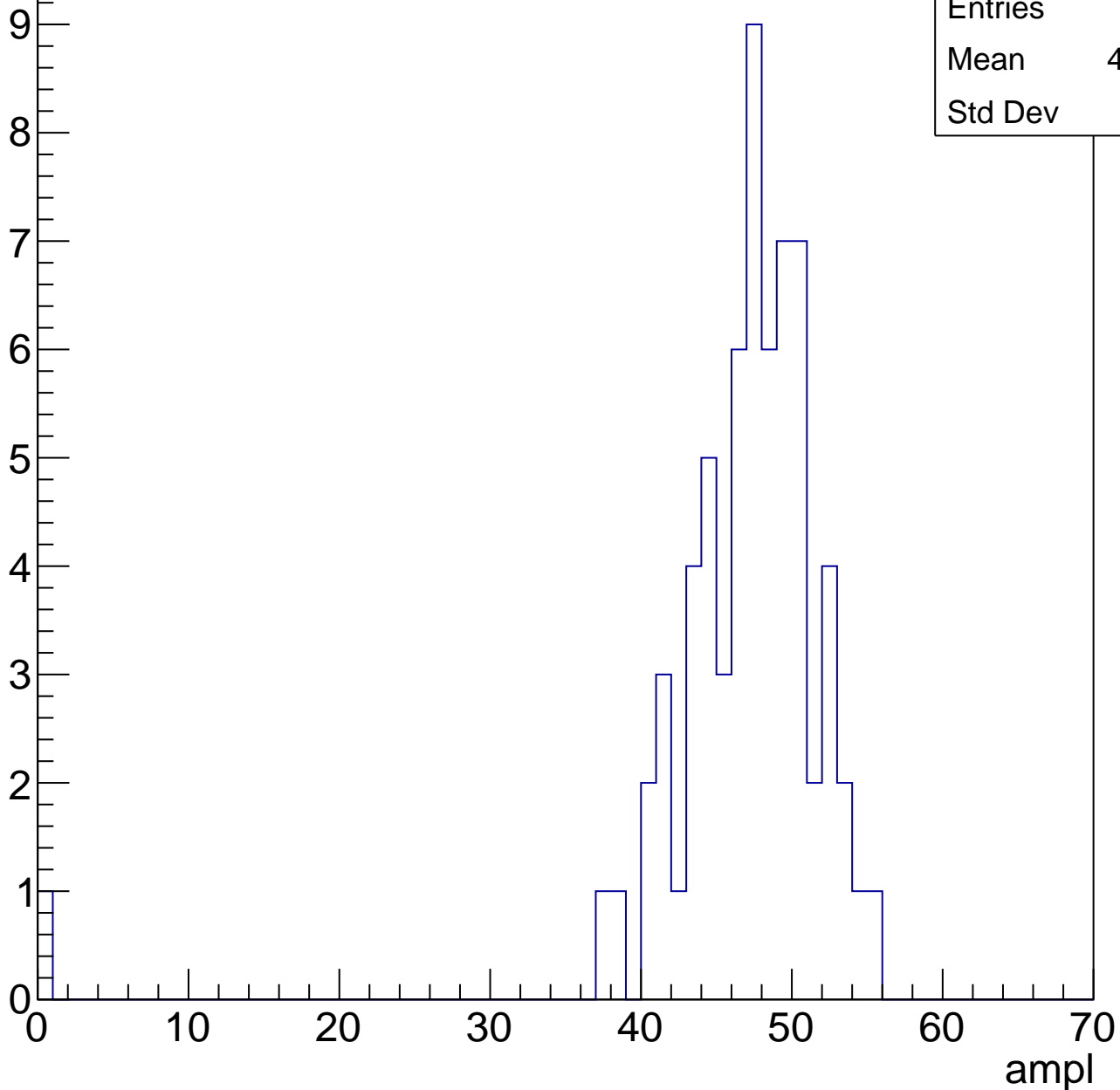


B1L103S, U10-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	46.24
Std Dev	6.88

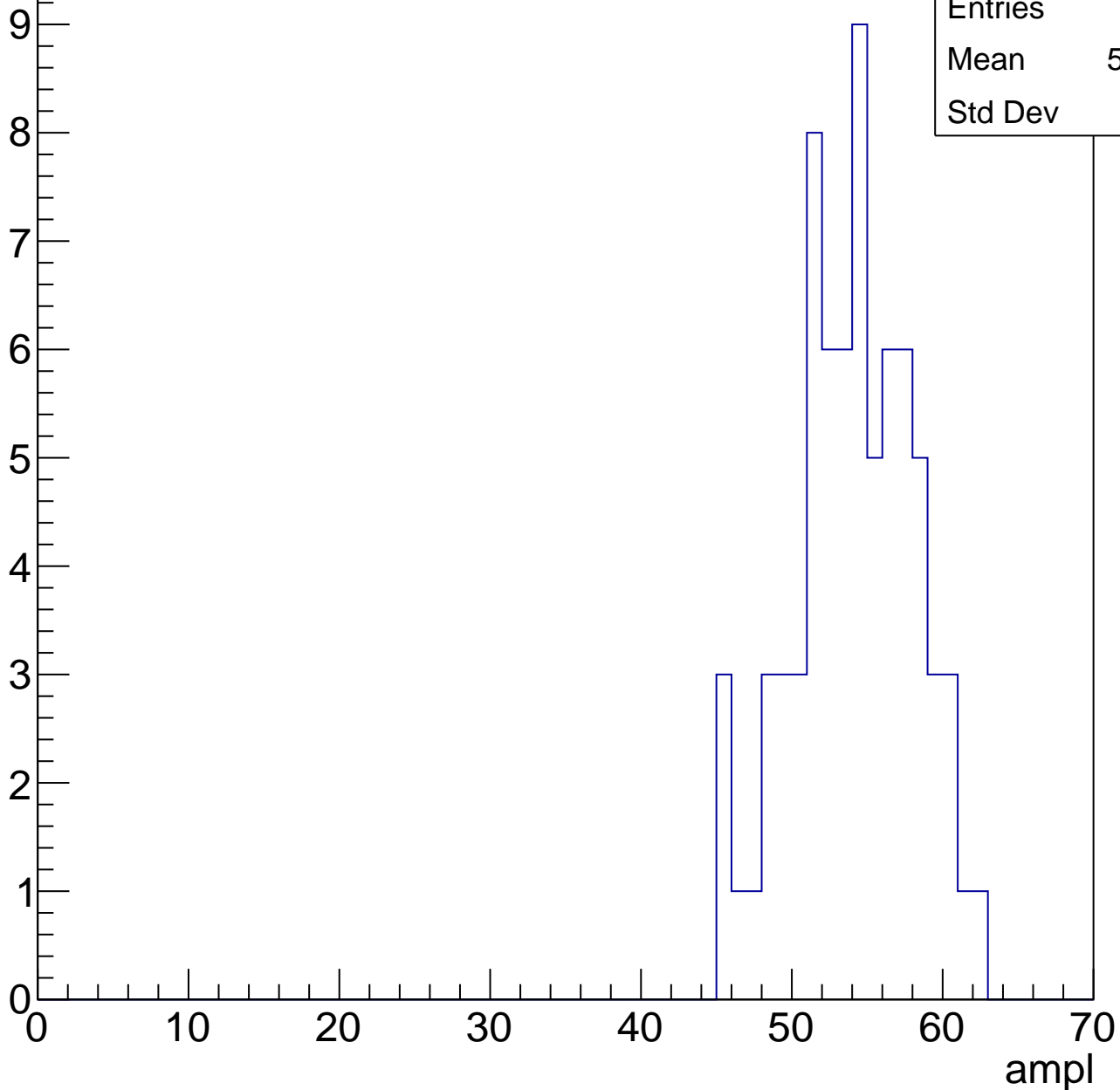


B1L103S, U10-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	53.64
Std Dev	3.95

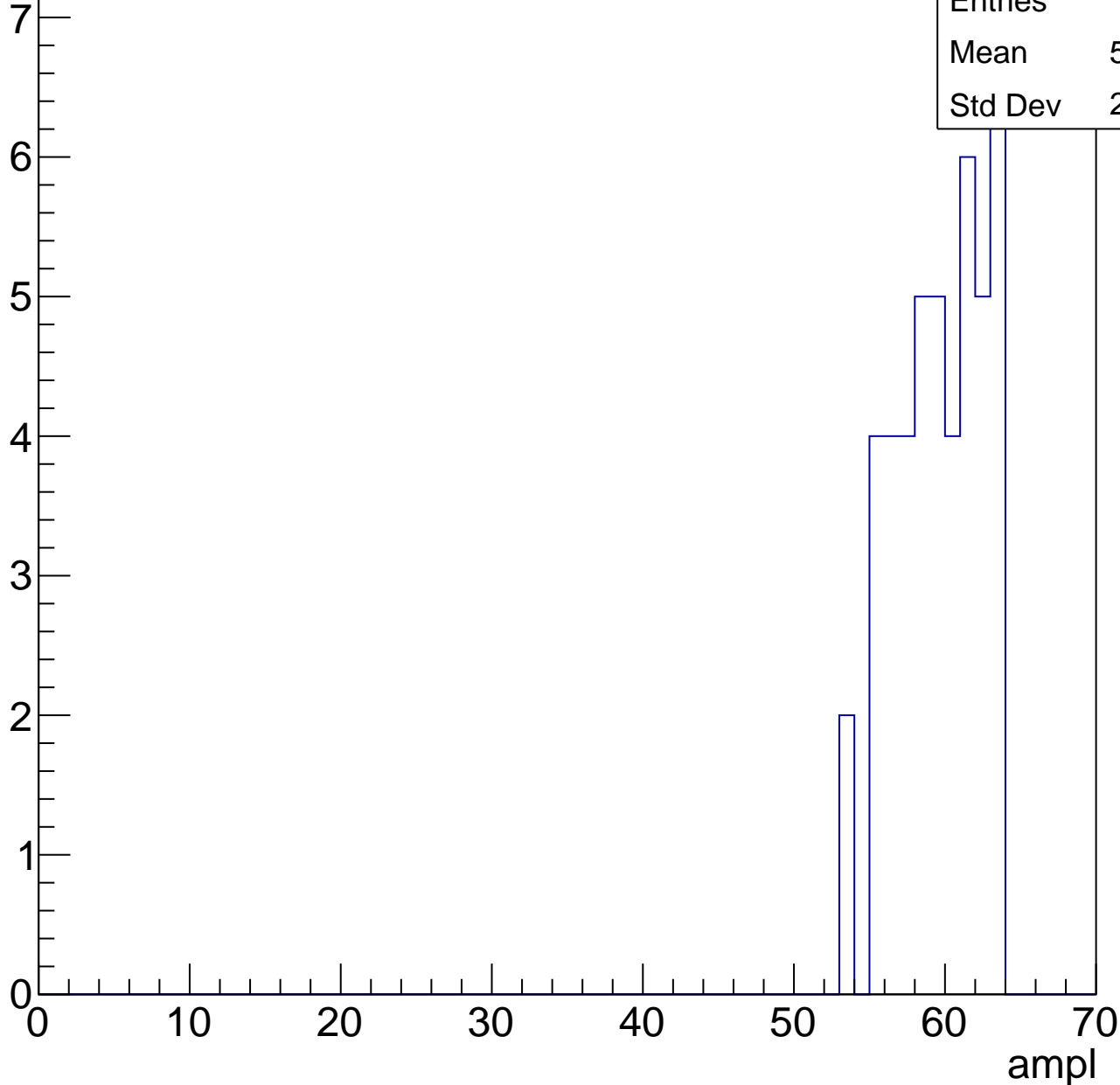


B1L103S, U10-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

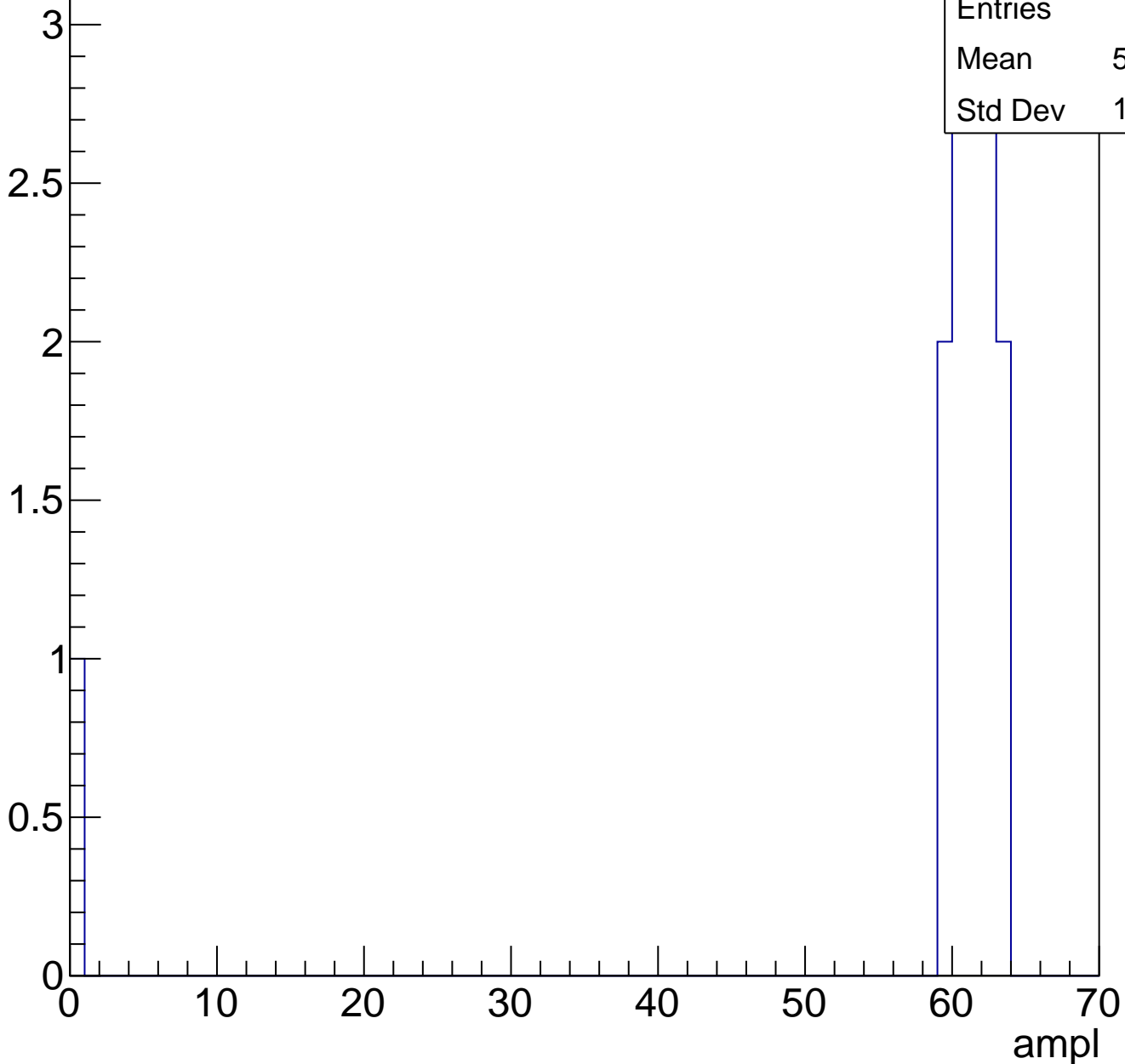
Entries	46
Mean	59.13
Std Dev	2.864



B1L103S, U10-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

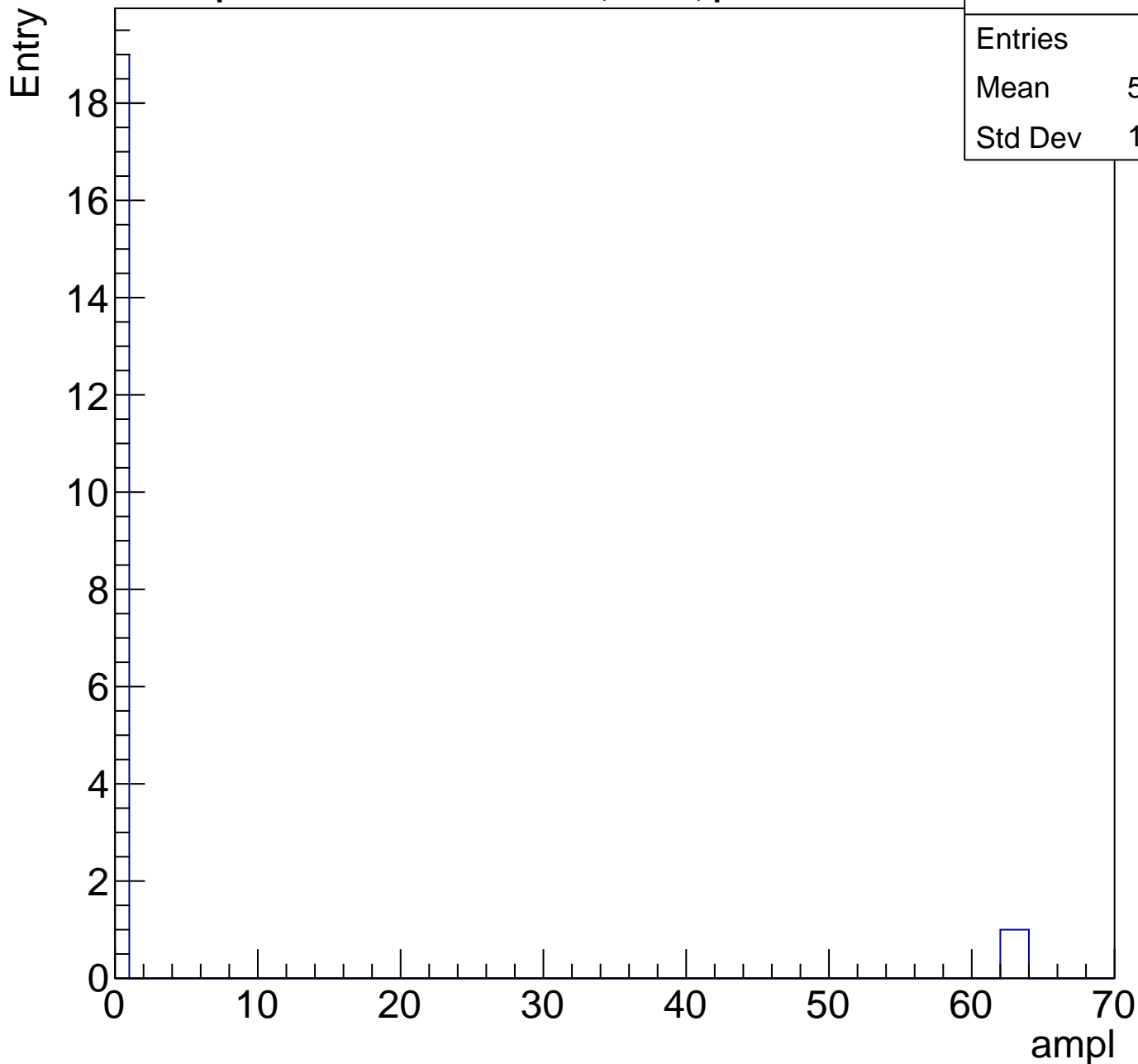
Entry



B1L103S, U10-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



B1L103S, U10-ch127, adc0

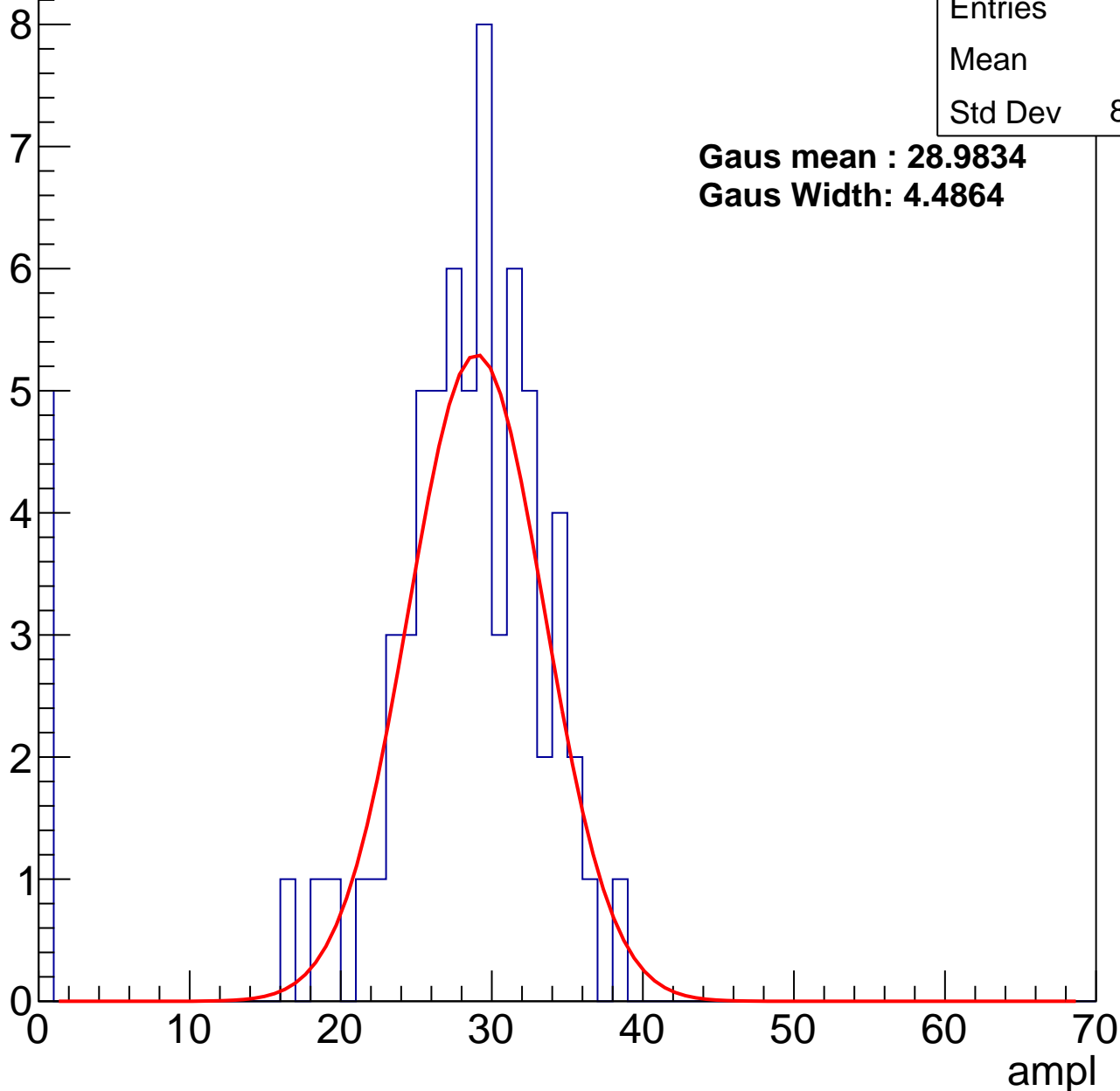
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	26.2
Std Dev	8.437

Gaus mean : 28.9834

Gaus Width: 4.4864



B1L103S, U10-ch127, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	34.32
Std Dev	6.84

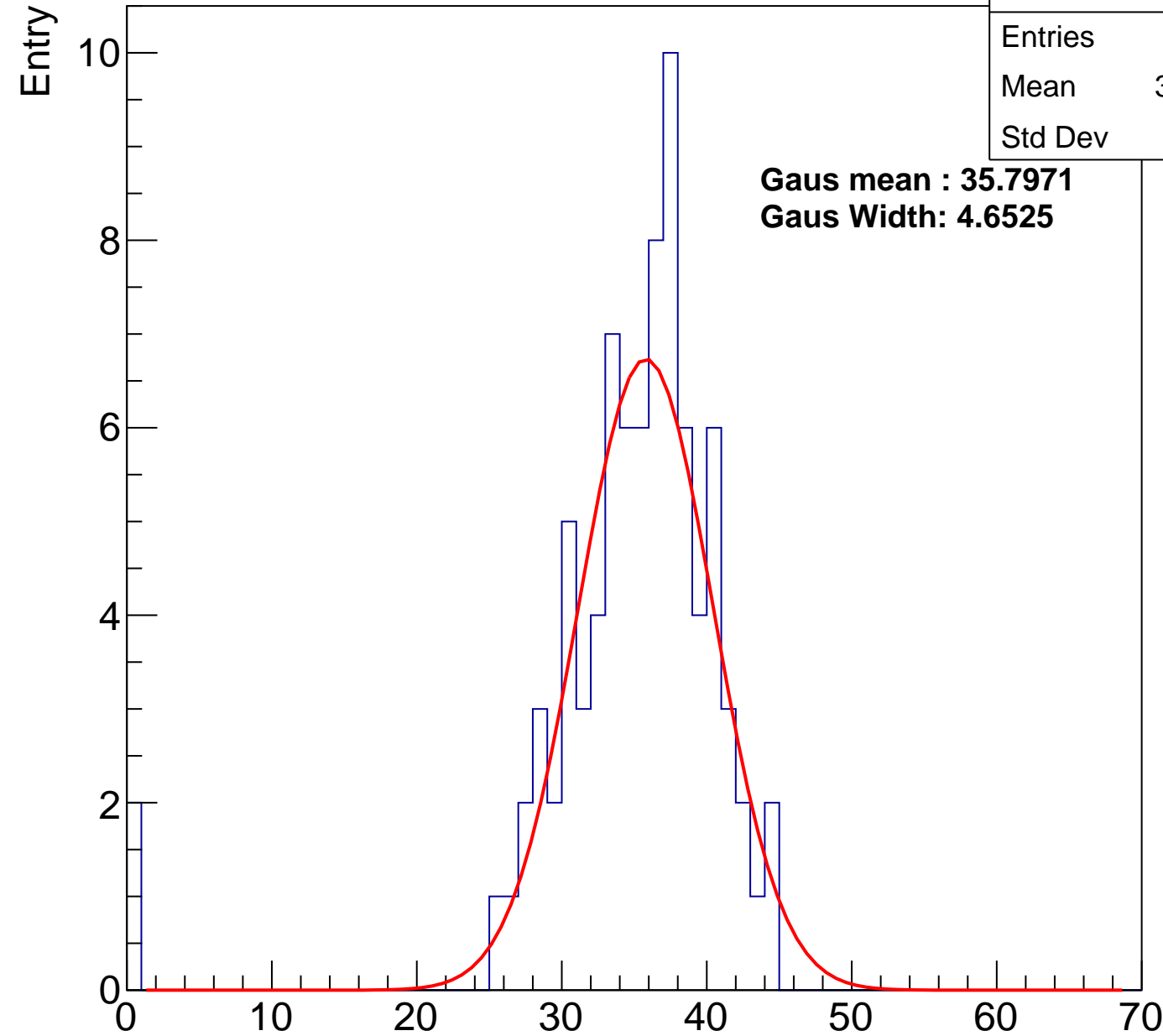
Gaus mean : 35.7971

Gaus Width: 4.6525

Entry

10
8
6
4
2
0

ampl



B1L103S, U10-ch127, adc2

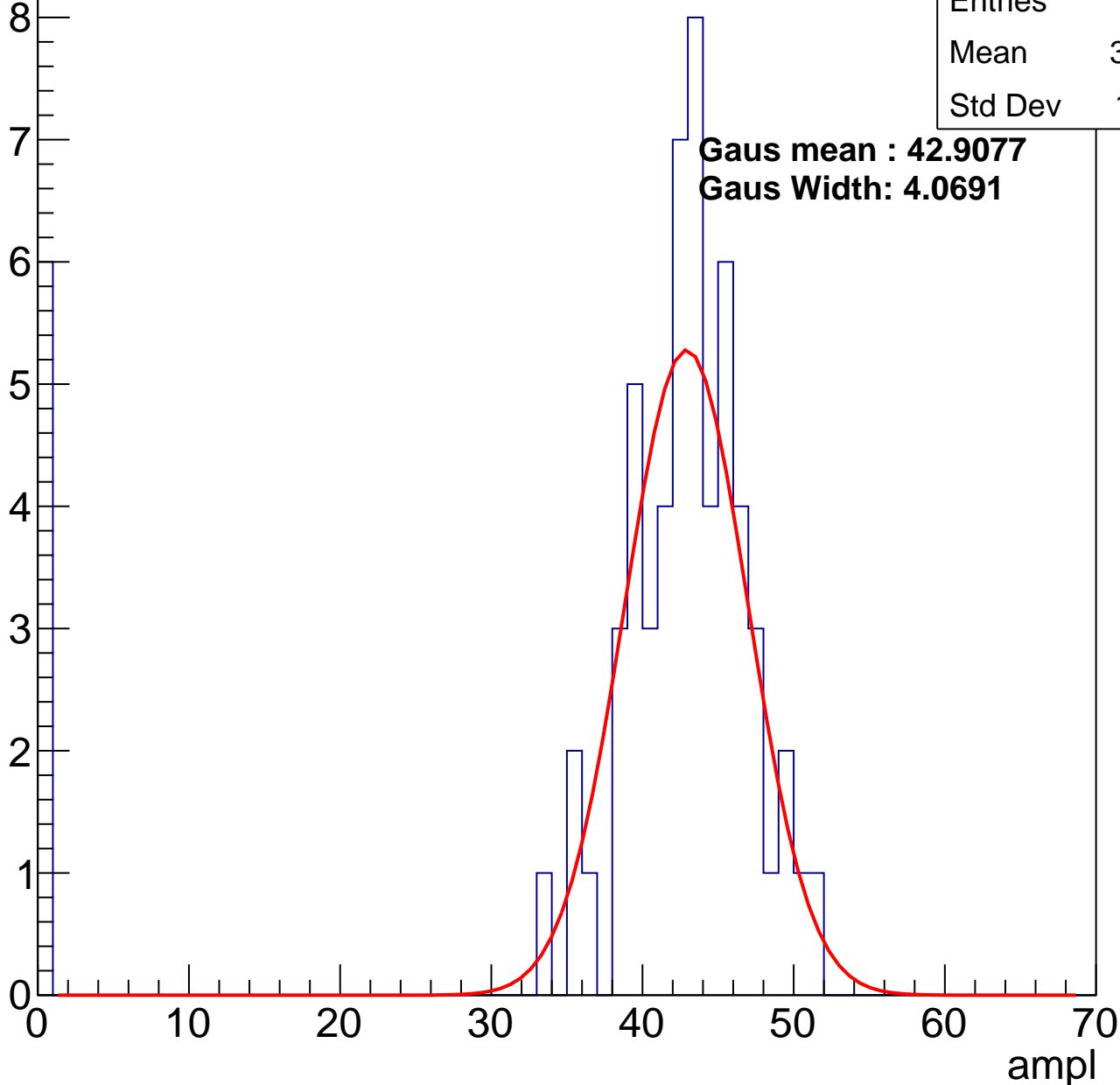
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	38.52
Std Dev	13.11

Gaus mean : 42.9077

Gaus Width: 4.0691

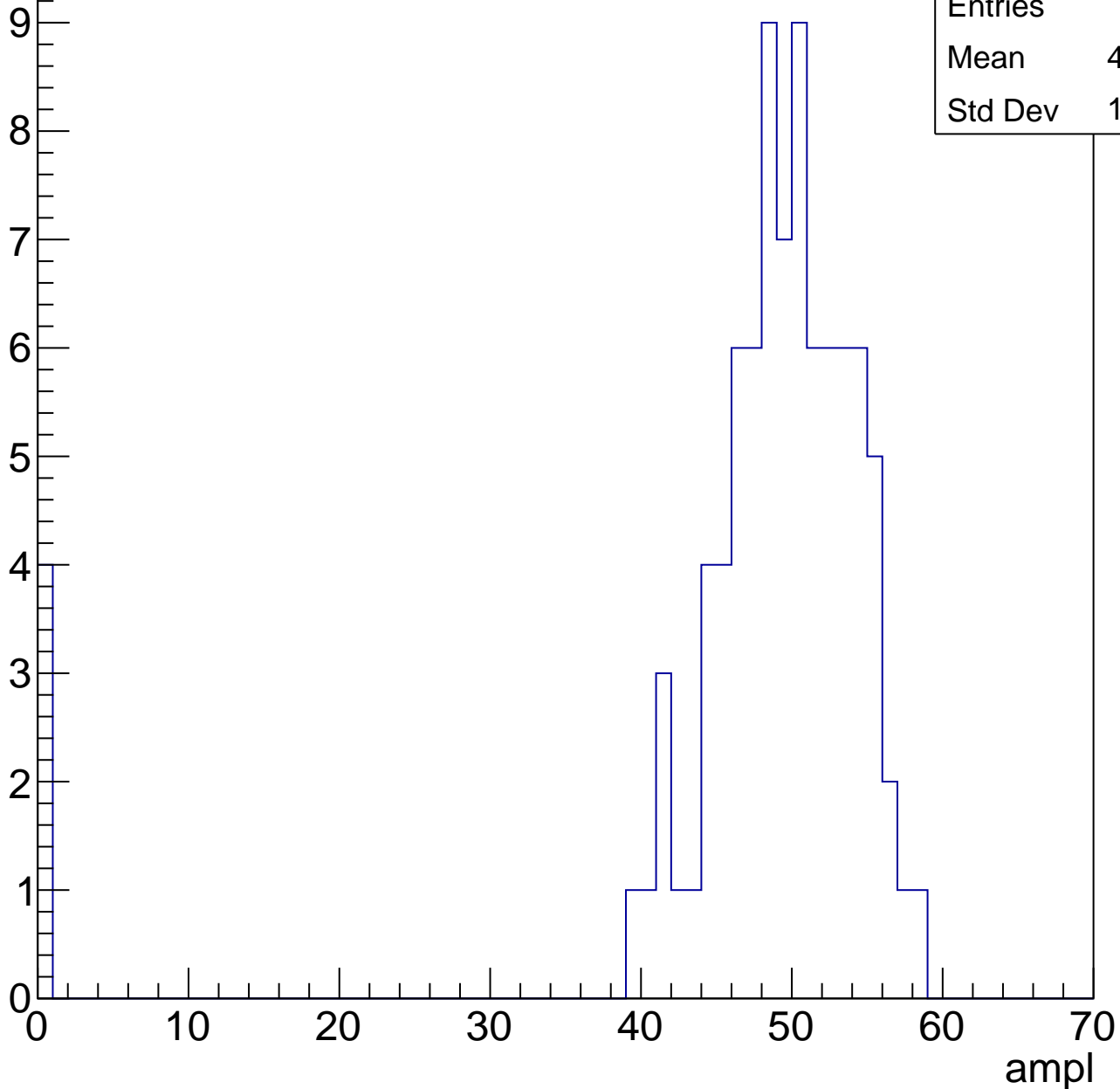


B1L103S, U10-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	47.06
Std Dev	10.98

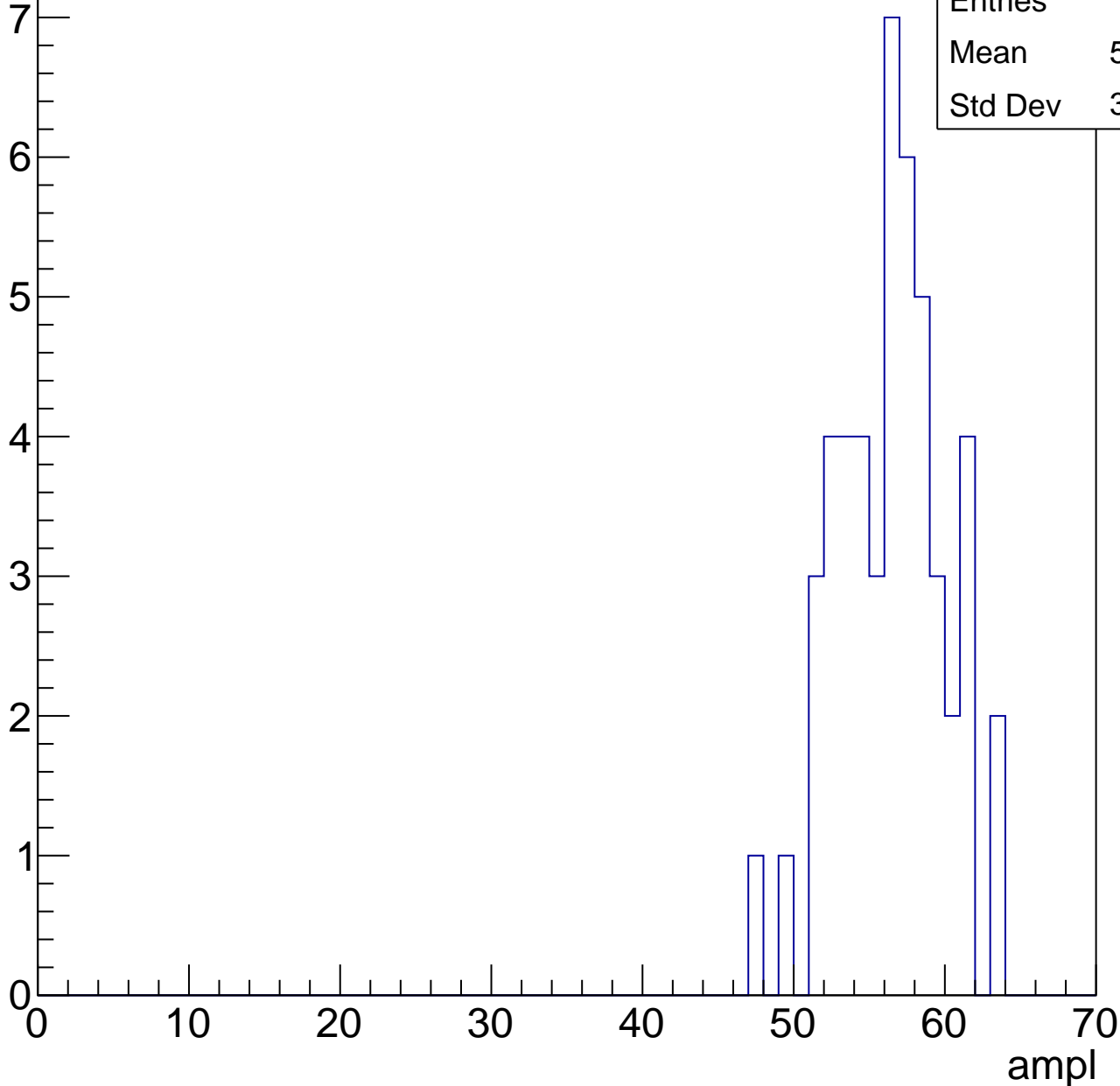


B1L103S, U10-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.94
Std Dev	3.519

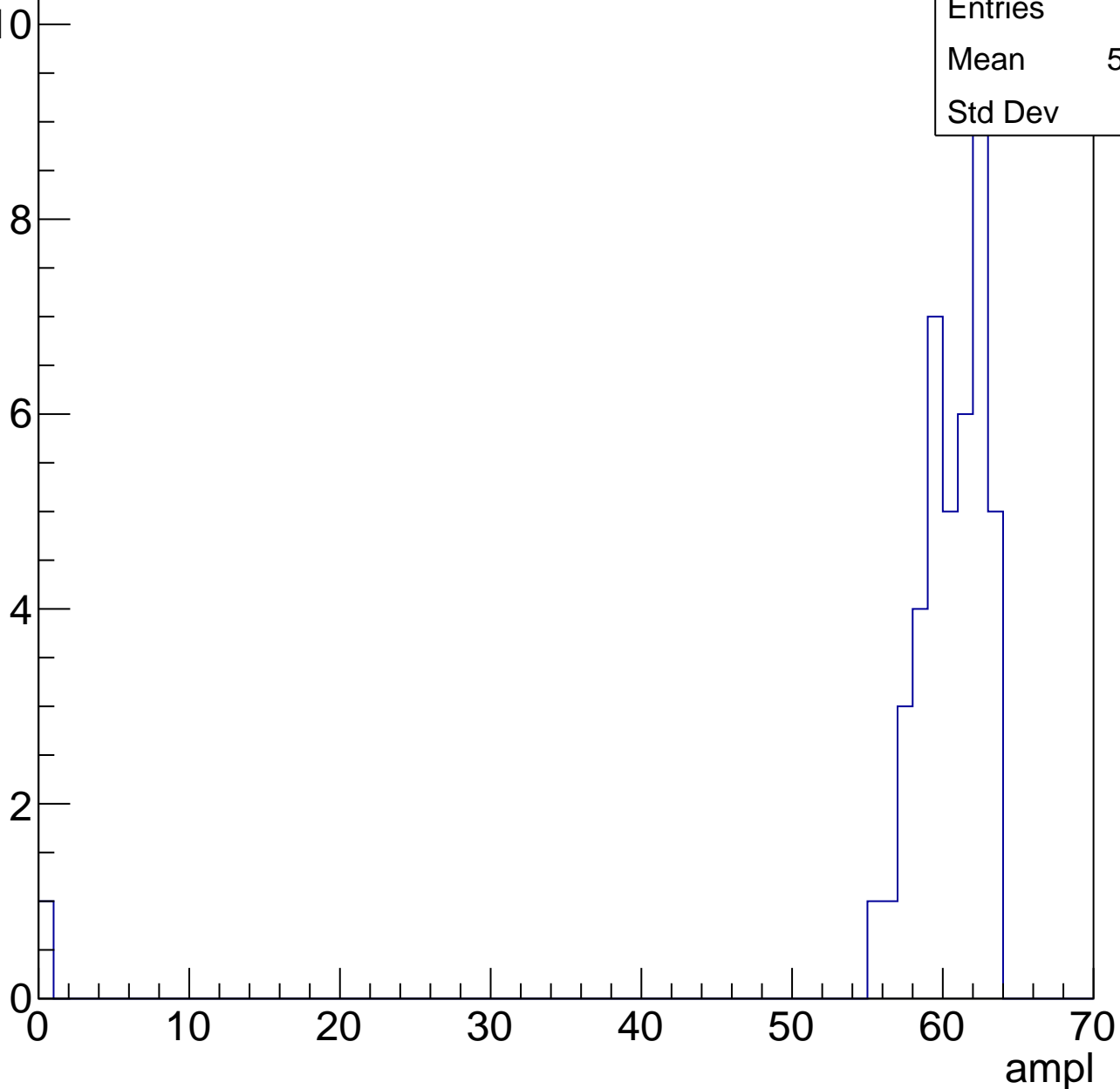


B1L103S, U10-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

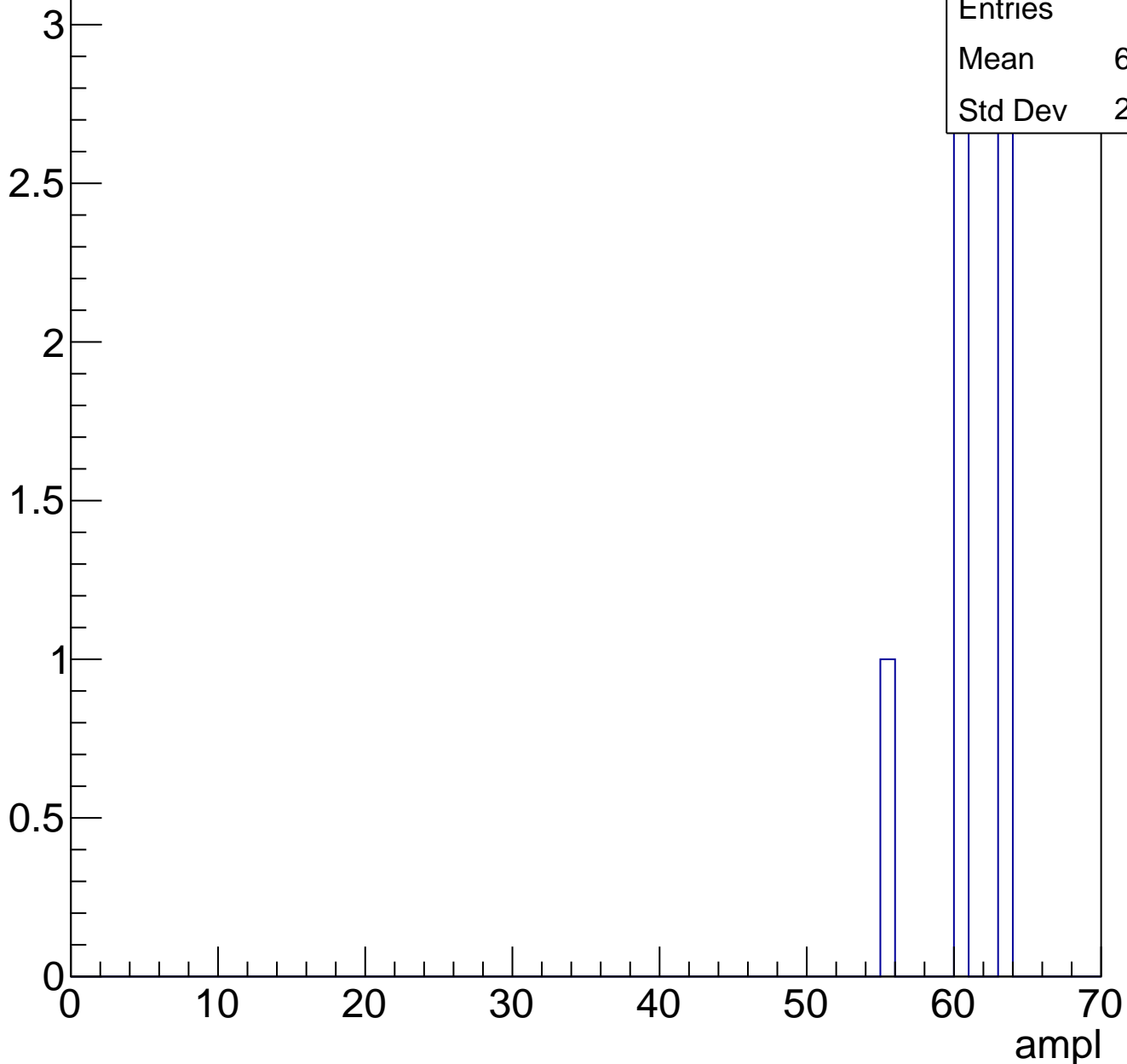
Entries	43
Mean	58.79
Std Dev	9.3



B1L103S, U10-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

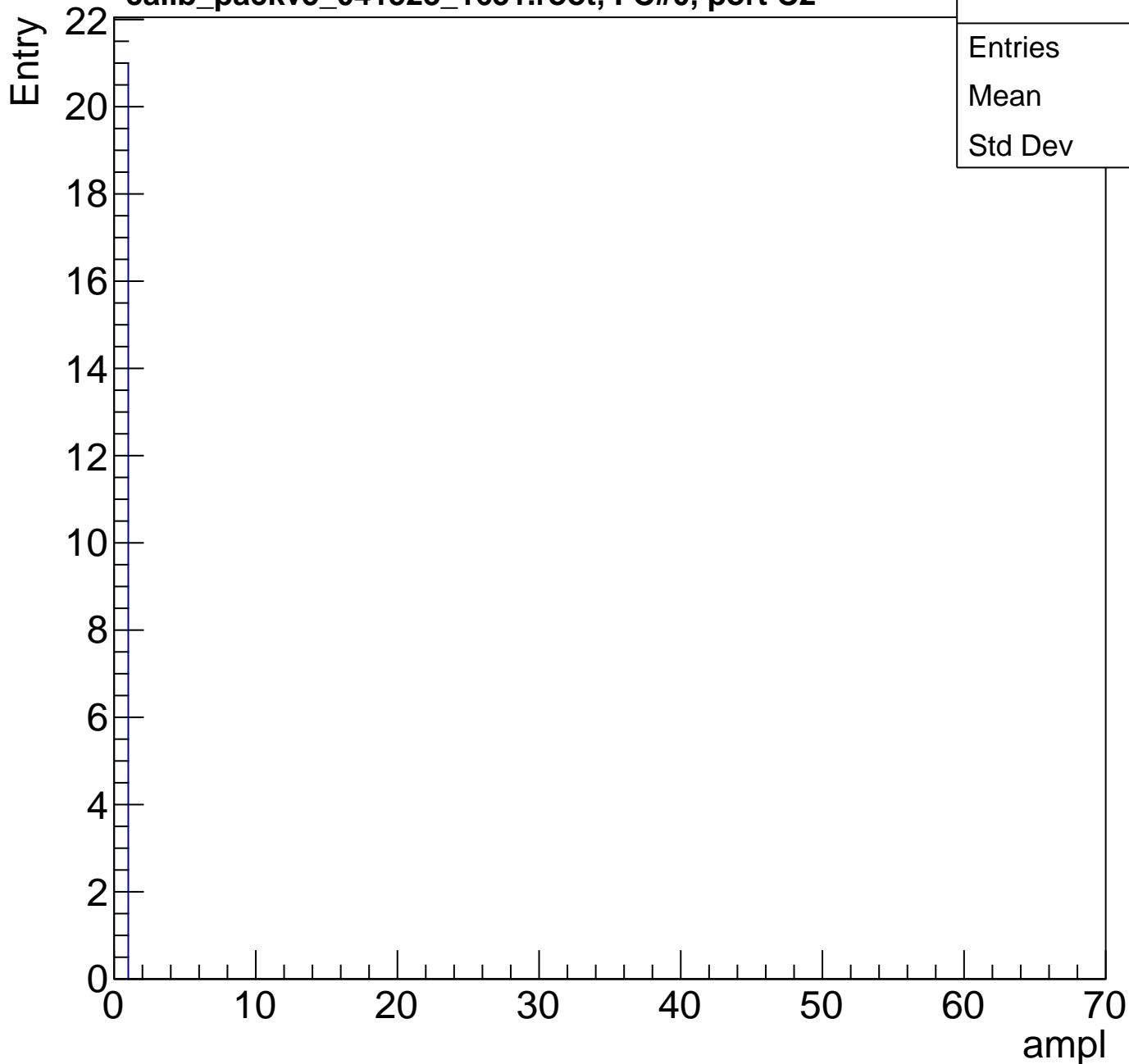
Entry



B1L103S, U10-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	0
Std Dev	0



B1L103S, U10-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	0
Std Dev	0

