



# B1L100S, U24-ch0

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.05
Std Dev	11.82

Turn on : 26.6703

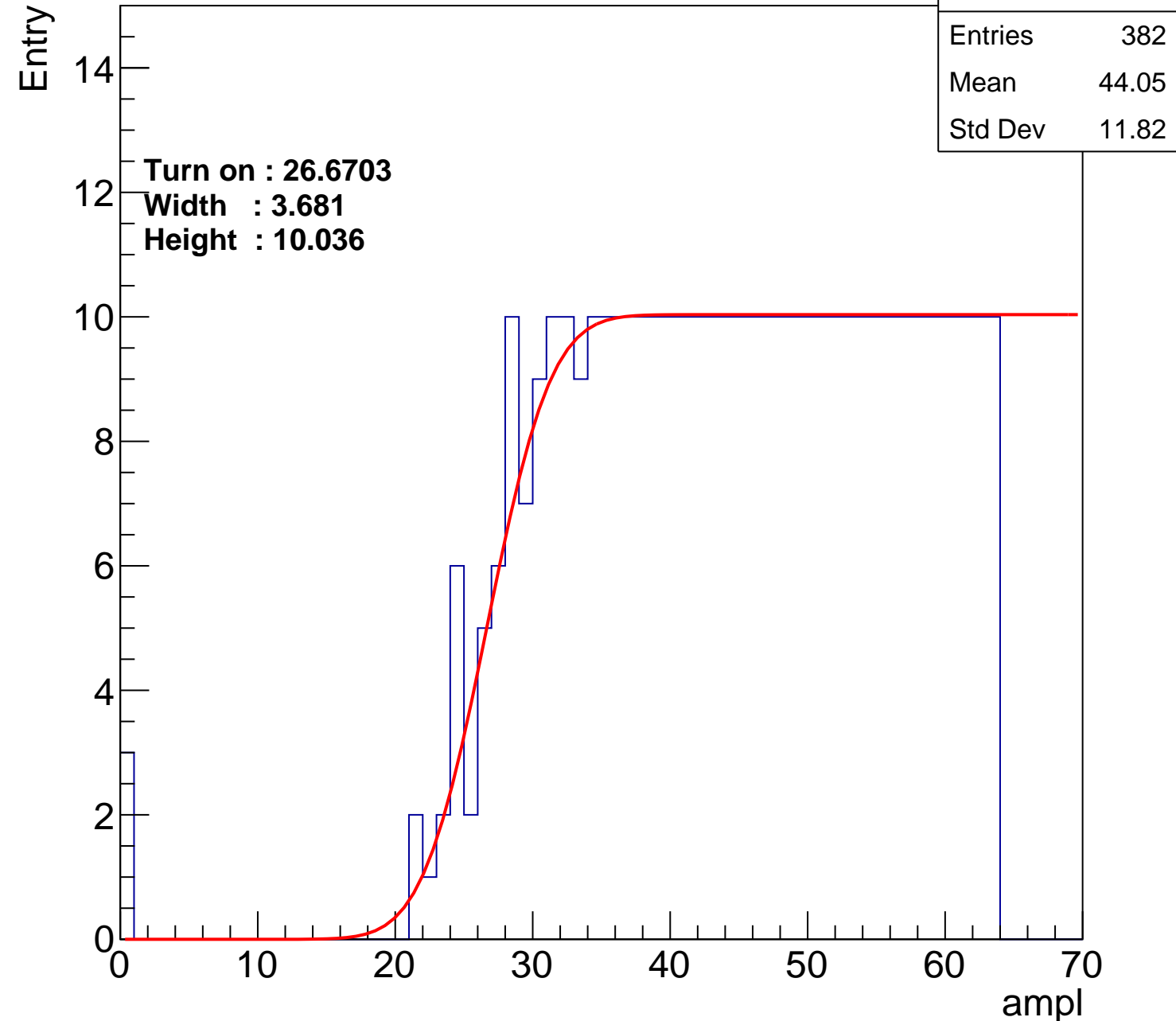
Width : 3.681

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch1

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.07
Std Dev	11.91

**Turn on : 26.3316**

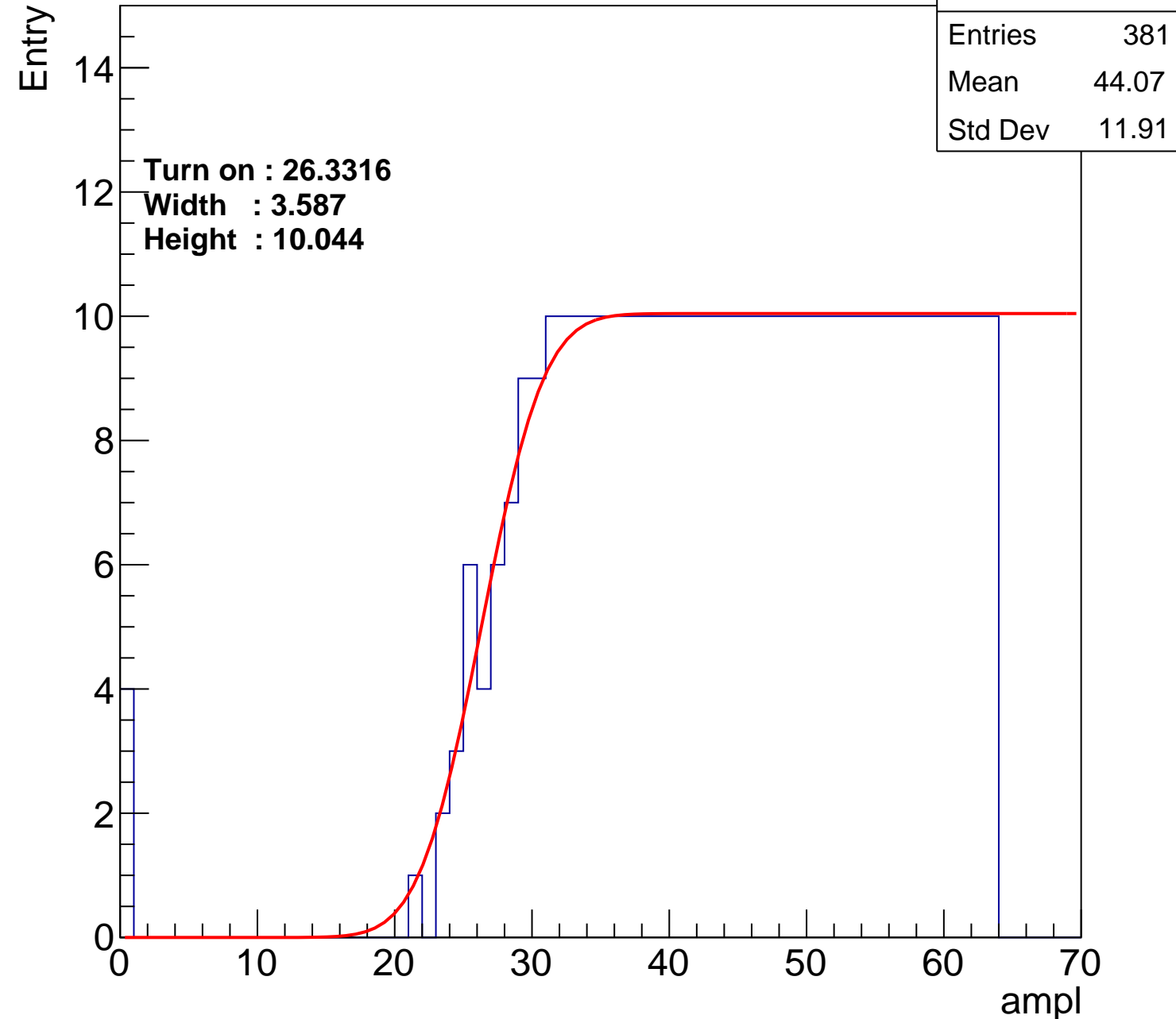
**Width : 3.587**

**Height : 10.044**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch2

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 26.2241

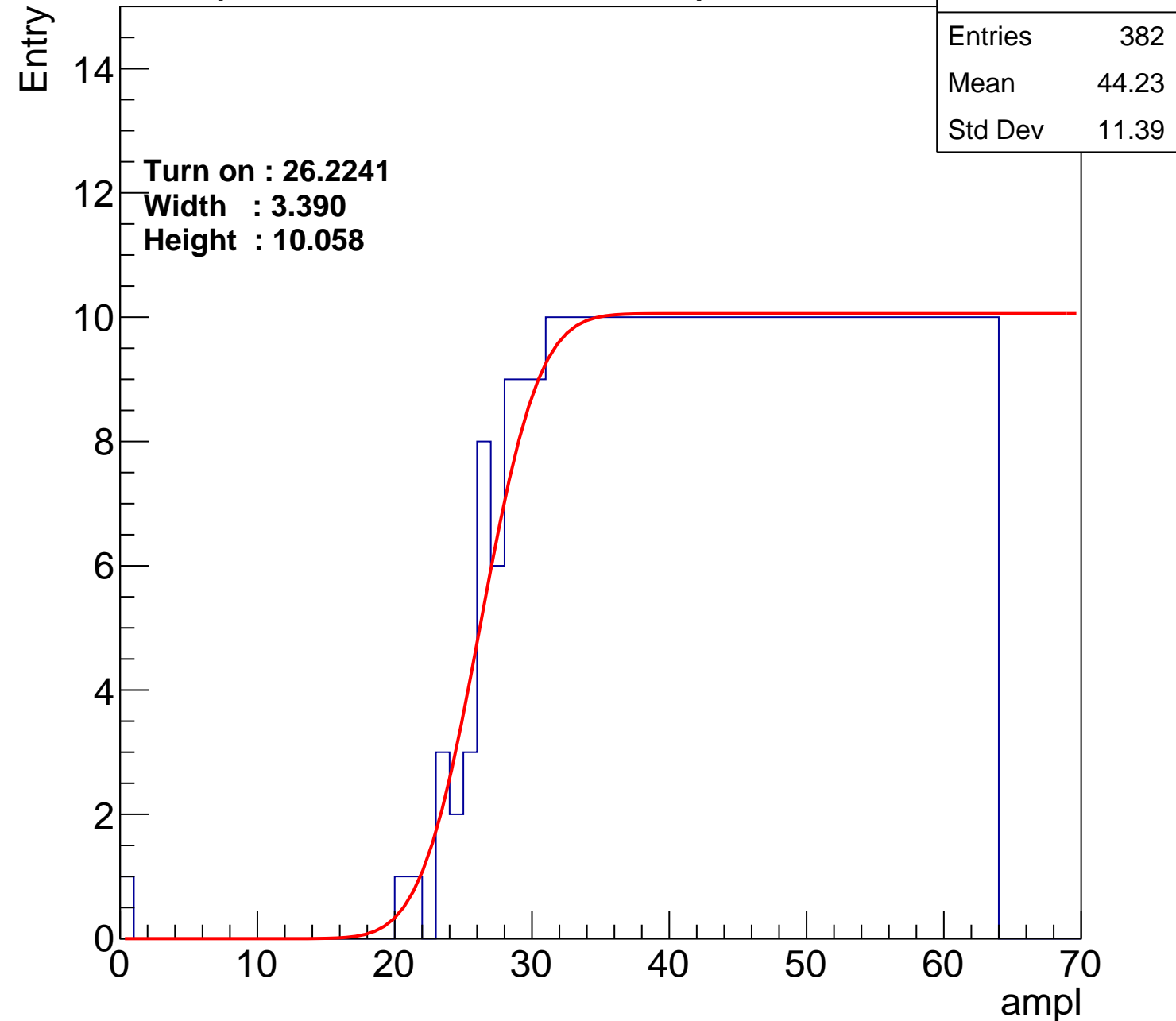
Width : 3.390

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch3

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.95
Std Dev	10.97

**Turn on : 27.2317**

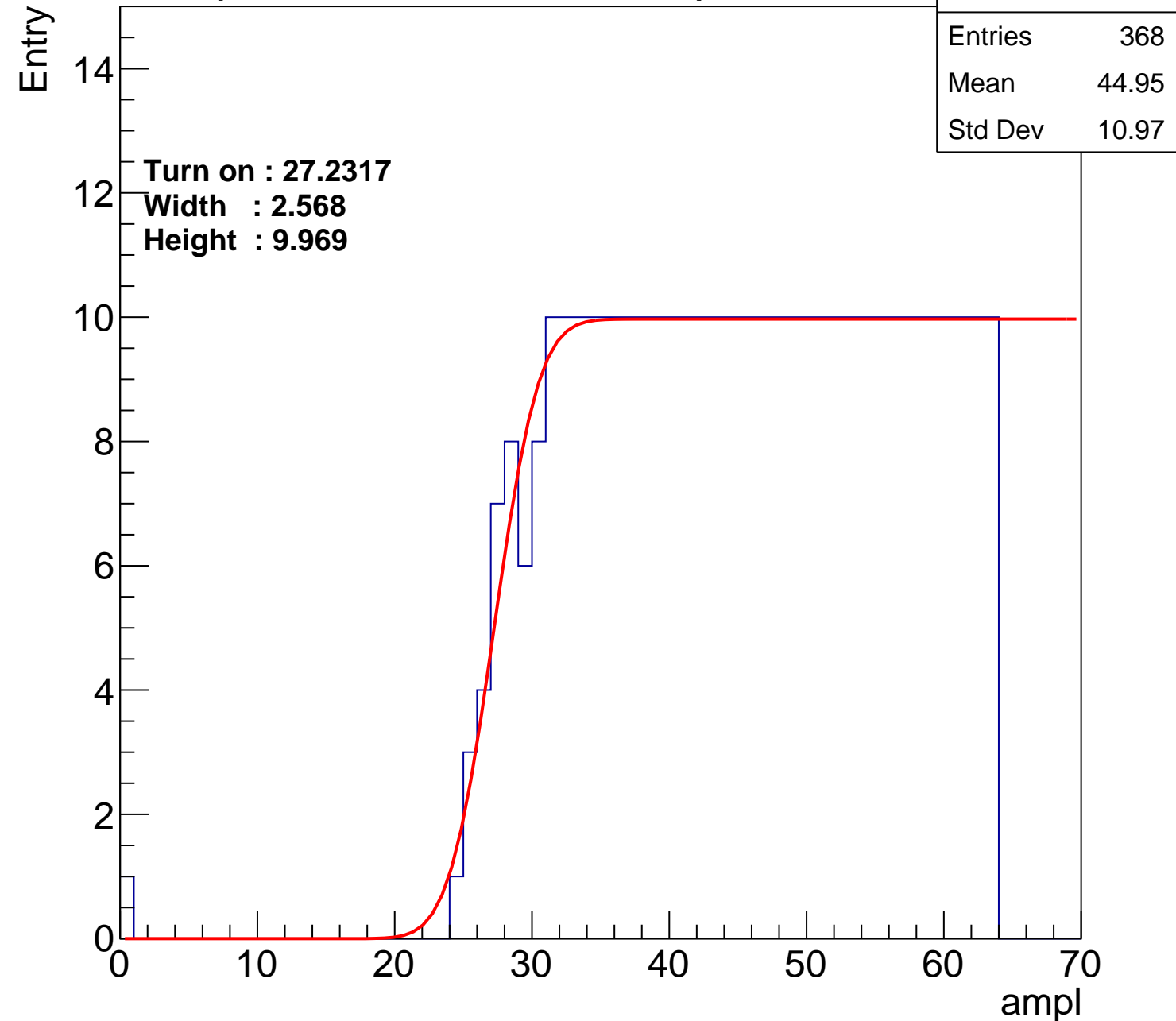
**Width : 2.568**

**Height : 9.969**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch4

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	371
Mean	44.71
Std Dev	11.28

**Turn on : 27.4664**

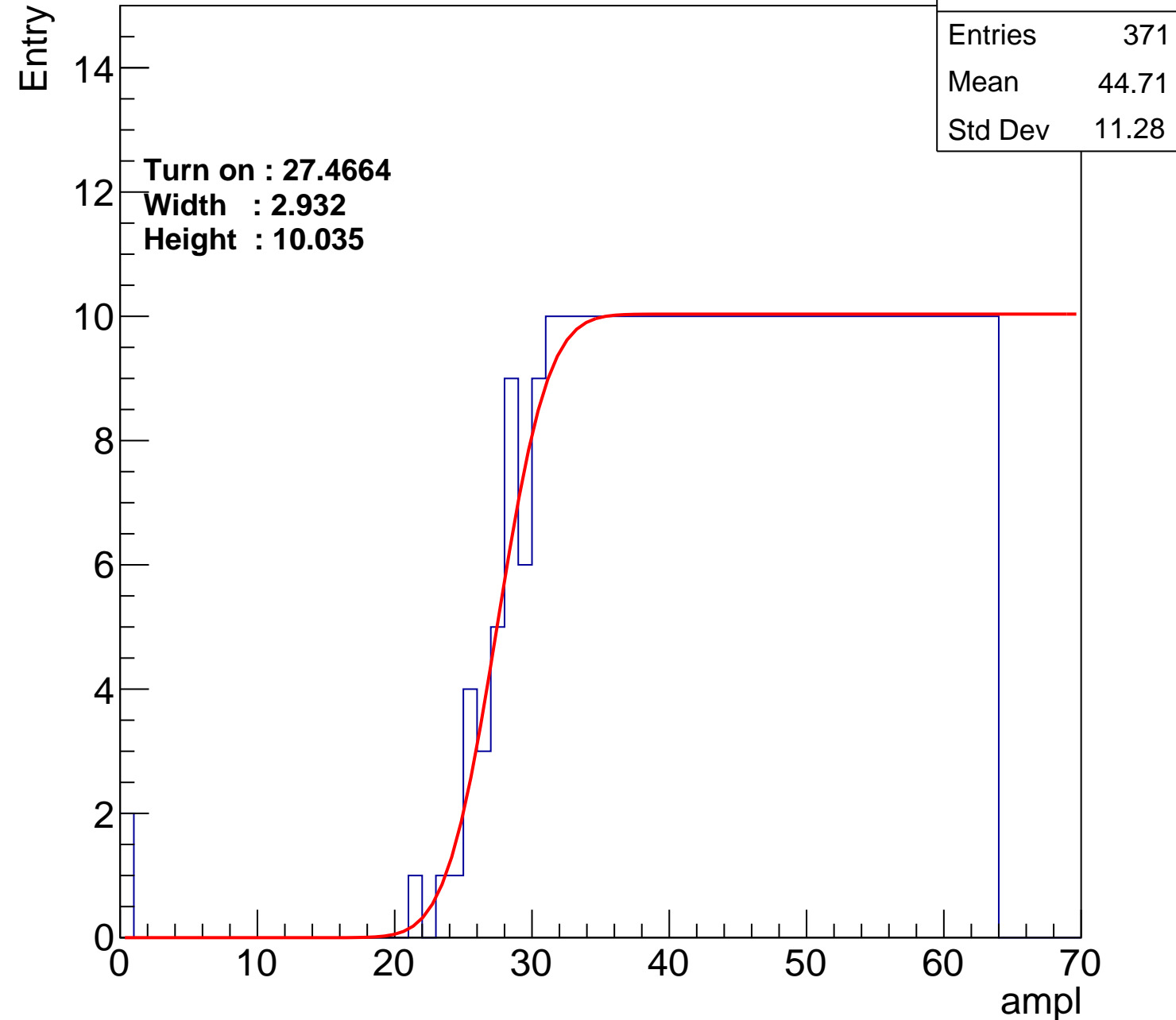
**Width : 2.932**

**Height : 10.035**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch5

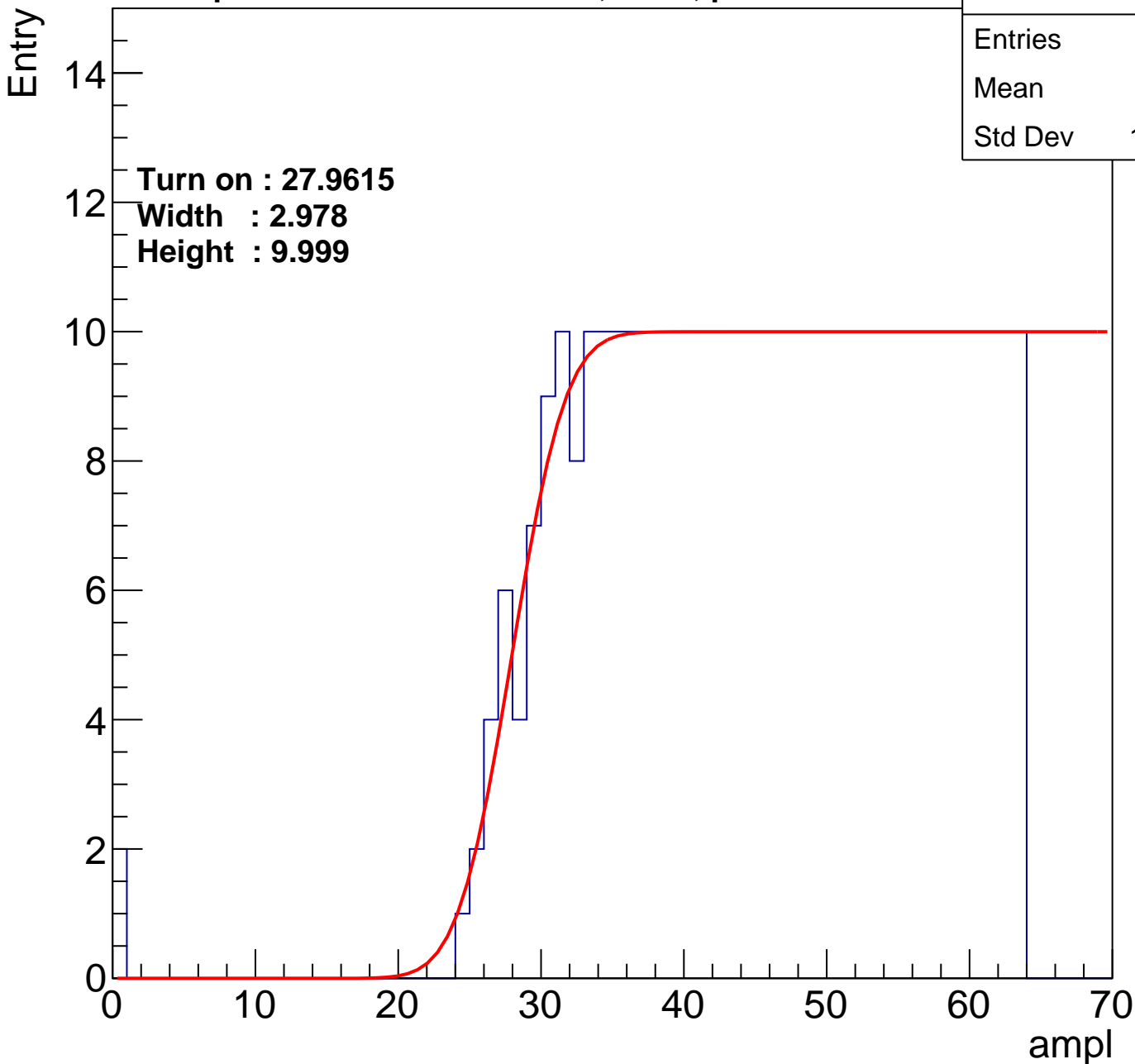
calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 27.9615

Width : 2.978

Height : 9.999



# B1L100S, U24-ch6

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	45.07
Std Dev	11.07

**Turn on : 27.8937**

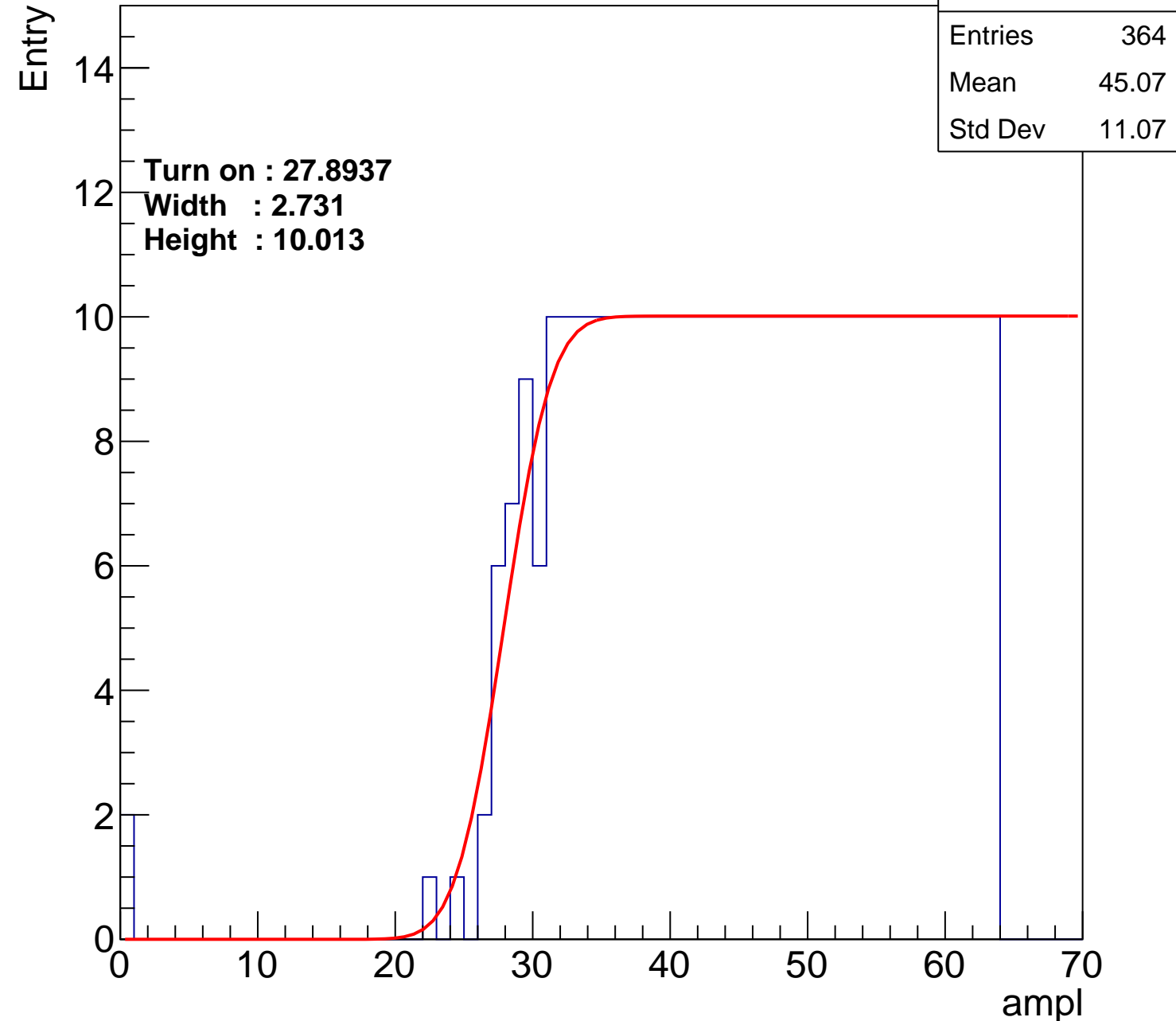
**Width : 2.731**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch7

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	379
Mean	44.42
Std Dev	11.23

**Turn on : 26.5327**

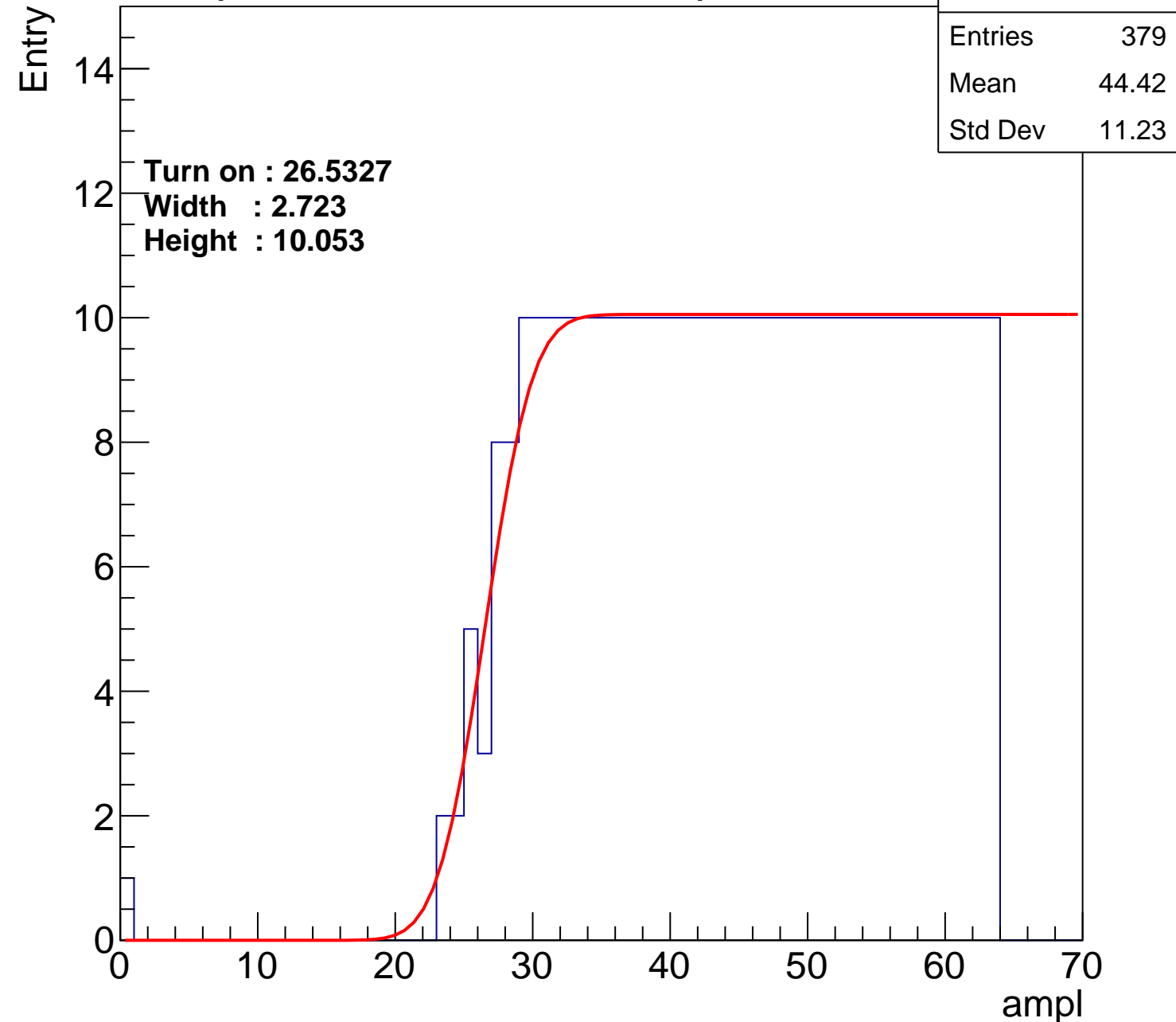
**Width : 2.723**

**Height : 10.053**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch8

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.26
Std Dev	11.47

**Turn on : 26.8990**

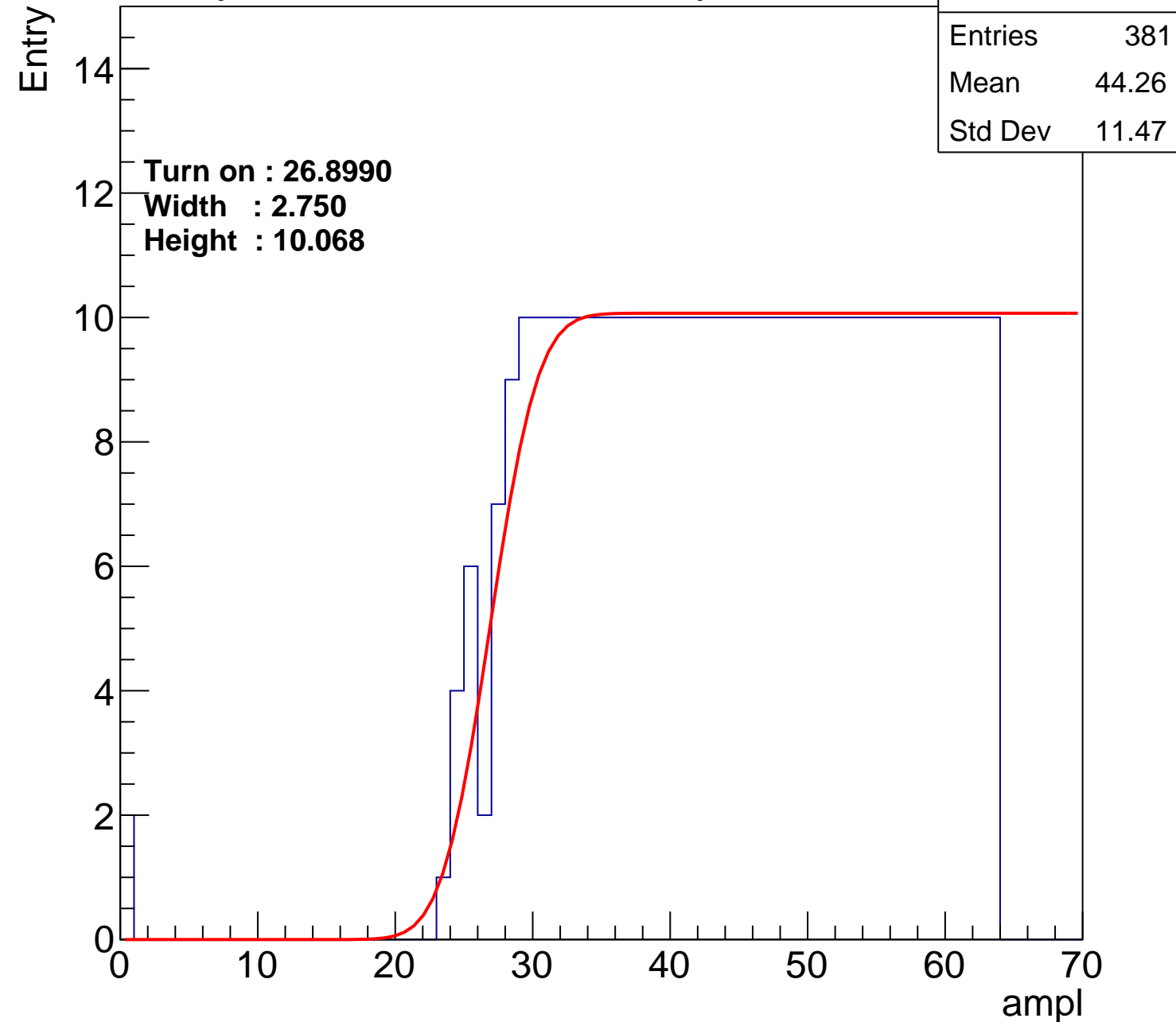
**Width : 2.750**

**Height : 10.068**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch9

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	44.93
Std Dev	11.48

Turn on : 28.1559

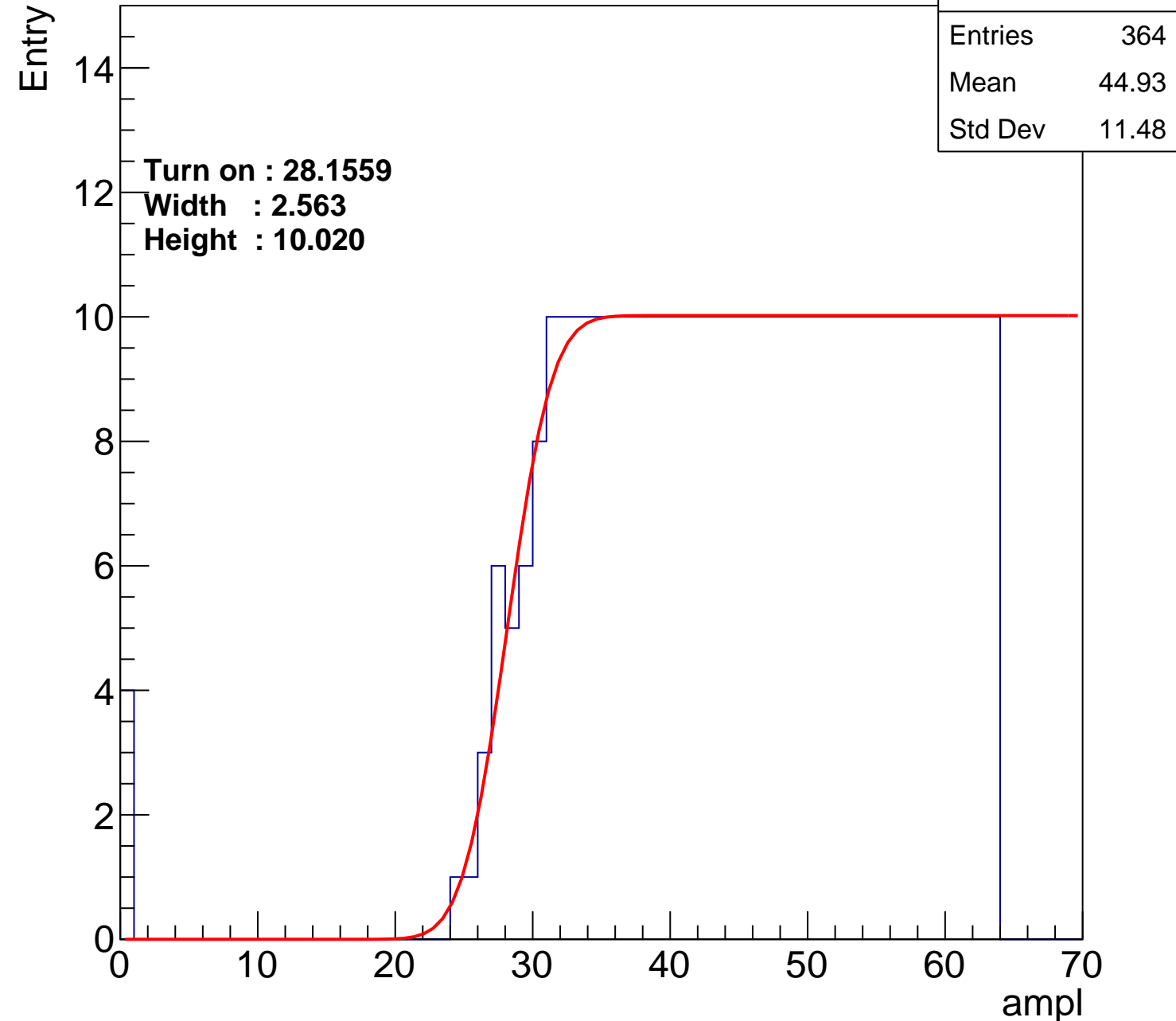
Width : 2.563

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch10

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	333
Mean	46.55
Std Dev	10.38

Turn on : 31.1539

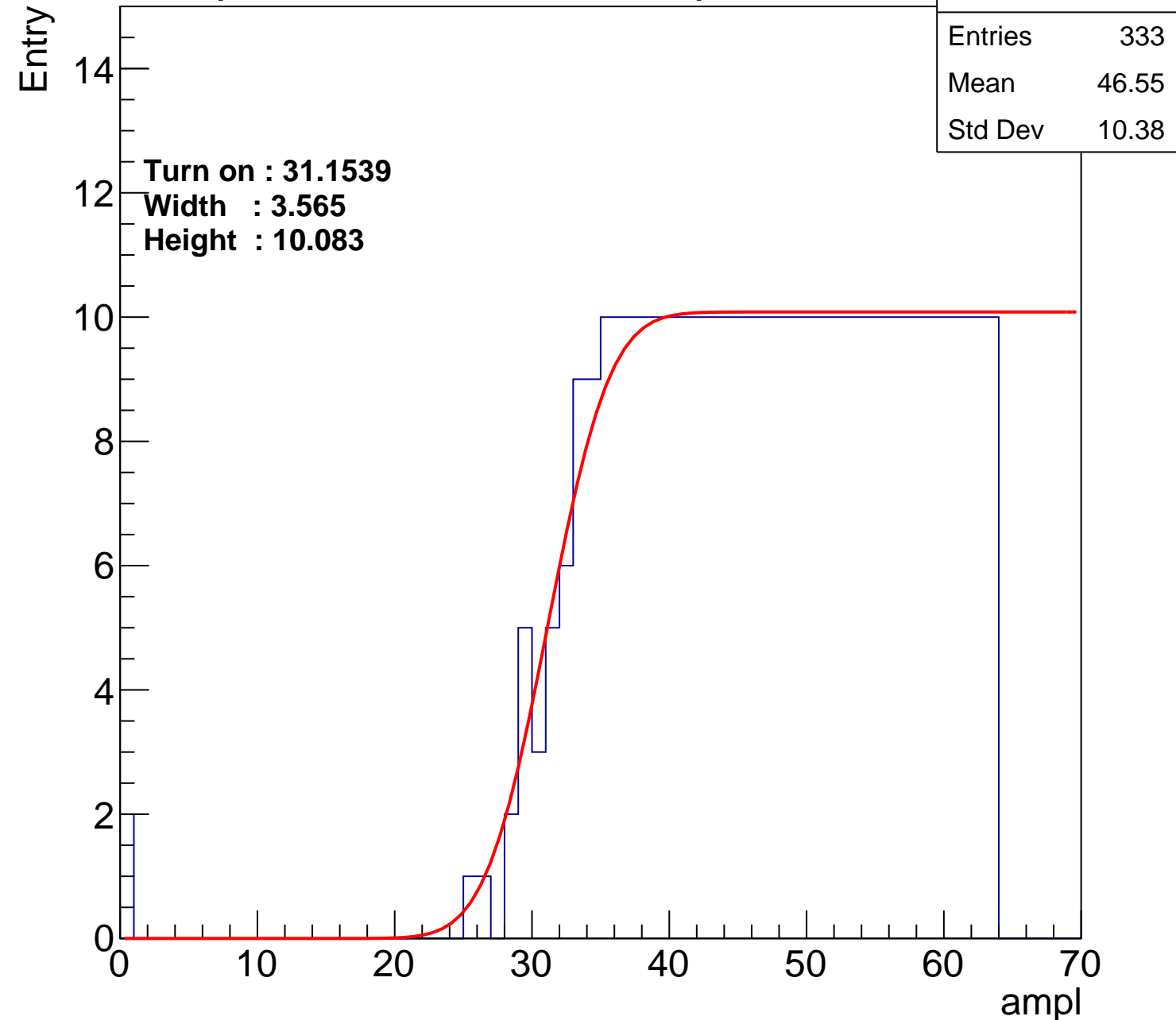
Width : 3.565

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch11

calib\_packv5\_042523\_0143.root, FC#4, port A2

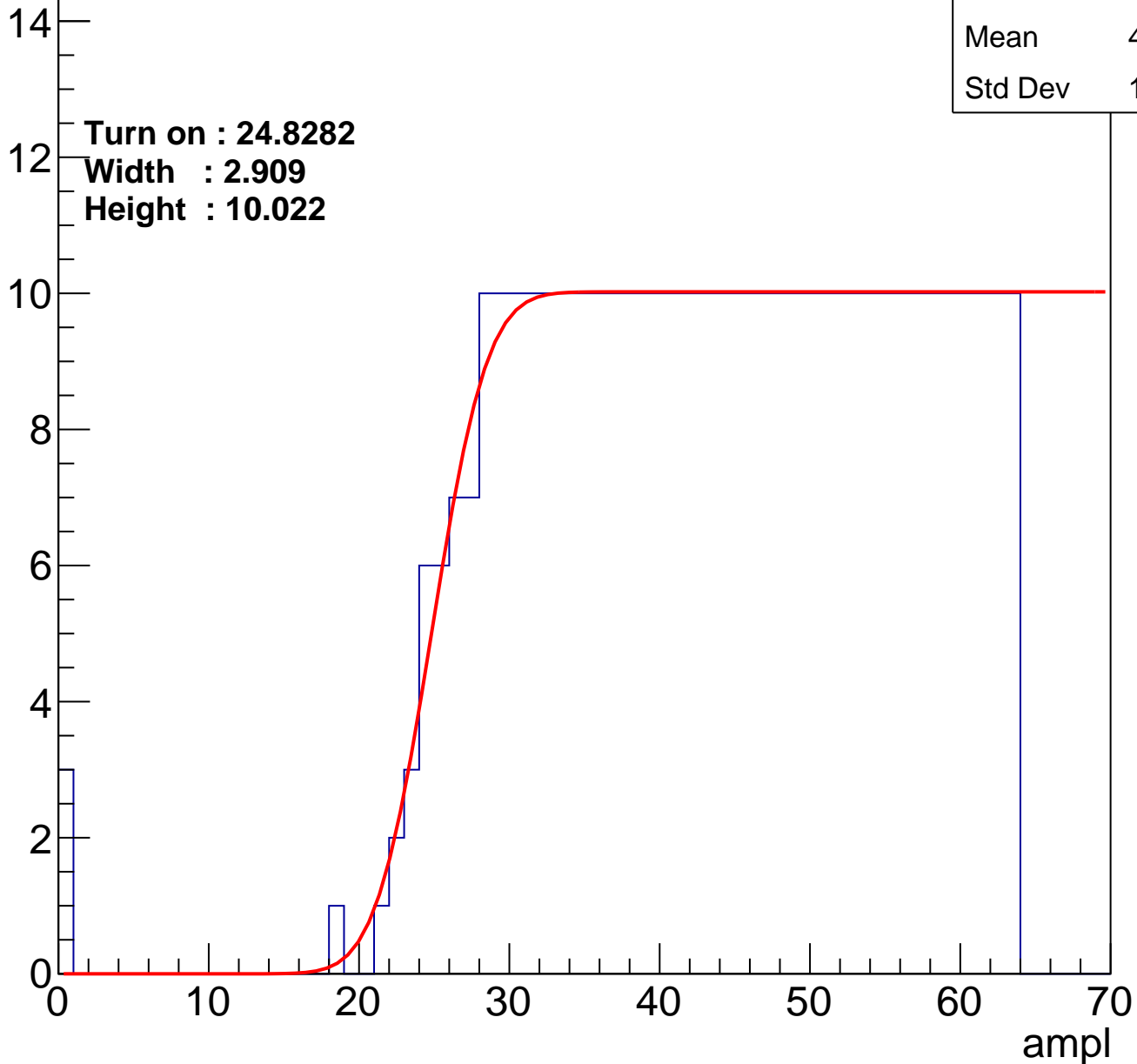
Entries	396
Mean	43.43
Std Dev	12.07

Turn on : 24.8282

Width : 2.909

Height : 10.022

Entry



# B1L100S, U24-ch12

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.1455

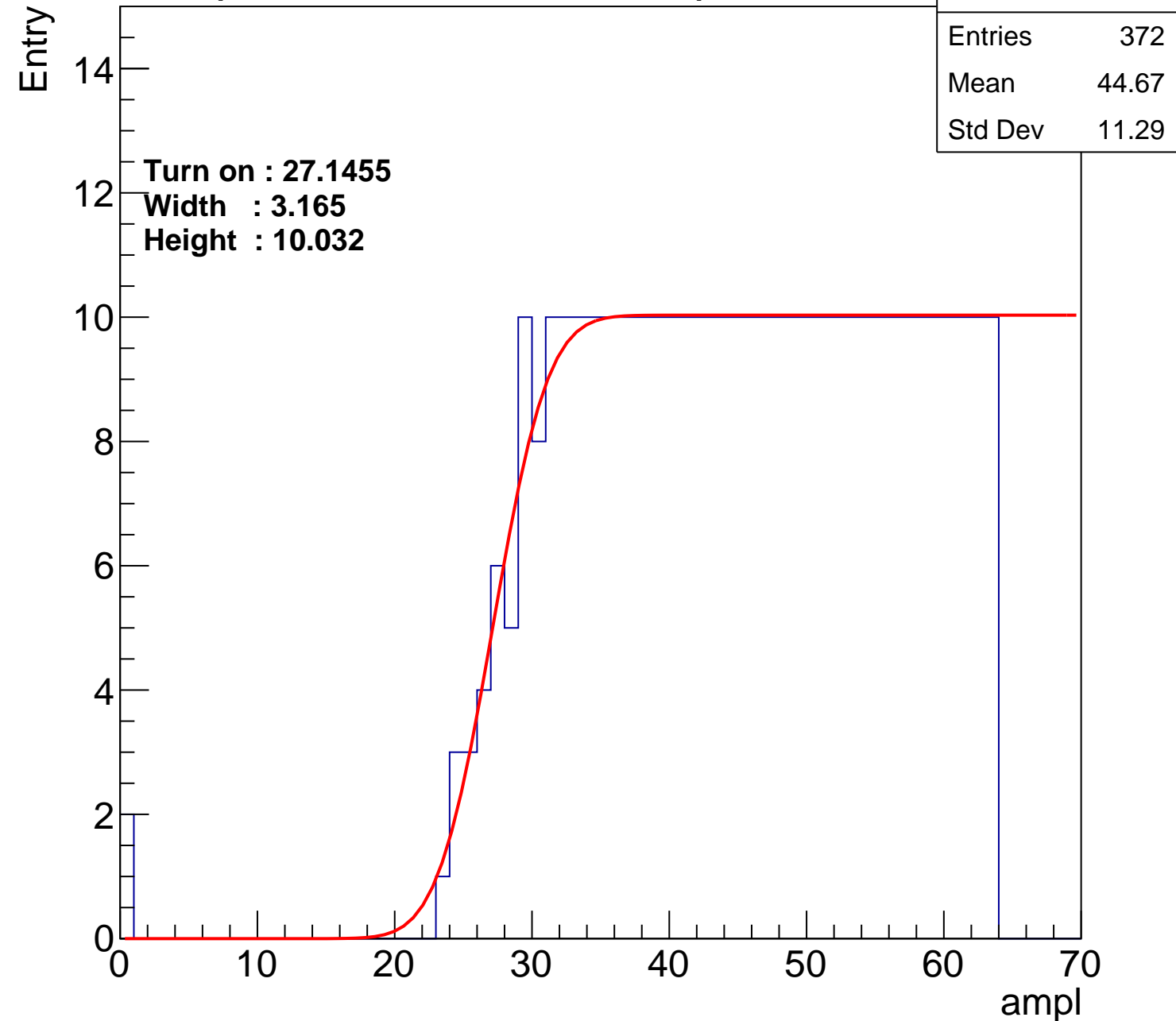
Width : 3.165

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch13

calib\_packv5\_042523\_0143.root, FC#4, port A2

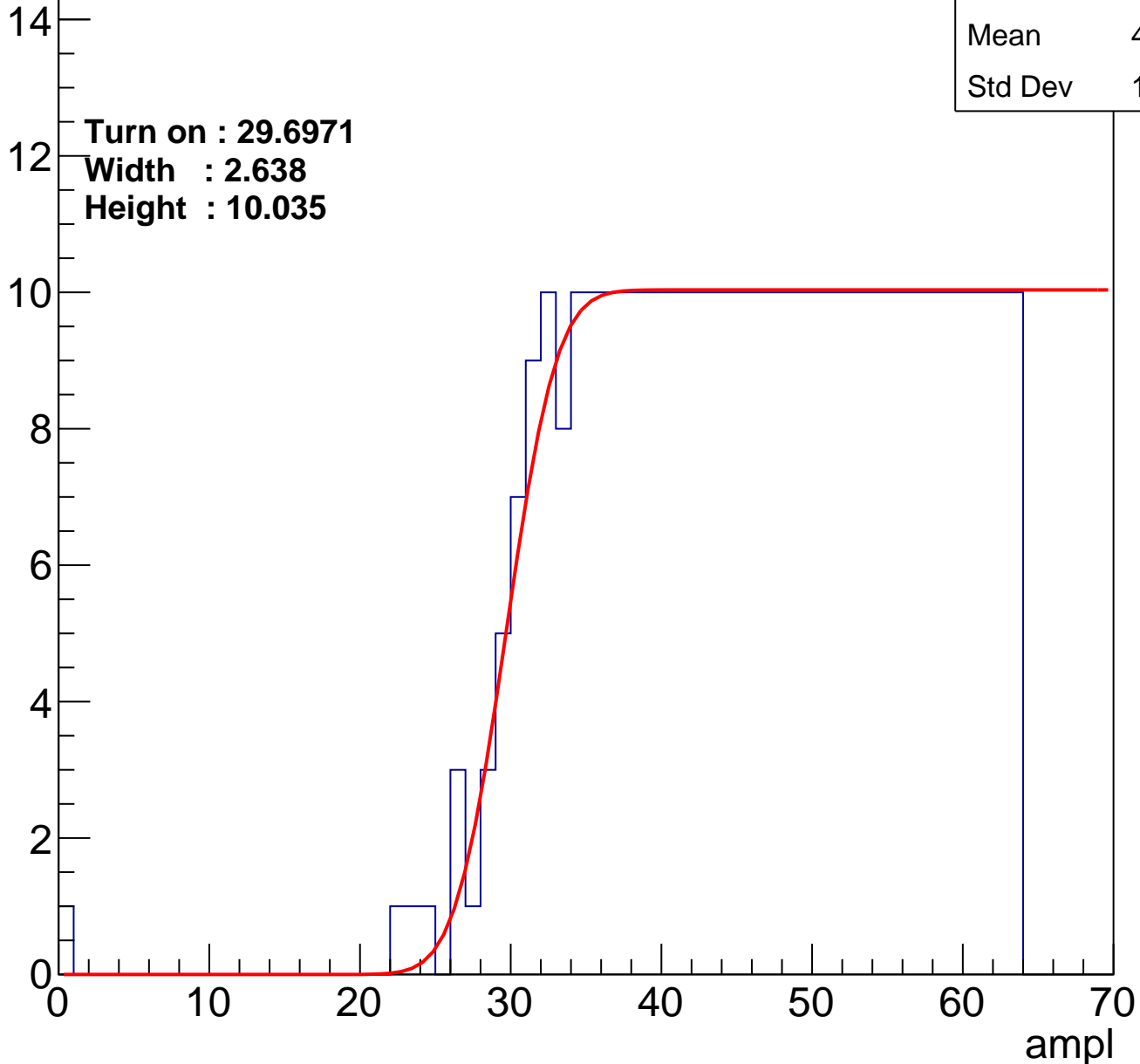
Entries	350
Mean	45.79
Std Dev	10.58

**Turn on : 29.6971**

**Width : 2.638**

**Height : 10.035**

Entry



# B1L100S, U24-ch14

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	387
Mean	43.87
Std Dev	11.84

**Turn on : 25.8807**

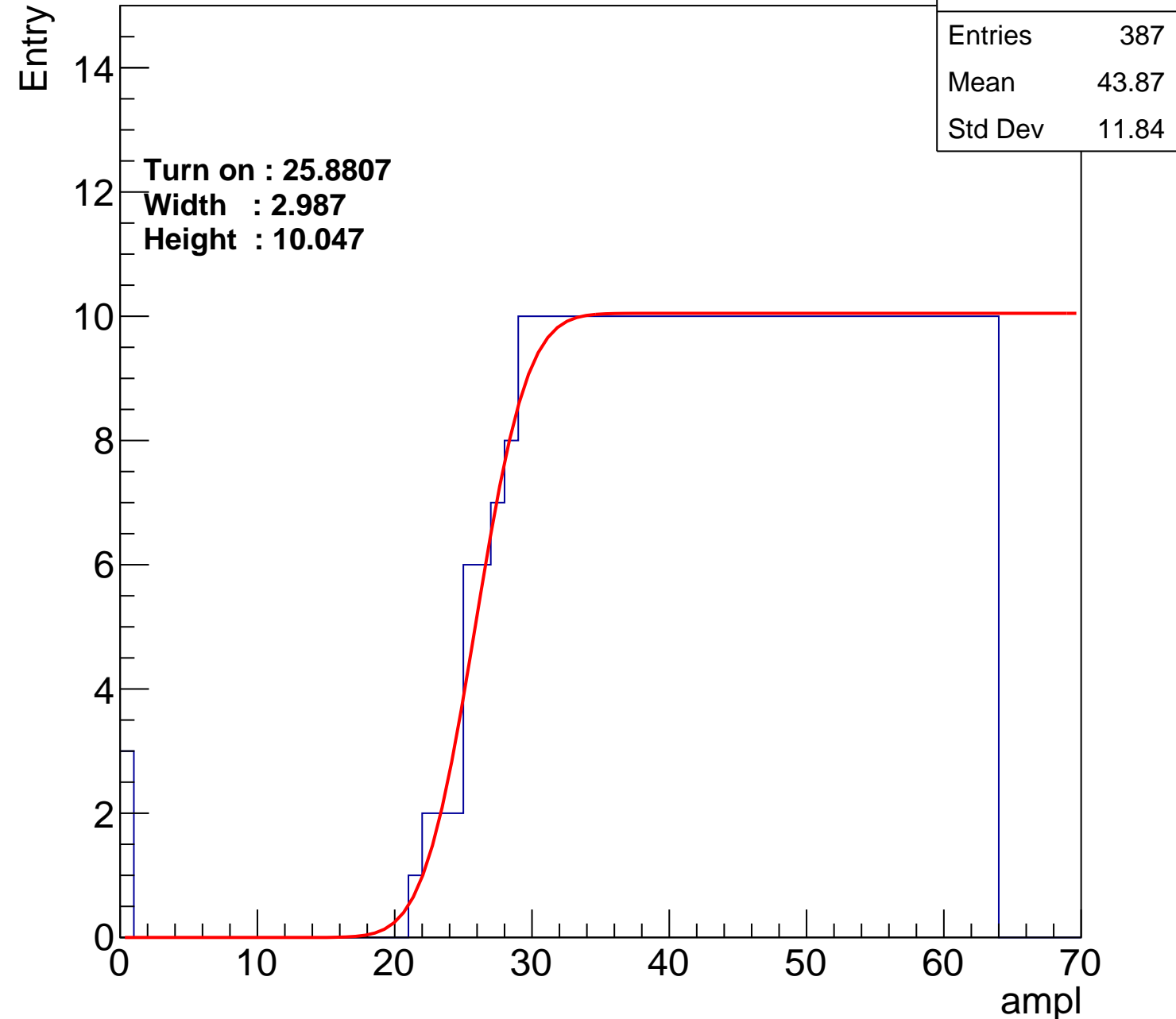
**Width : 2.987**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch15

calib\_packv5\_042523\_0143.root, FC#4, port A2

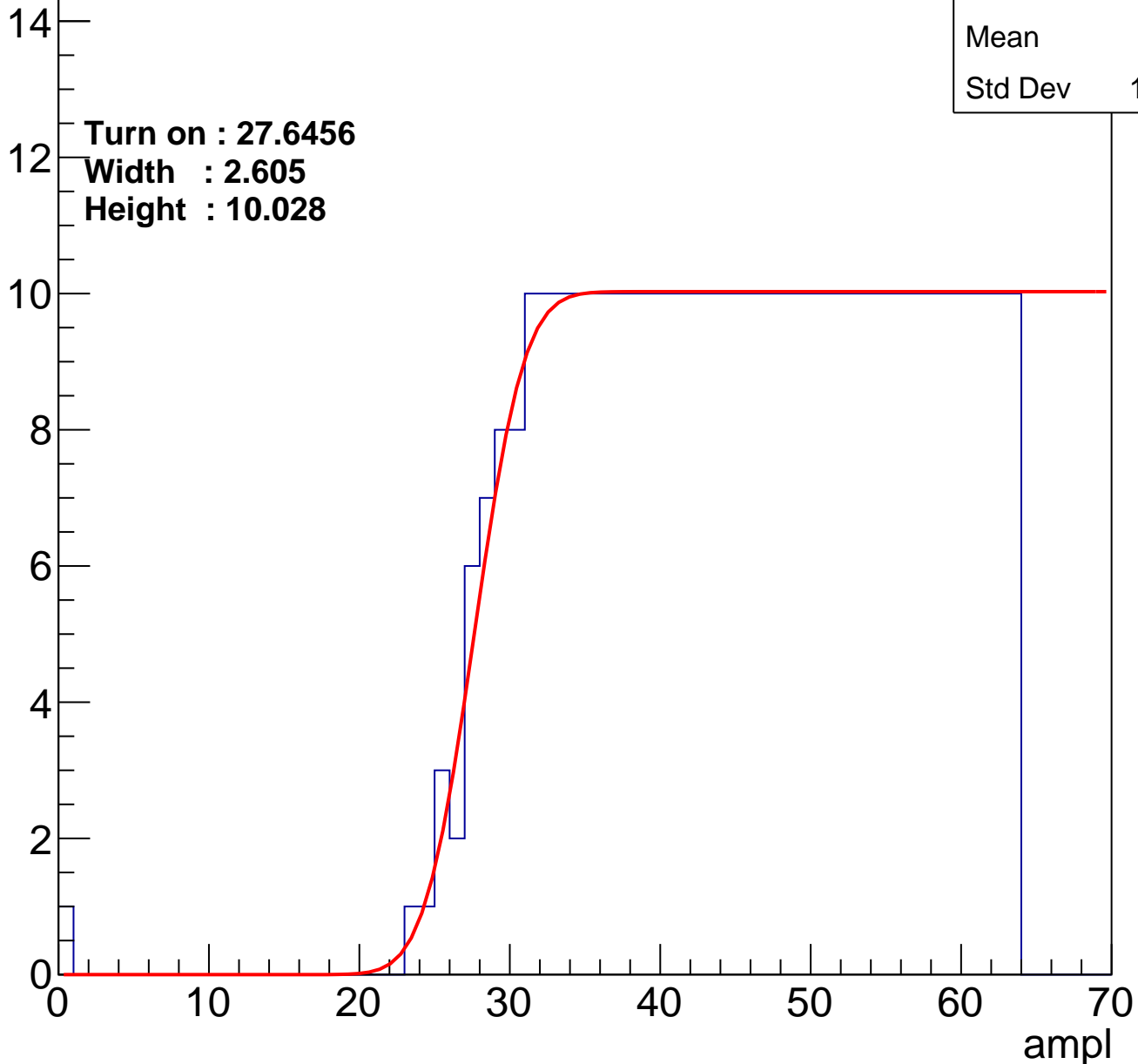
Entries	367
Mean	45
Std Dev	10.94

Turn on : 27.6456

Width : 2.605

Height : 10.028

Entry



# B1L100S, U24-ch16

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.27
Std Dev	10.76

**Turn on : 27.9758**

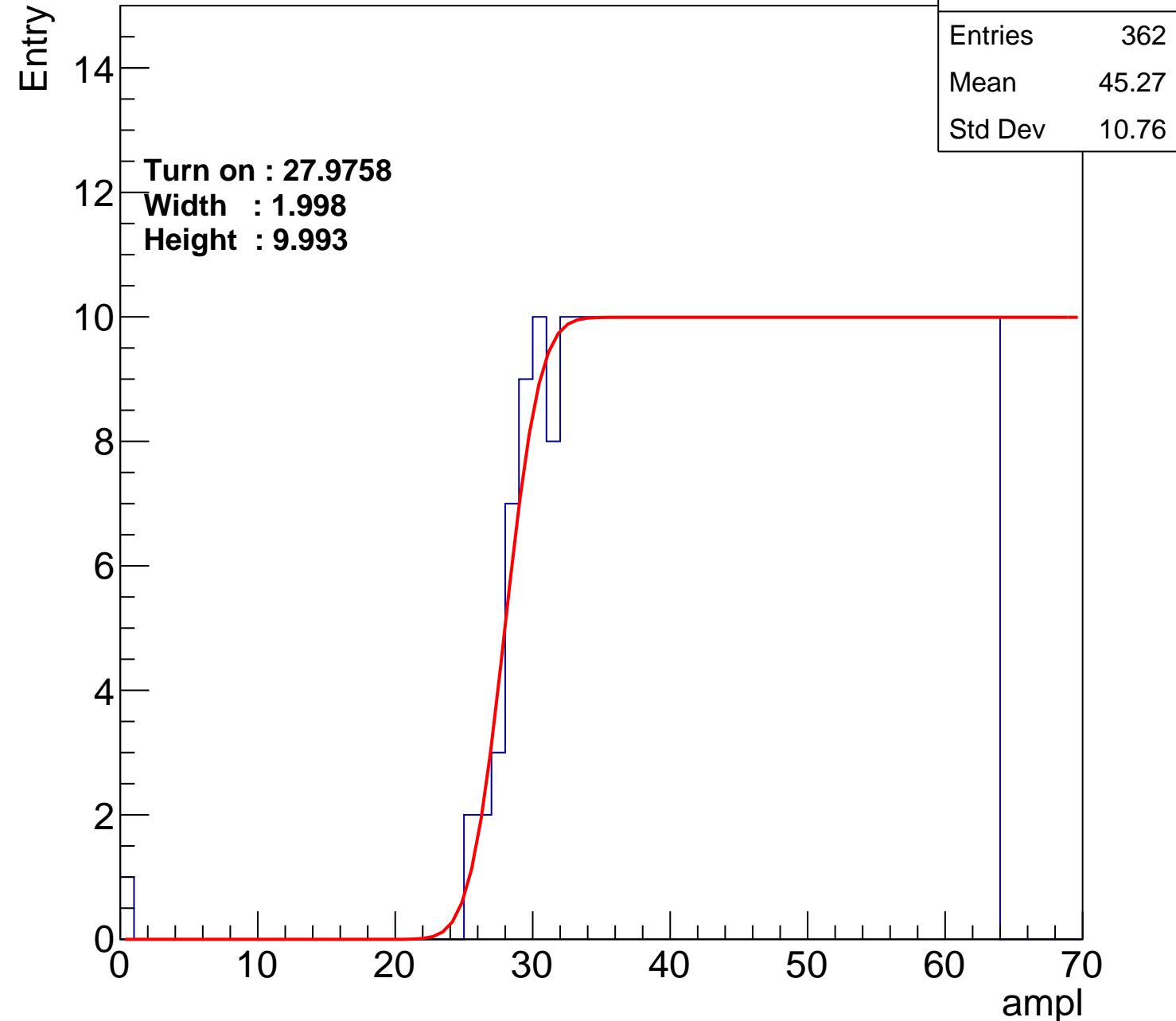
**Width : 1.998**

**Height : 9.993**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch17

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	358
Mean	45.38
Std Dev	10.9

Turn on : 28.8691

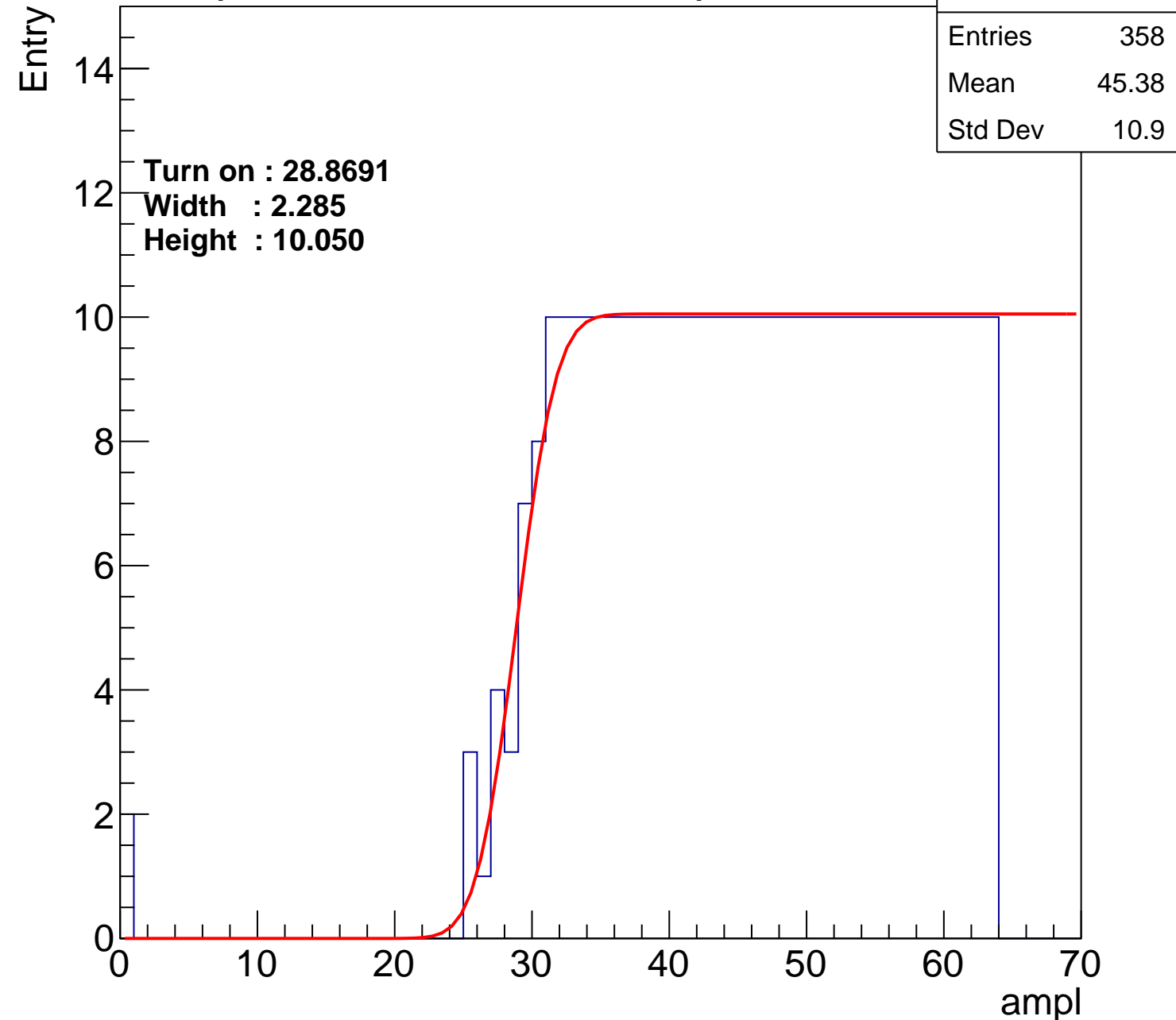
Width : 2.285

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch18

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	373
Mean	44.59
Std Dev	11.36

**Turn on : 26.9075**

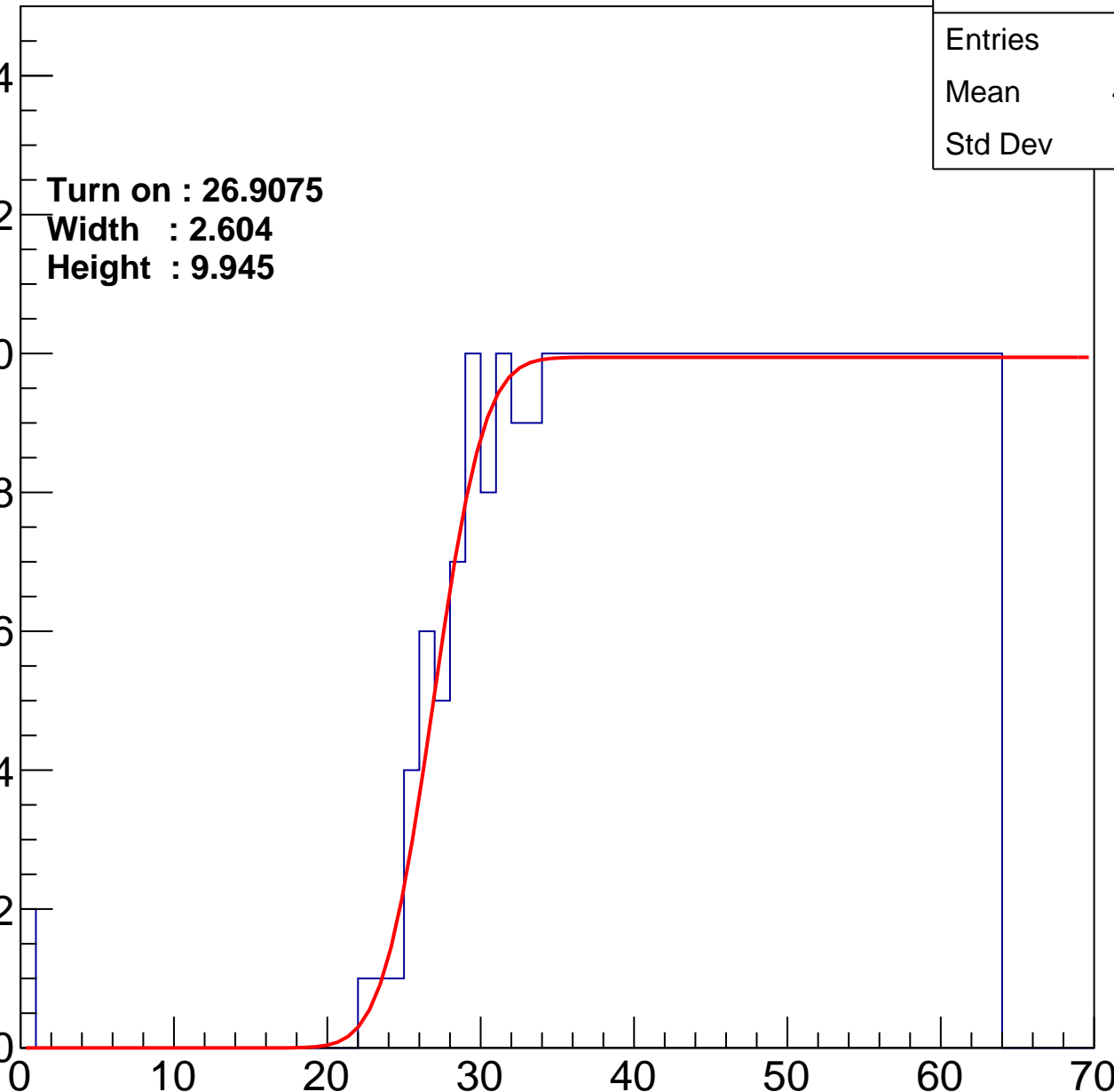
**Width : 2.604**

**Height : 9.945**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch19

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	45.09
Std Dev	11.04

Turn on : 28.1128

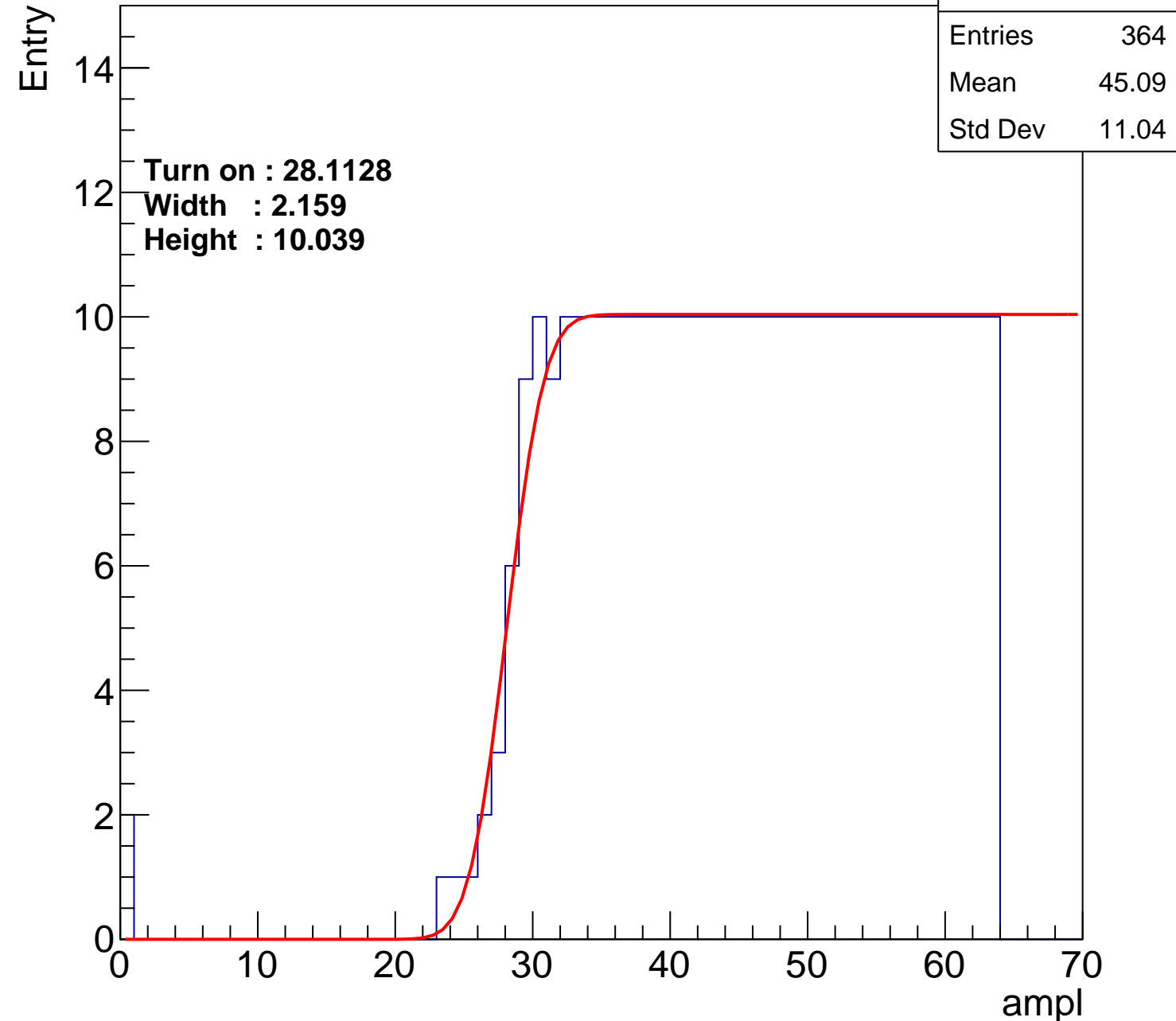
Width : 2.159

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch20

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.86
Std Dev	11.01

Turn on : 27.0750

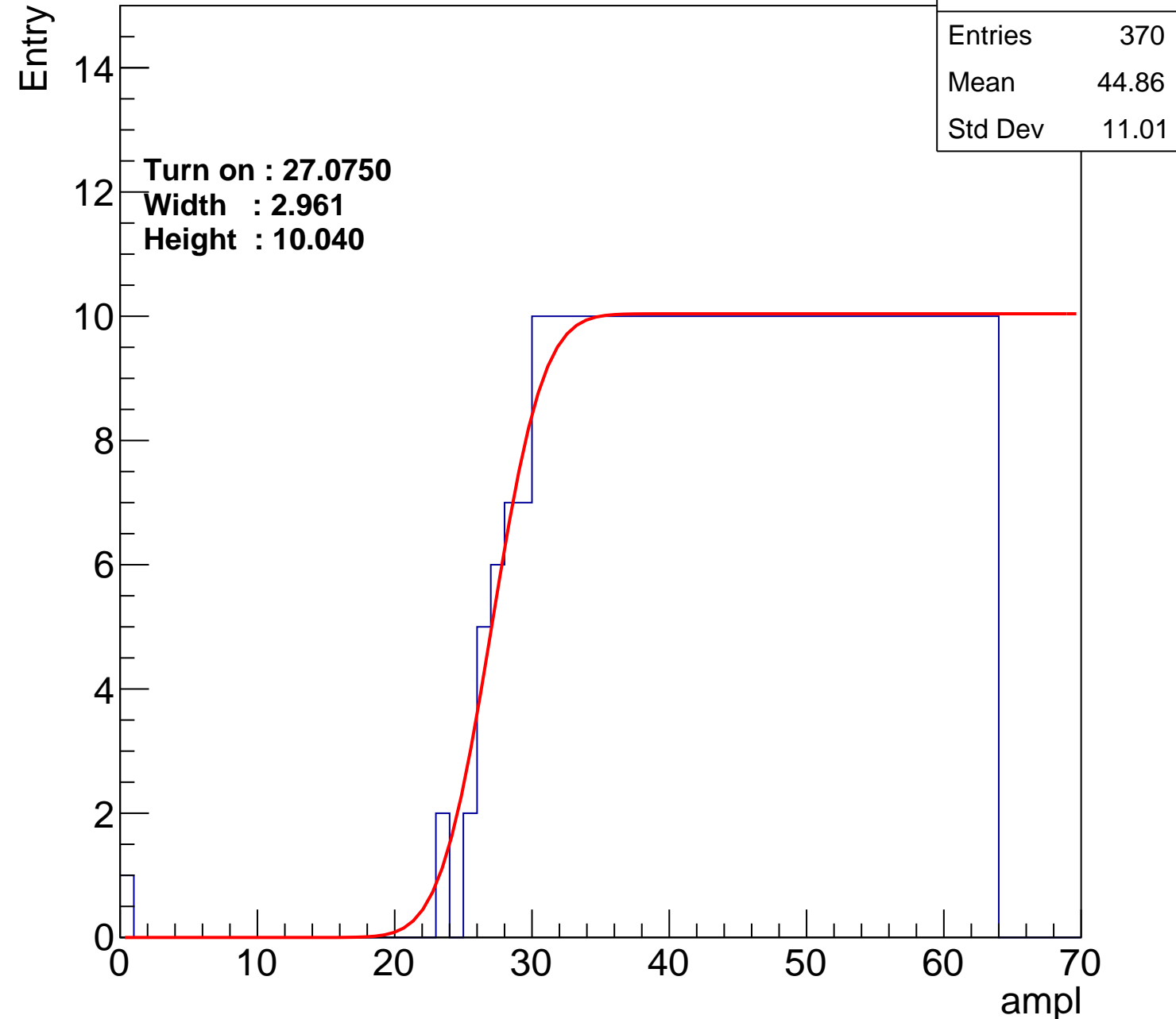
Width : 2.961

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch21

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.51
Std Dev	11.53

**Turn on : 27.9895**

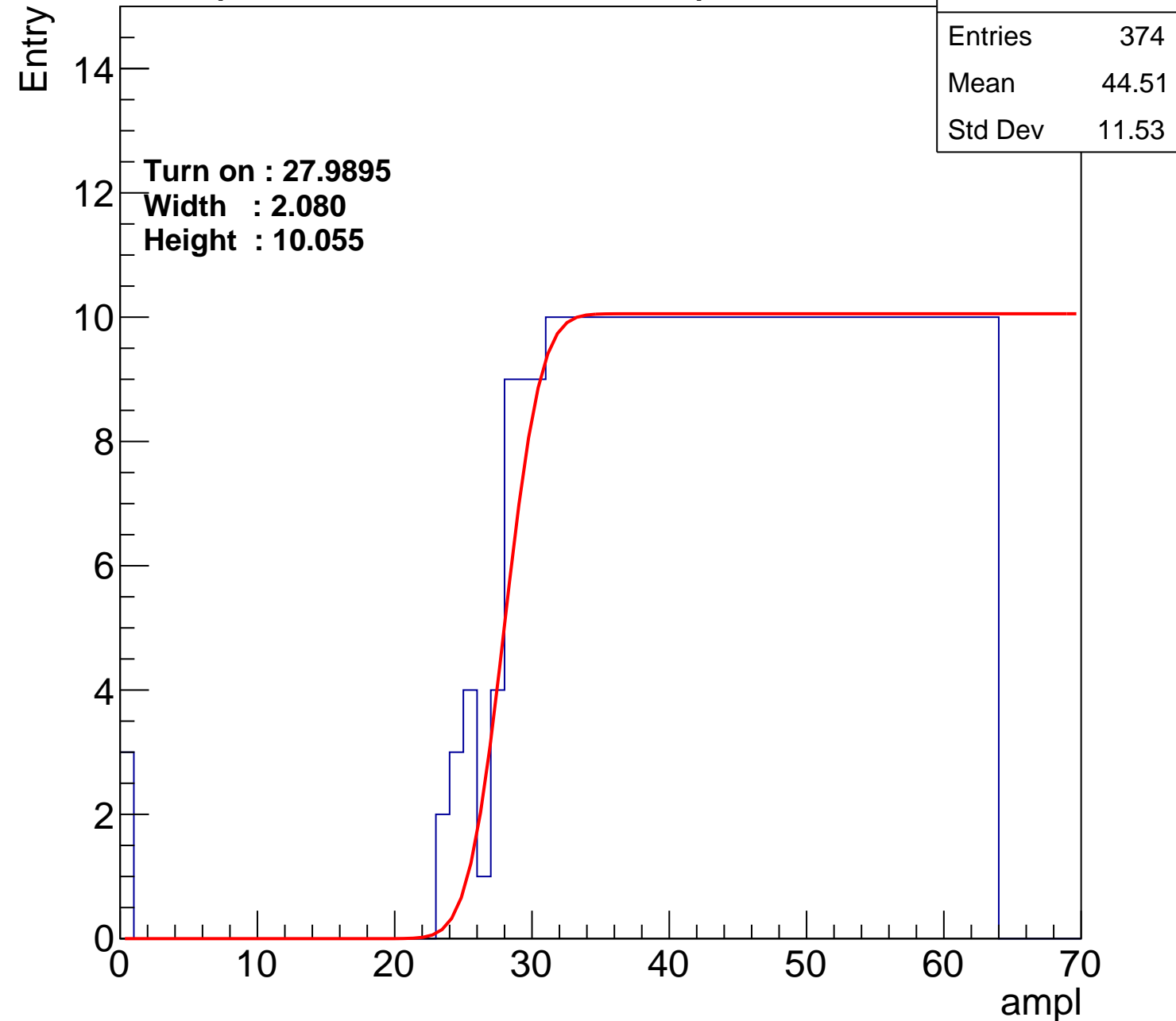
**Width : 2.080**

**Height : 10.055**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch22

calib\_packv5\_042523\_0143.root, FC#4, port A2

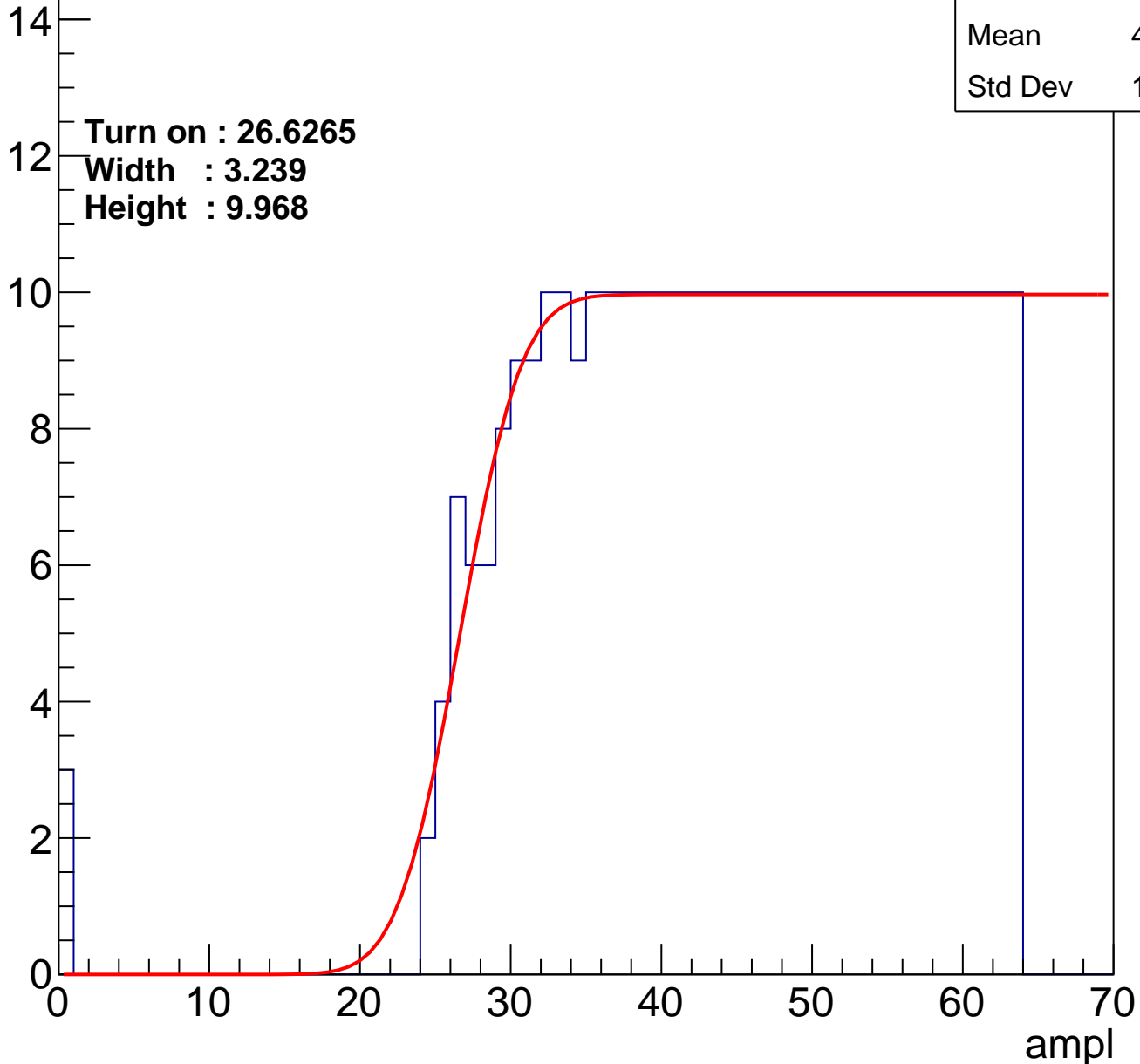
Entries	373
Mean	44.52
Std Dev	11.54

Turn on : 26.6265

Width : 3.239

Height : 9.968

Entry





# B1L100S, U24-ch23

calib\_packv5\_042523\_0143.root, FC#4, port A2

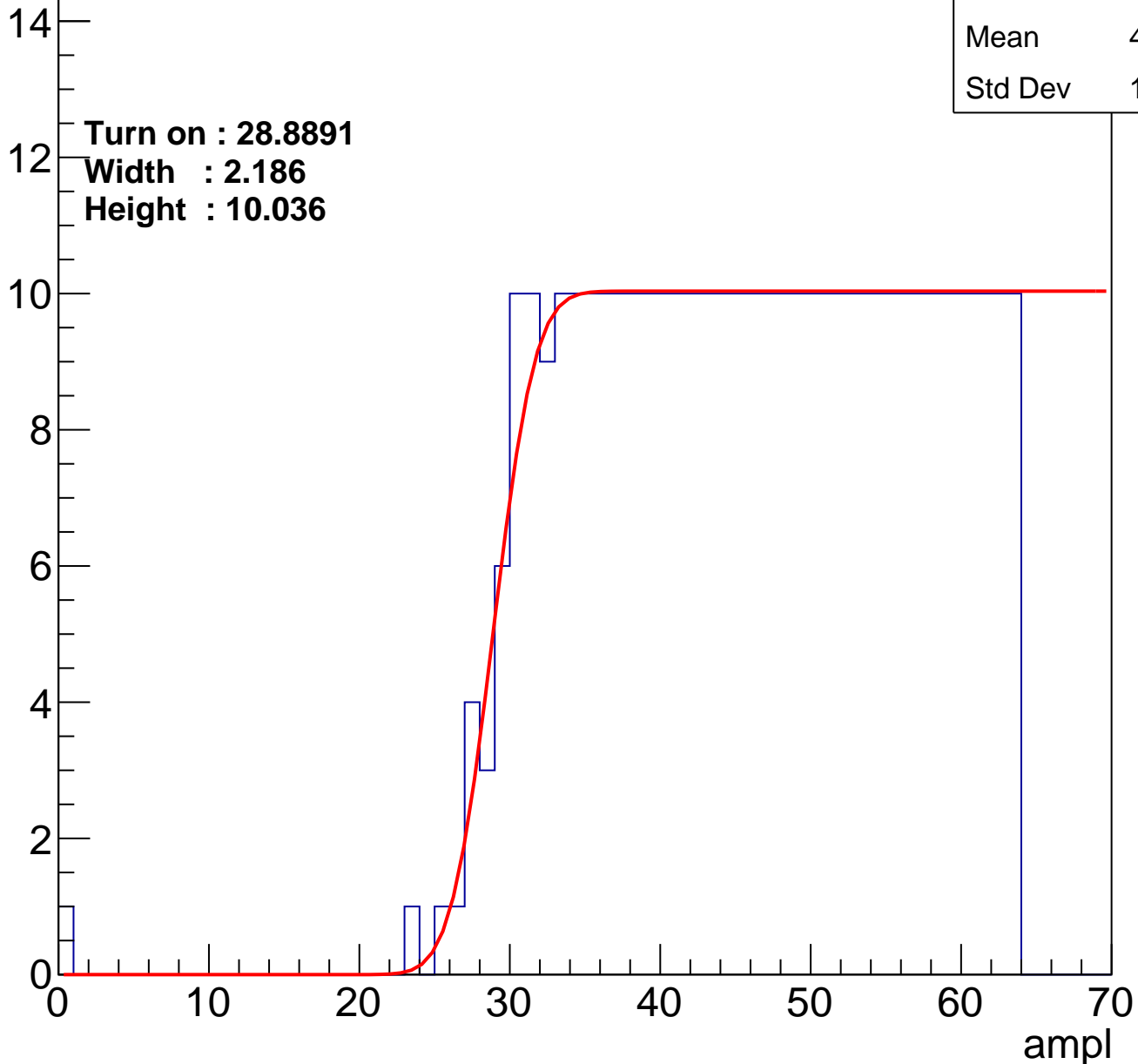
Entries	356
Mean	45.56
Std Dev	10.62

Turn on : 28.8891

Width : 2.186

Height : 10.036

Entry



# B1L100S, U24-ch24

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	363
Mean	45.11
Std Dev	11.07

**Turn on : 28.1923**

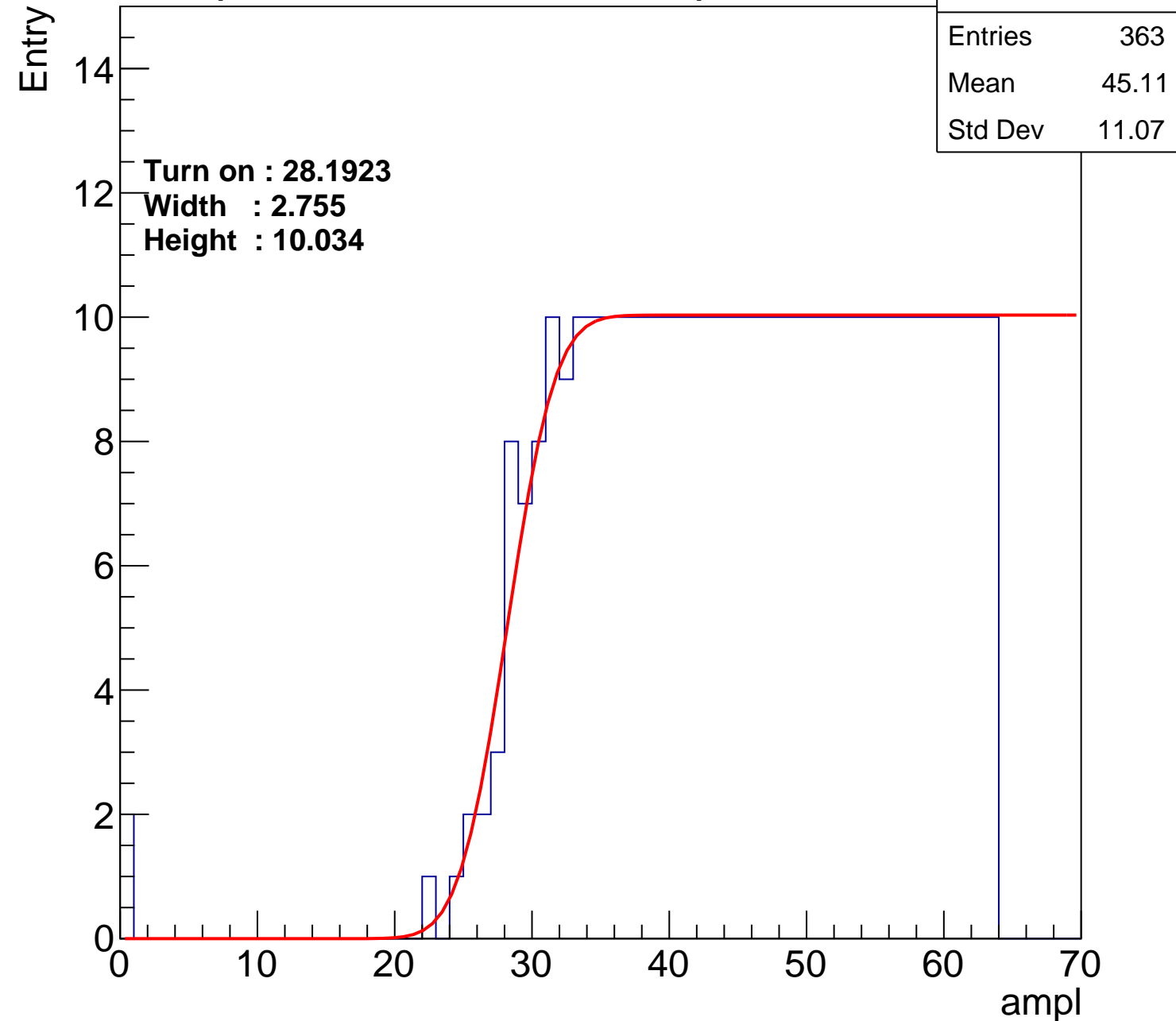
**Width : 2.755**

**Height : 10.034**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch25

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	365
Mean	45
Std Dev	11.13

Turn on : 27.6117

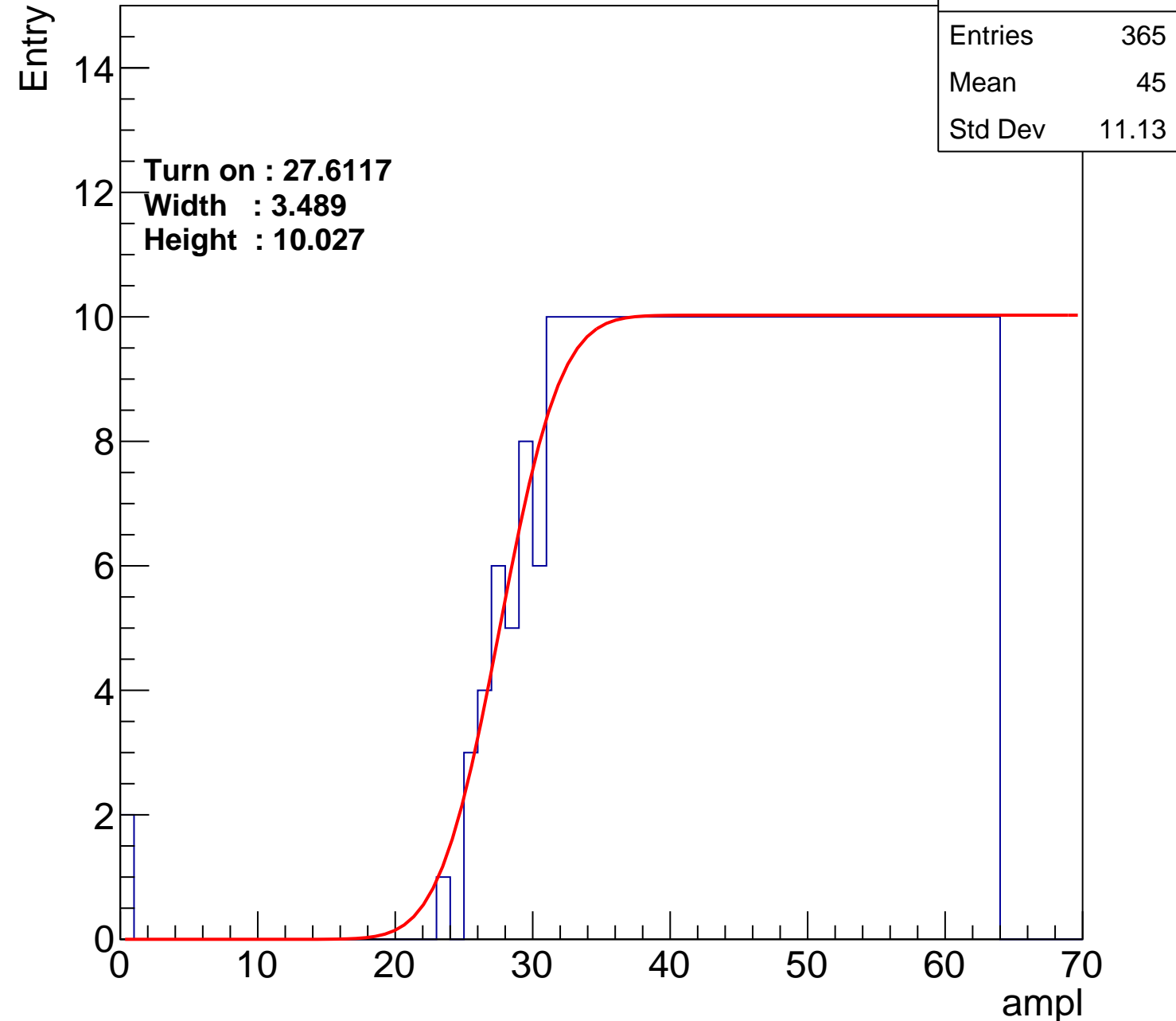
Width : 3.489

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch26

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	365
Mean	45.02
Std Dev	11.09

**Turn on : 27.9145**

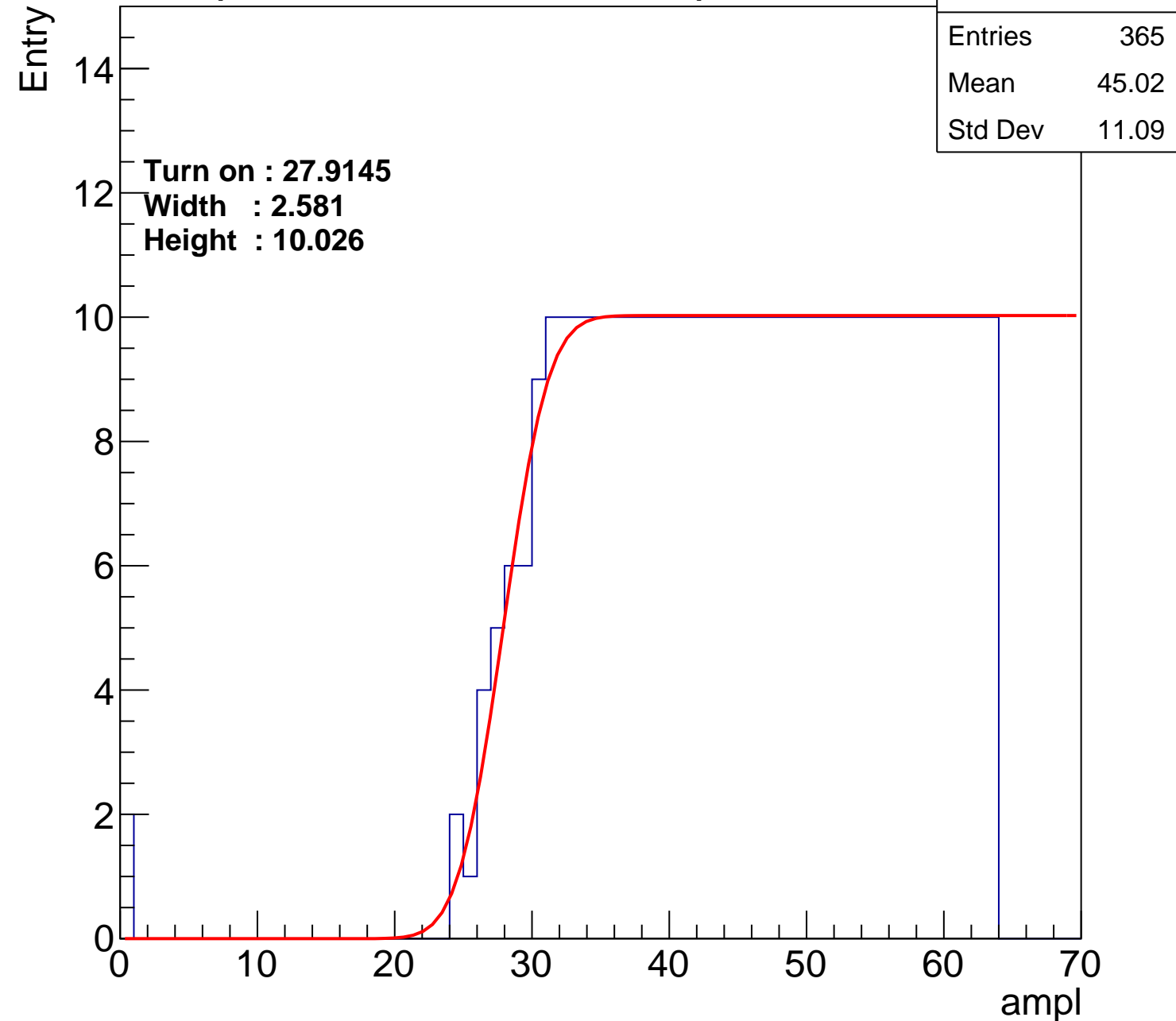
**Width : 2.581**

**Height : 10.026**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch27

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.05
Std Dev	11.31

Turn on : 28.2656

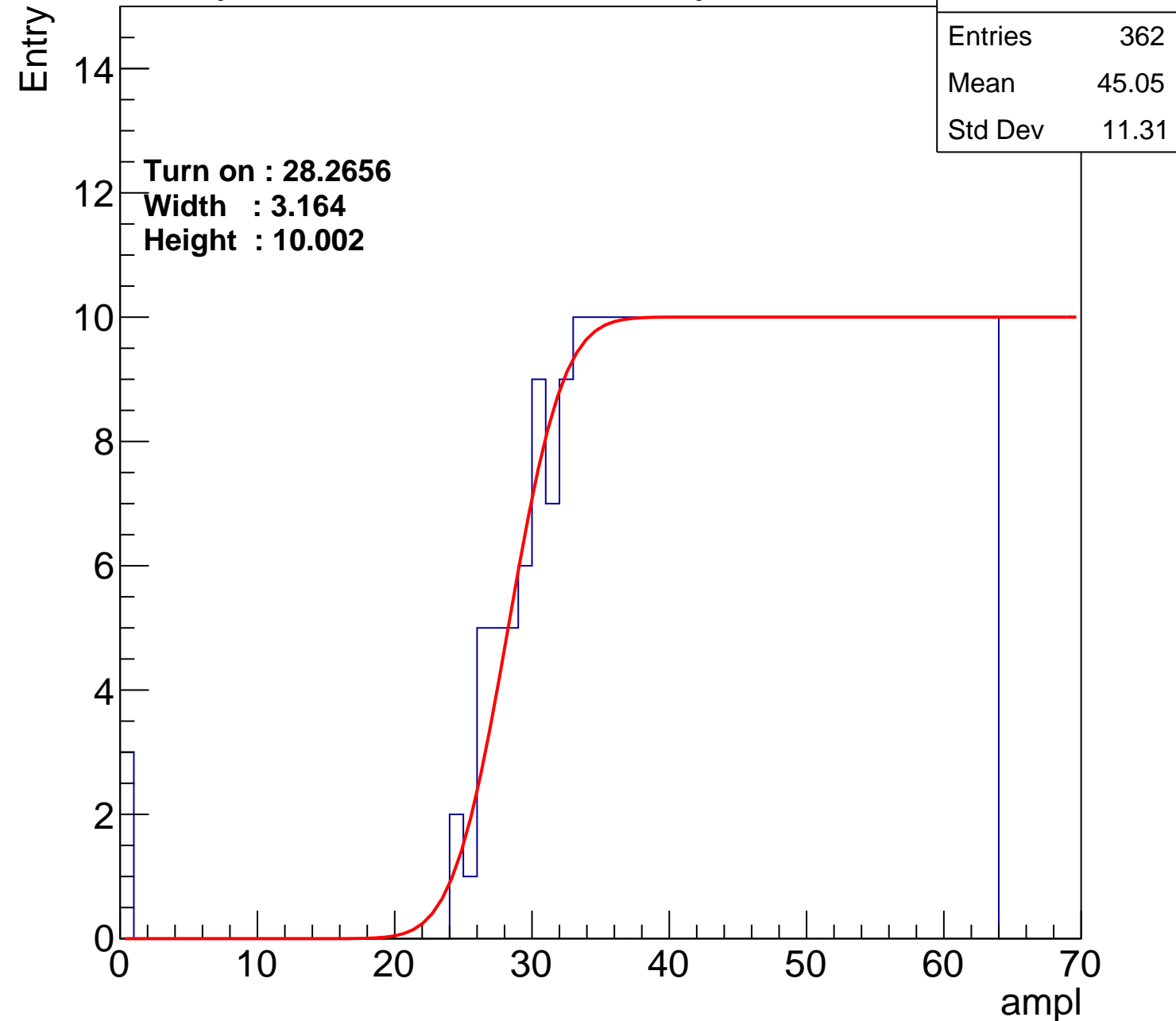
Width : 3.164

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch28

calib\_packv5\_042523\_0143.root, FC#4, port A2

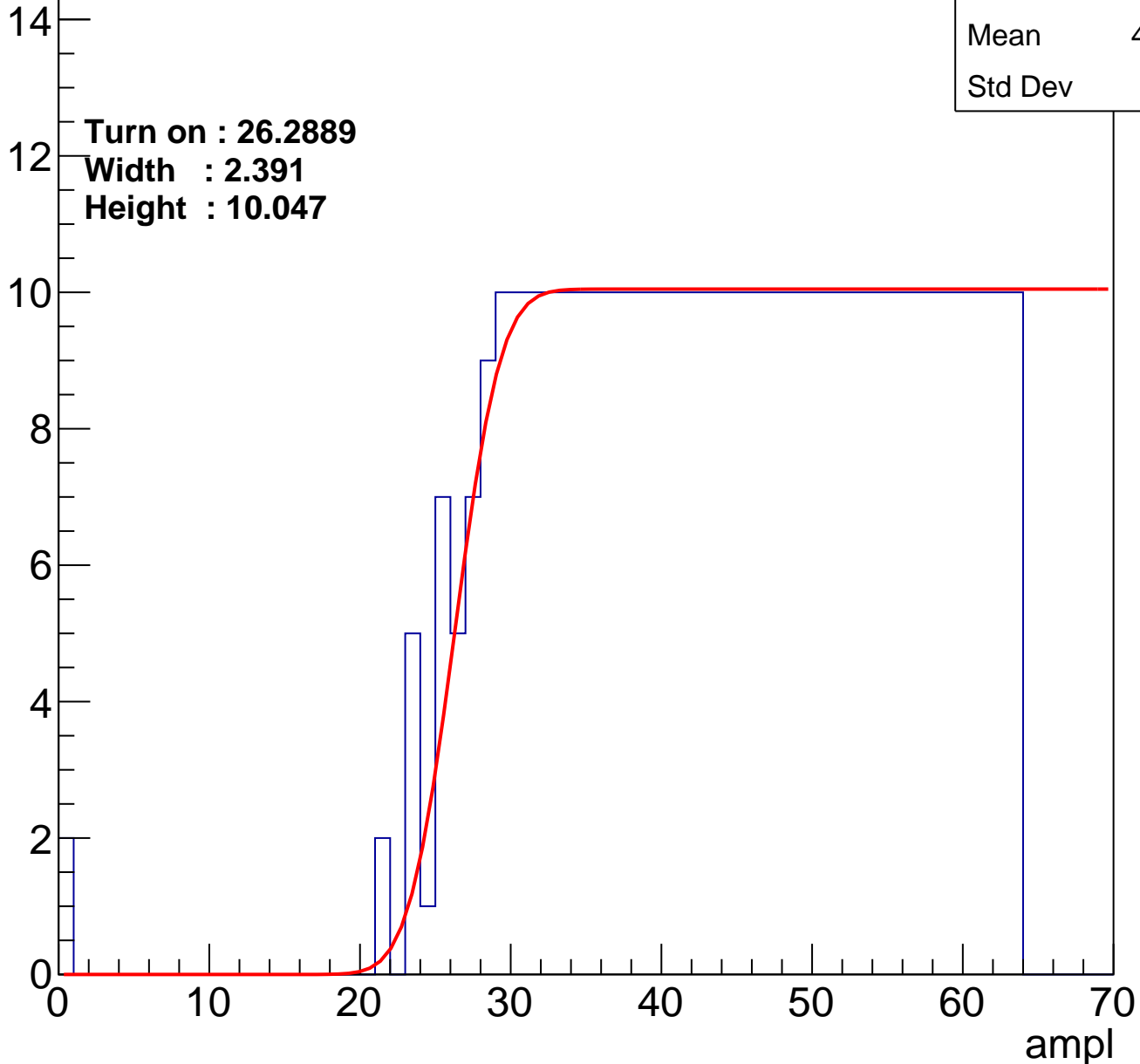
Entries	388
Mean	43.88
Std Dev	11.7

**Turn on : 26.2889**

**Width : 2.391**

**Height : 10.047**

Entry



# B1L100S, U24-ch29

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 27.9112

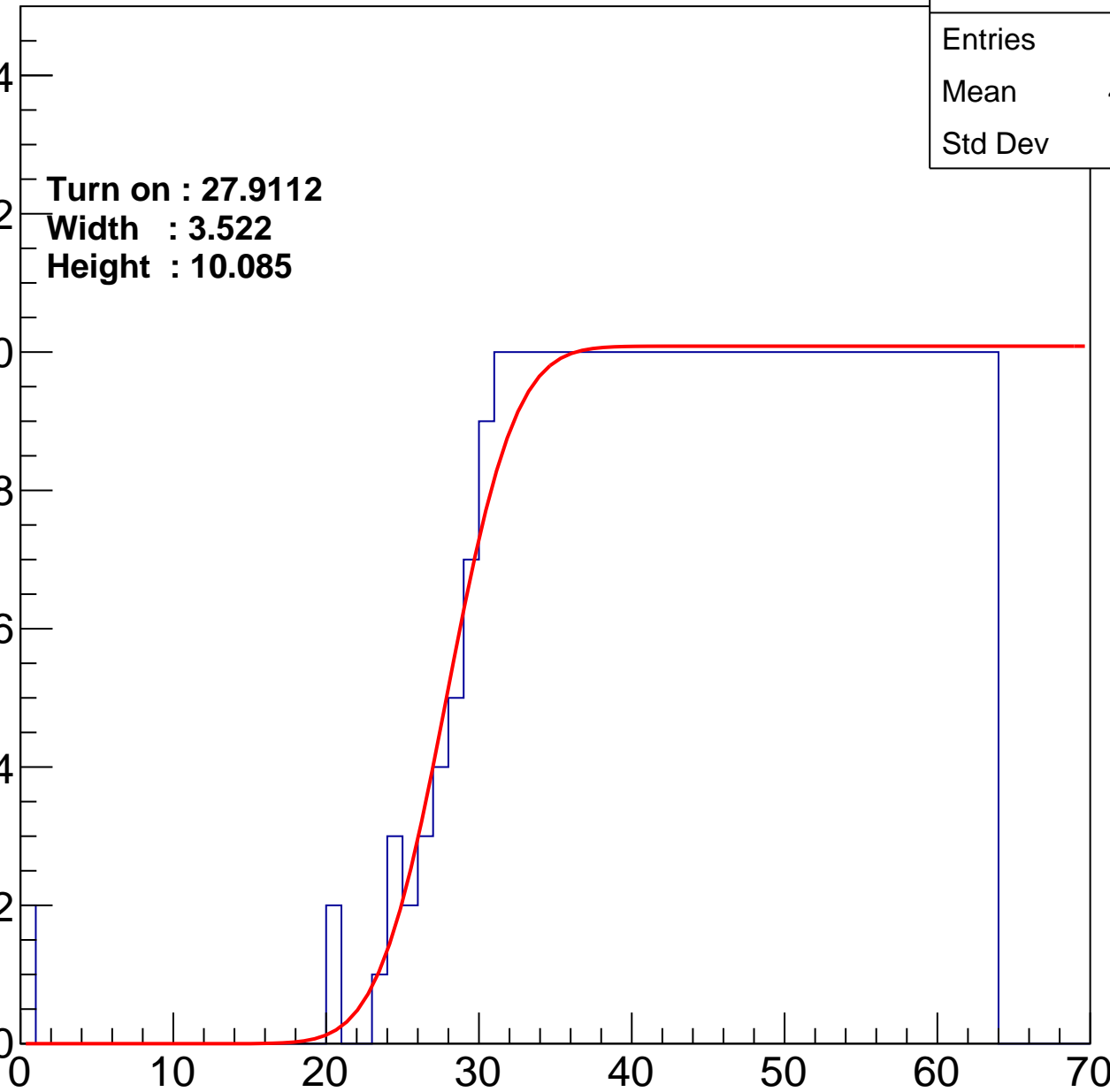
Width : 3.522

Height : 10.085

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch30

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.69
Std Dev	11.43

Turn on : 27.4672

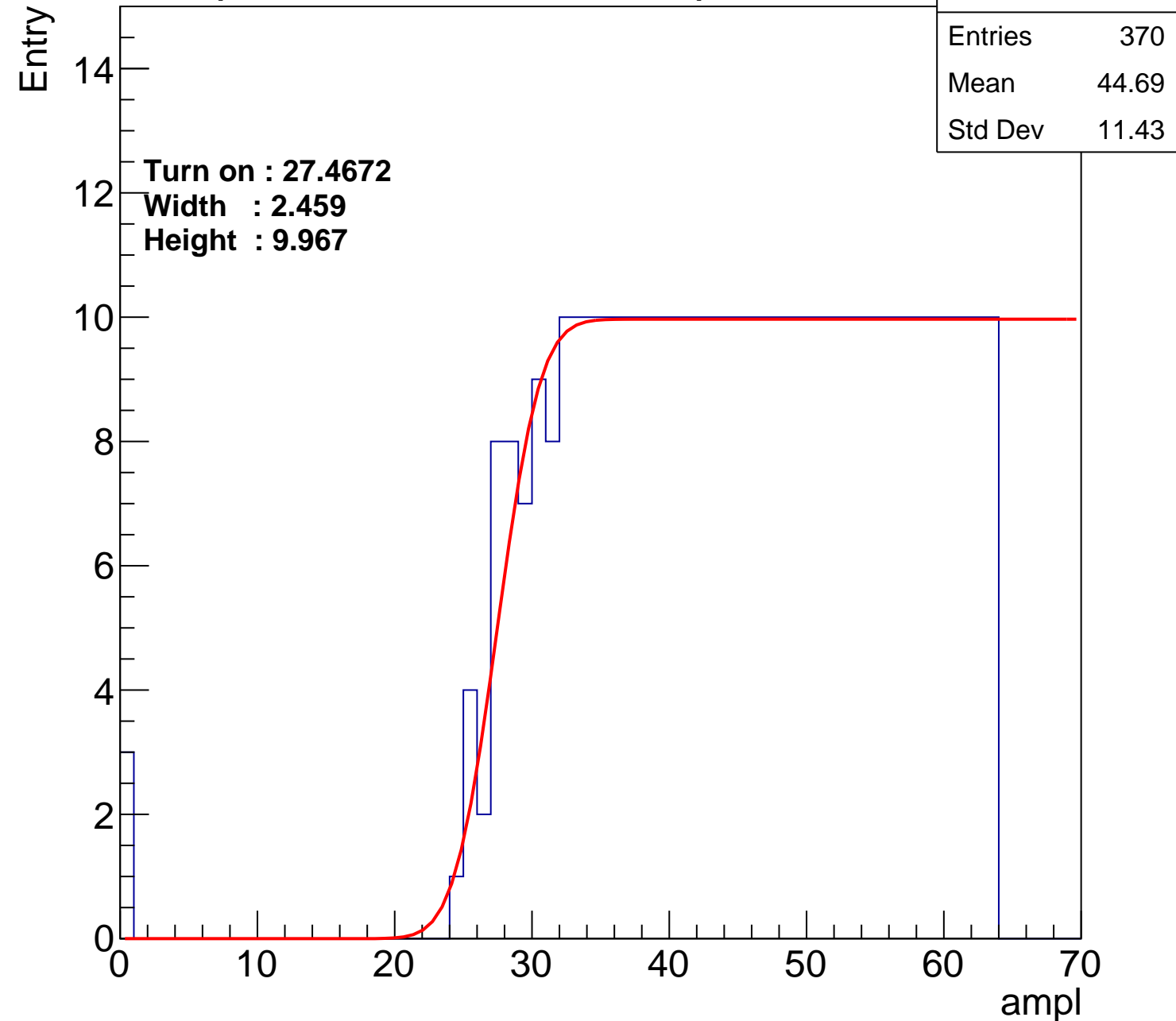
Width : 2.459

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch31

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.85
Std Dev	11.07

Turn on : 27.5592

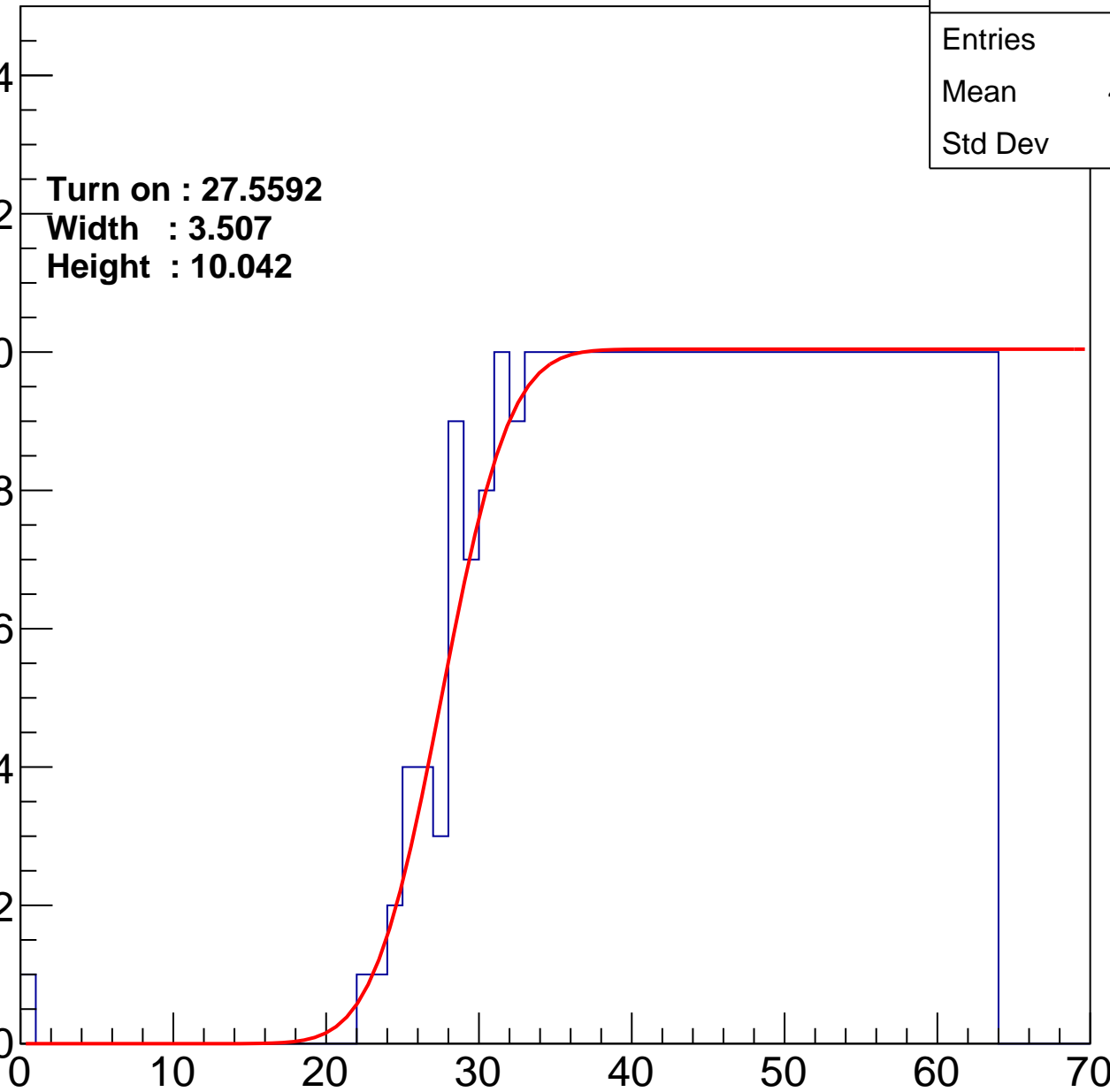
Width : 3.507

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch32

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.37
Std Dev	11.77

Turn on : 27.5681

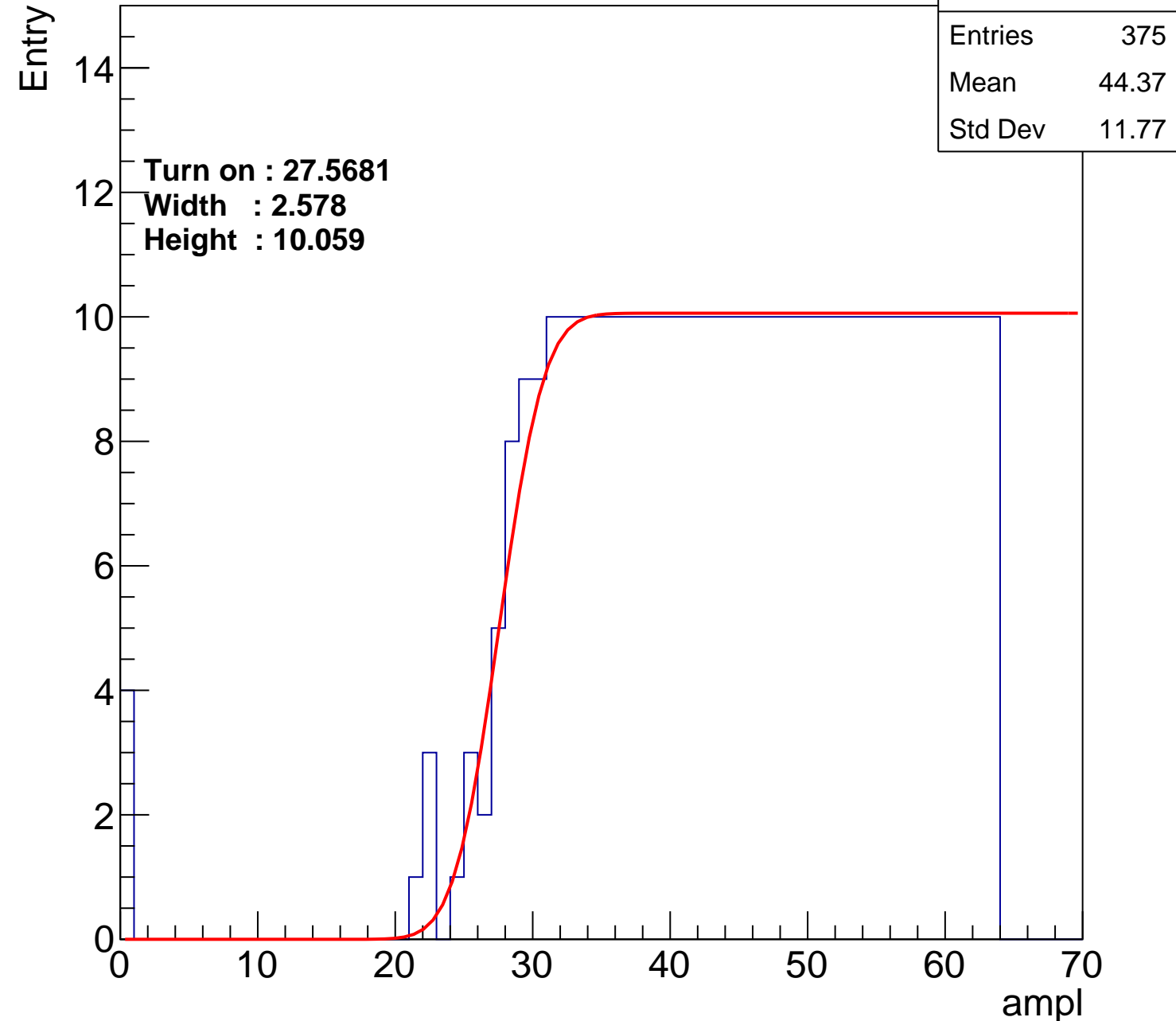
Width : 2.578

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch33

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.13
Std Dev	11.77

**Turn on : 27.0480**

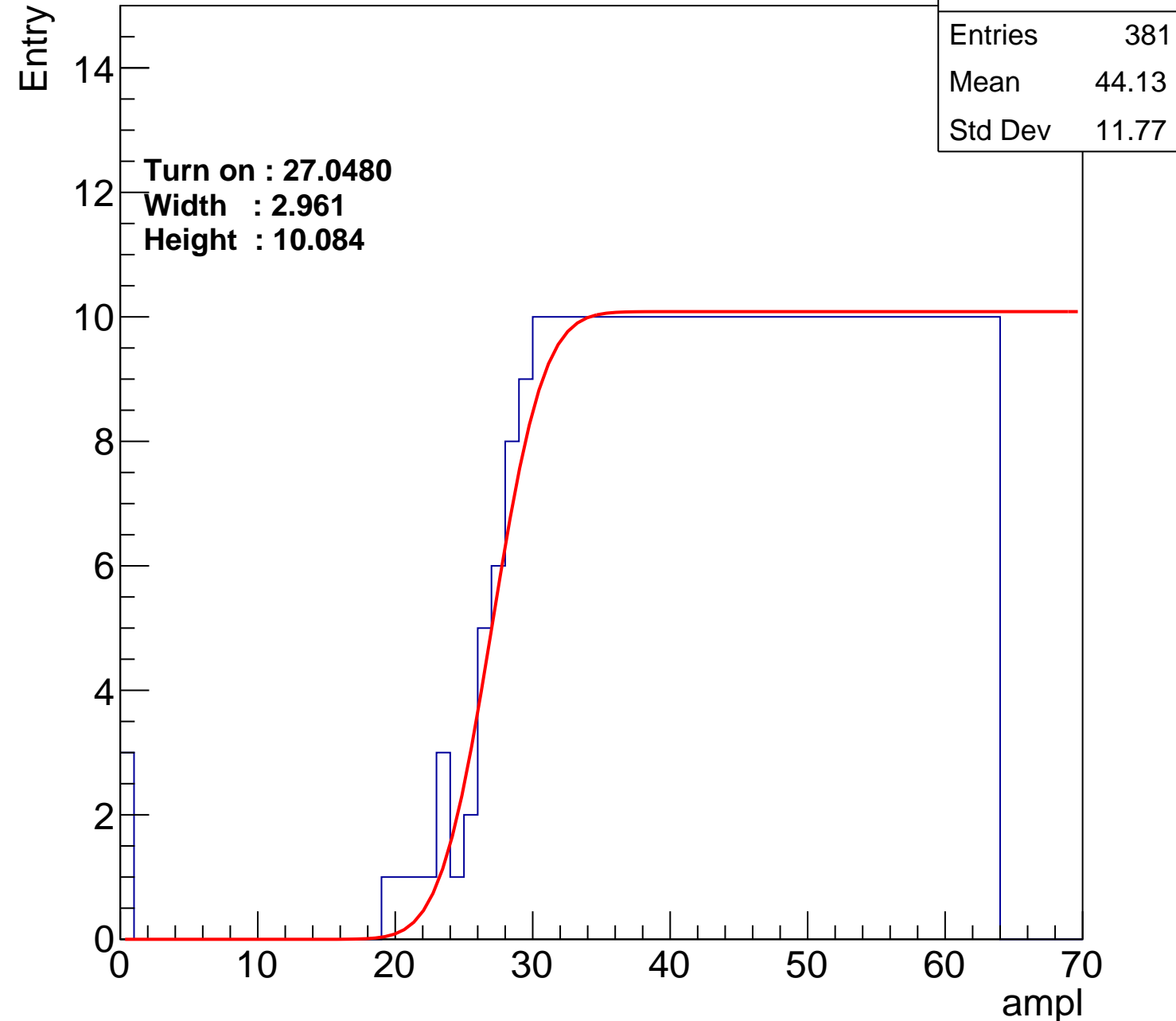
**Width : 2.961**

**Height : 10.084**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch34

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	351
Mean	45.62
Std Dev	10.99

Turn on : 29.1105

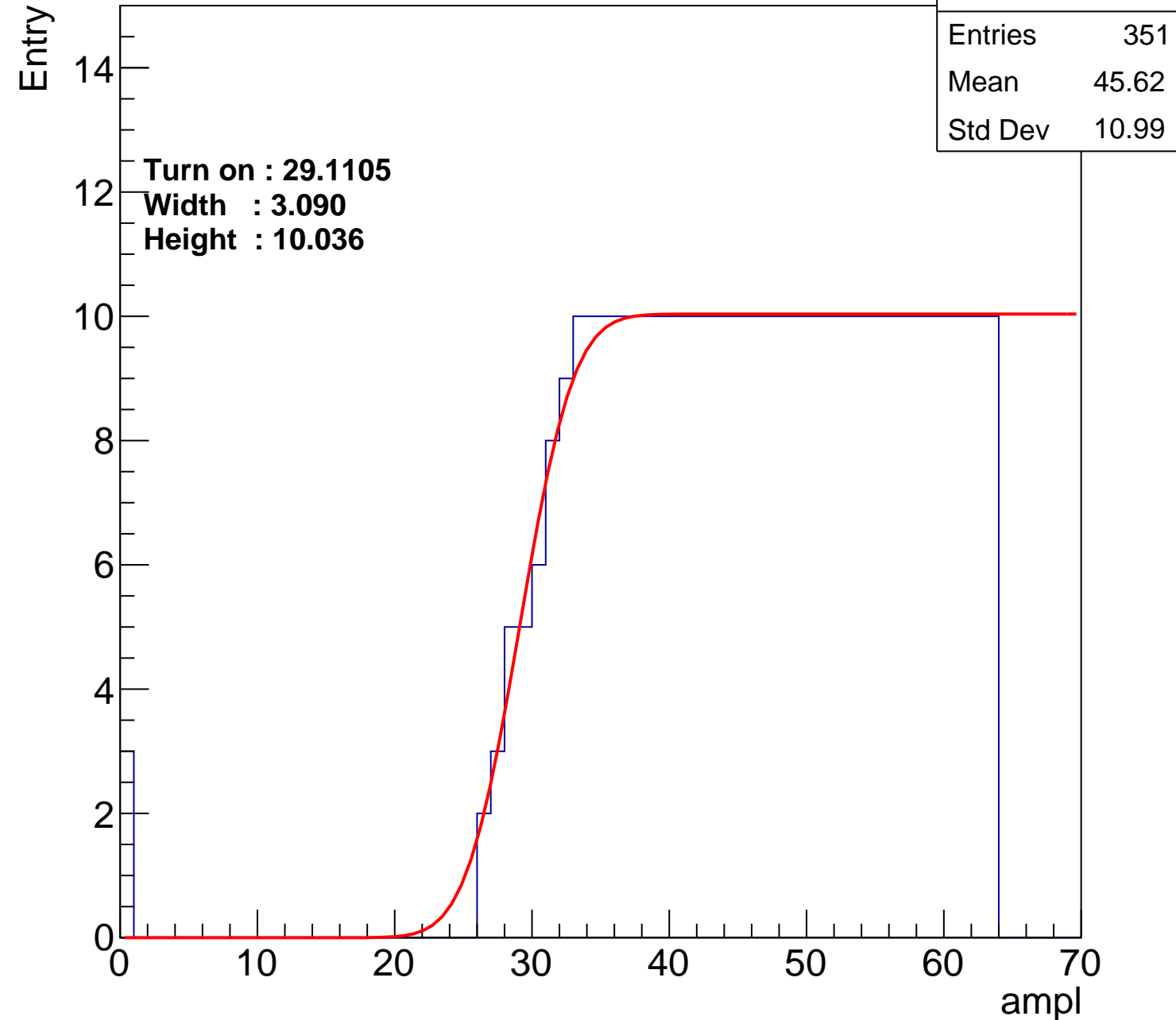
Width : 3.090

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch35

calib\_packv5\_042523\_0143.root, FC#4, port A2

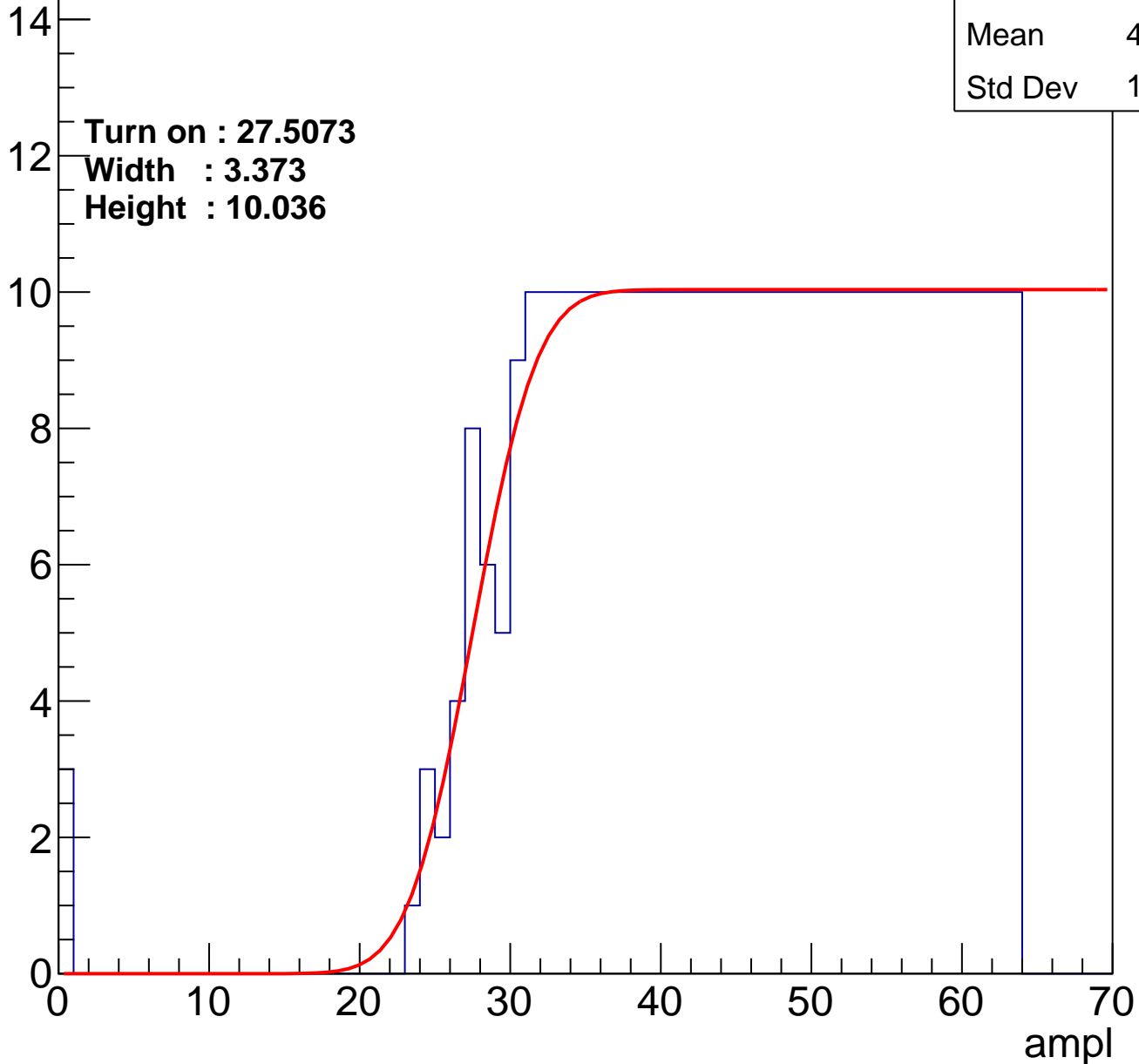
Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.5073

Width : 3.373

Height : 10.036

Entry



# B1L100S, U24-ch36

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	367
Mean	44.85
Std Dev	11.27

Turn on : 28.5813

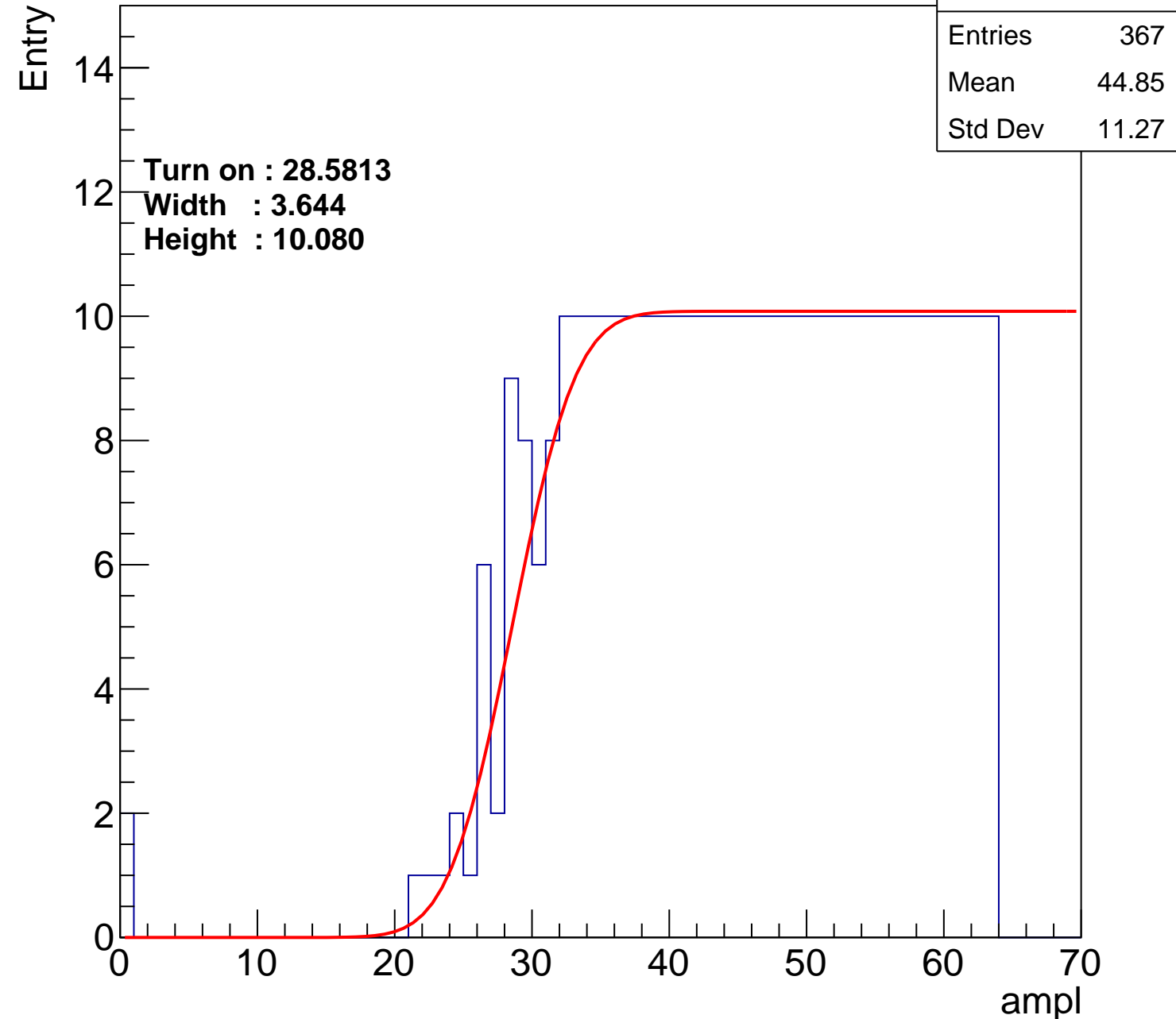
Width : 3.644

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch37

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	365
Mean	44.95
Std Dev	11.3

Turn on : 27.2466

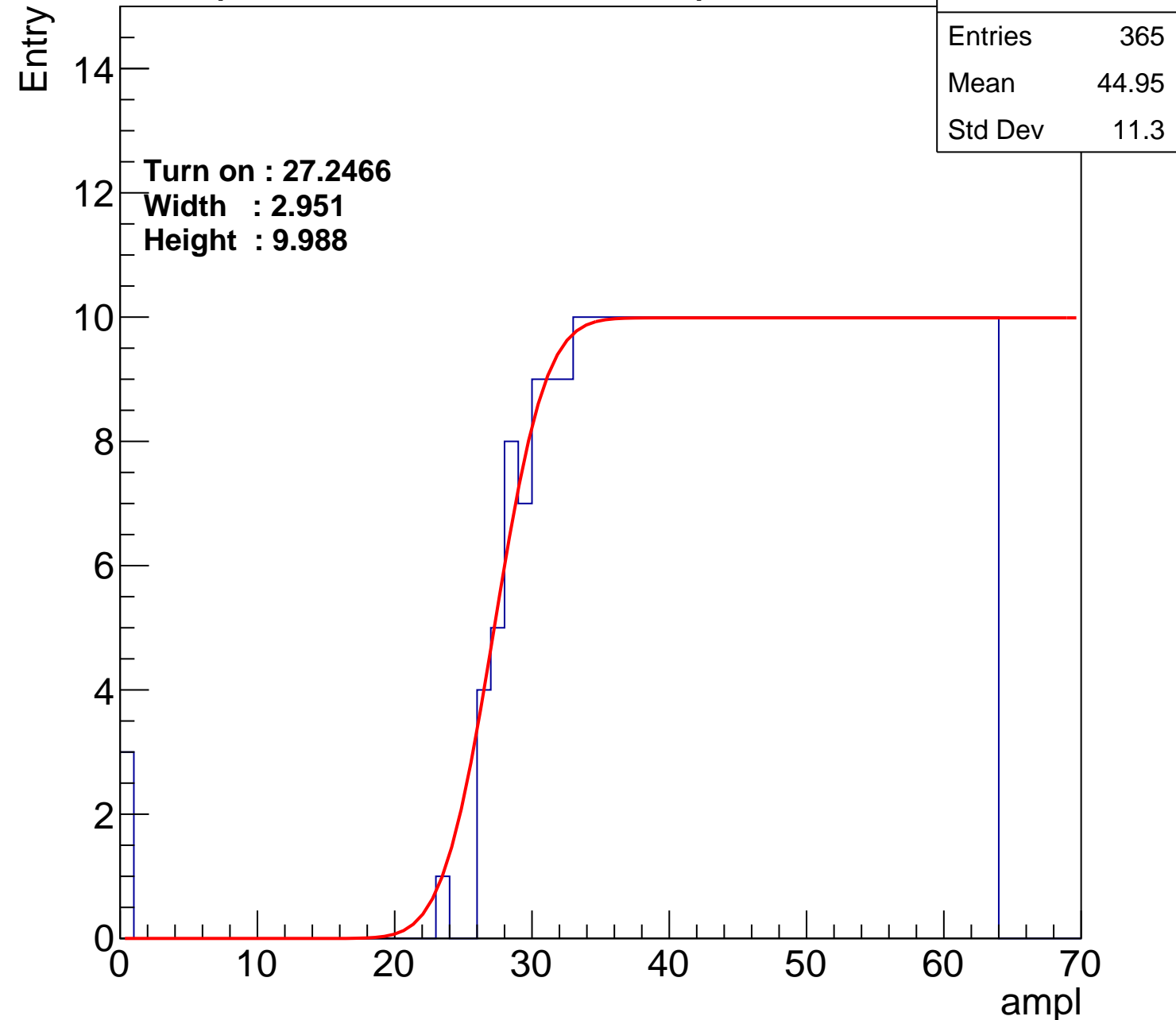
Width : 2.951

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch38

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	43.96
Std Dev	12.05

**Turn on : 26.9641**

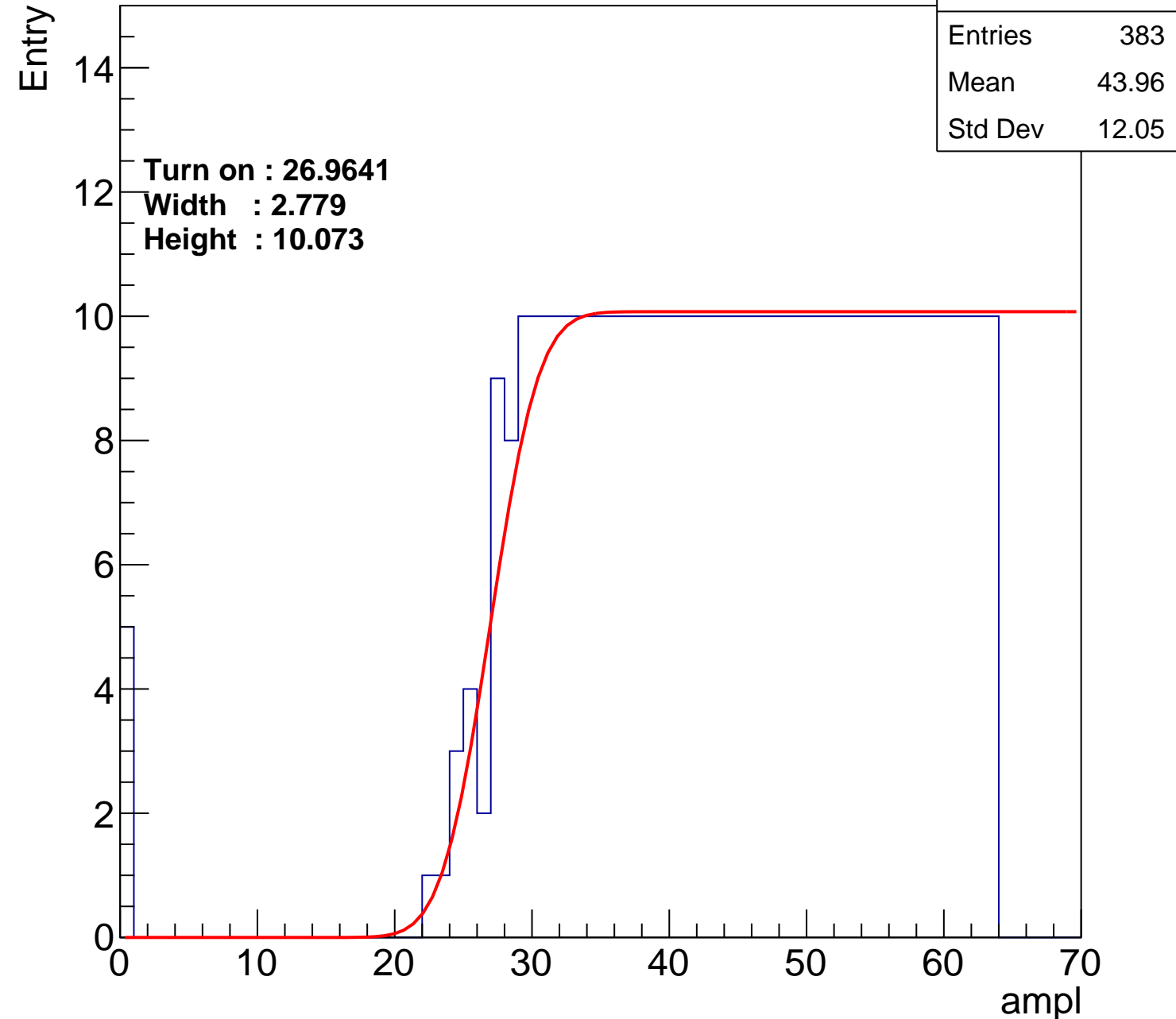
**Width : 2.779**

**Height : 10.073**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch39

calib\_packv5\_042523\_0143.root, FC#4, port A2

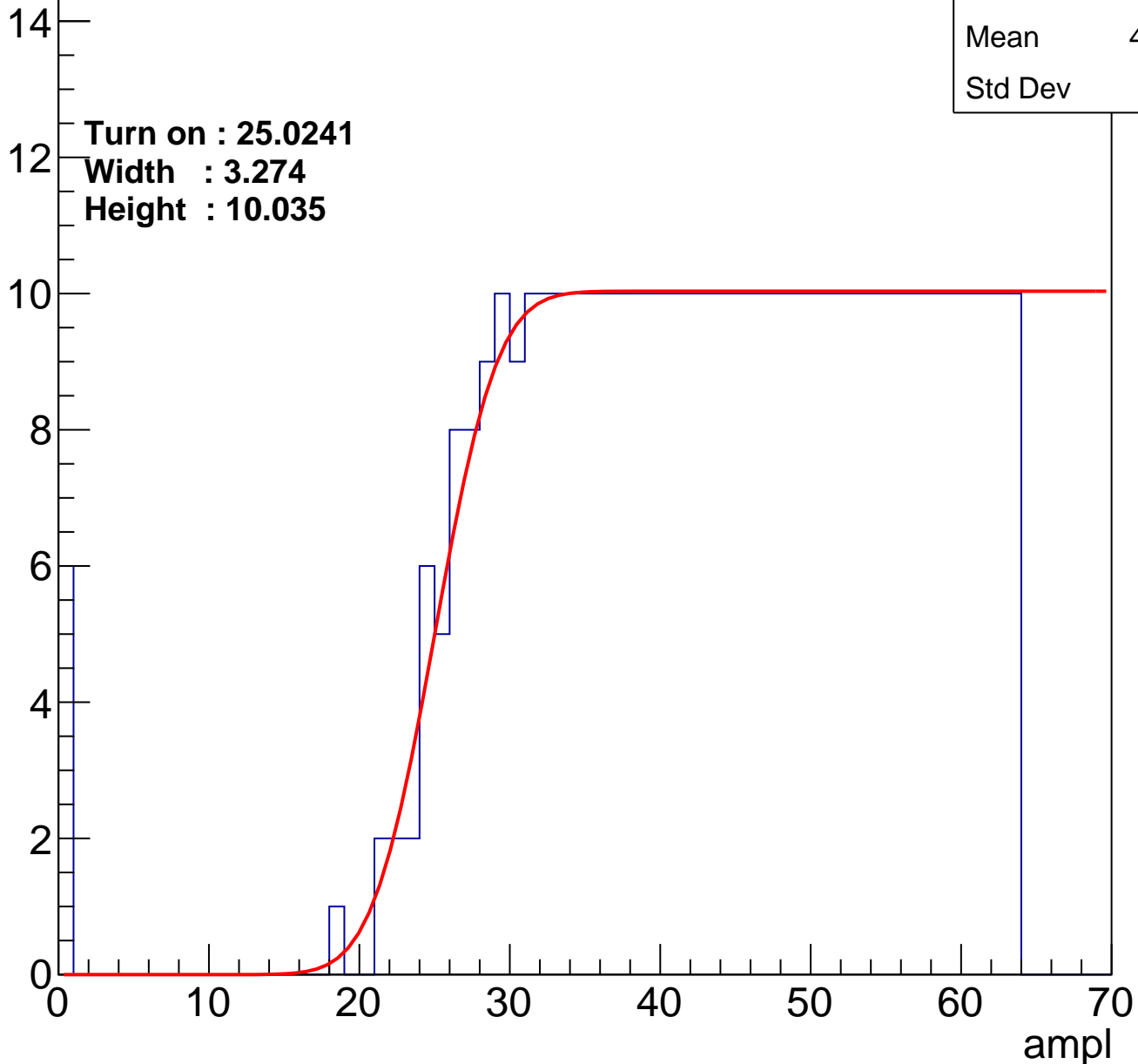
Entries	398
Mean	43.13
Std Dev	12.6

Turn on : 25.0241

Width : 3.274

Height : 10.035

Entry



# B1L100S, U24-ch40

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.77
Std Dev	11.3

Turn on : 27.2392

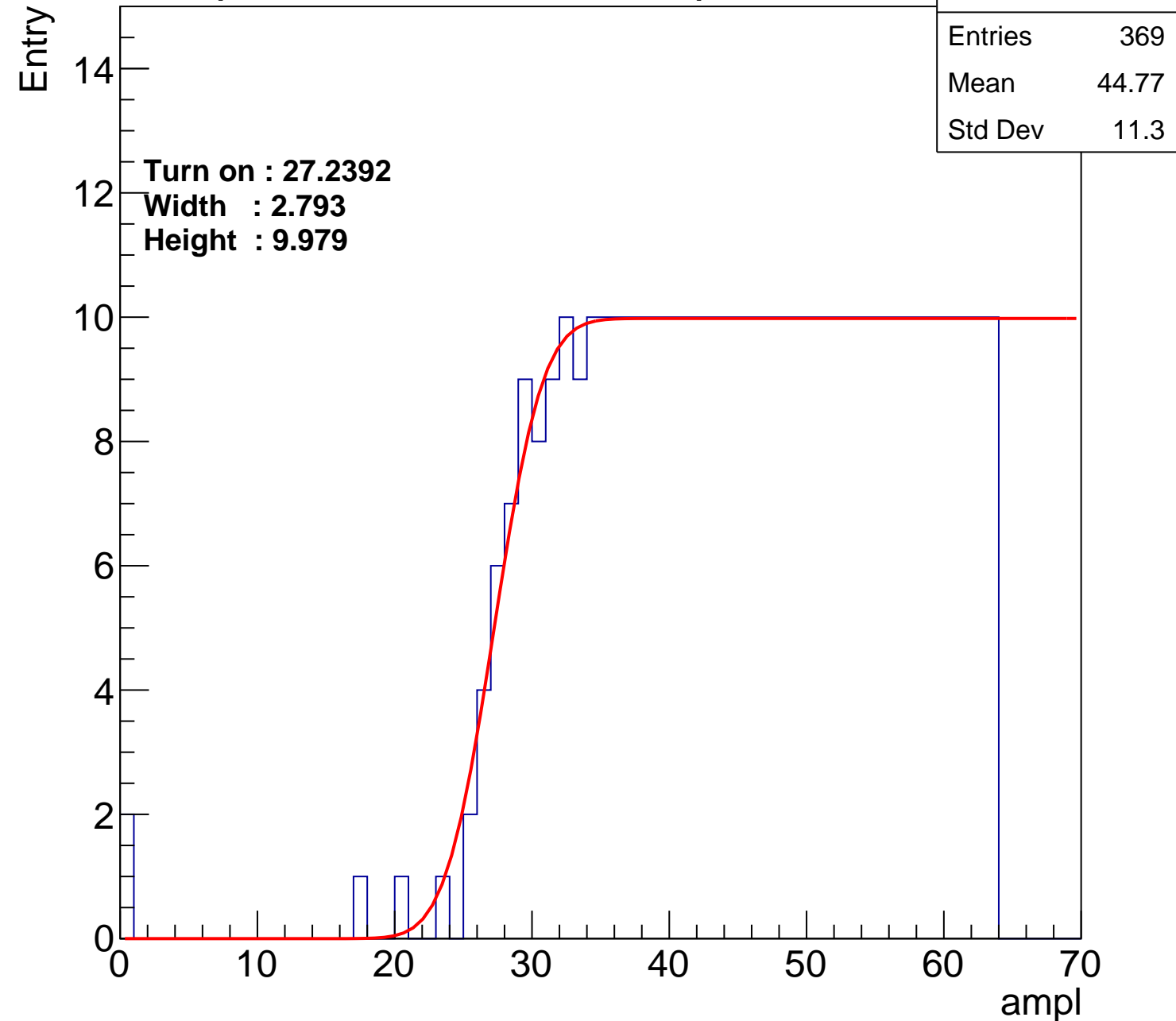
Width : 2.793

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch41

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.1960

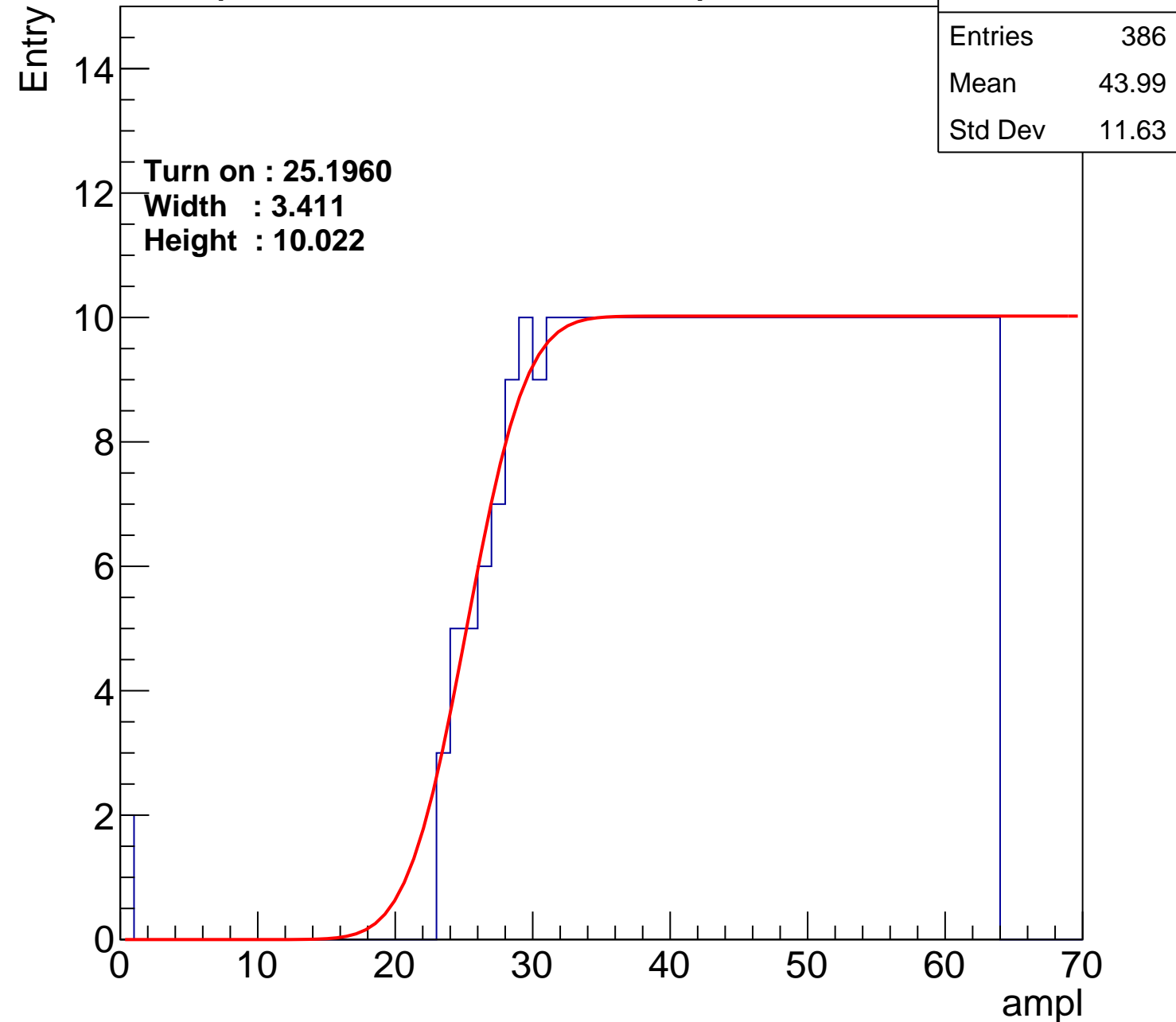
Width : 3.411

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch42

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	44.02
Std Dev	11.65

Turn on : 26.3328

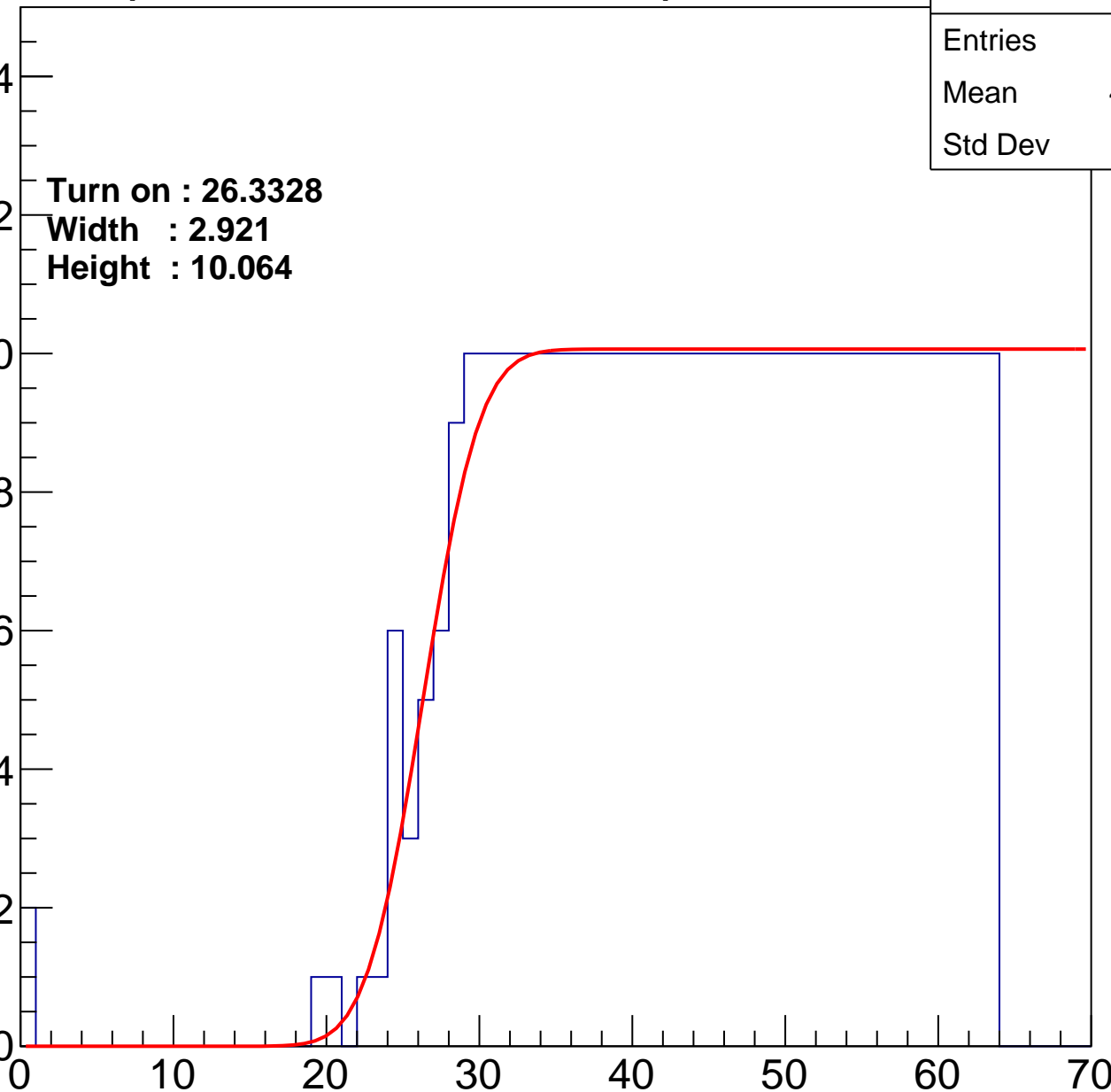
Width : 2.921

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch43

calib\_packv5\_042523\_0143.root, FC#4, port A2

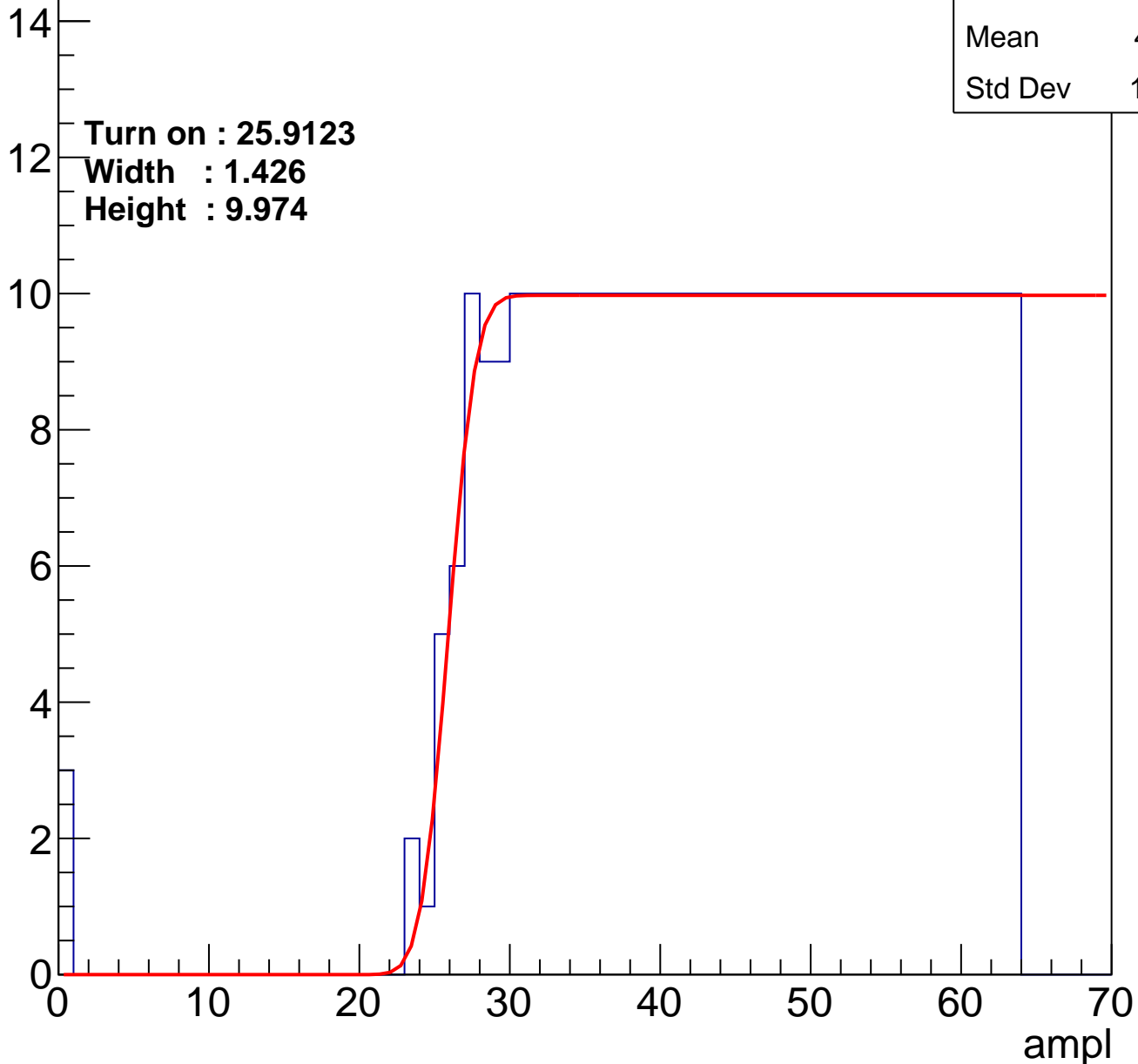
Entries	385
Mean	44.01
Std Dev	11.72

**Turn on : 25.9123**

**Width : 1.426**

**Height : 9.974**

Entry



# B1L100S, U24-ch44

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 27.0018

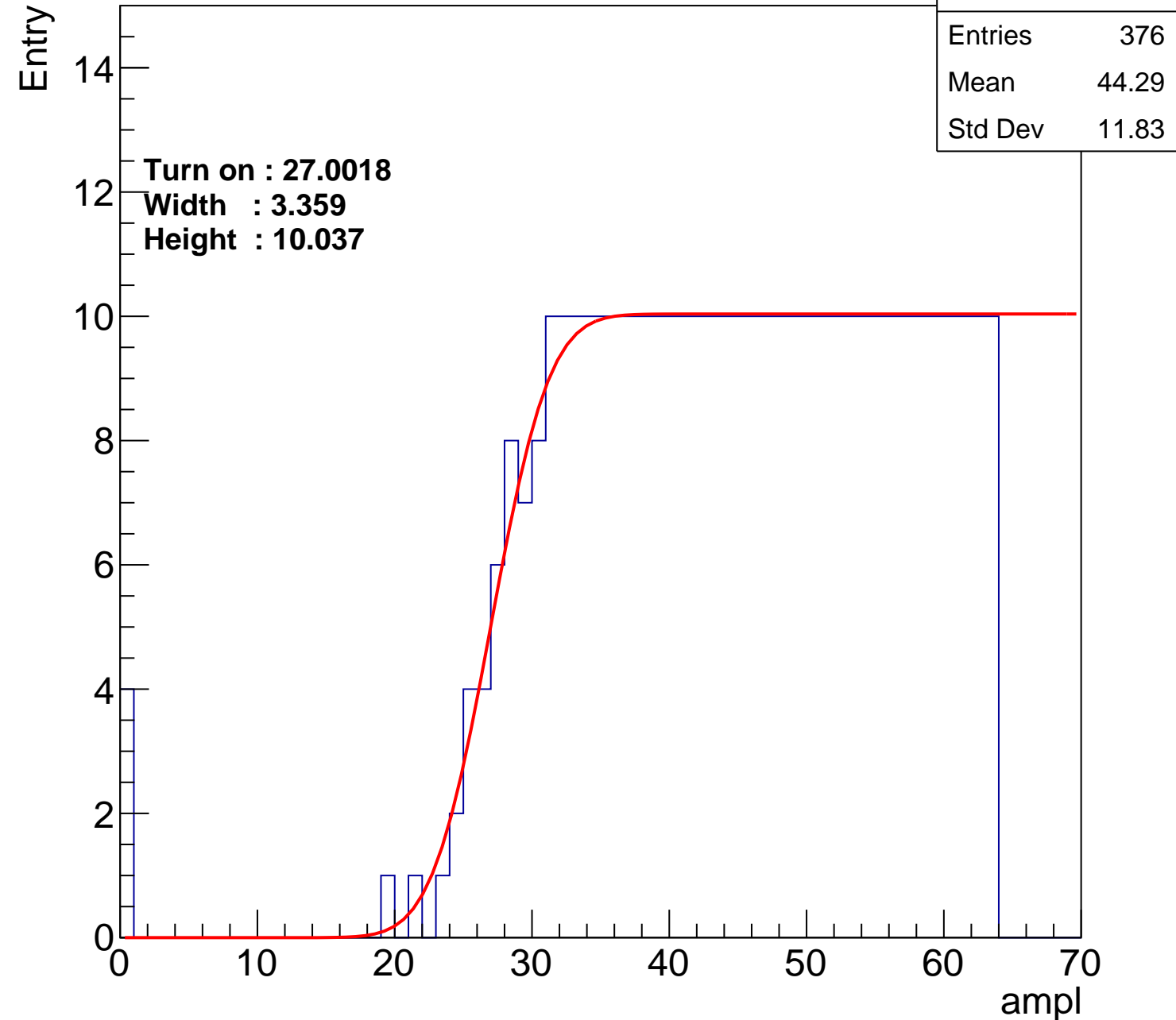
Width : 3.359

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch45

calib\_packv5\_042523\_0143.root, FC#4, port A2

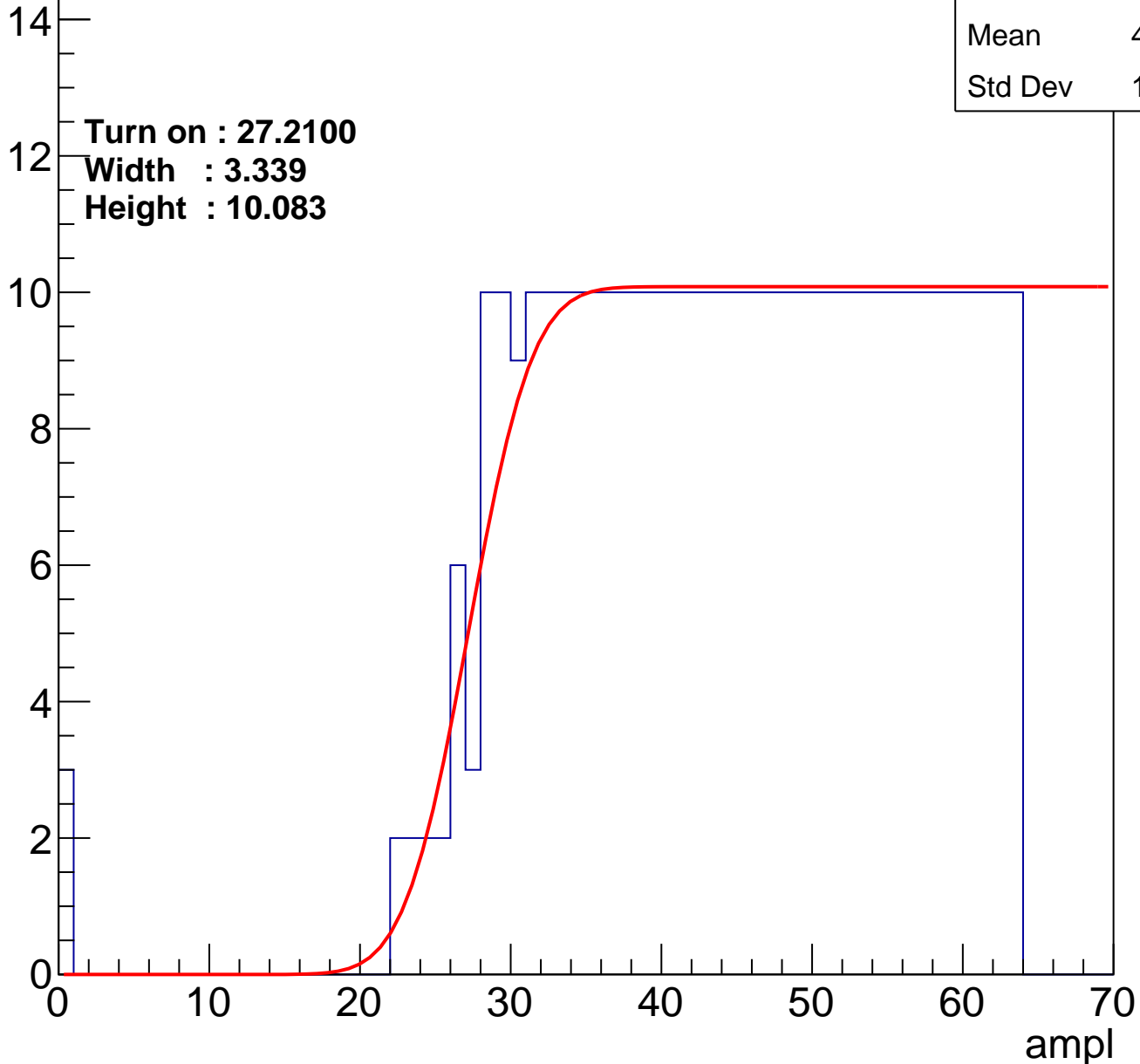
Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 27.2100

Width : 3.339

Height : 10.083

Entry



# B1L100S, U24-ch46

calib\_packv5\_042523\_0143.root, FC#4, port A2

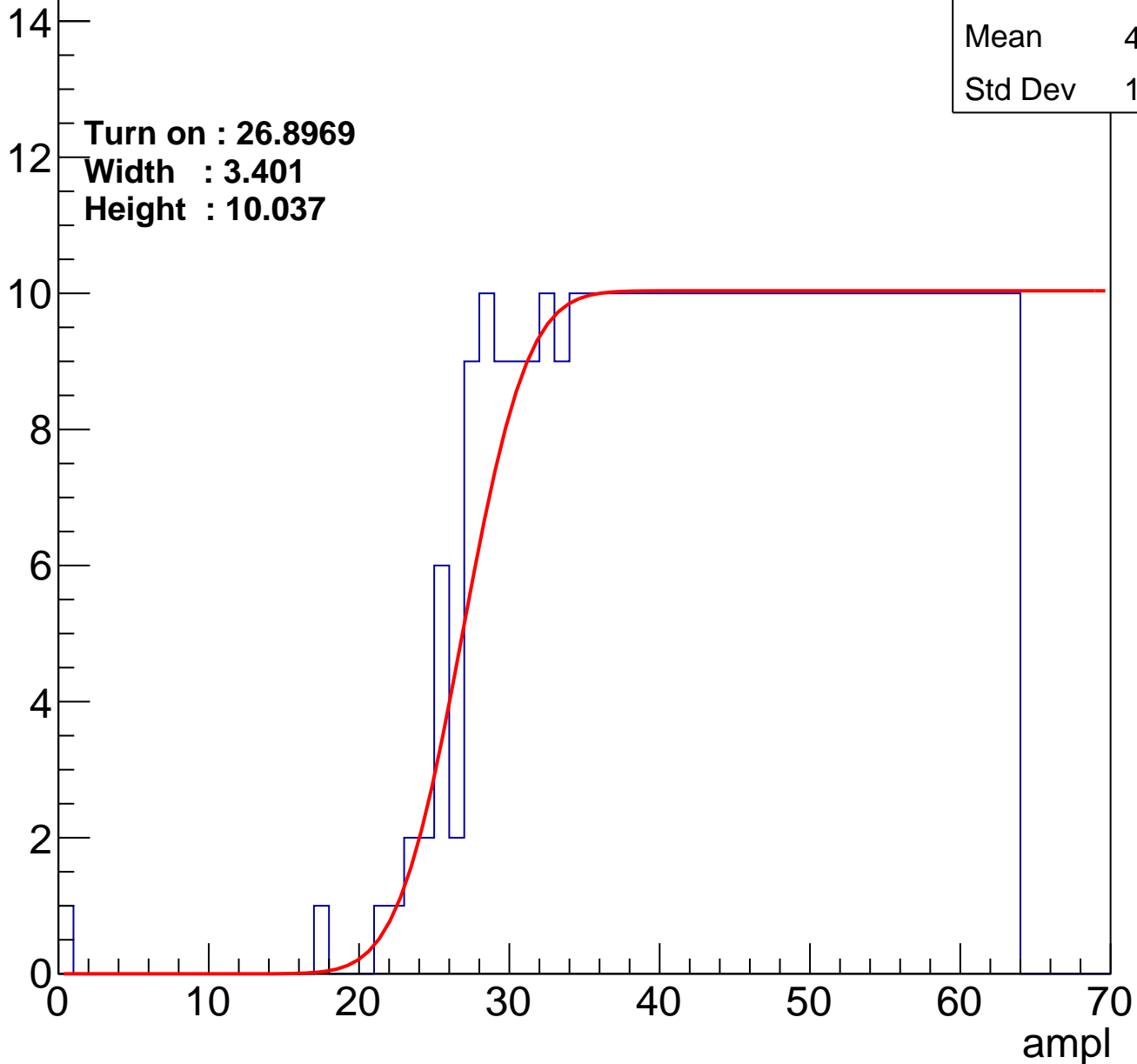
Entries	381
Mean	44.24
Std Dev	11.42

**Turn on : 26.8969**

**Width : 3.401**

**Height : 10.037**

Entry





# B1L100S, U24-ch47

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	365
Mean	44.84
Std Dev	11.56

Turn on : 28.1906

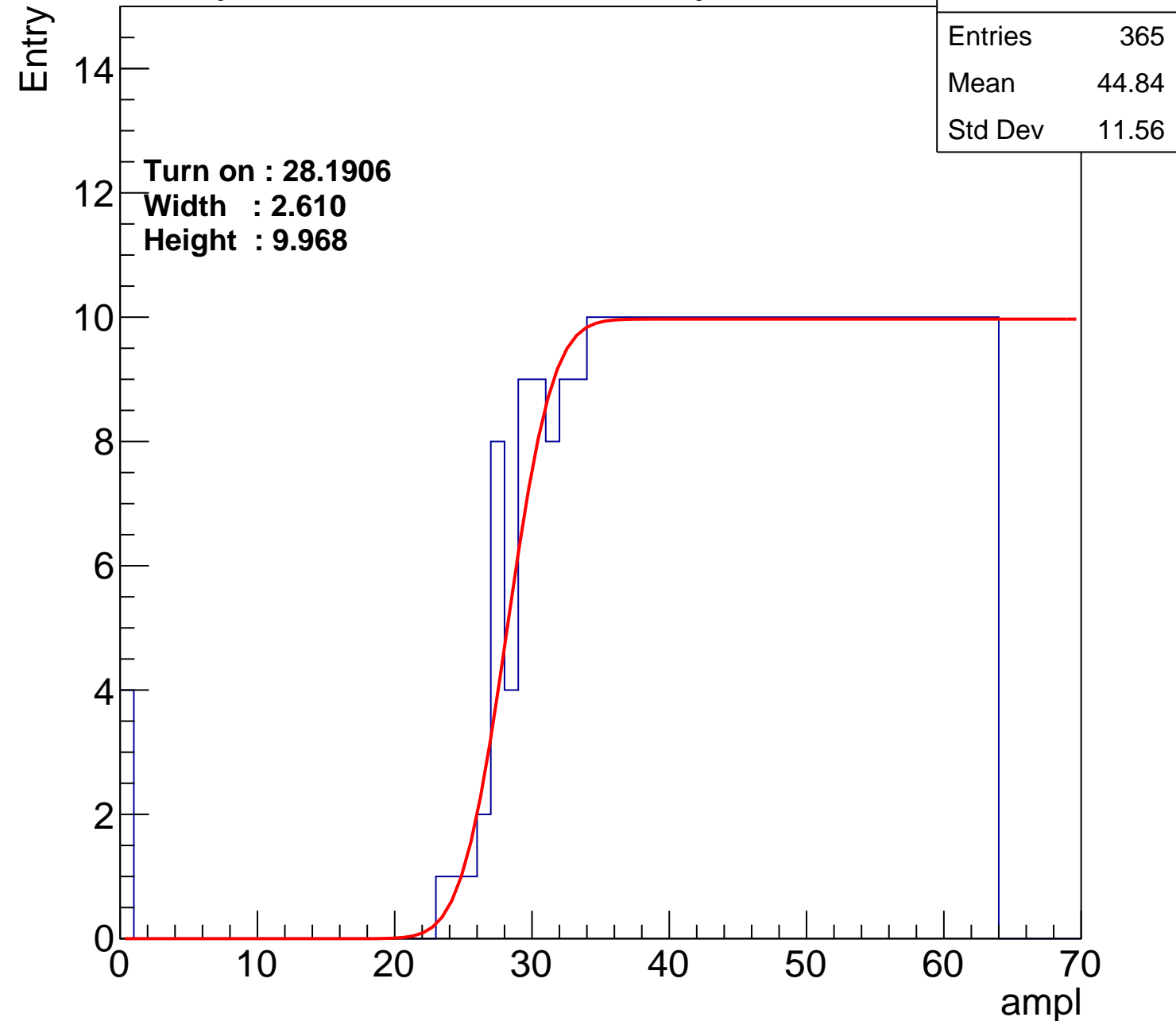
Width : 2.610

Height : 9.968

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch48

calib\_packv5\_042523\_0143.root, FC#4, port A2

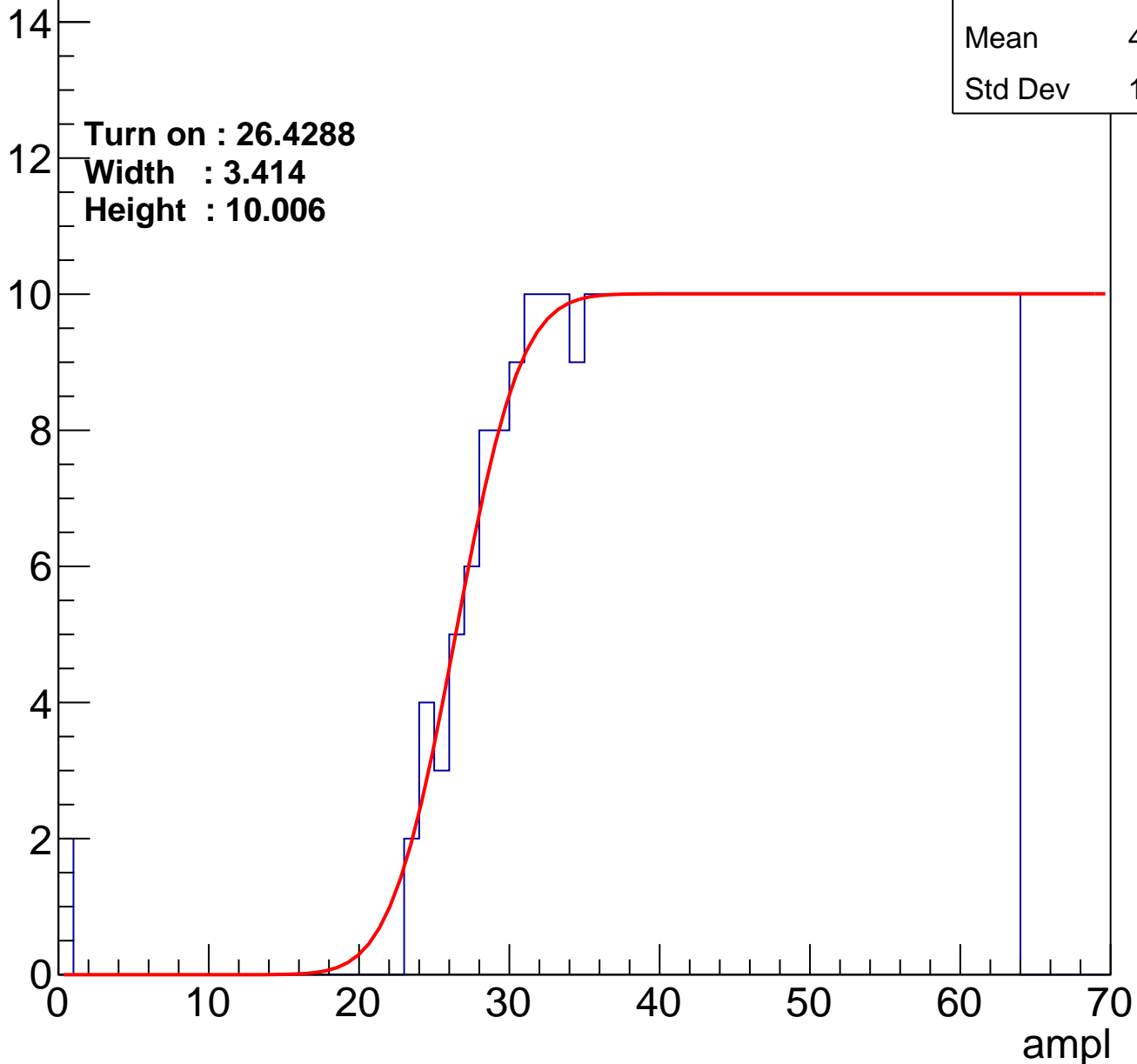
Entries	376
Mean	44.44
Std Dev	11.43

Turn on : 26.4288

Width : 3.414

Height : 10.006

Entry



# B1L100S, U24-ch49

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	351
Mean	45.61
Std Dev	11.01

**Turn on : 29.5058**

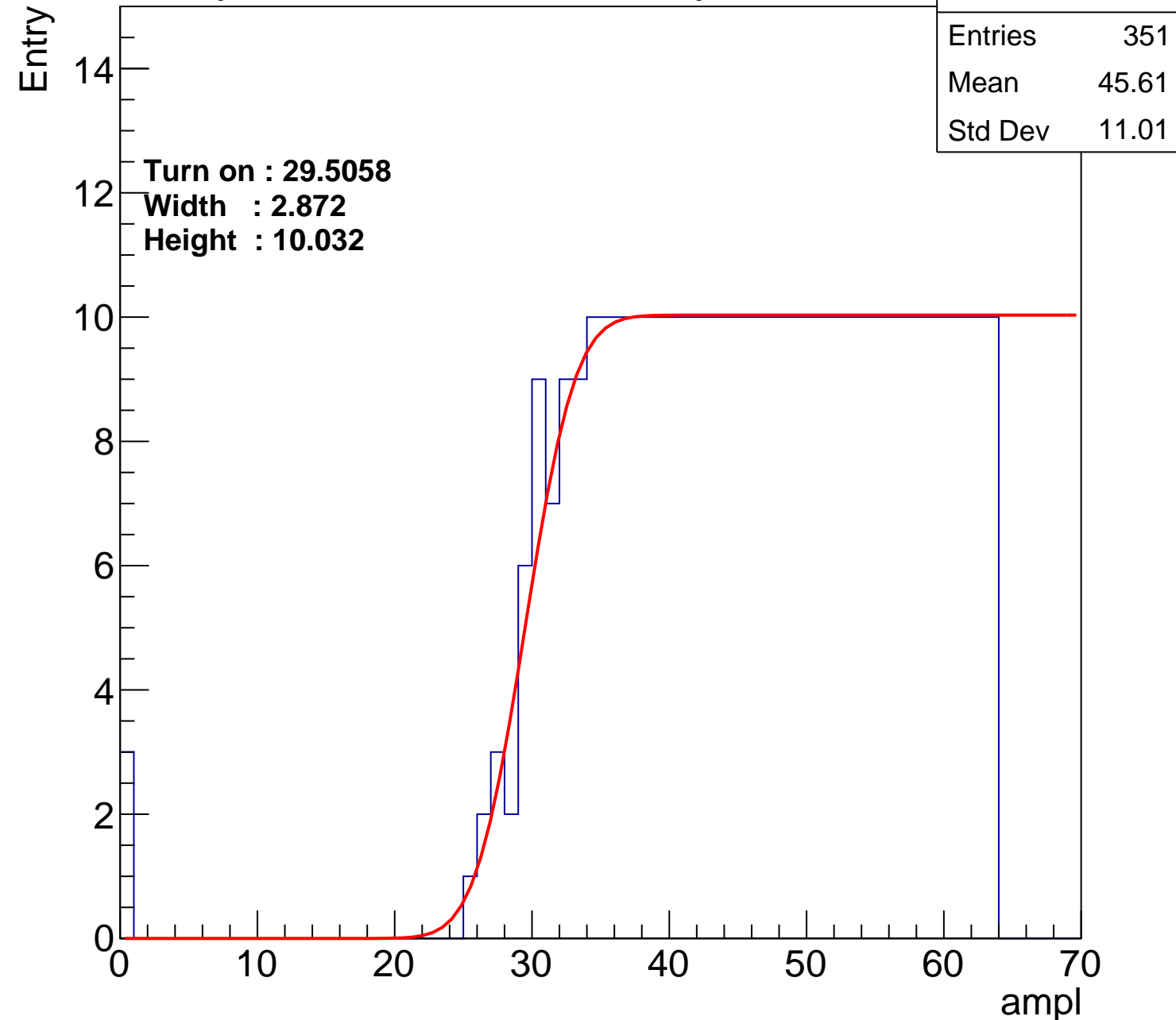
**Width : 2.872**

**Height : 10.032**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#4, port A2**

**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Turn on : 24.8221  
Width : 3.239  
Height : 10.034



# B1L100S, U24-ch51

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	361
Mean	45.29
Std Dev	10.8

Turn on : 28.4675

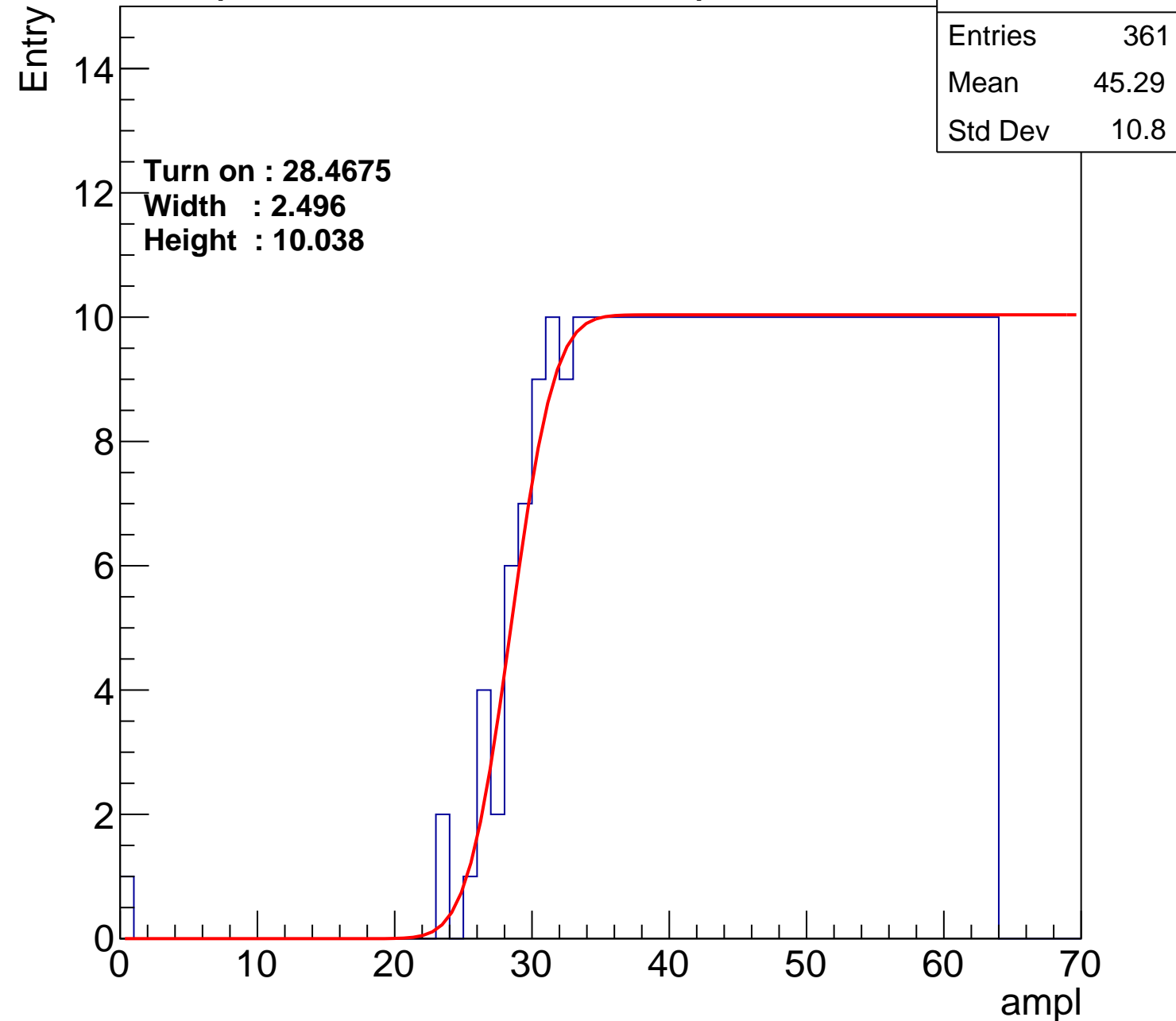
Width : 2.496

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch52

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.07
Std Dev	11.27

**Turn on : 28.3293**

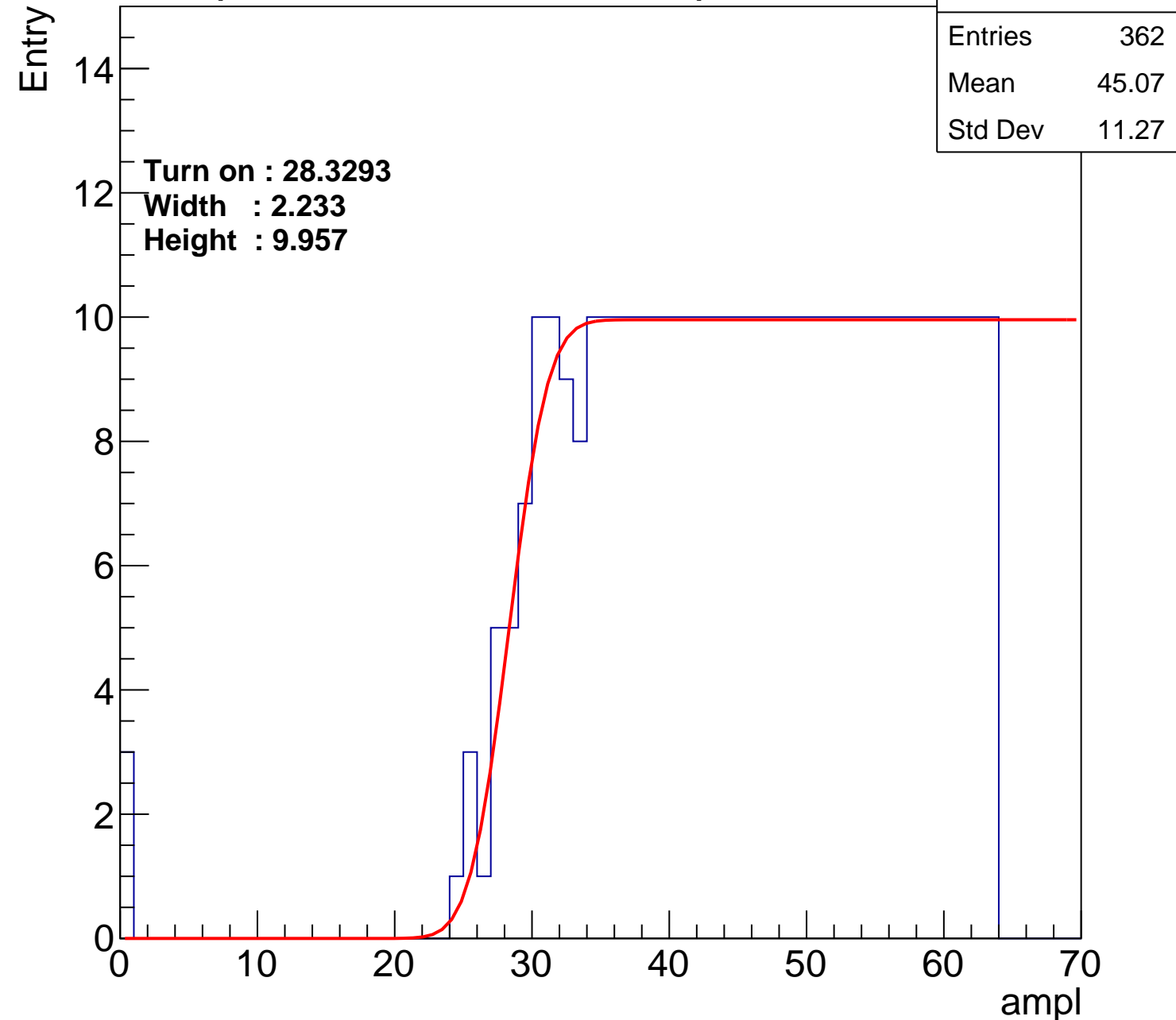
**Width : 2.233**

**Height : 9.957**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch53

calib\_packv5\_042523\_0143.root, FC#4, port A2

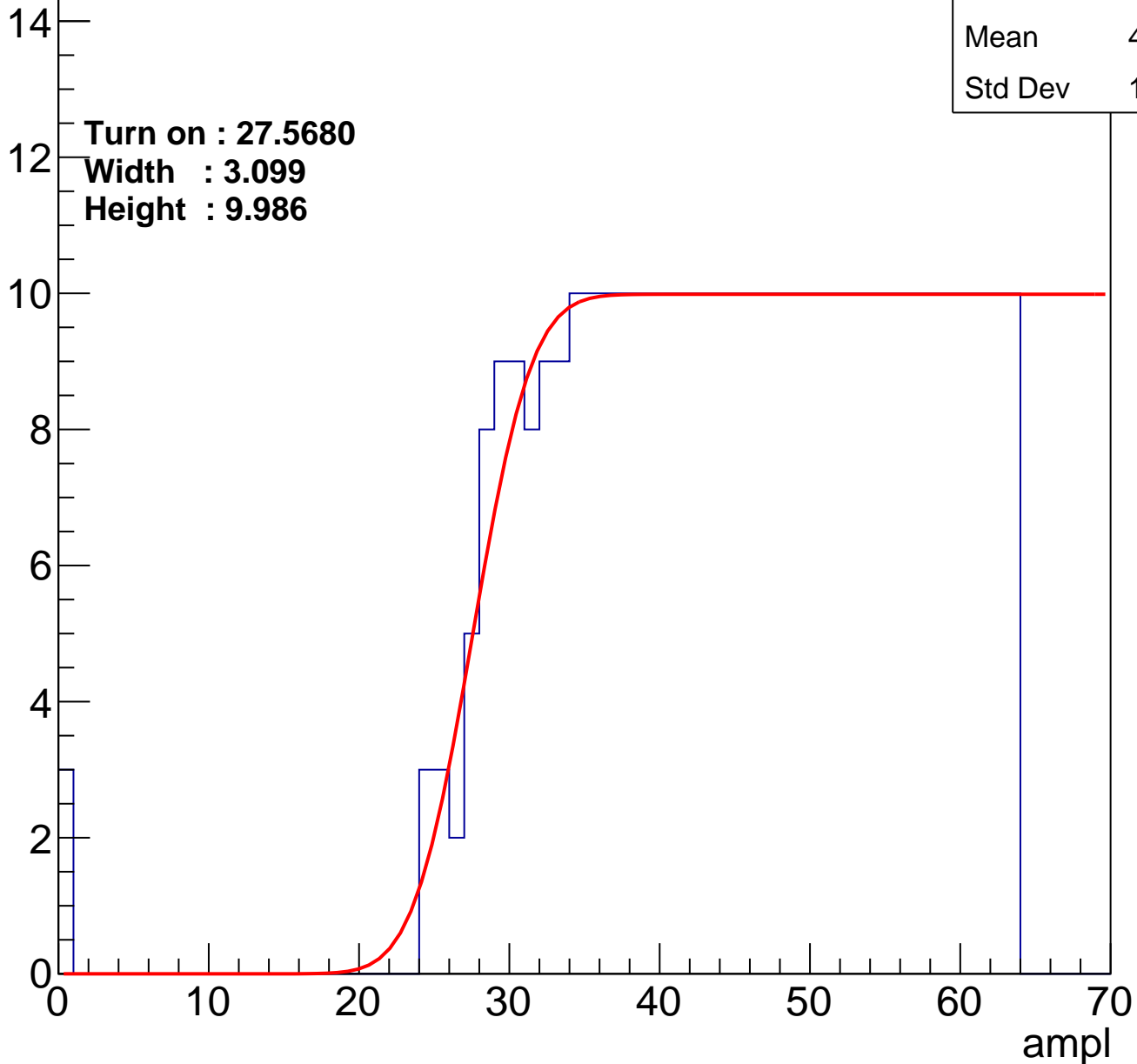
Entries	368
Mean	44.76
Std Dev	11.43

Turn on : 27.5680

Width : 3.099

Height : 9.986

Entry



# B1L100S, U24-ch54

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	408
Mean	42.61
Std Dev	12.93

Turn on : 24.0438

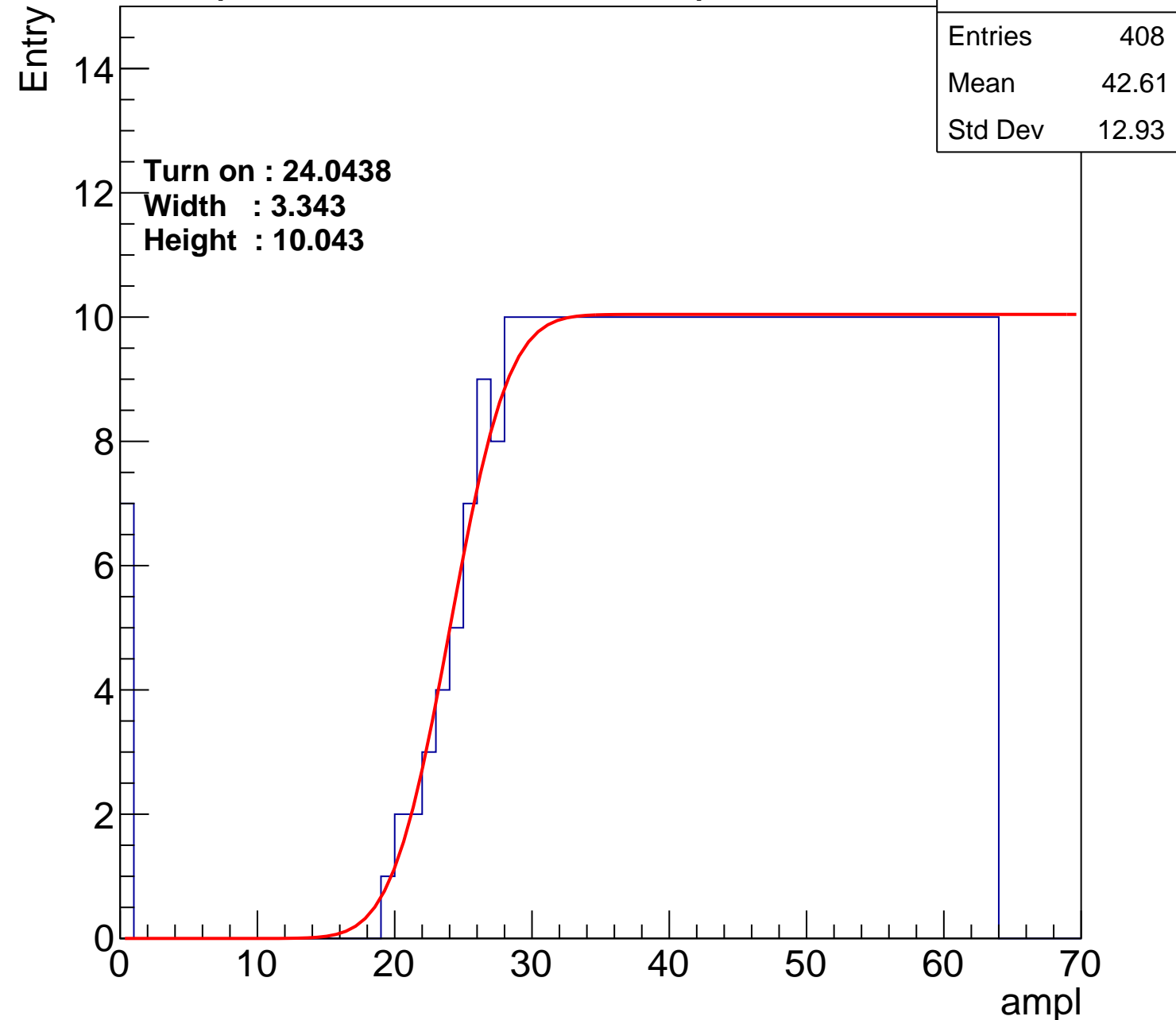
Width : 3.343

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch55

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.02
Std Dev	11.44

Turn on : 28.2941

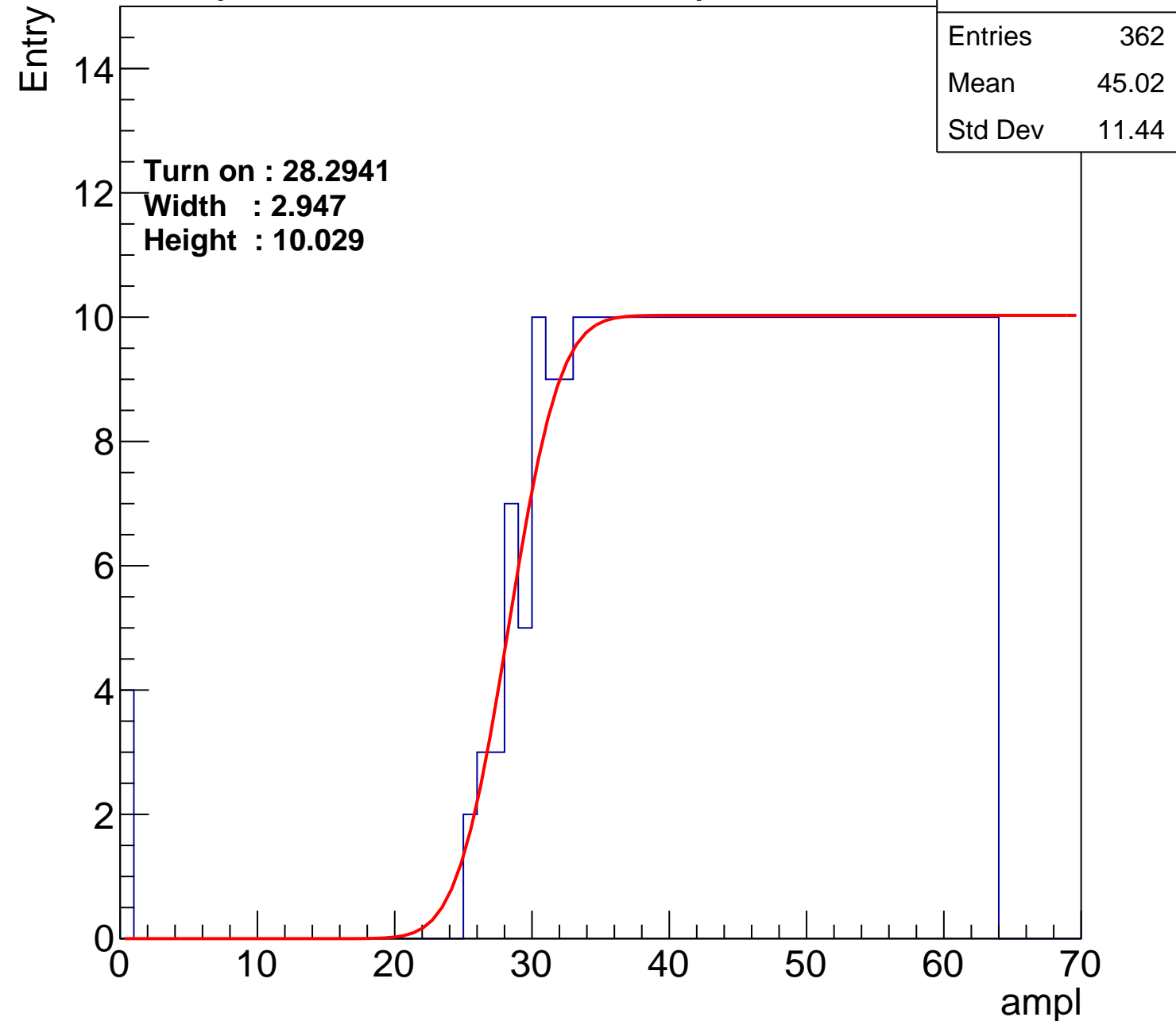
Width : 2.947

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch56

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 26.4191

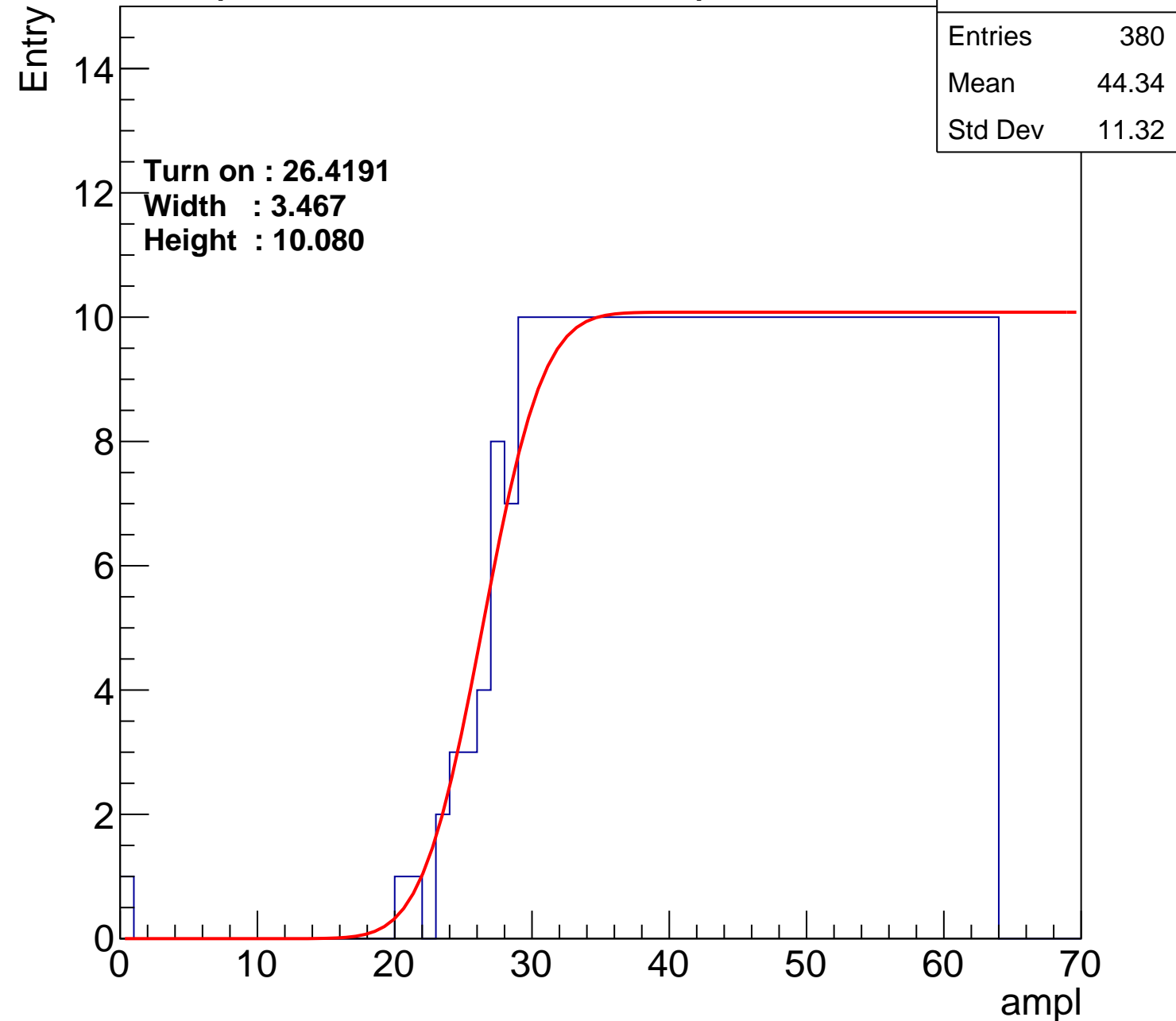
Width : 3.467

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch57

calib\_packv5\_042523\_0143.root, FC#4, port A2

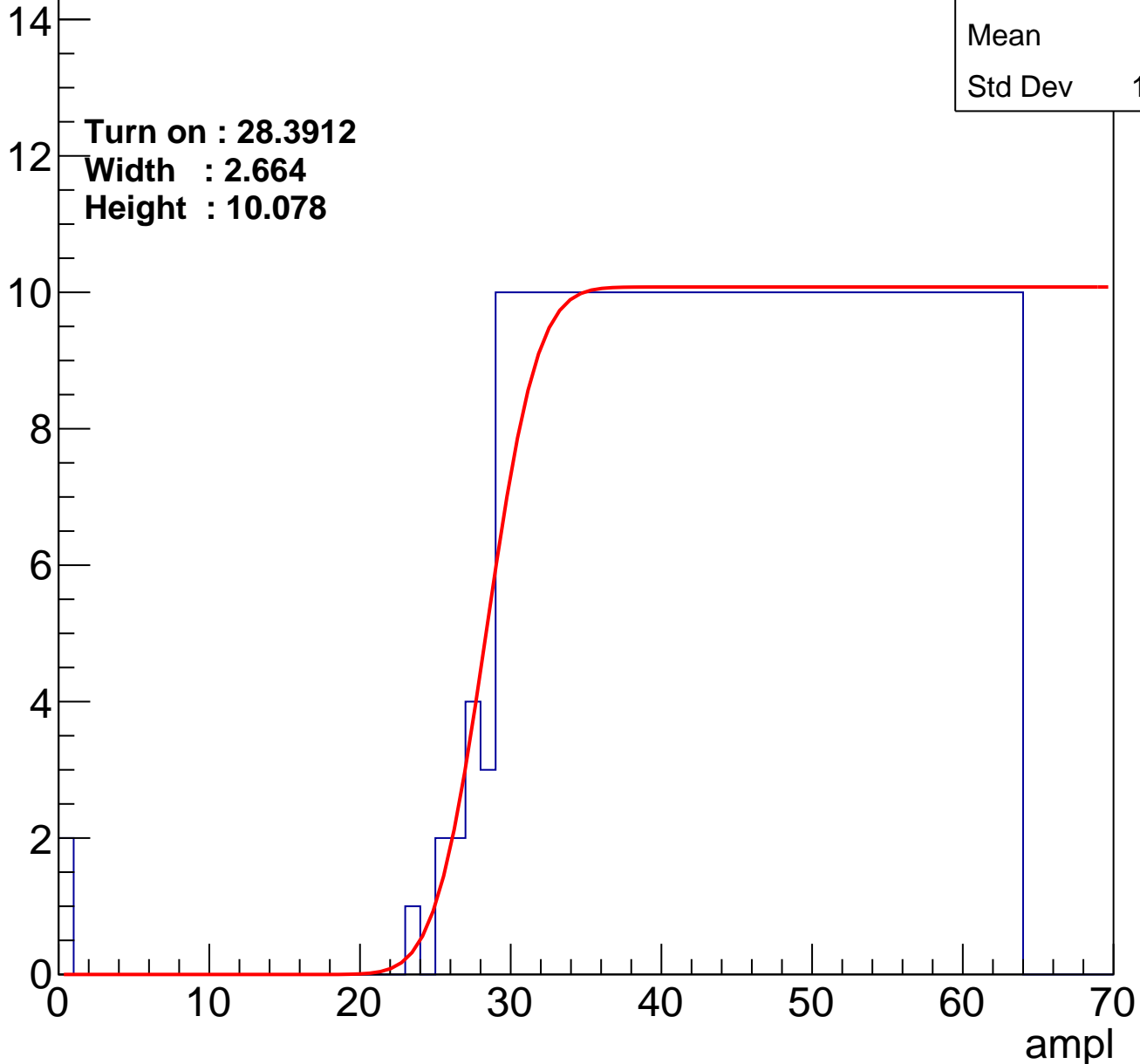
Entries	364
Mean	45.1
Std Dev	11.03

Turn on : 28.3912

Width : 2.664

Height : 10.078

Entry

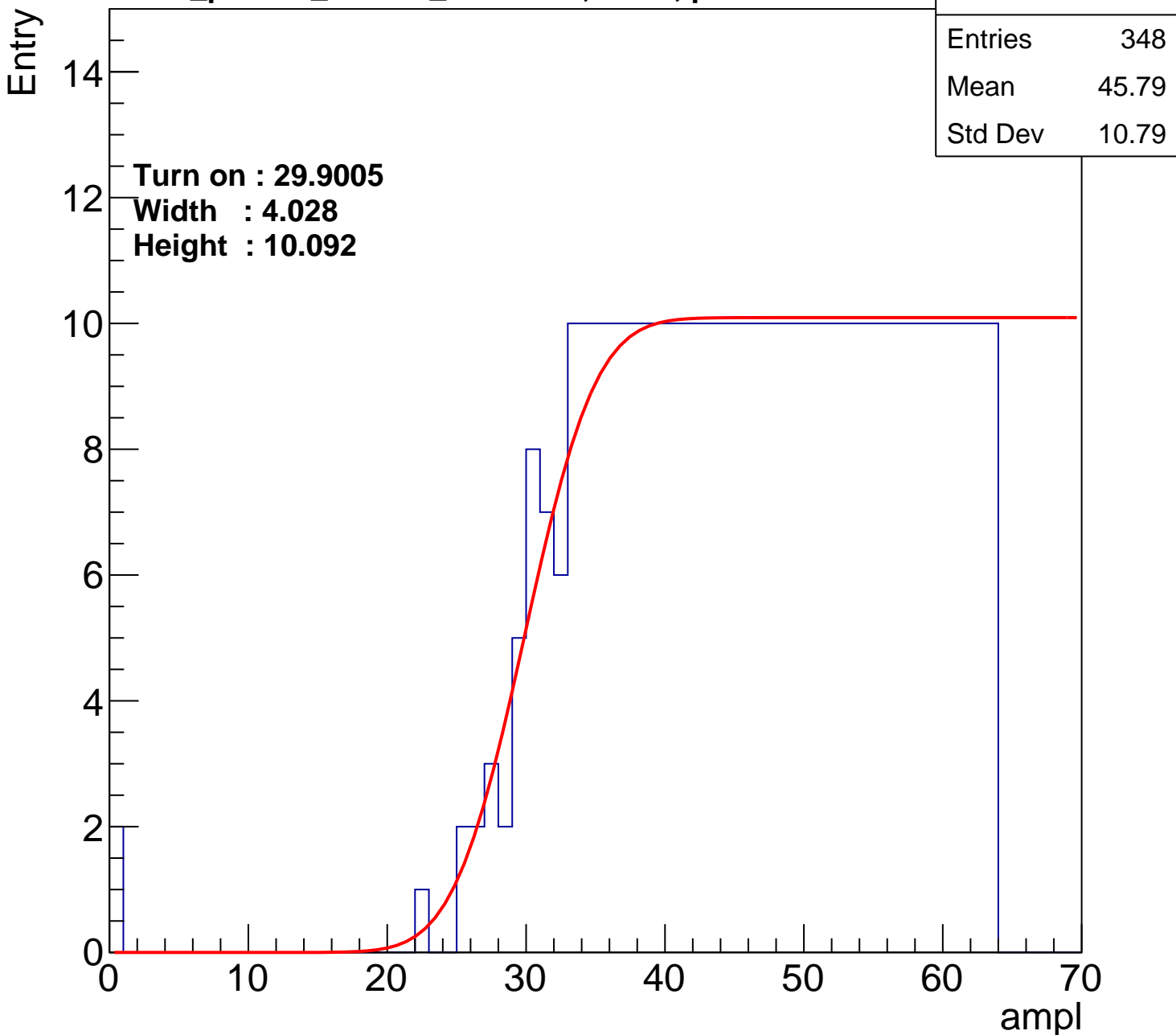


**calib\_packv5\_042523\_0143.root, FC#4, port A2**

**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	348
Mean	45.79
Std Dev	10.79

**Height : 10.092**



# B1L100S, U24-ch59

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.5679

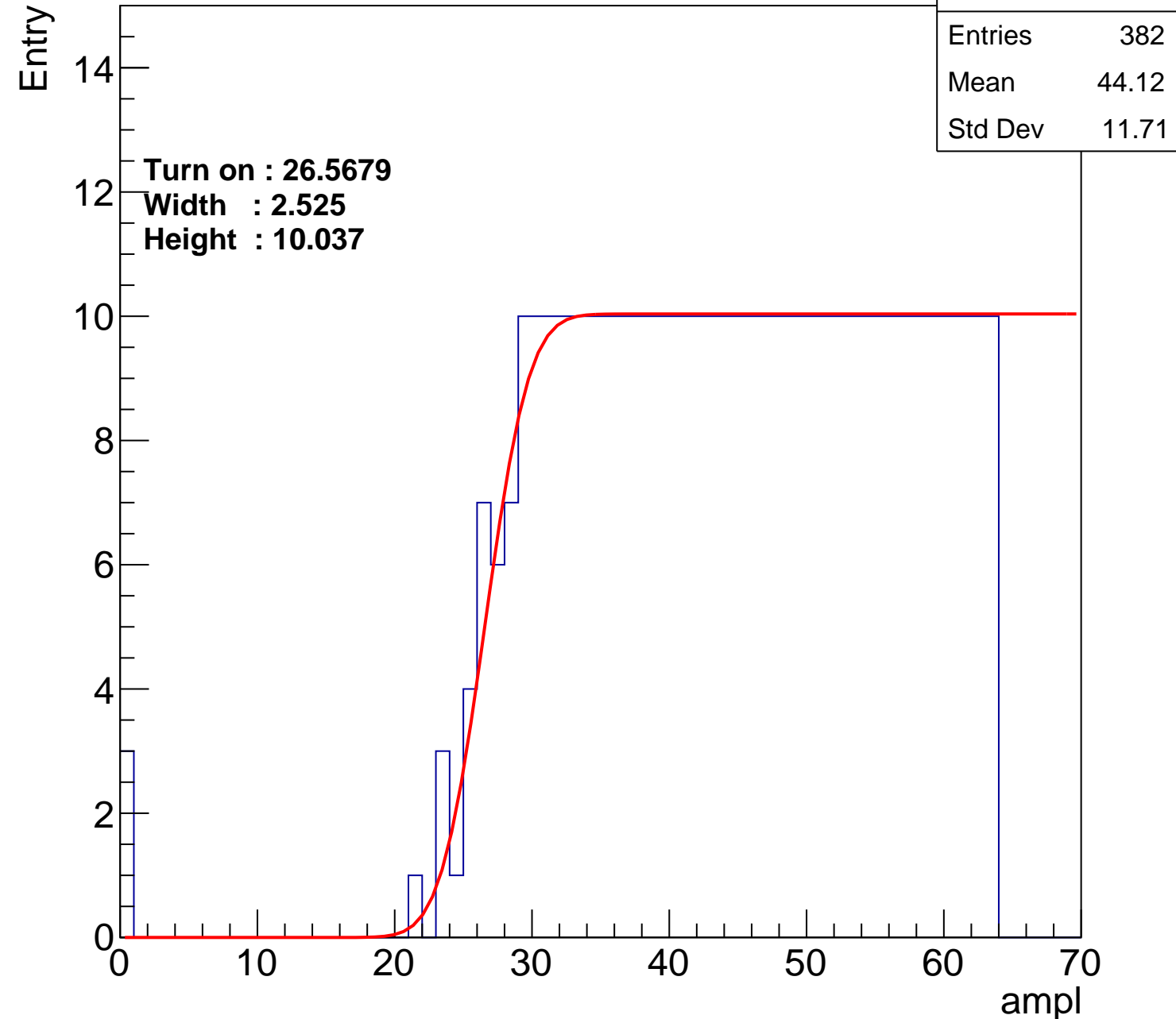
Width : 2.525

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch60

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.38
Std Dev	11.67

Turn on : 27.2100

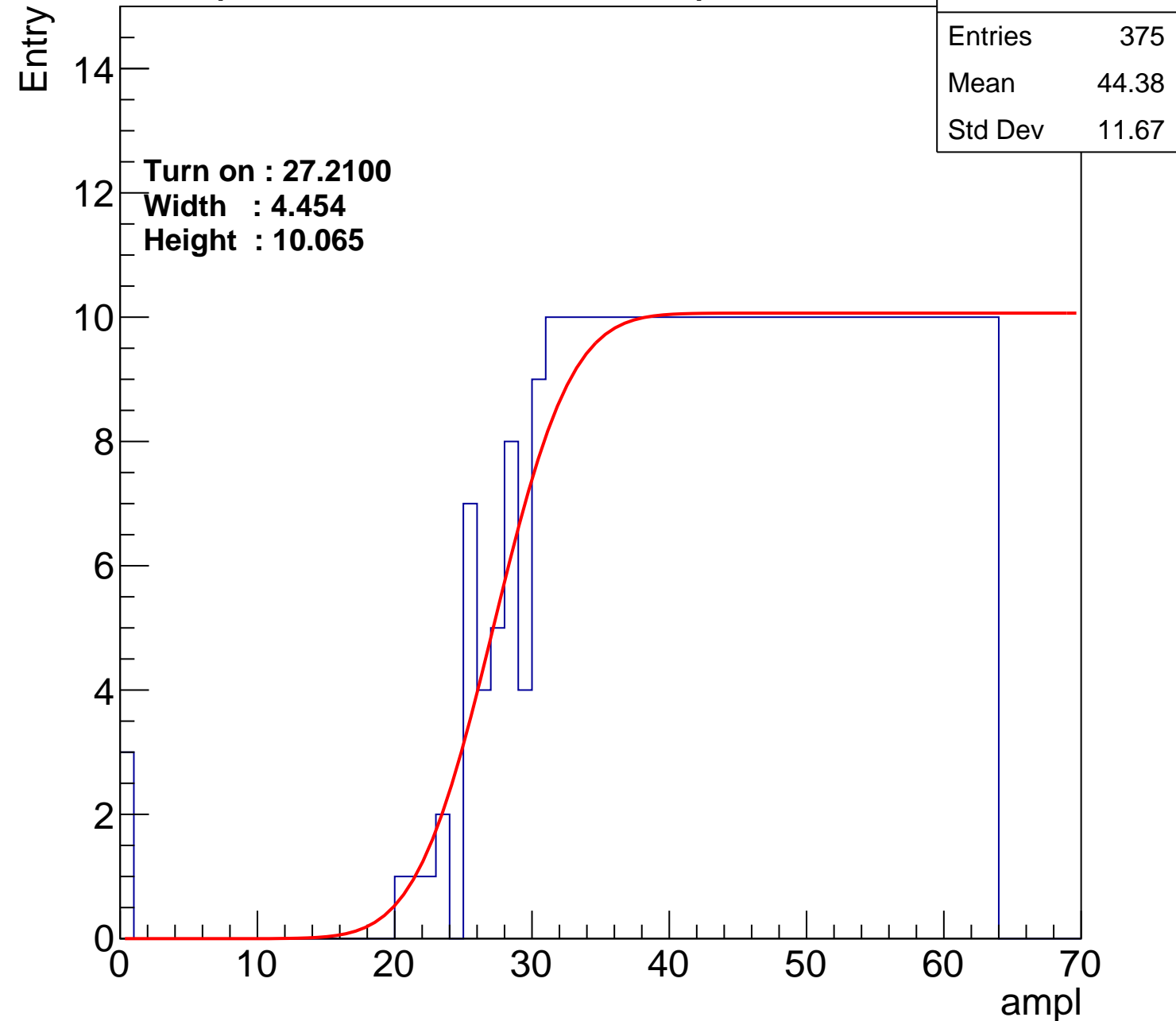
Width : 4.454

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch61

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.2
Std Dev	11.5

Turn on : 26.3429

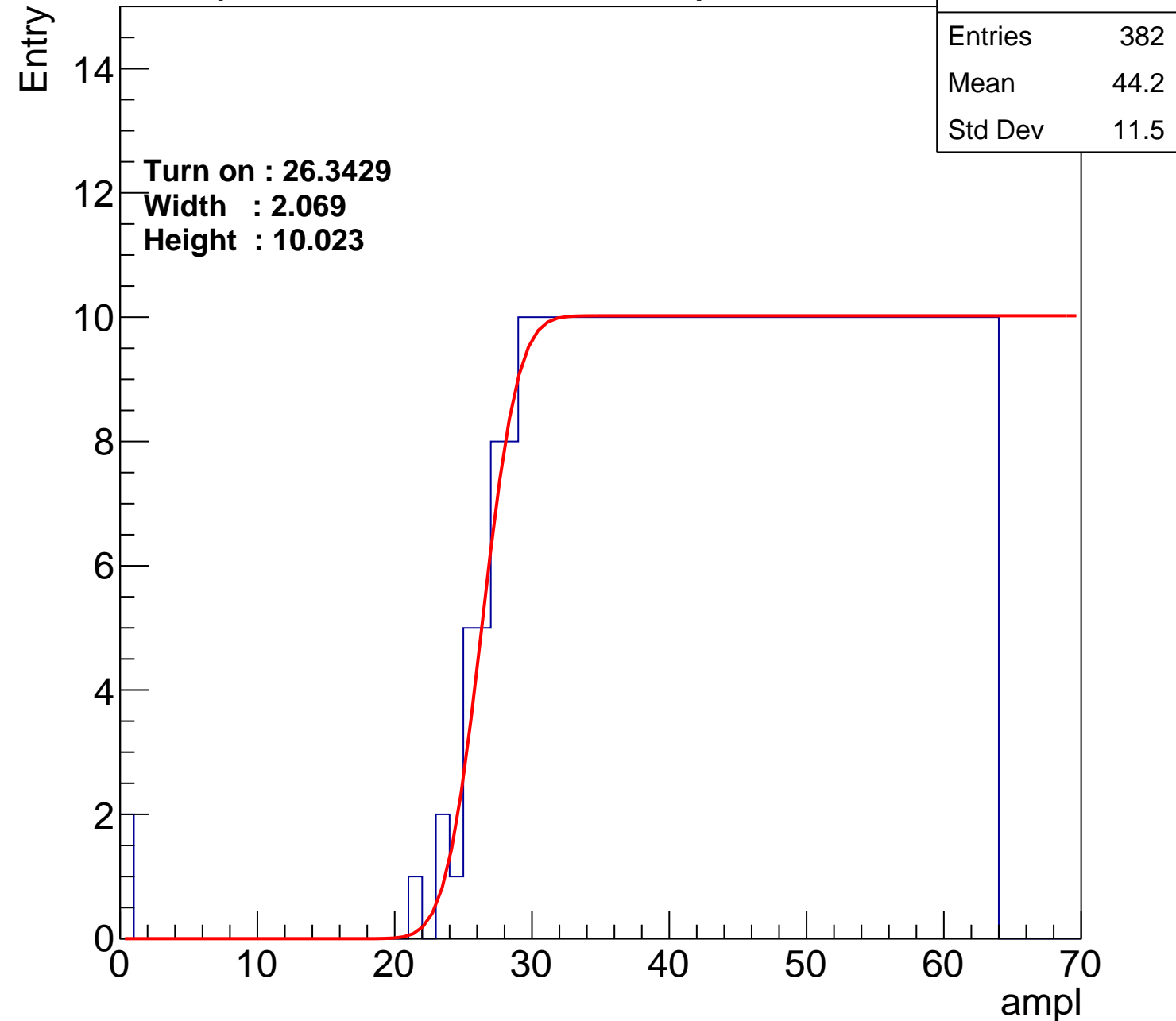
Width : 2.069

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch62

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.3423

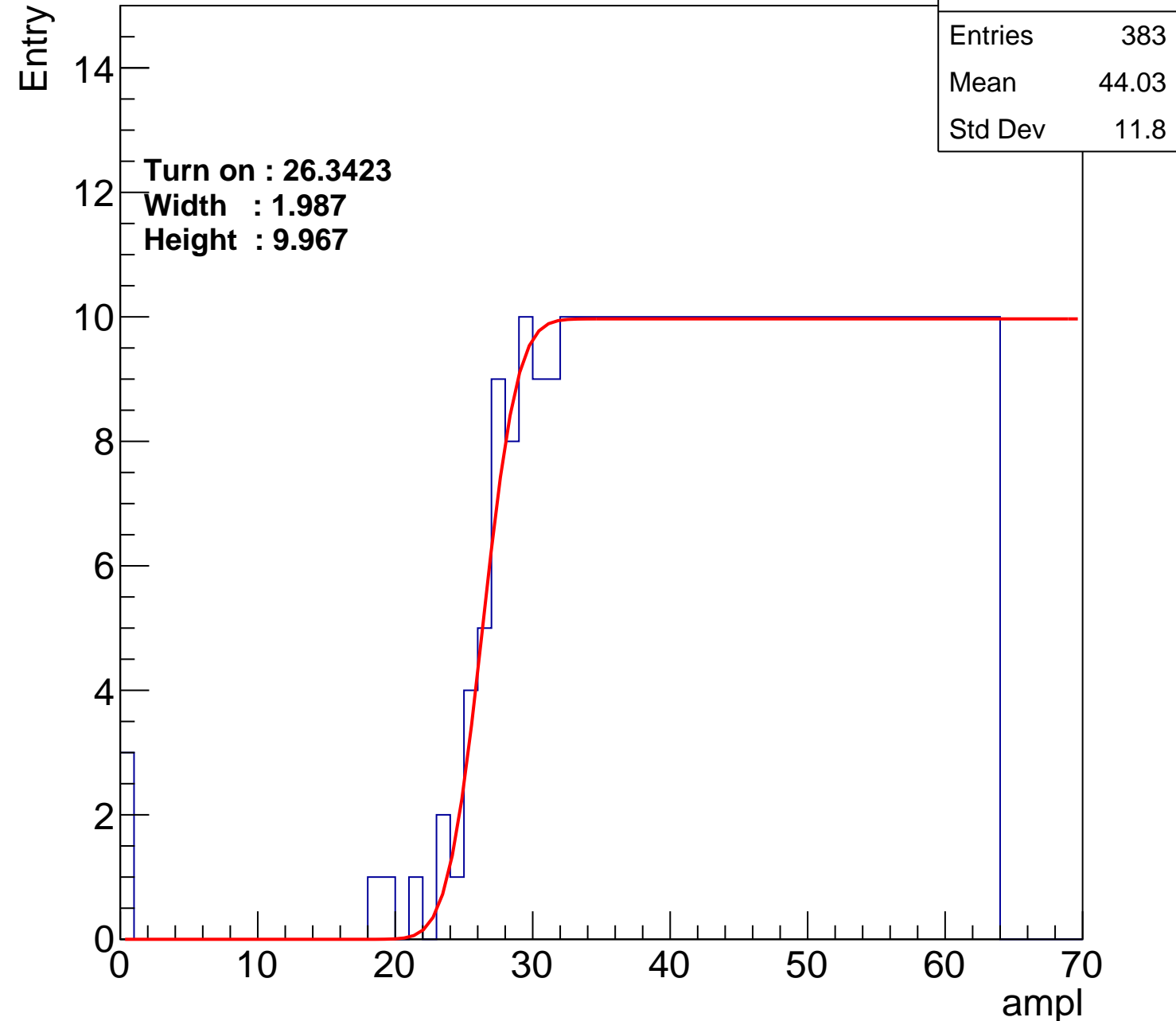
Width : 1.987

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch63

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 26.2650

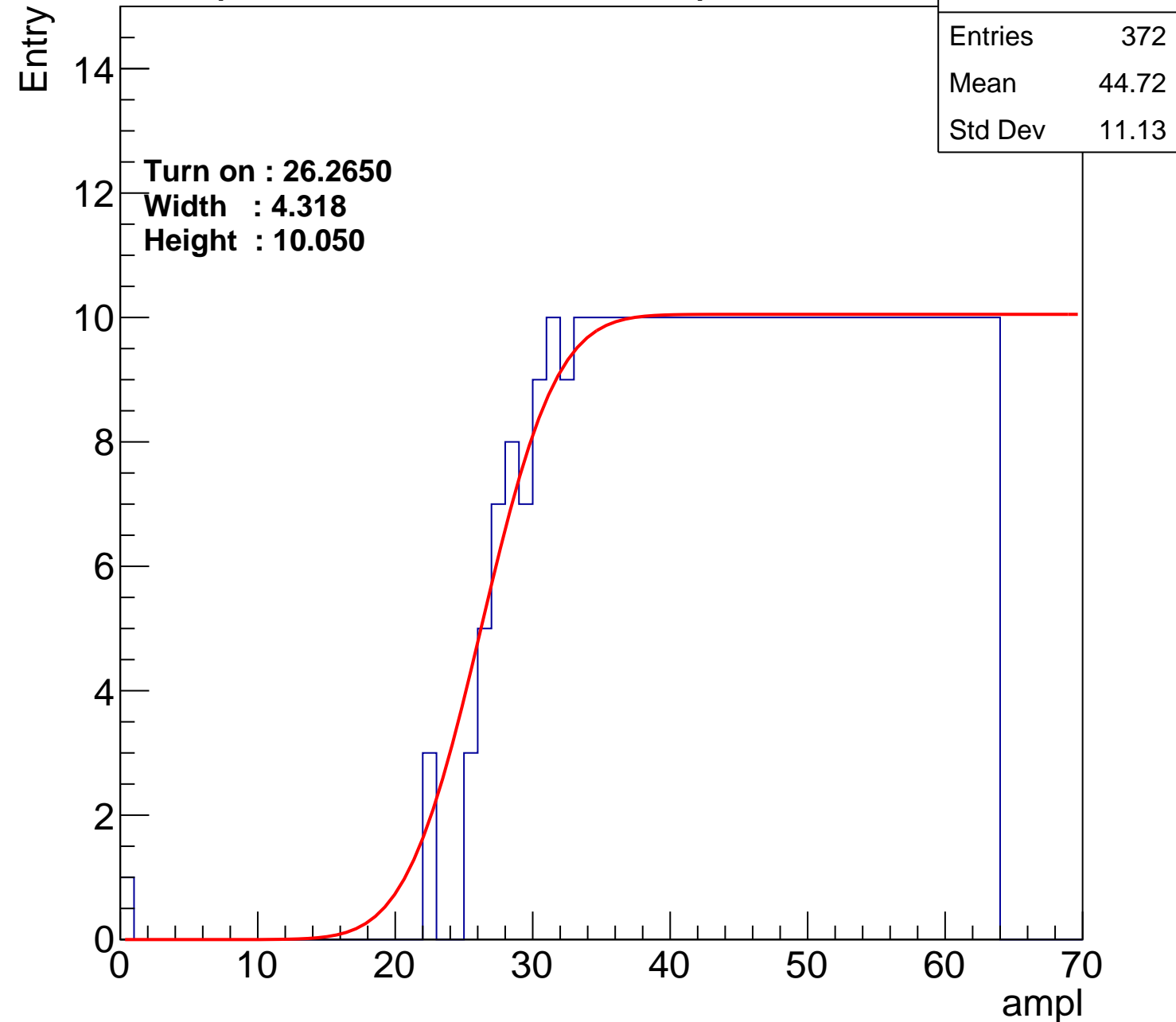
Width : 4.318

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch64

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	378
Mean	44.35
Std Dev	11.47

Turn on : 26.2398

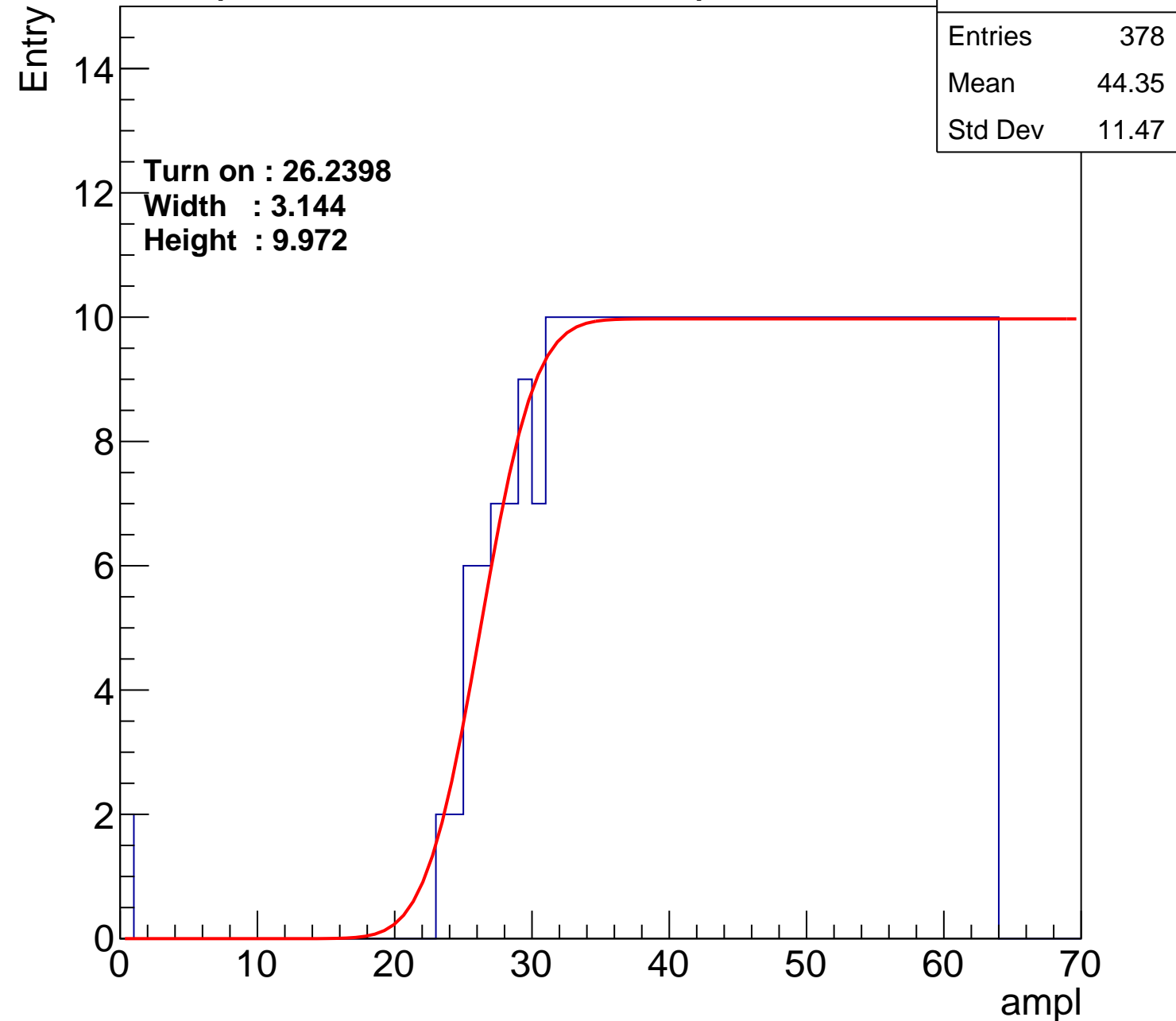
Width : 3.144

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch65

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.5115

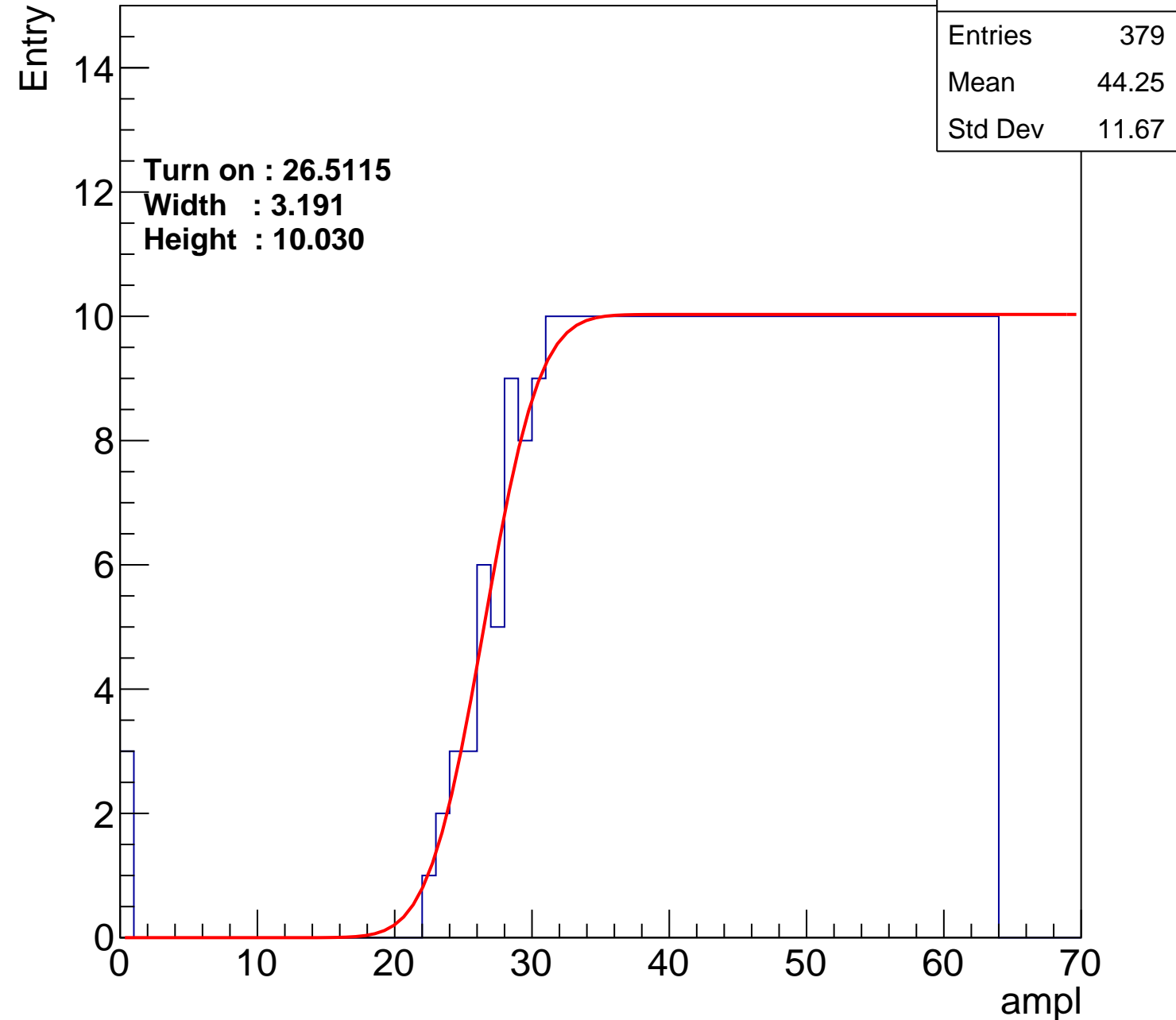
Width : 3.191

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch66

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	358
Mean	45.35
Std Dev	10.95

Turn on : 28.7068

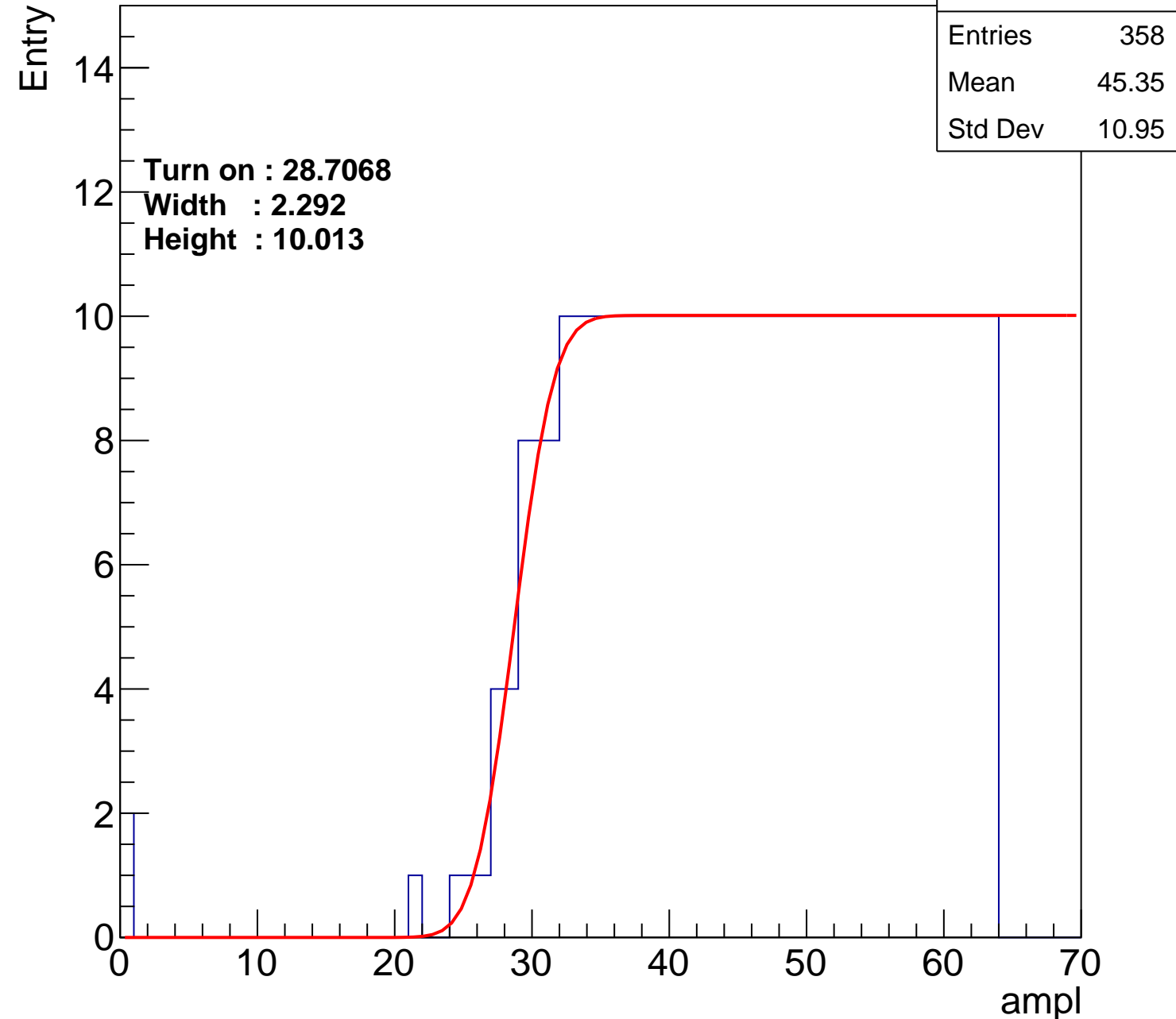
Width : 2.292

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch67

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 26.6534

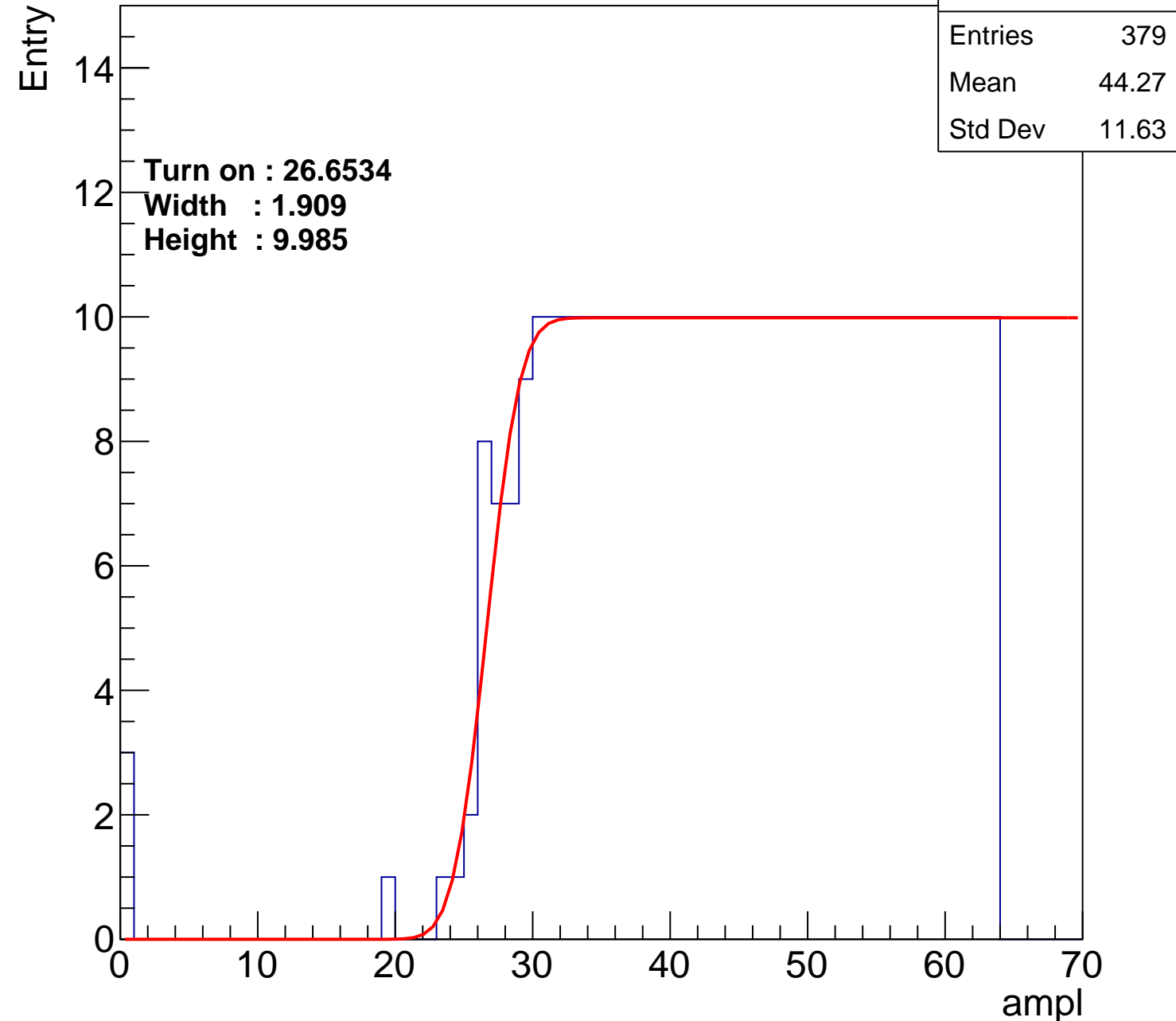
Width : 1.909

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch68

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 26.7565

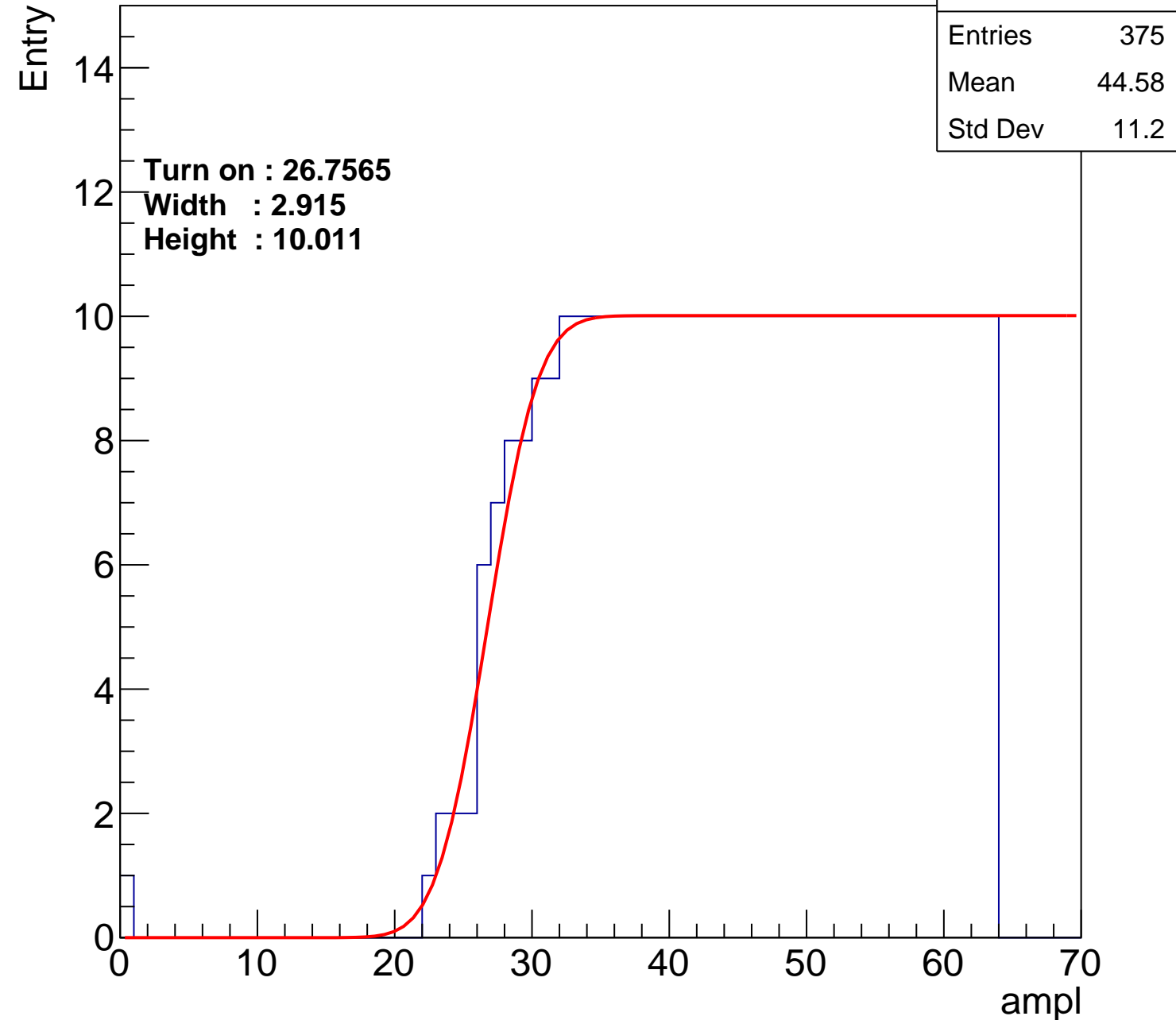
Width : 2.915

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch69

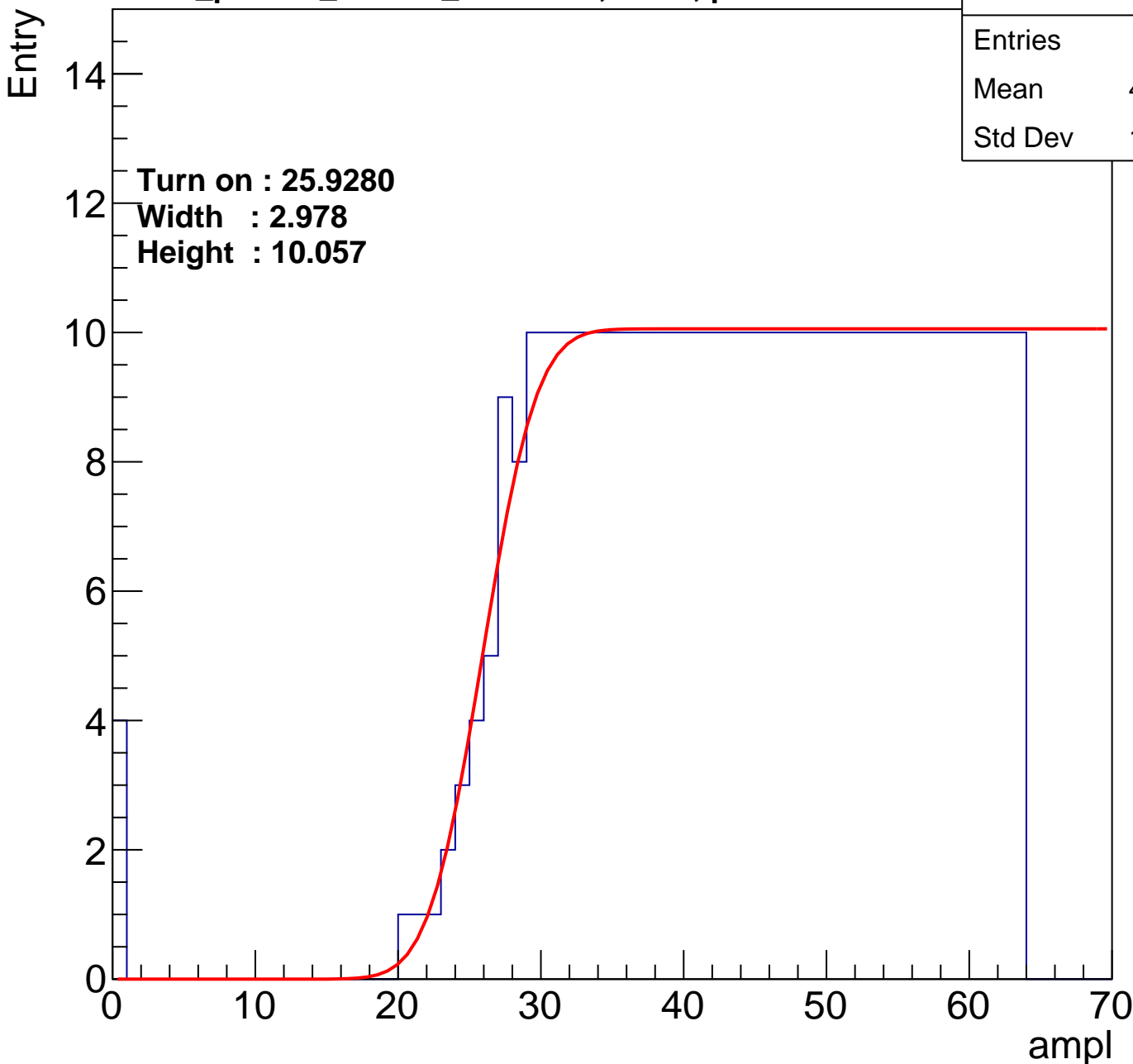
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	388
Mean	43.76
Std Dev	12.04

**Turn on : 25.9280**

**Width : 2.978**

**Height : 10.057**



# B1L100S, U24-ch70

calib\_packv5\_042523\_0143.root, FC#4, port A2

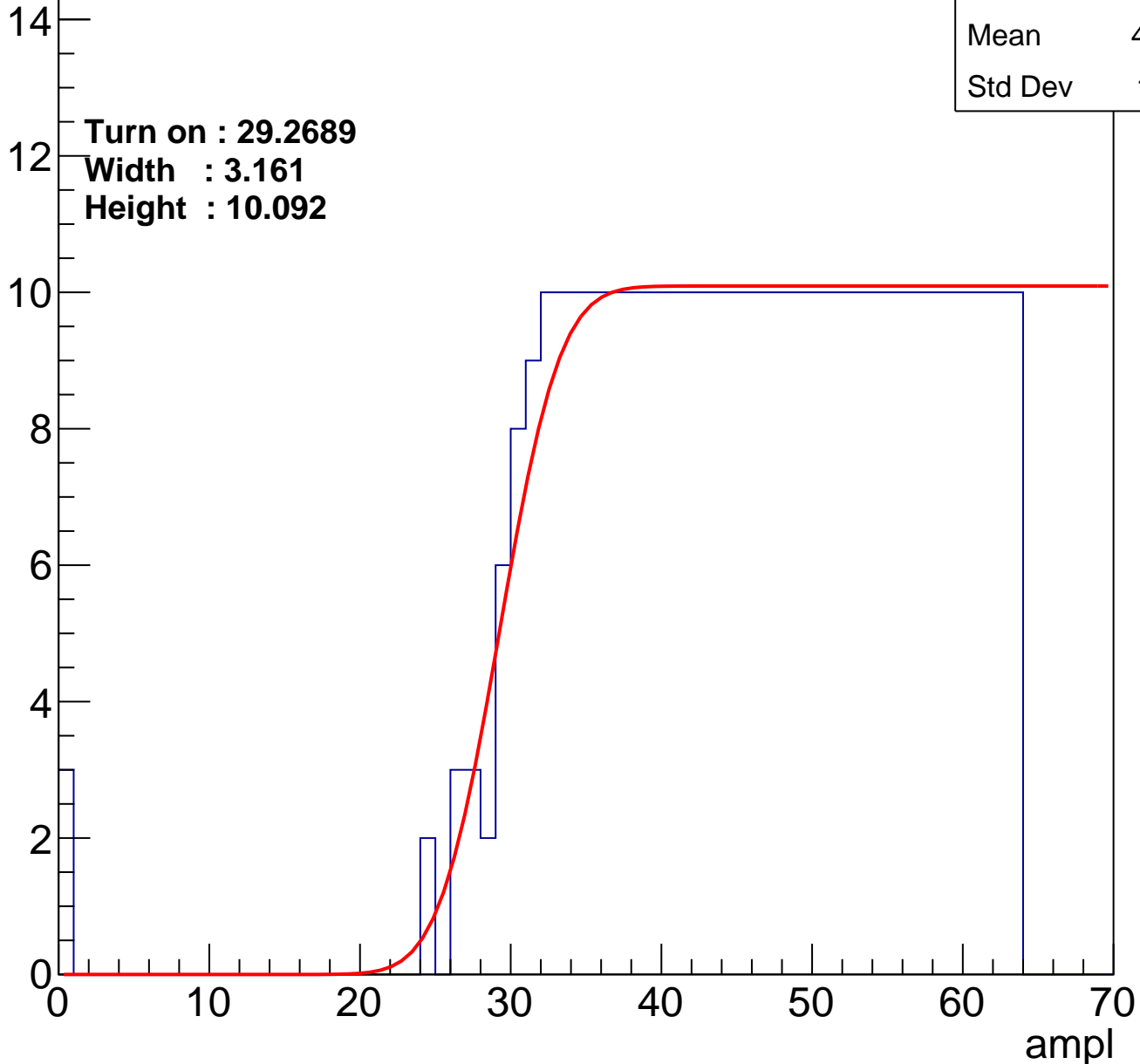
Entries	356
Mean	45.38
Std Dev	11.11

Turn on : 29.2689

Width : 3.161

Height : 10.092

Entry





# B1L100S, U24-ch71

calib\_packv5\_042523\_0143.root, FC#4, port A2

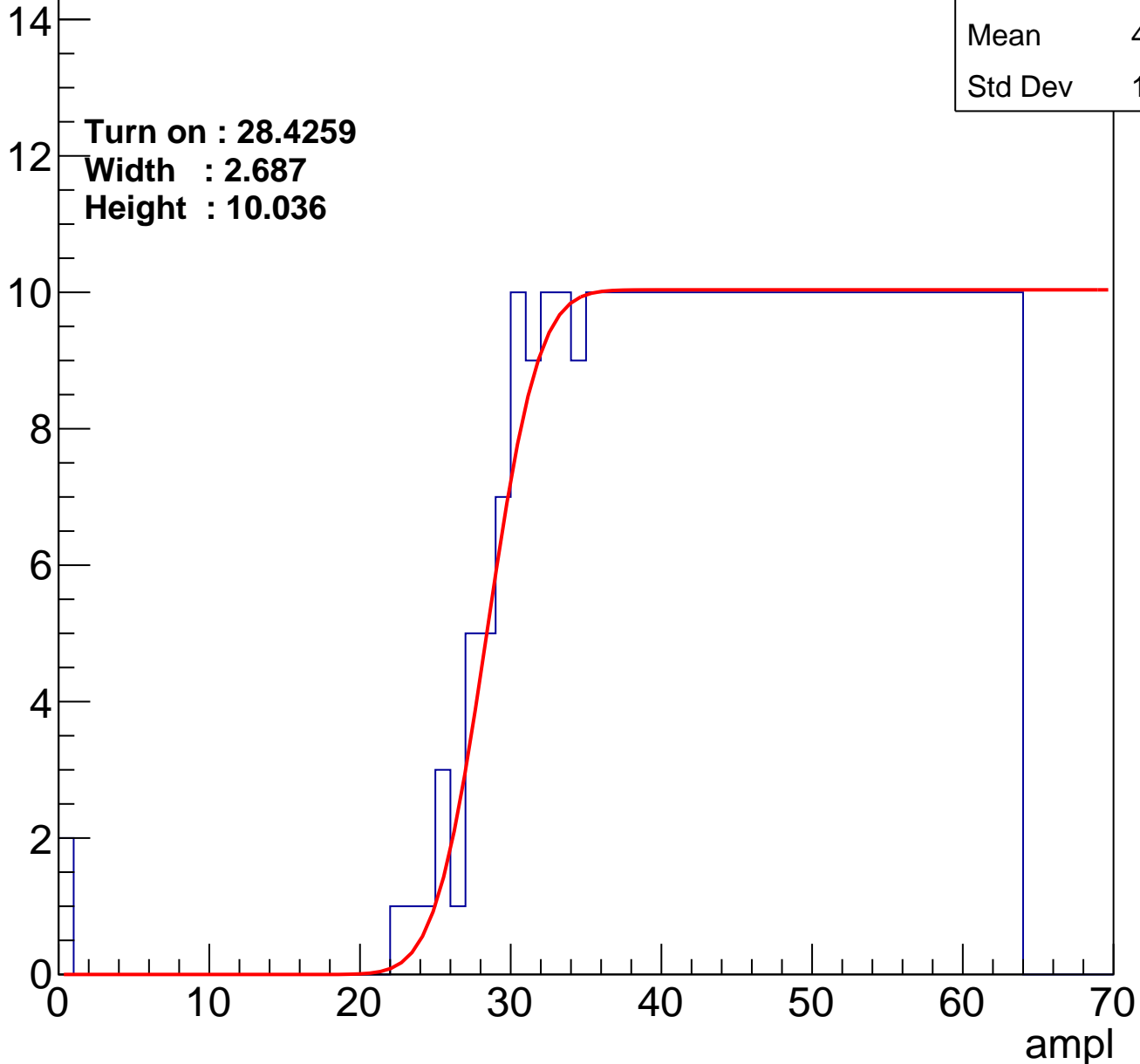
Entries	364
Mean	45.04
Std Dev	11.13

Turn on : 28.4259

Width : 2.687

Height : 10.036

Entry



# B1L100S, U24-ch72

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.44
Std Dev	11.44

Turn on : 27.0545

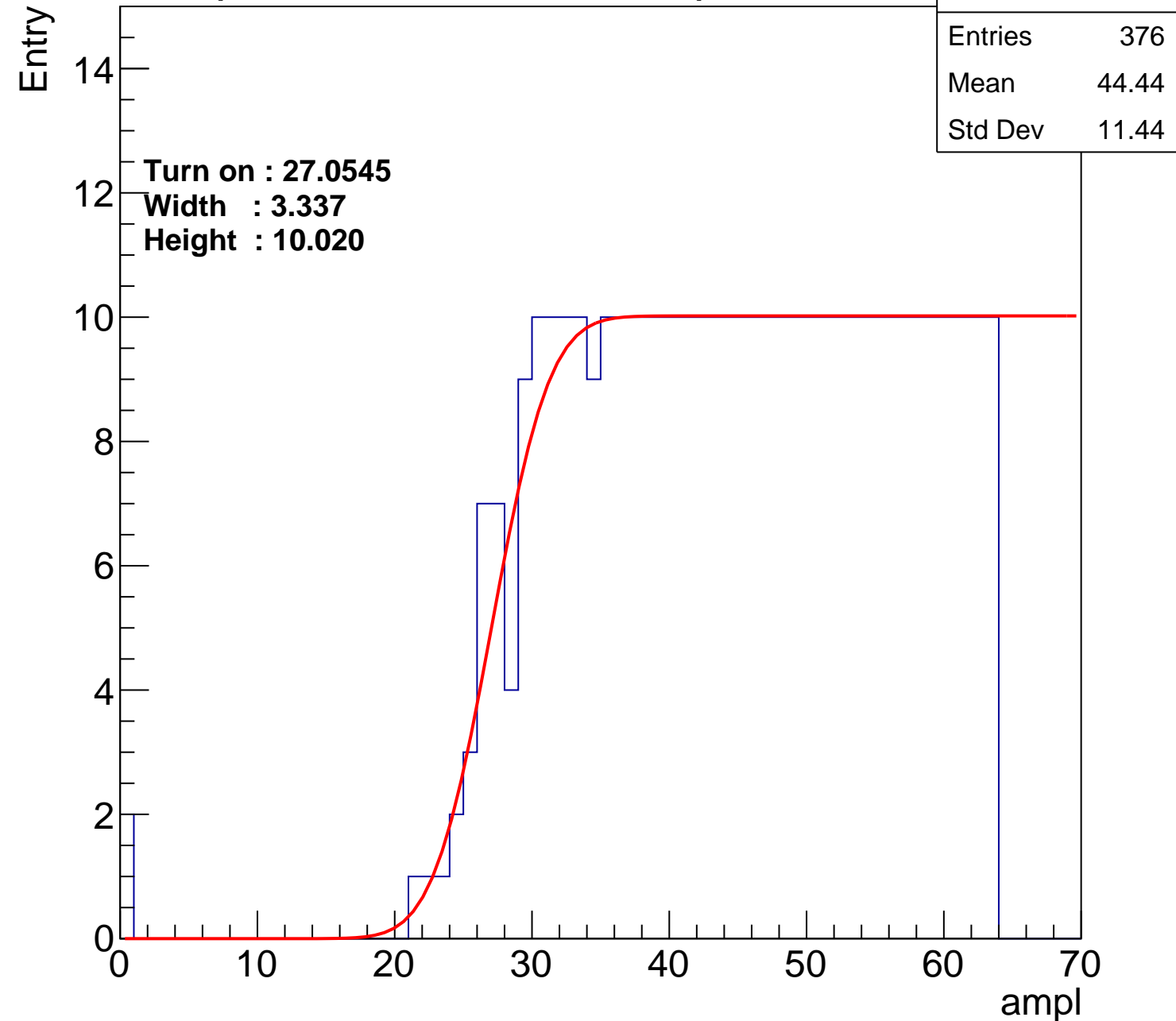
Width : 3.337

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch73

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	396
Mean	43.27
Std Dev	12.44

Turn on : 25.1080

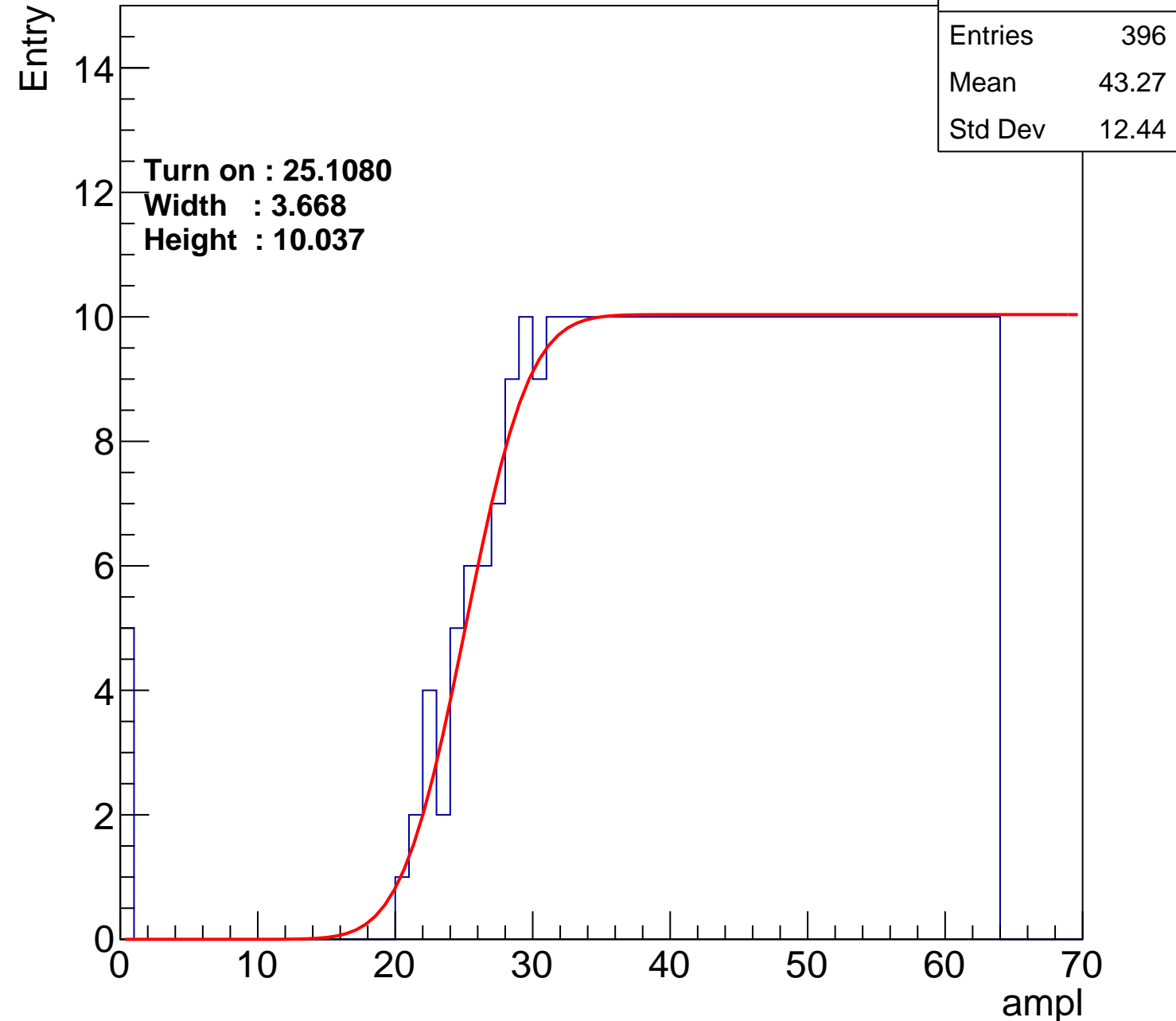
Width : 3.668

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch74

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.54
Std Dev	11.21

Turn on : 26.5350

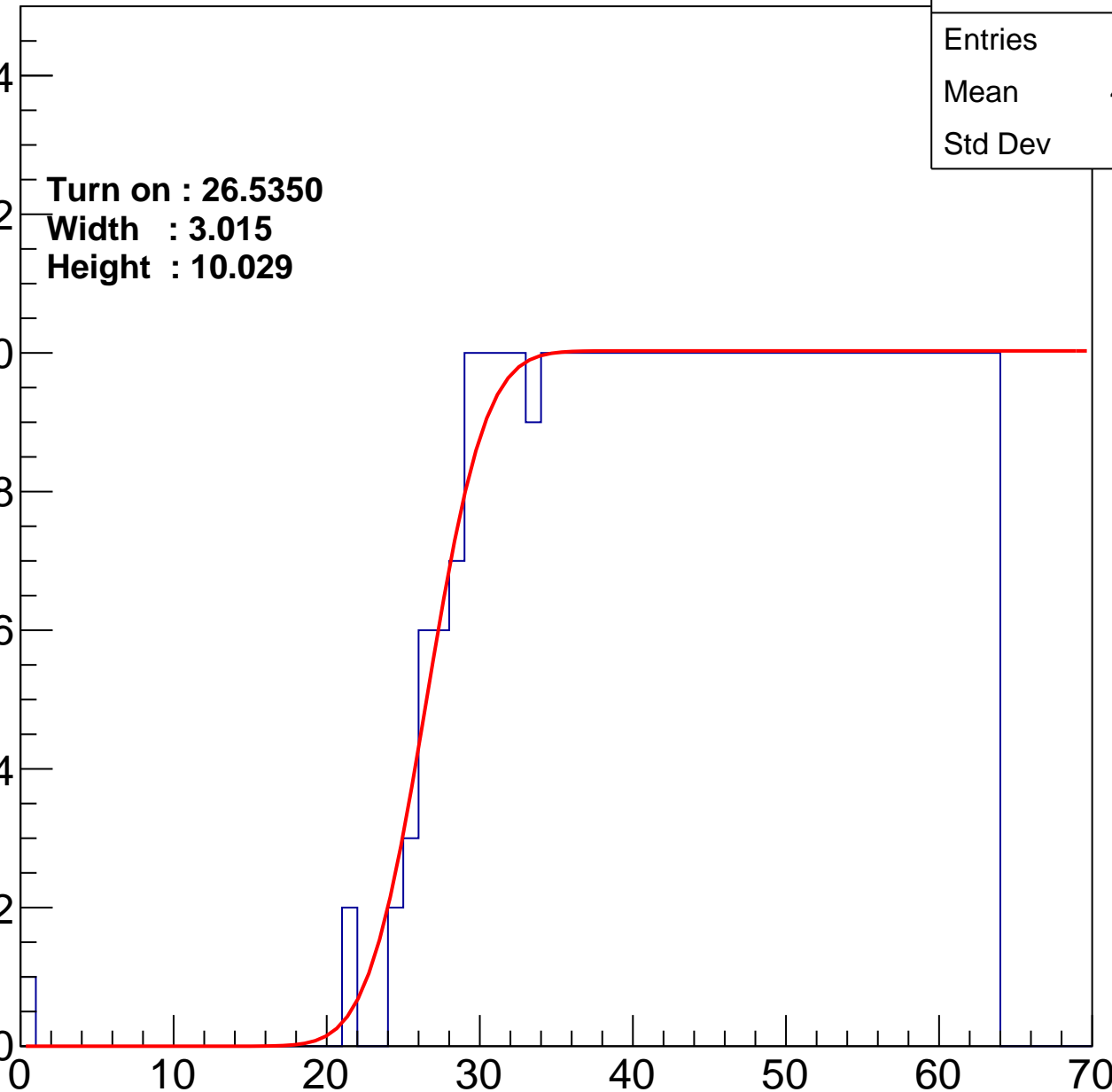
Width : 3.015

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch75

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.68
Std Dev	11.47

Turn on : 28.1689

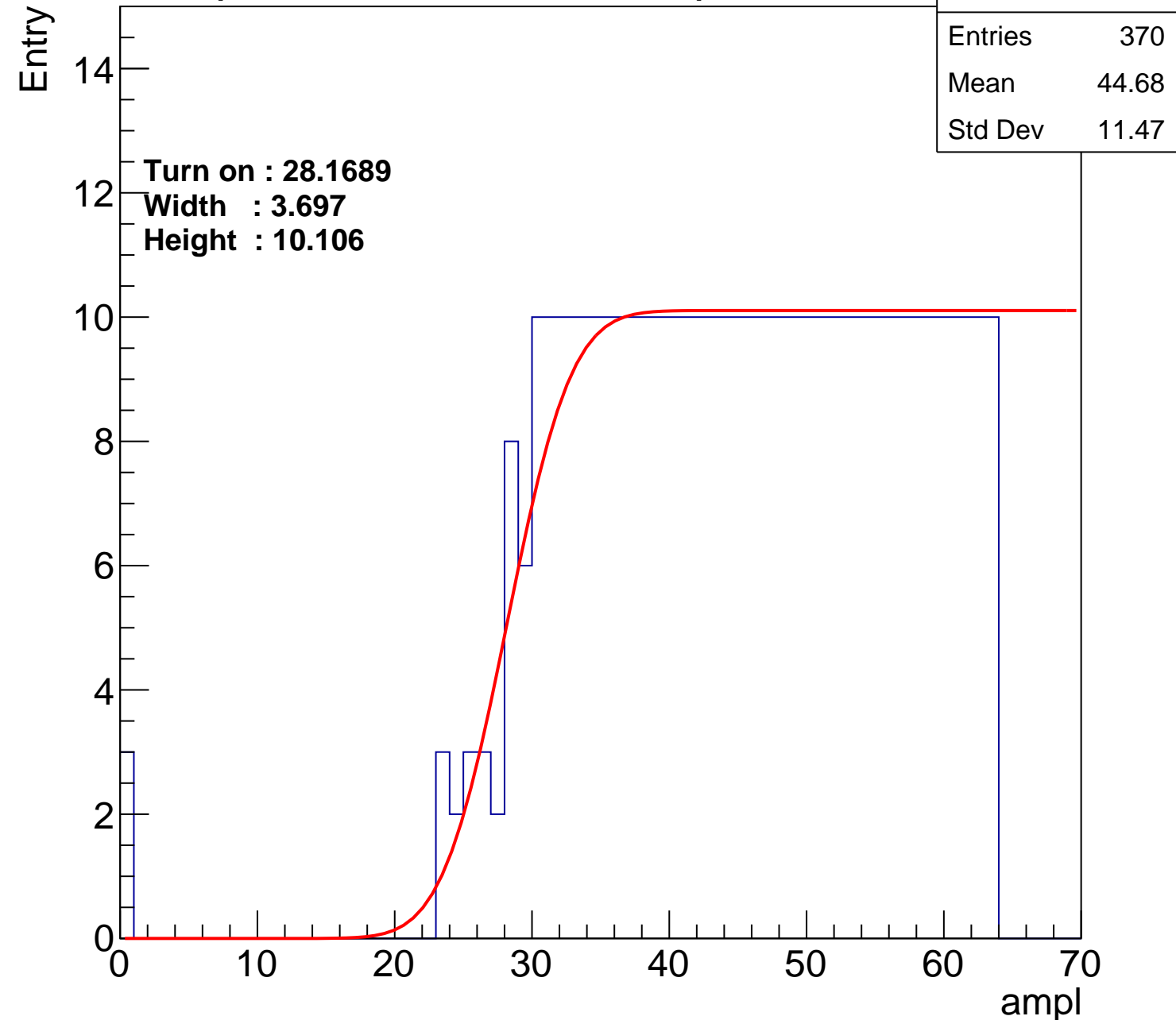
Width : 3.697

Height : 10.106

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch76

calib\_packv5\_042523\_0143.root, FC#4, port A2

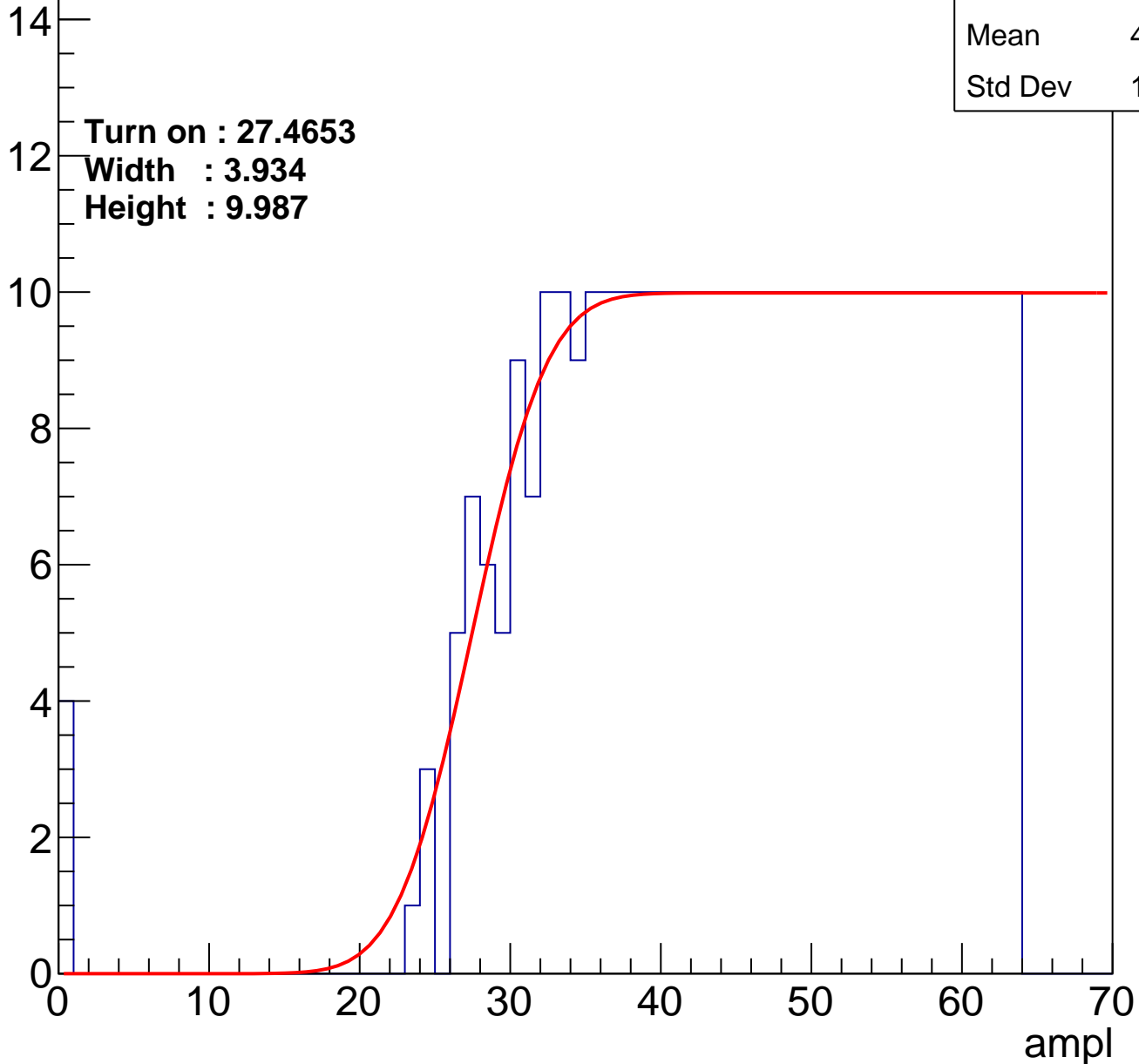
Entries	366
Mean	44.75
Std Dev	11.63

**Turn on : 27.4653**

**Width : 3.934**

**Height : 9.987**

Entry



# B1L100S, U24-ch77

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 27.6389

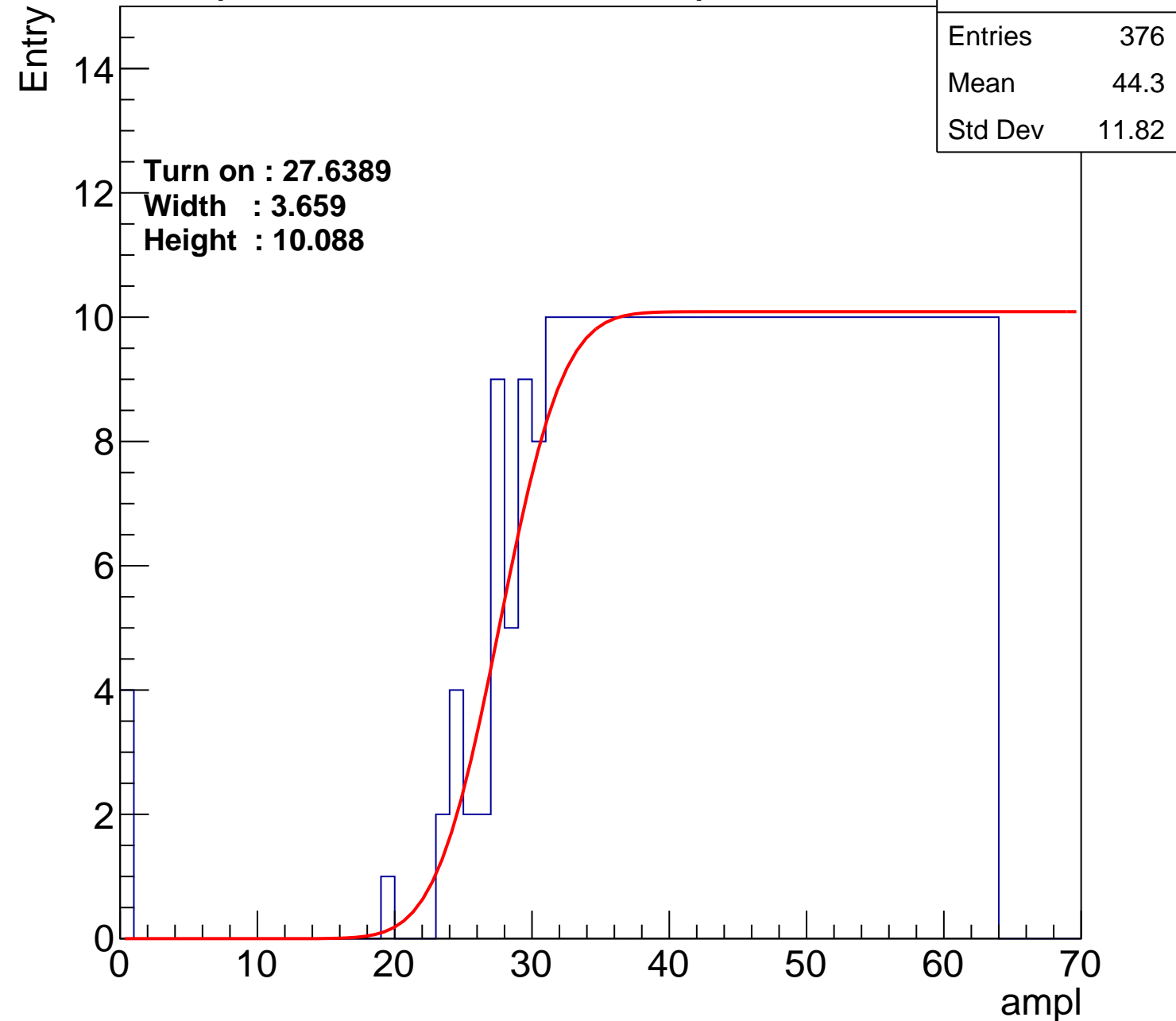
Width : 3.659

Height : 10.088

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch78

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.71
Std Dev	11.43

Turn on : 27.5072

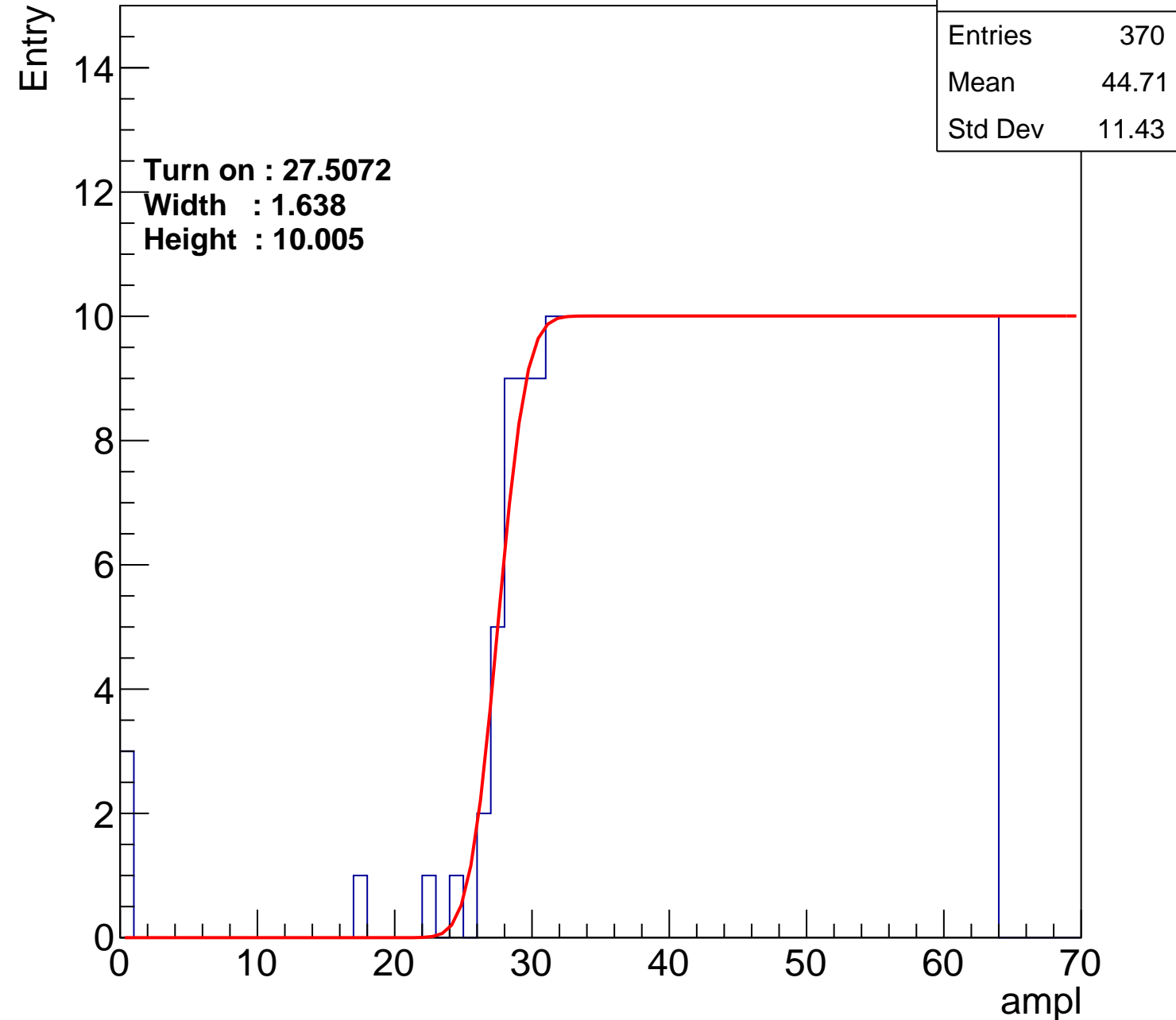
Width : 1.638

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch79

calib\_packv5\_042523\_0143.root, FC#4, port A2

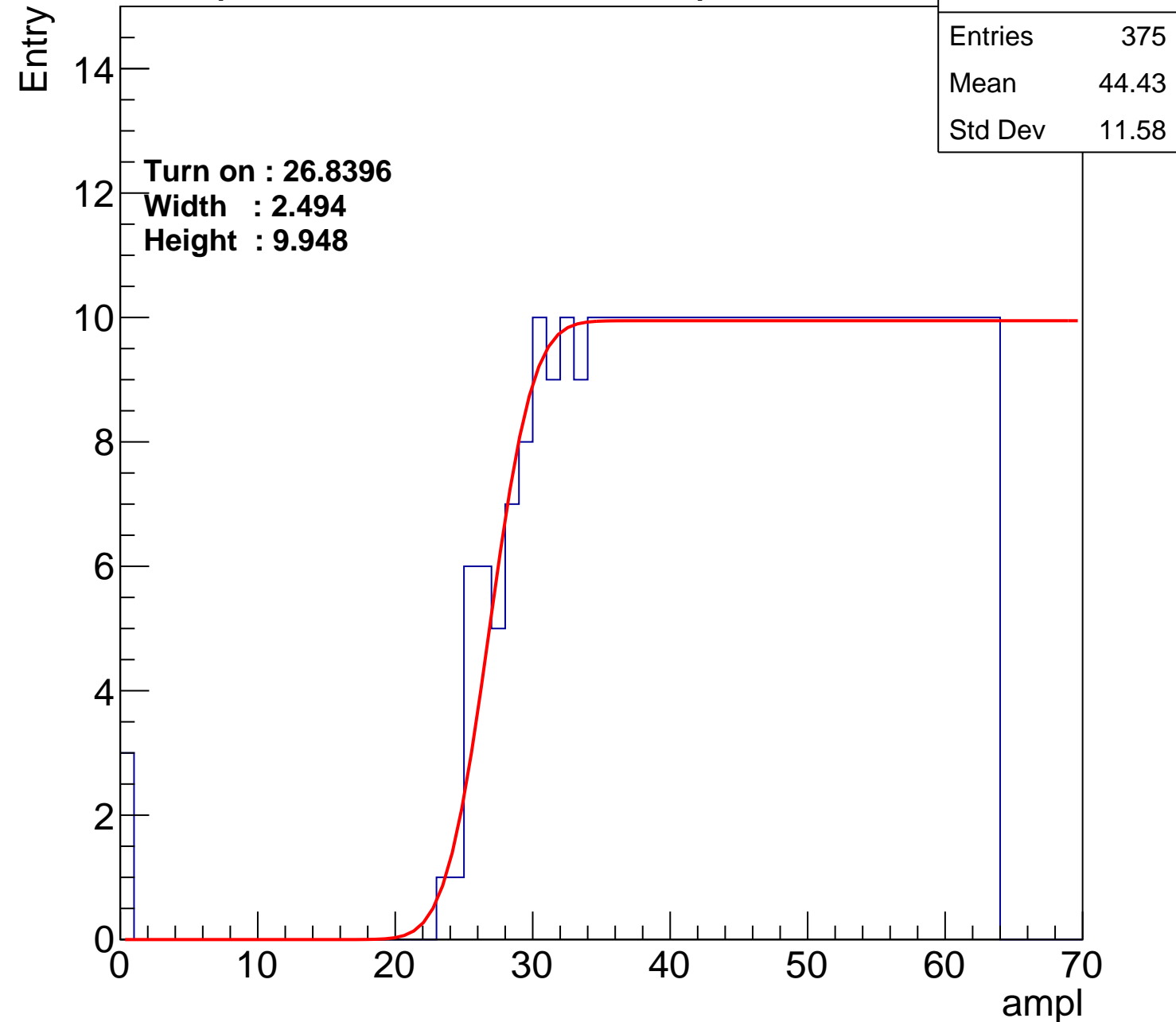
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.8396  
Width : 2.494  
Height : 9.948

Entries	375
Mean	44.43
Std Dev	11.58

ampl



# B1L100S, U24-ch80

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 28.2844

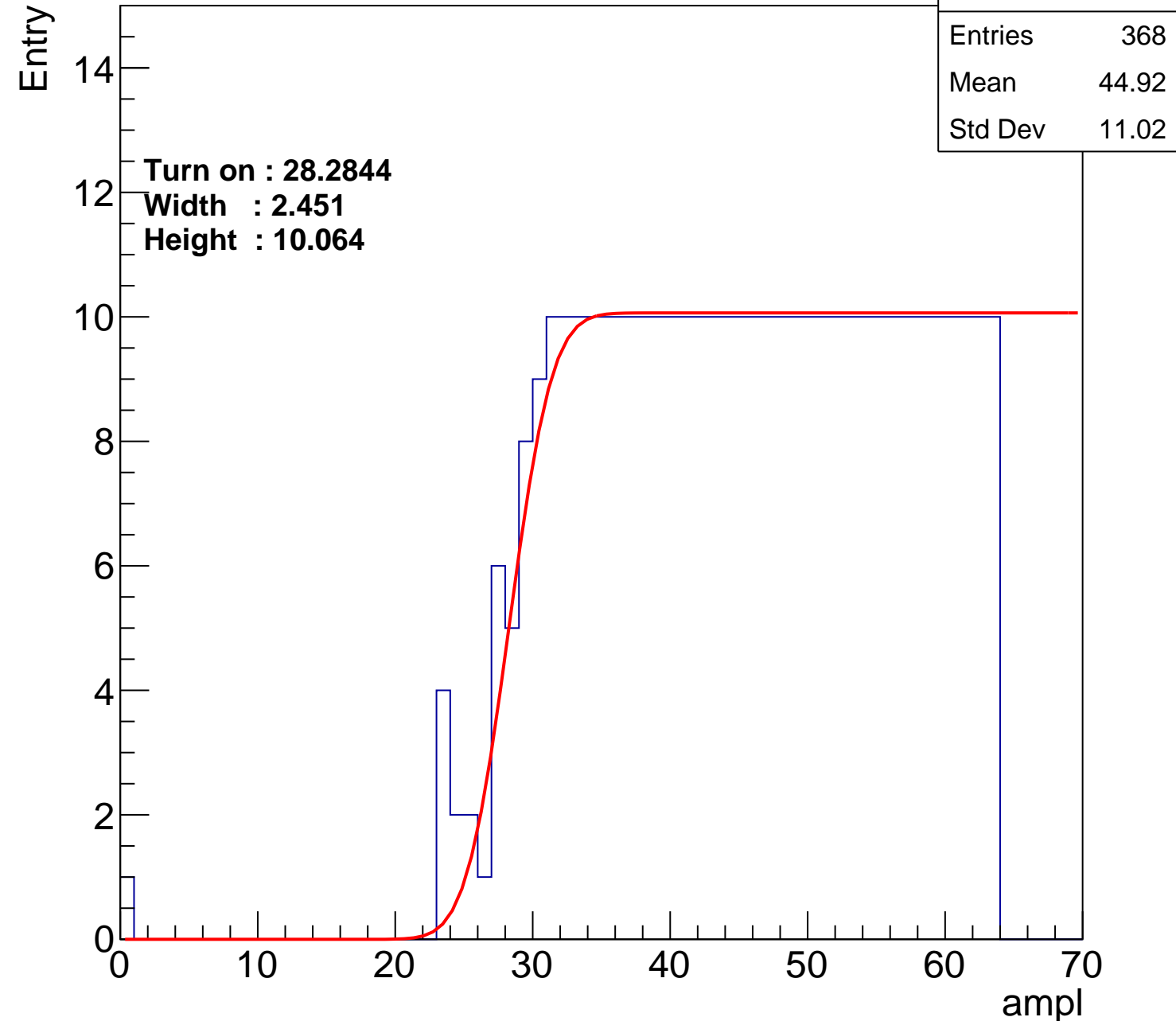
Width : 2.451

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch81

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.0800

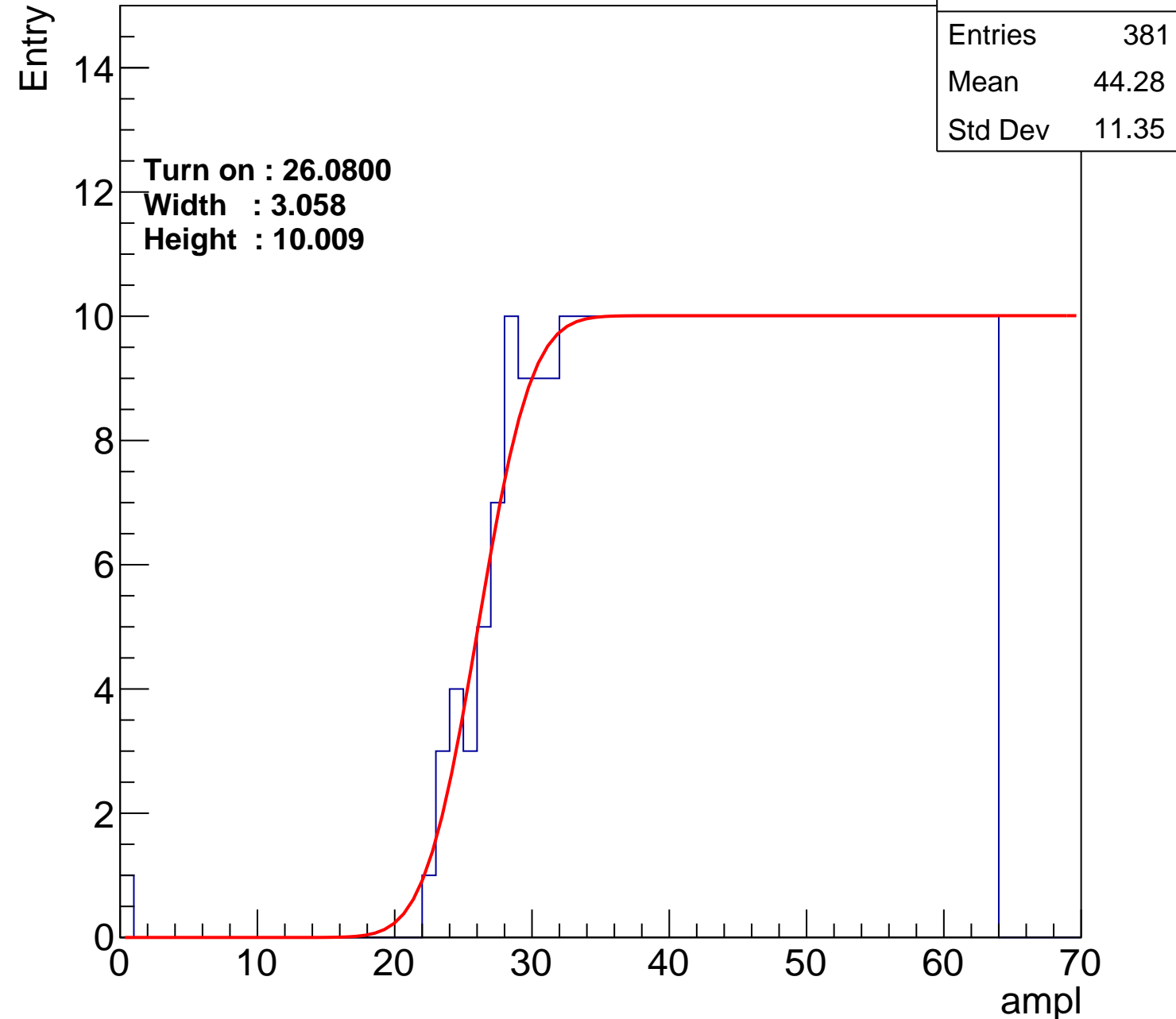
Width : 3.058

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch82

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.92
Std Dev	11.58

Turn on : 25.8174

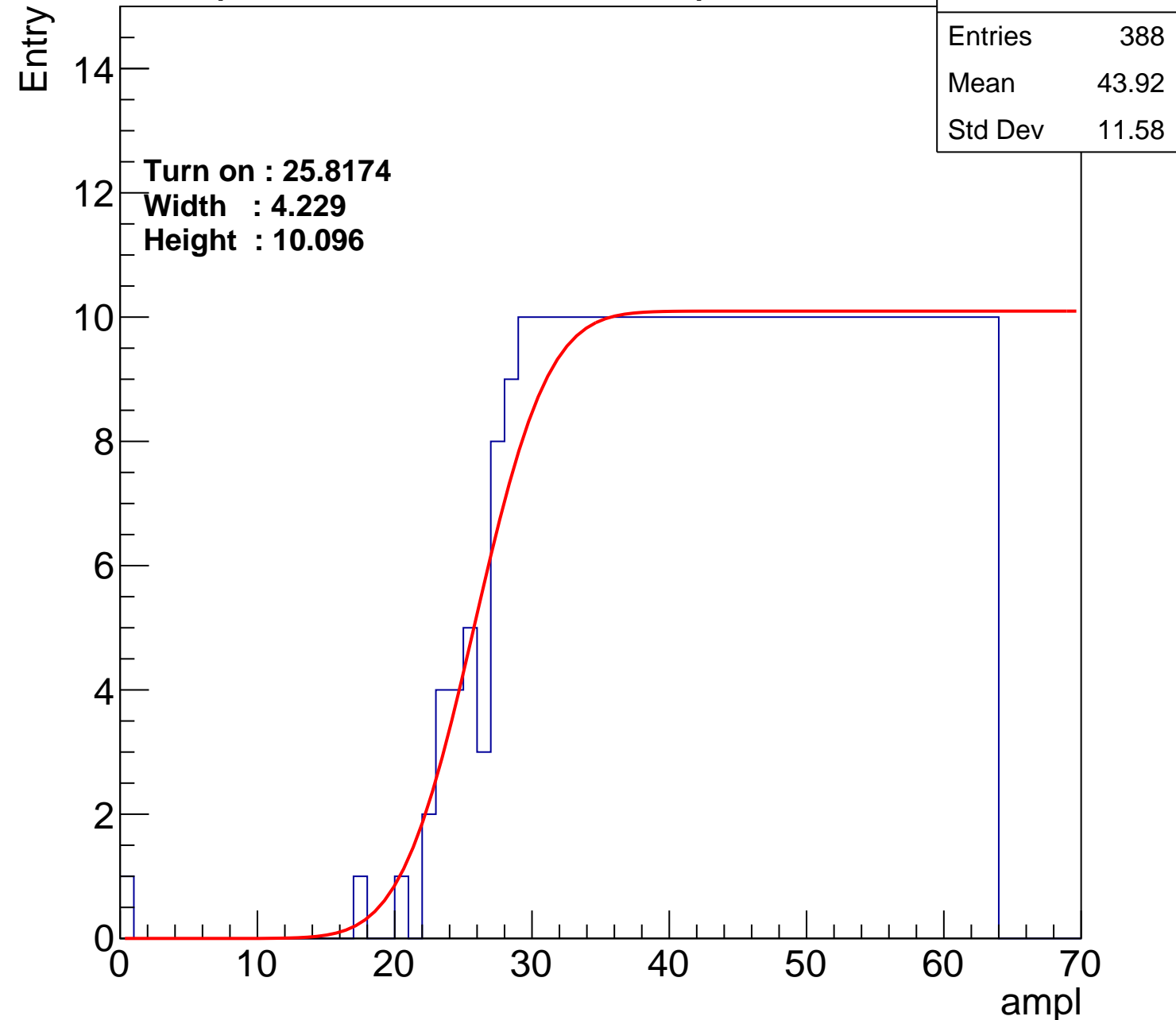
Width : 4.229

Height : 10.096

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch83

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	402
Mean	43.26
Std Dev	11.9

Turn on : 24.1058

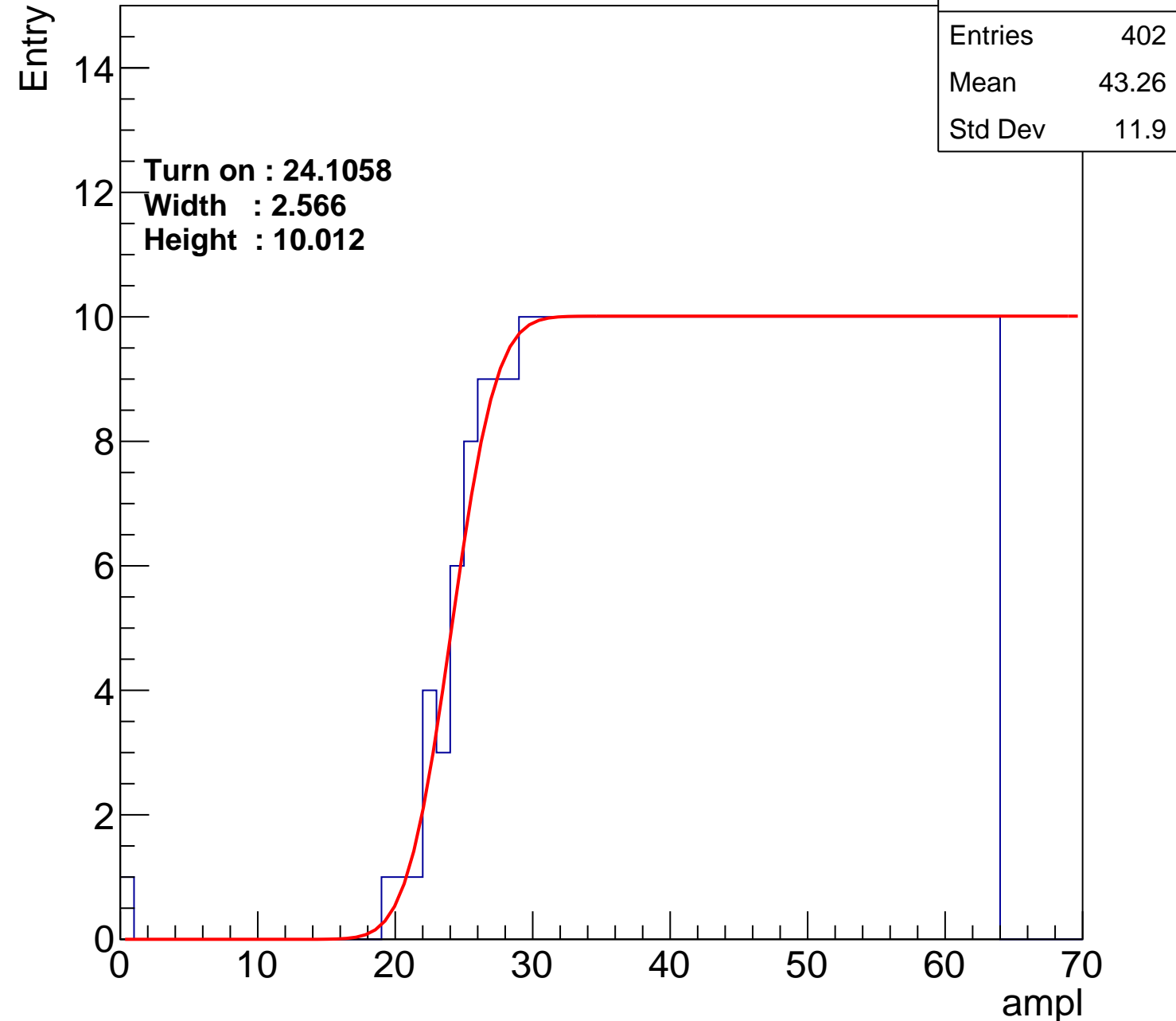
Width : 2.566

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch84

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	378
Mean	44.25
Std Dev	11.71

Turn on : 26.8178

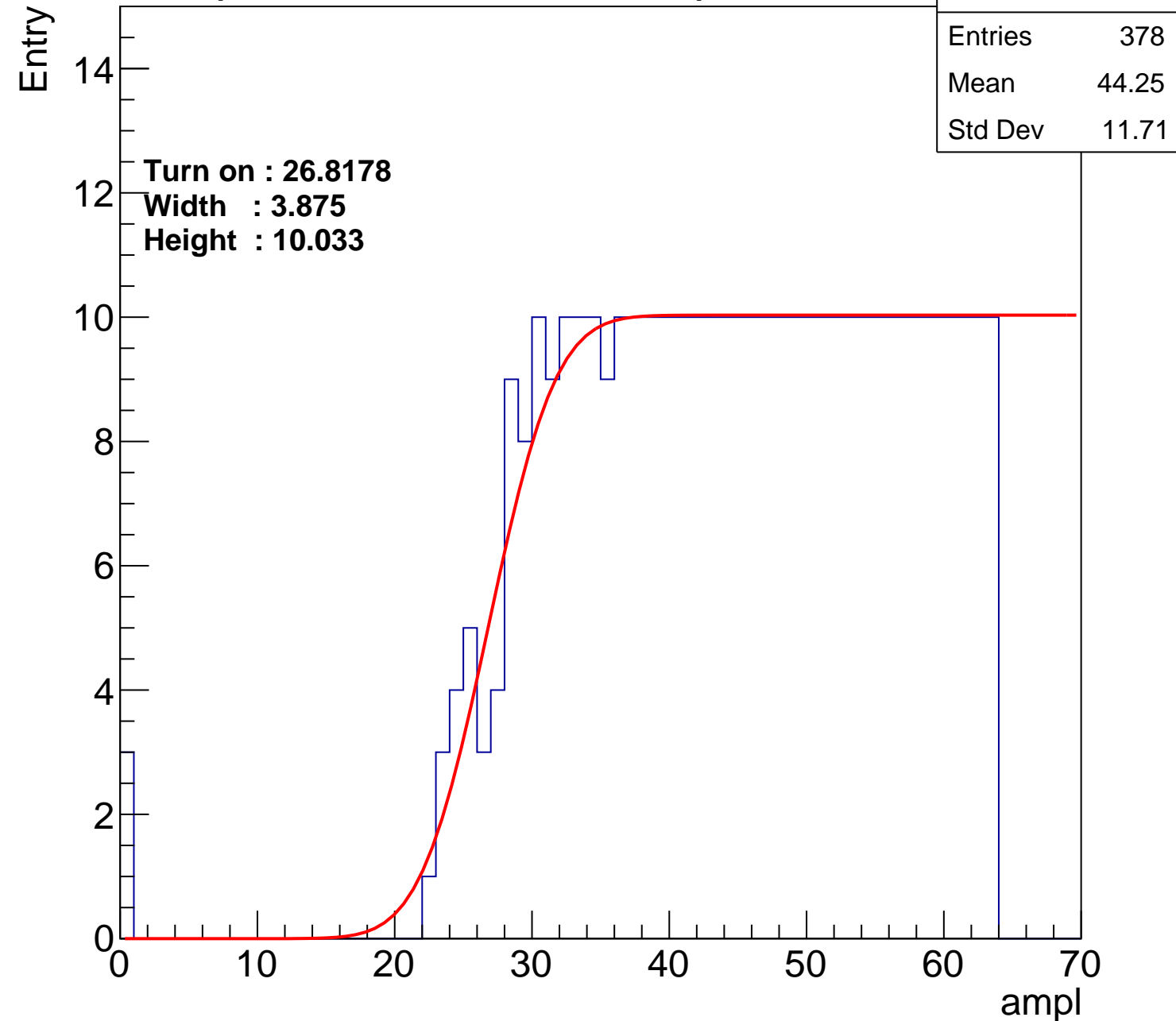
Width : 3.875

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch85

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.1476

Width : 1.777

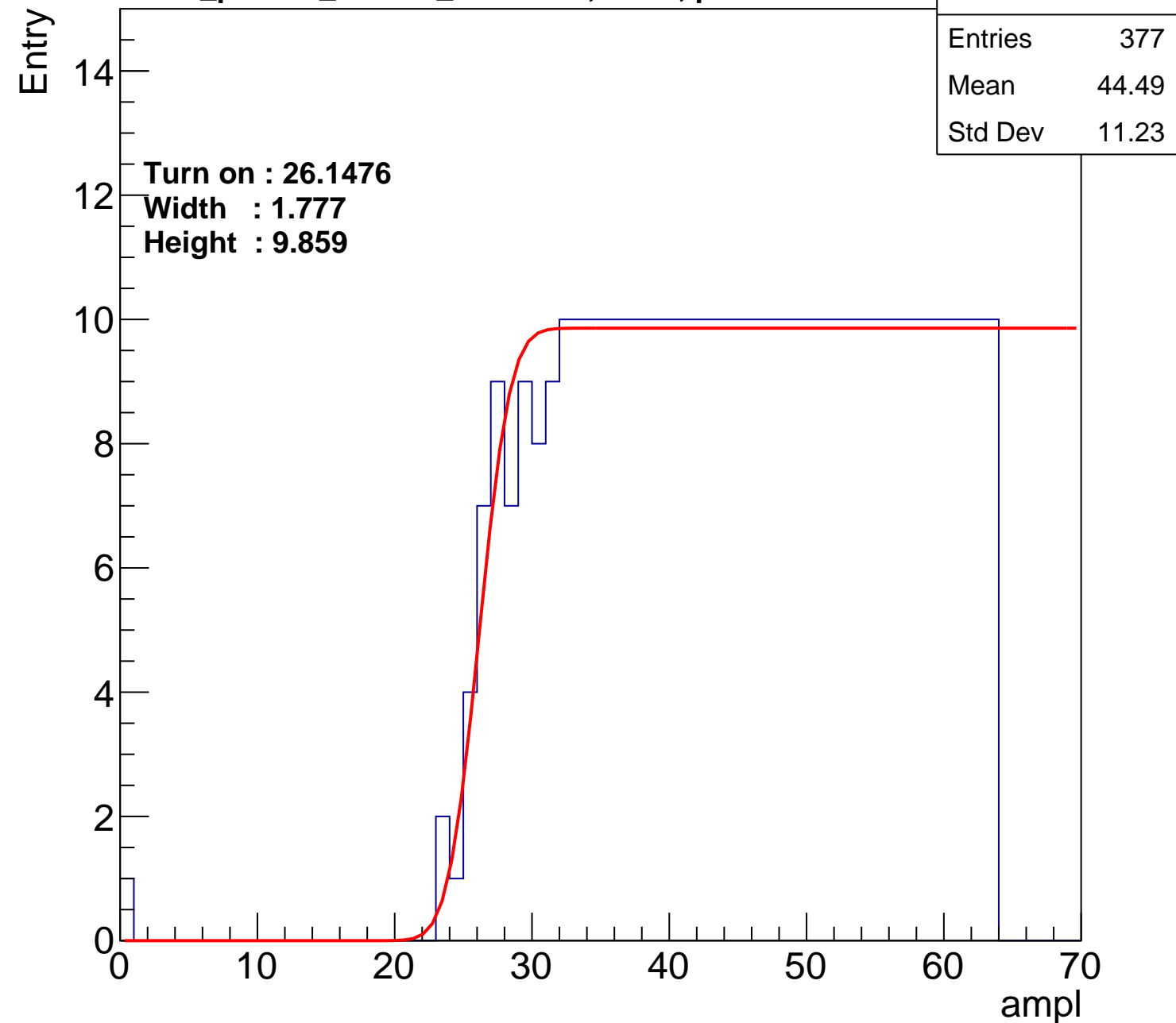
Height : 9.859

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L100S, U24-ch86

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.83
Std Dev	11.16

Turn on : 27.3794

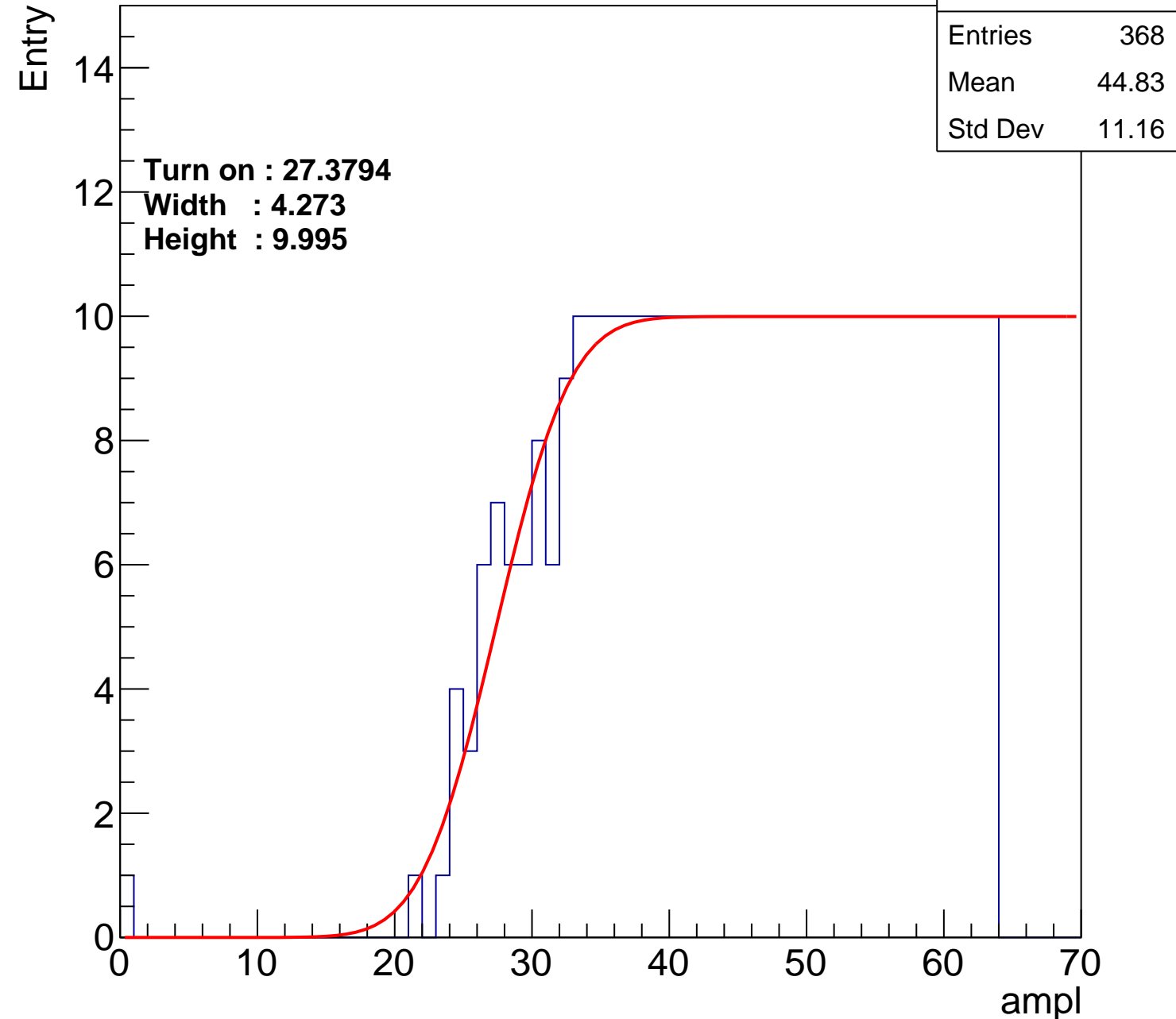
Width : 4.273

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch87

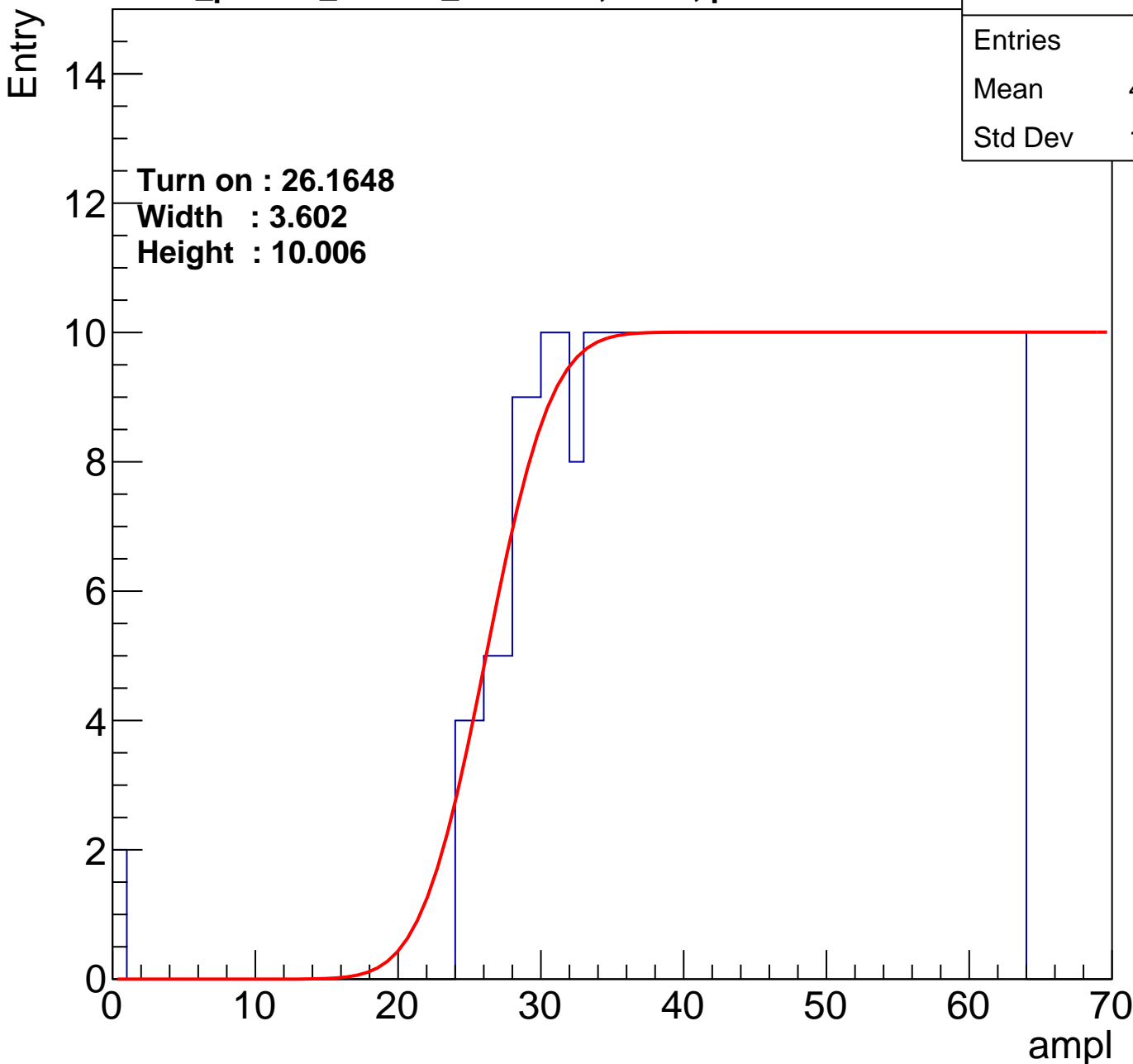
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	376
Mean	44.47
Std Dev	11.39

**Turn on : 26.1648**

**Width : 3.602**

**Height : 10.006**



# B1L100S, U24-ch88

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.7004

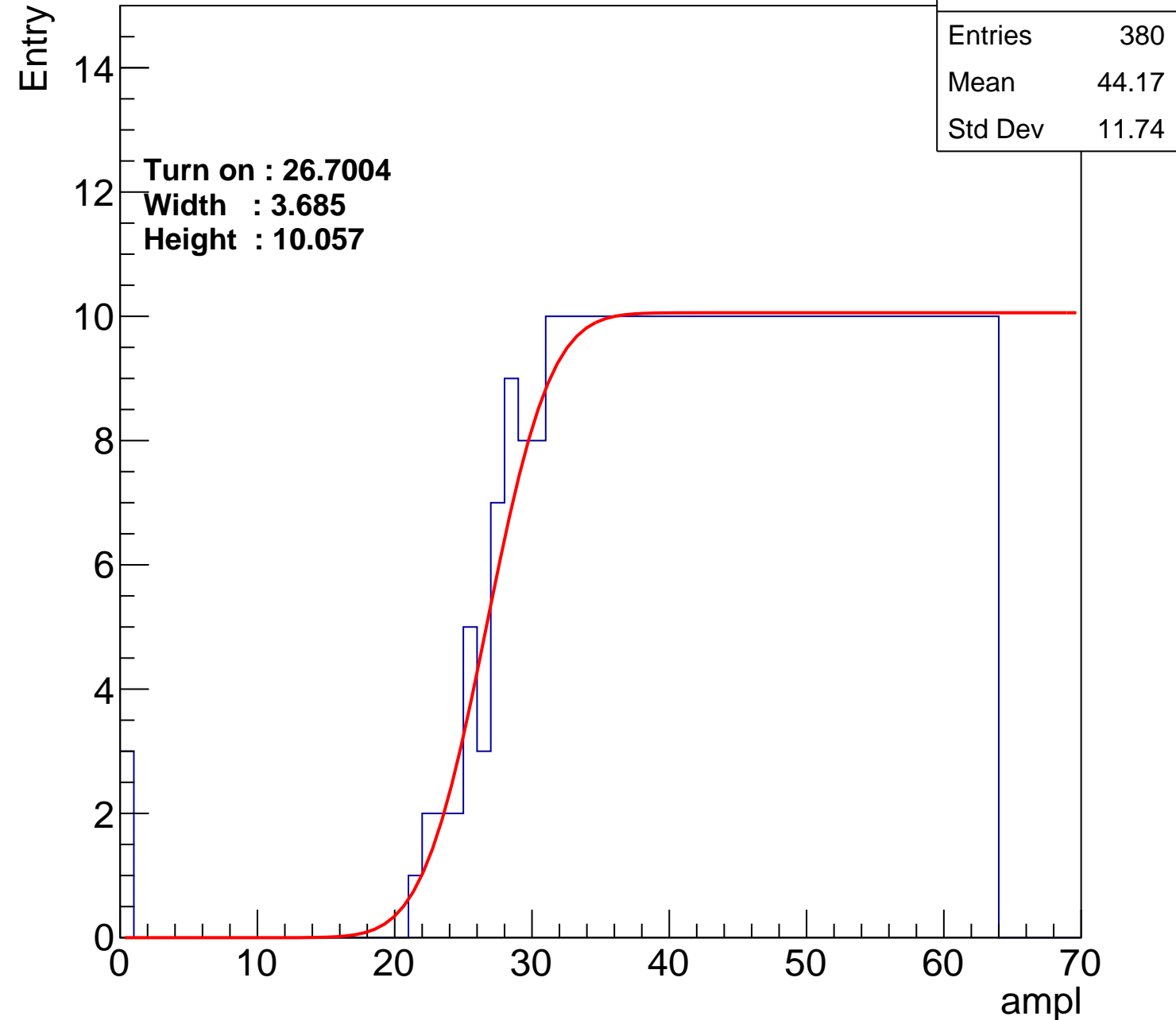
Width : 3.685

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch89

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	384
Mean	43.92
Std Dev	11.98

Turn on : 26.4420

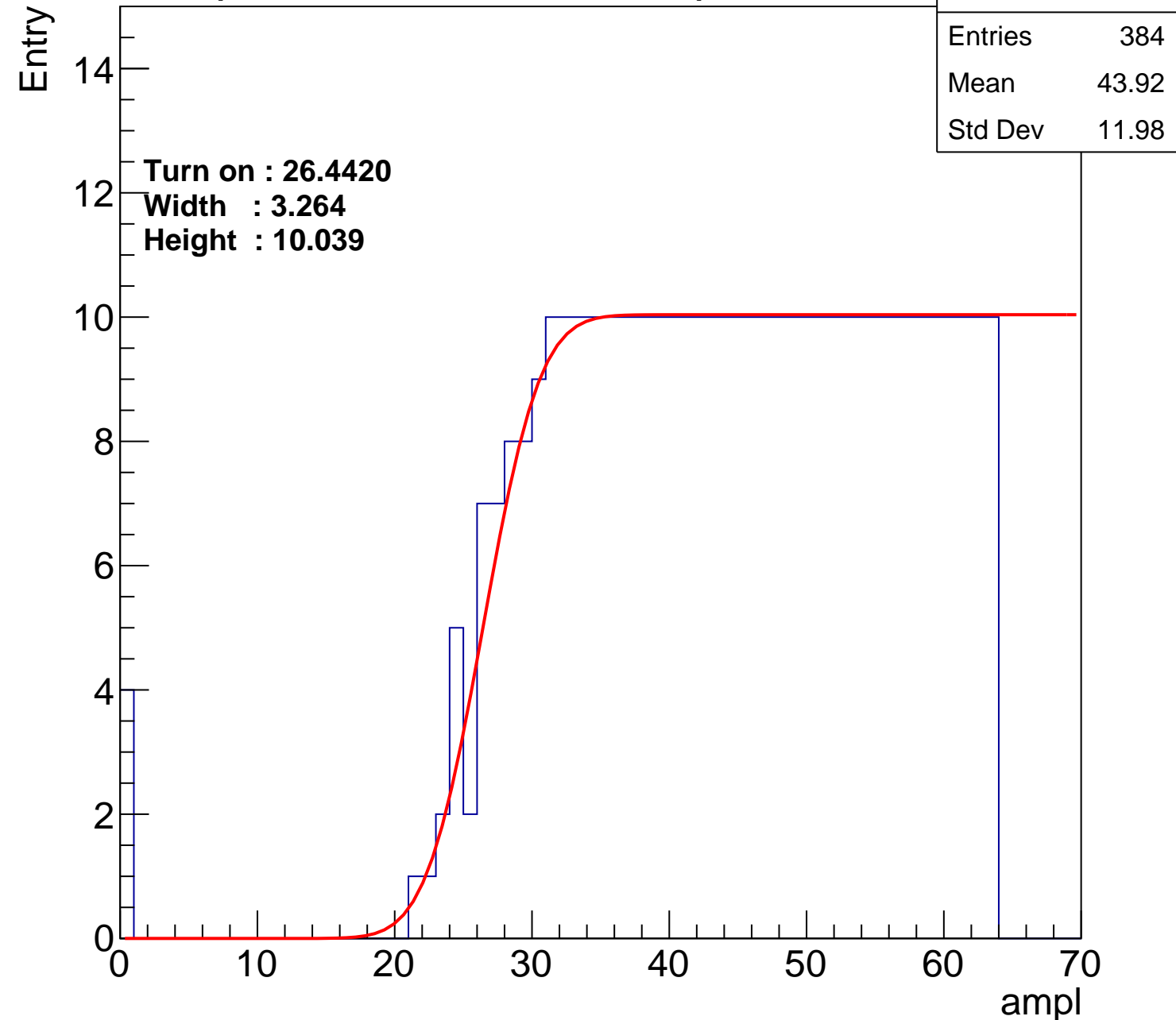
Width : 3.264

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch90

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.75
Std Dev	11.97

**Turn on : 25.3447**

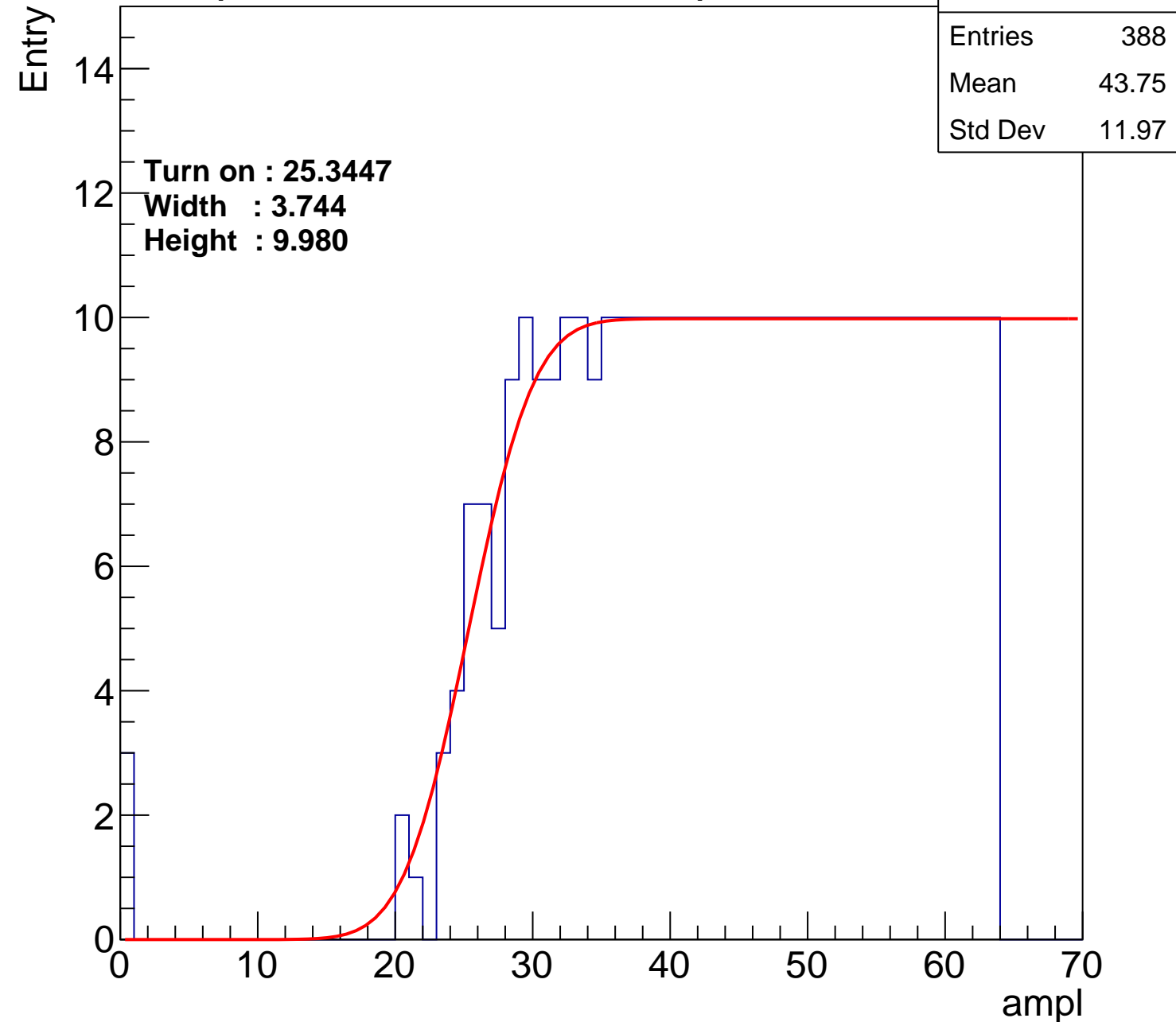
**Width : 3.744**

**Height : 9.980**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch91

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.91
Std Dev	10.98

Turn on : 27.6784

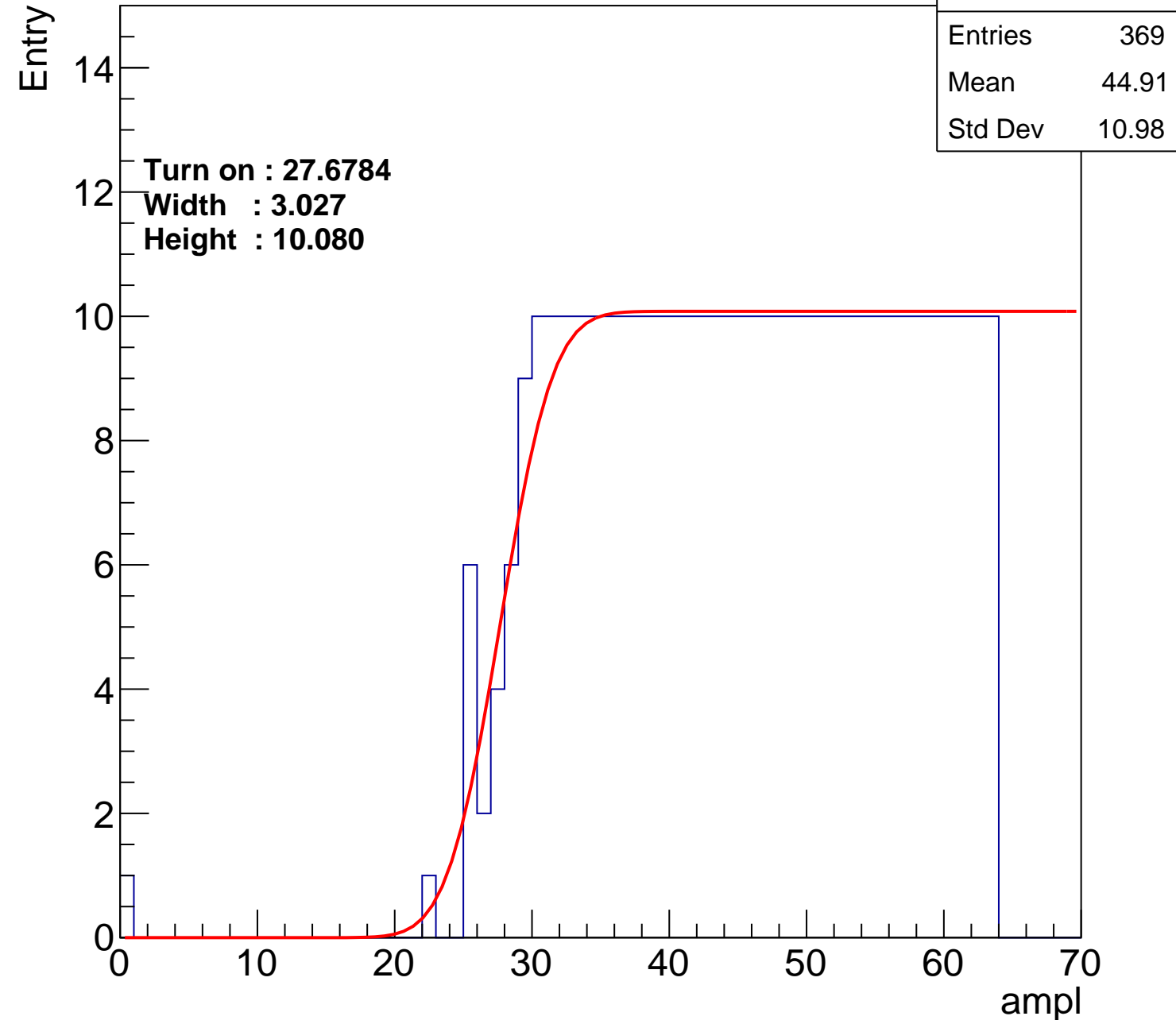
Width : 3.027

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch92

calib\_packv5\_042523\_0143.root, FC#4, port A2

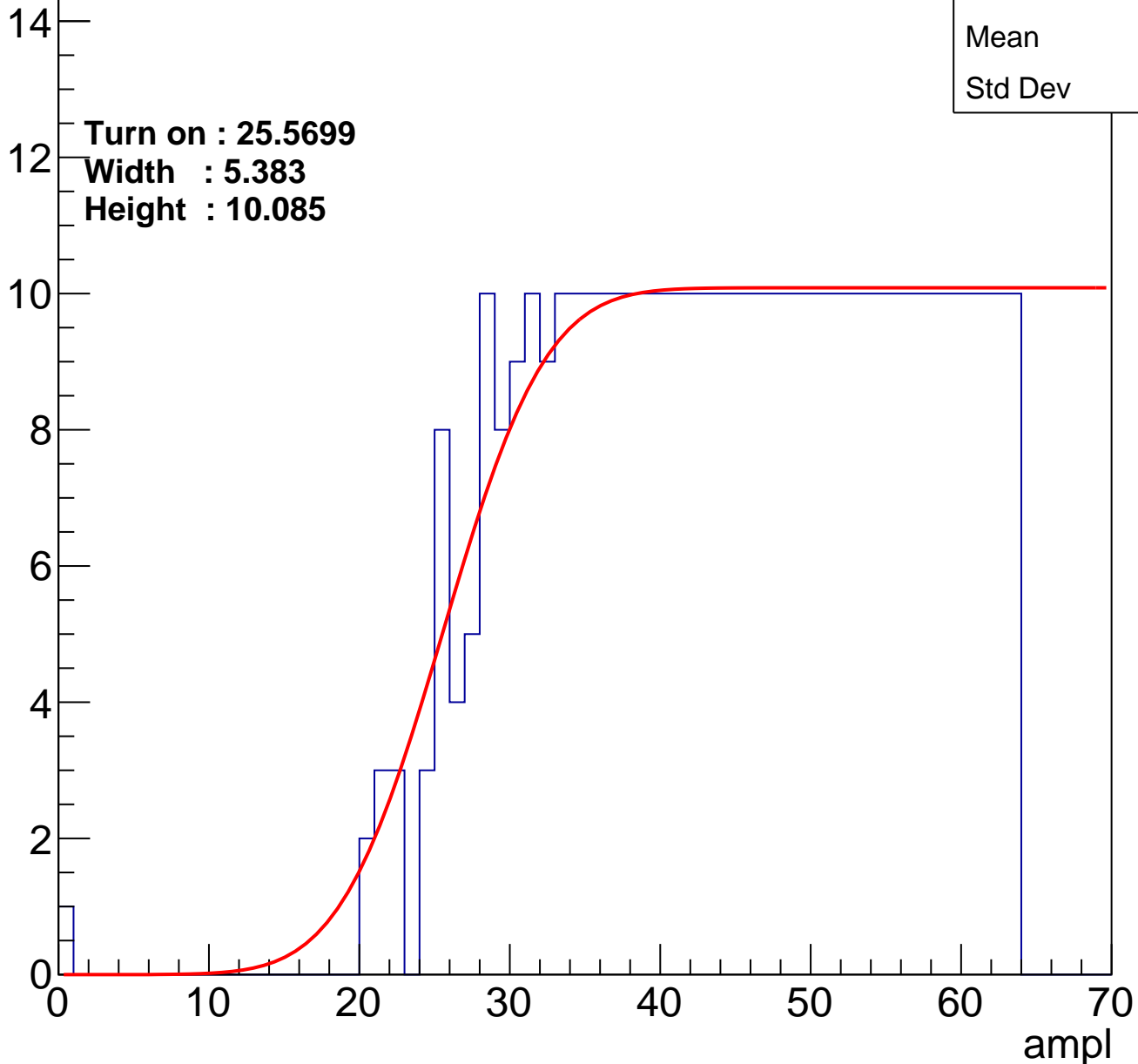
Entries	385
Mean	44
Std Dev	11.6

Turn on : 25.5699

Width : 5.383

Height : 10.085

Entry



# B1L100S, U24-ch93

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.55
Std Dev	11.65

Turn on : 27.7628

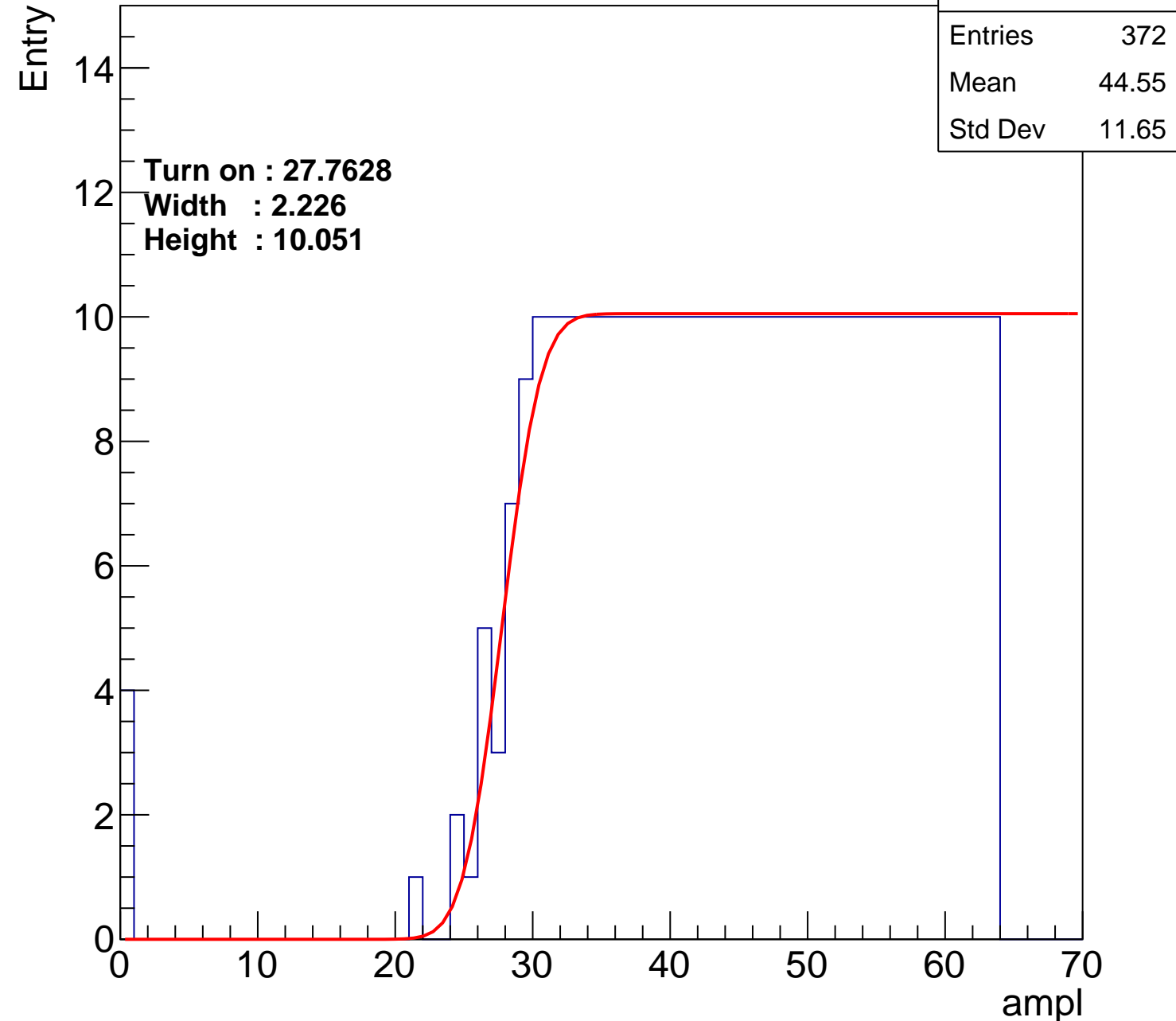
Width : 2.226

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch94

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	400
Mean	43.29
Std Dev	12.01

Turn on : 24.1149

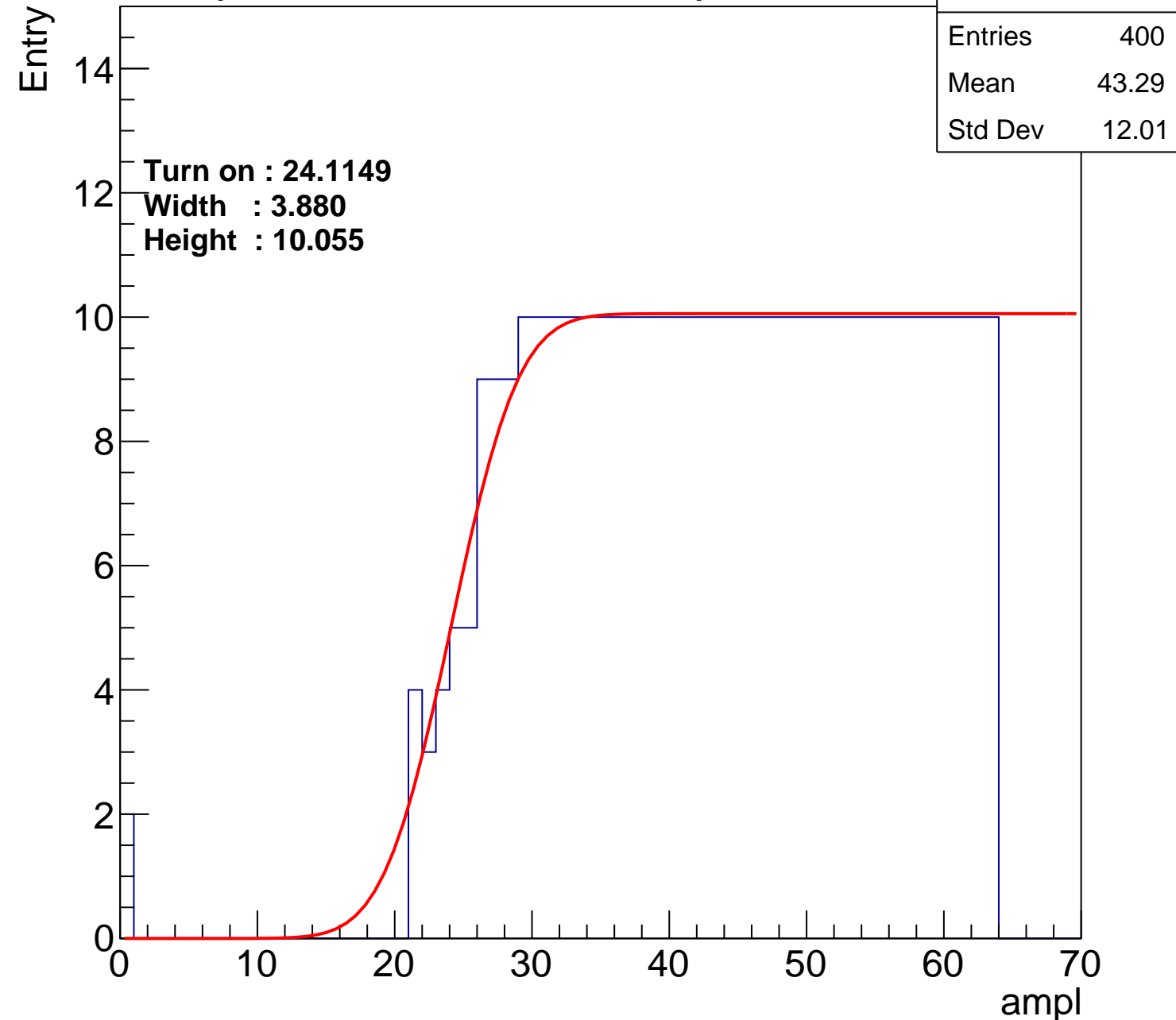
Width : 3.880

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch95

calib\_packv5\_042523\_0143.root, FC#4, port A2

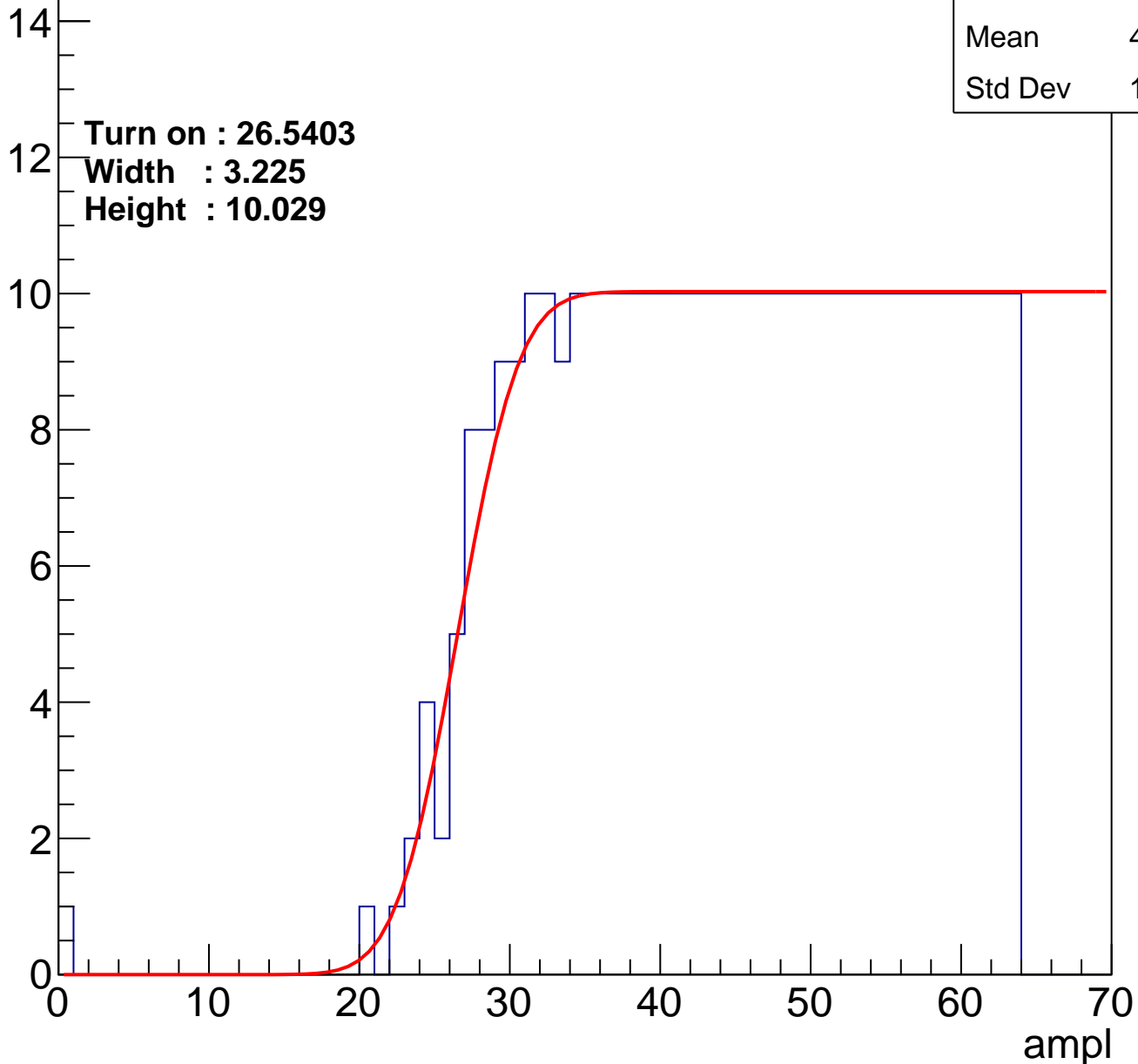
Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 26.5403

Width : 3.225

Height : 10.029

Entry



# B1L100S, U24-ch96

calib\_packv5\_042523\_0143.root, FC#4, port A2

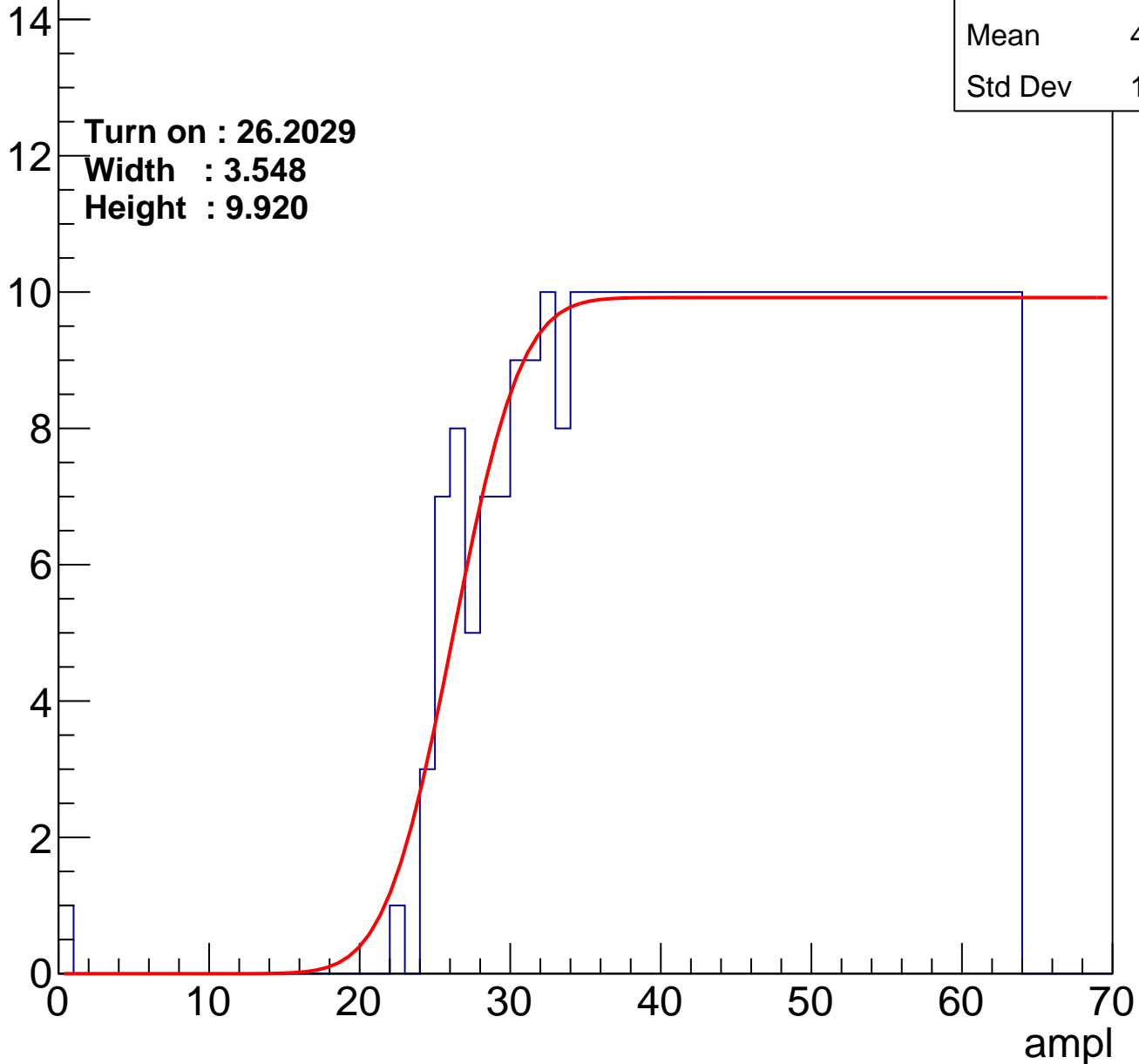
Entries	375
Mean	44.52
Std Dev	11.28

Turn on : 26.2029

Width : 3.548

Height : 9.920

Entry



# B1L100S, U24-ch97

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.1507

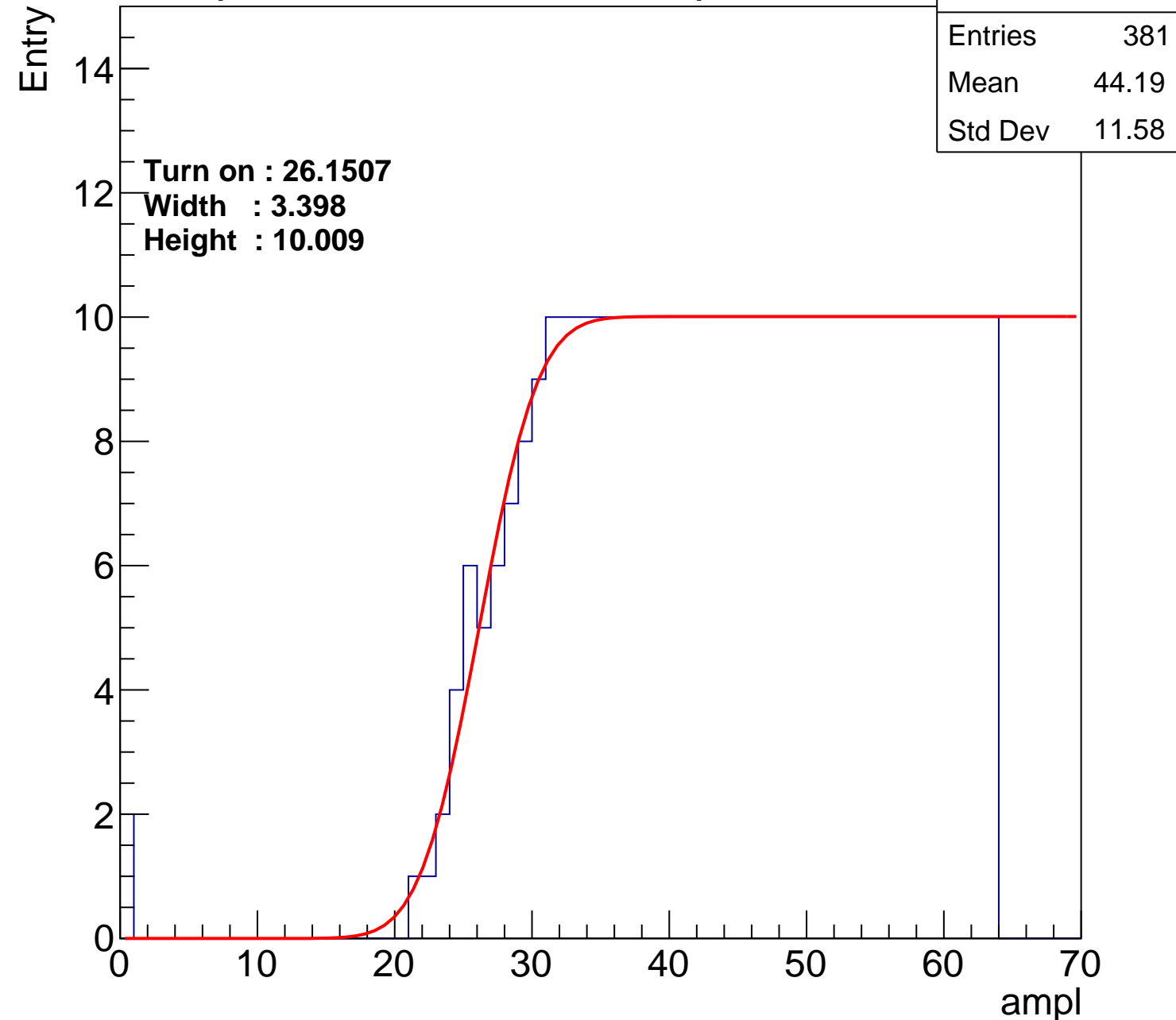
Width : 3.398

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch98

calib\_packv5\_042523\_0143.root, FC#4, port A2

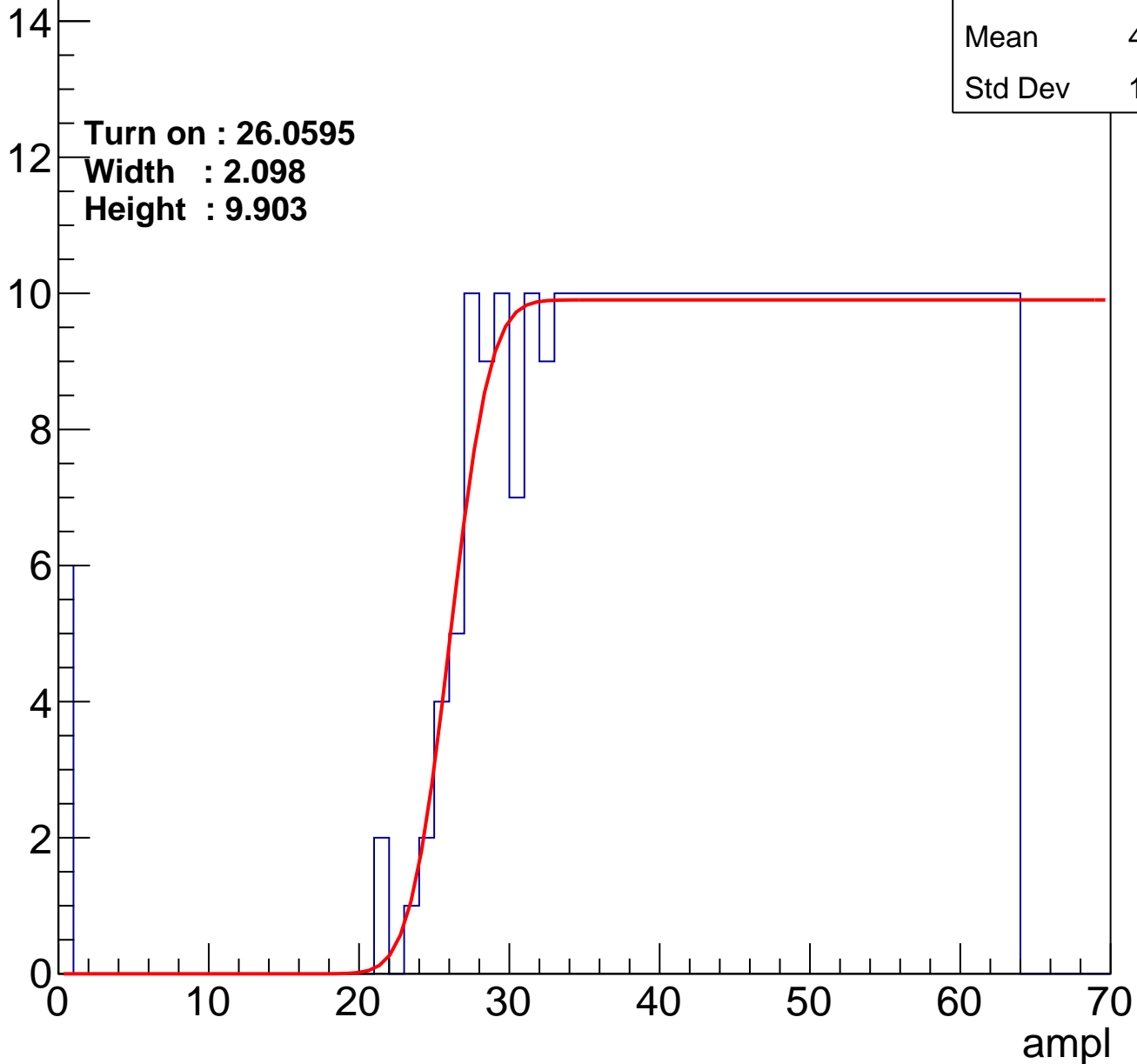
Entries	385
Mean	43.75
Std Dev	12.33

Turn on : 26.0595

Width : 2.098

Height : 9.903

Entry



# B1L100S, U24-ch99

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.5061

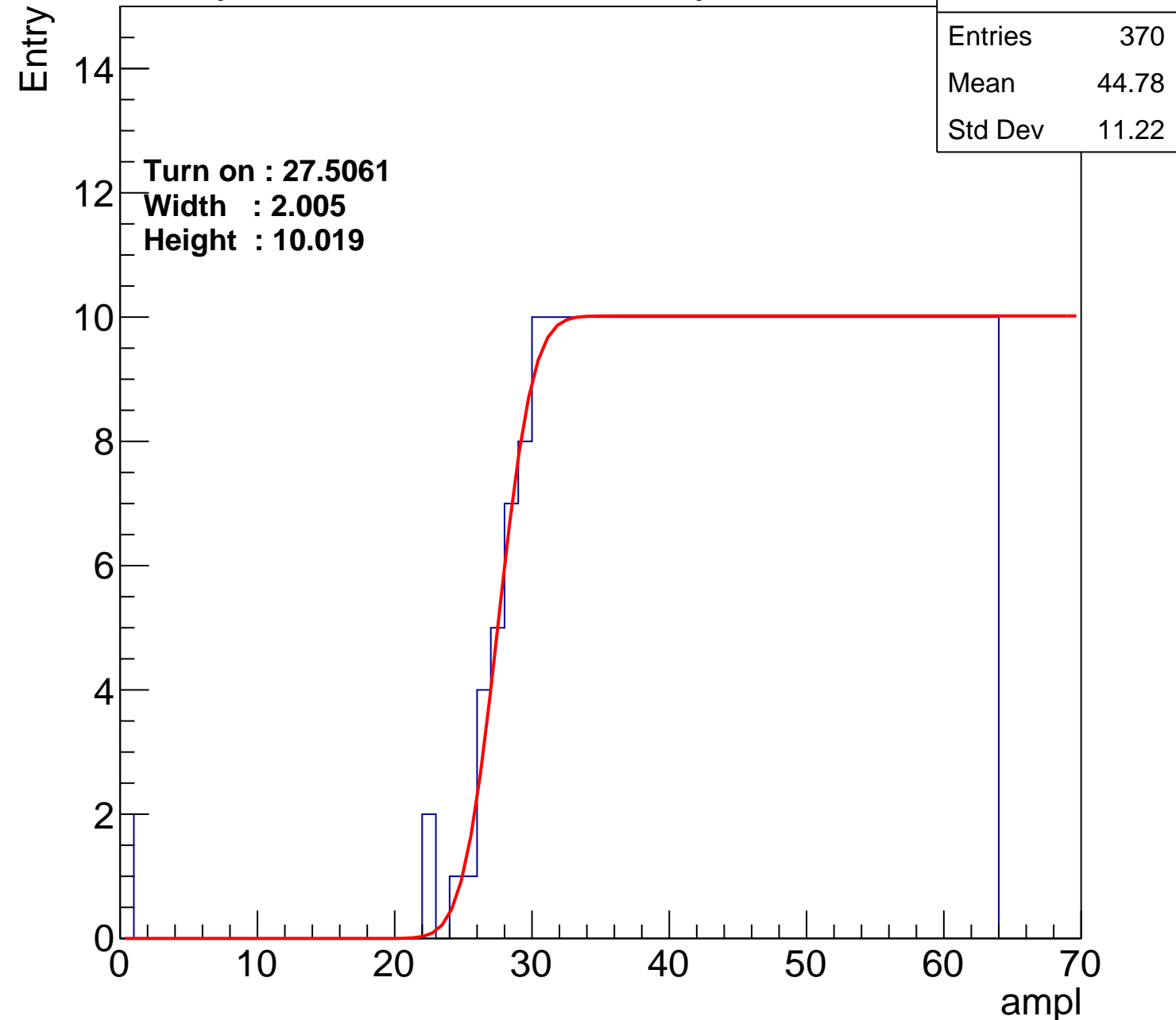
Width : 2.005

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch100

calib\_packv5\_042523\_0143.root, FC#4, port A2

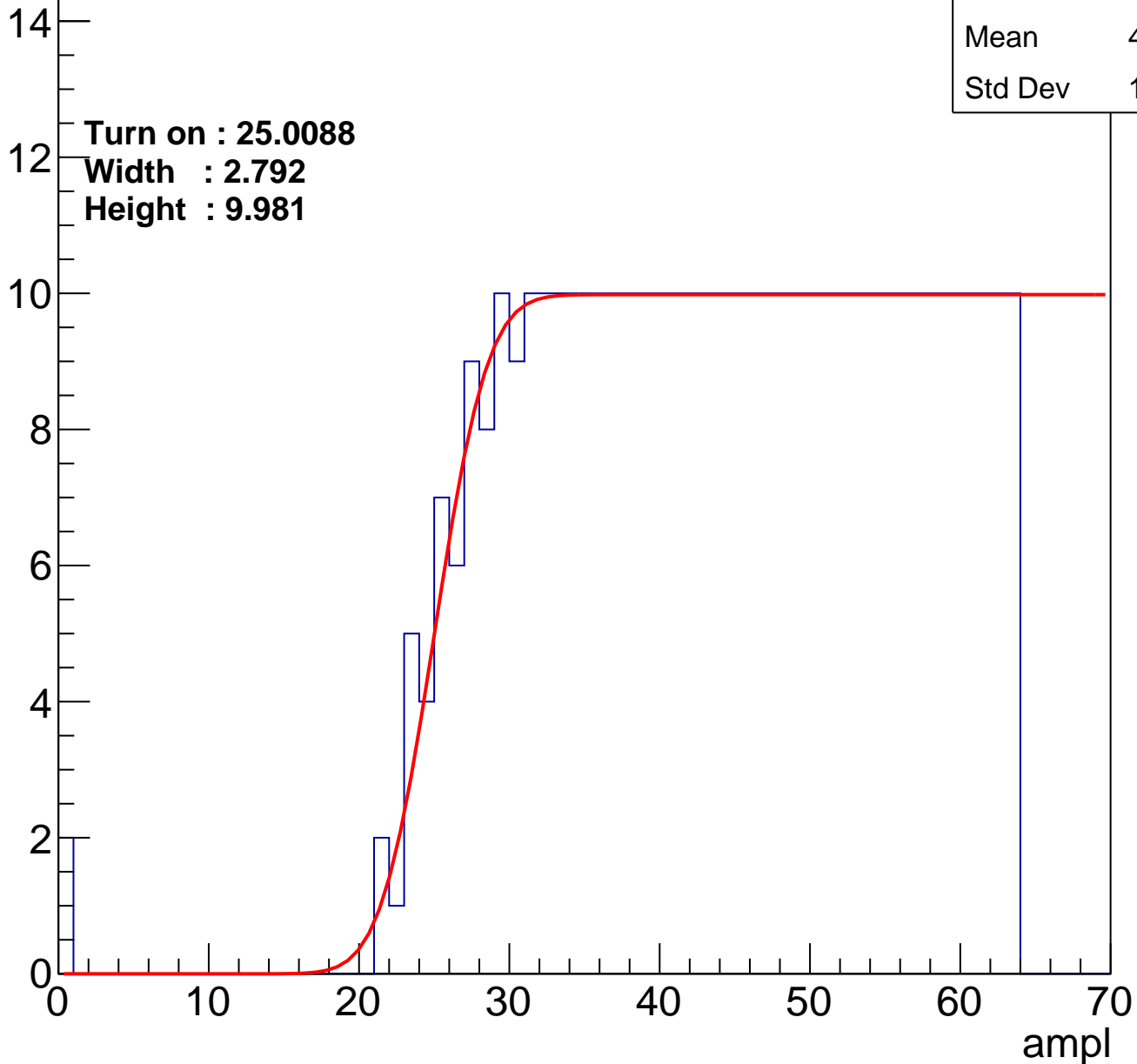
Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 25.0088

Width : 2.792

Height : 9.981

Entry



# B1L100S, U24-ch101

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	392
Mean	43.5
Std Dev	12.21

Turn on : 25.8643

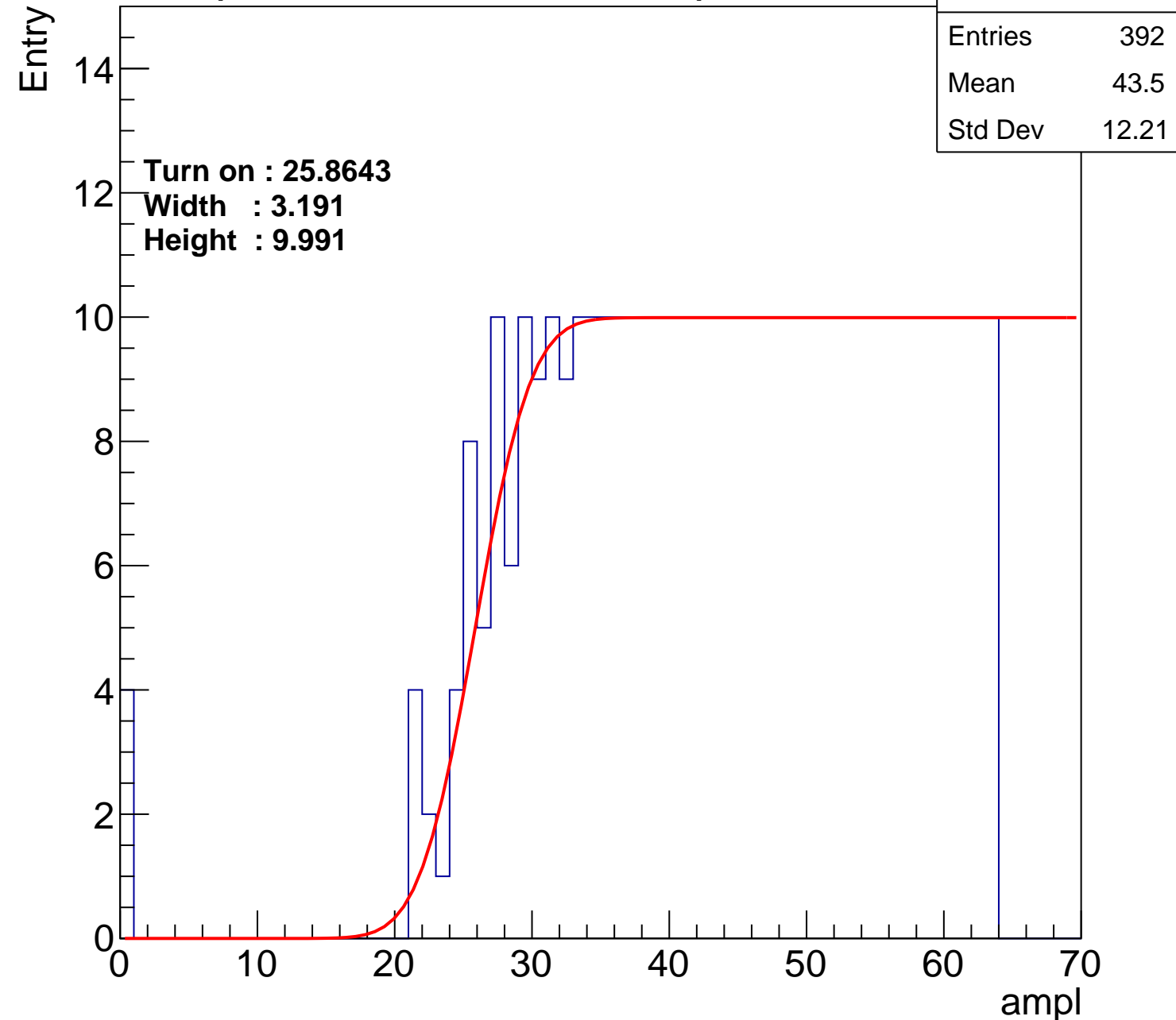
Width : 3.191

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch102

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	399
Mean	43.18
Std Dev	12.41

Turn on : 24.5783

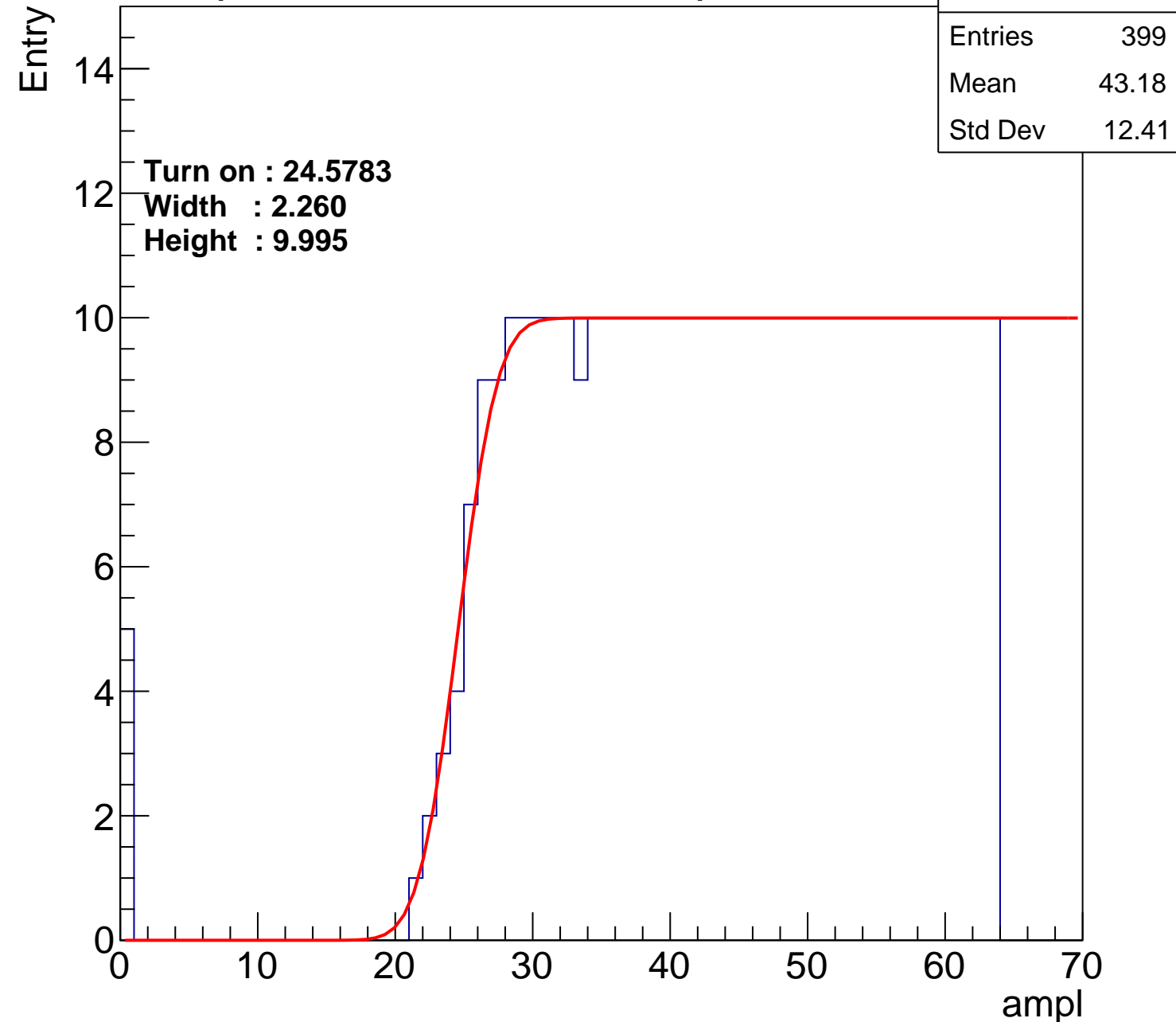
Width : 2.260

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch103

calib\_packv5\_042523\_0143.root, FC#4, port A2

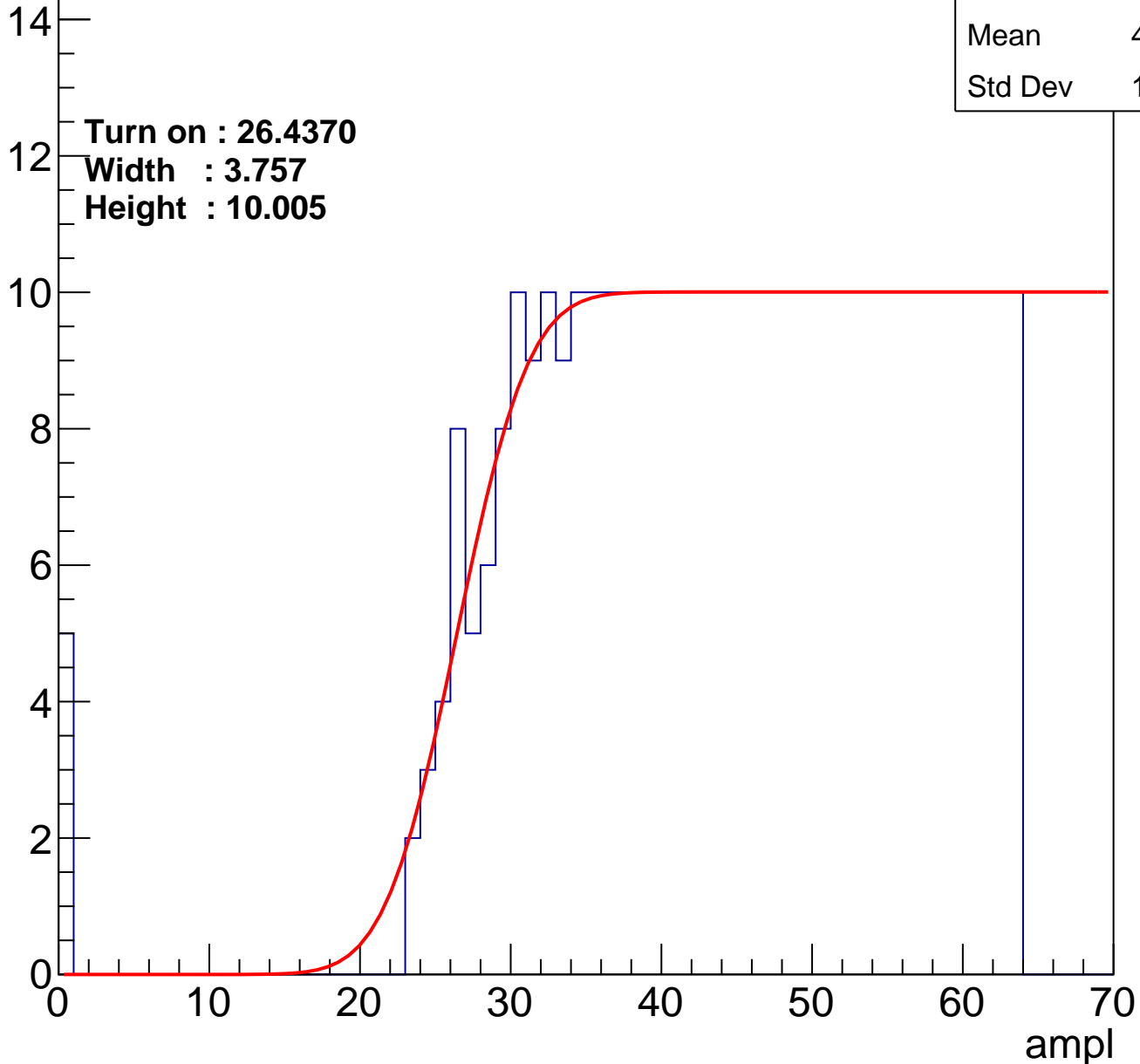
Entries	379
Mean	44.08
Std Dev	12.06

Turn on : 26.4370

Width : 3.757

Height : 10.005

Entry



# B1L100S, U24-ch104

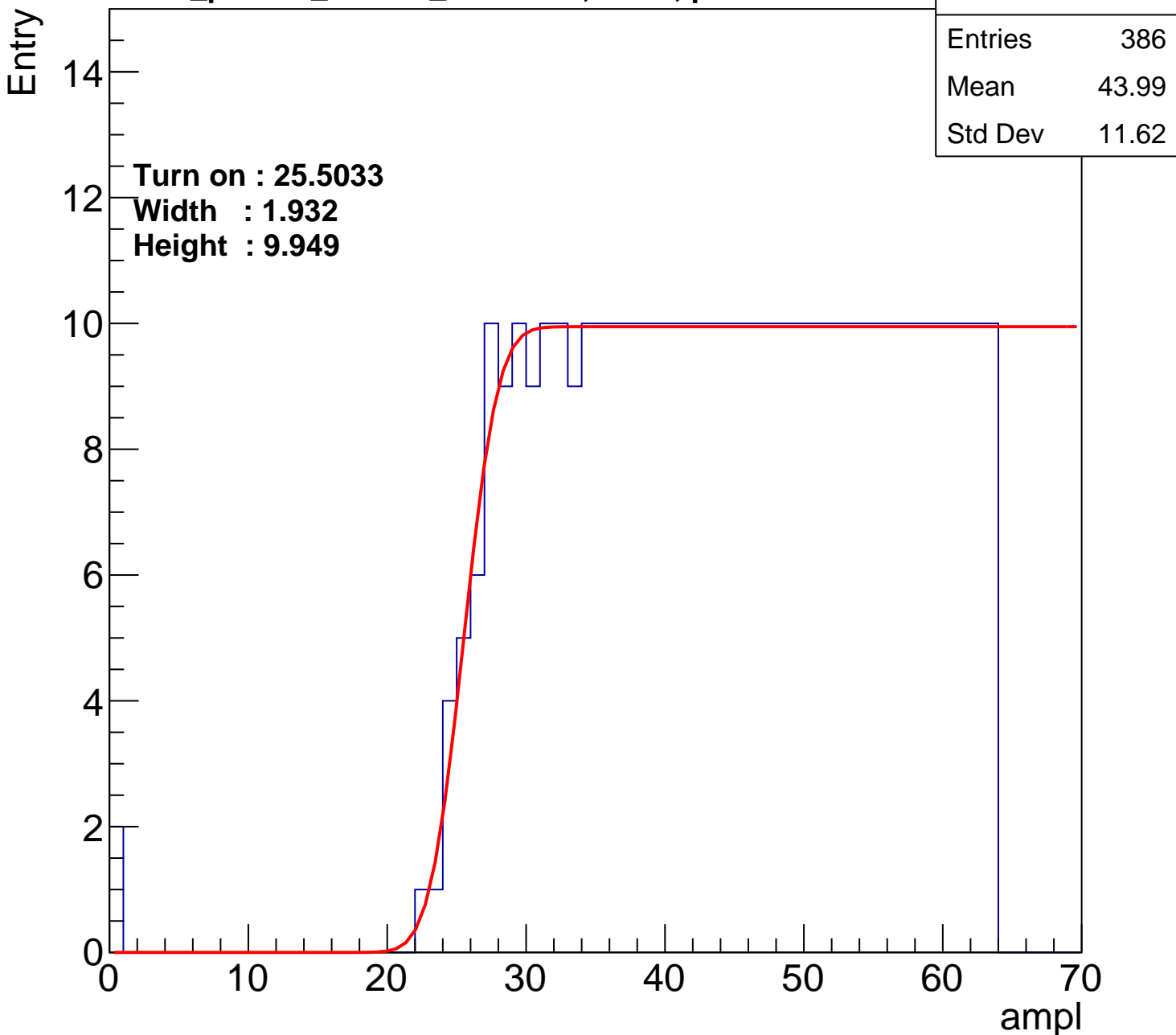
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

**Turn on : 25.5033**

**Width : 1.932**

**Height : 9.949**

Entries	386
Mean	43.99
Std Dev	11.62



# B1L100S, U24-ch105

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.93
Std Dev	10.99

Turn on : 27.4644

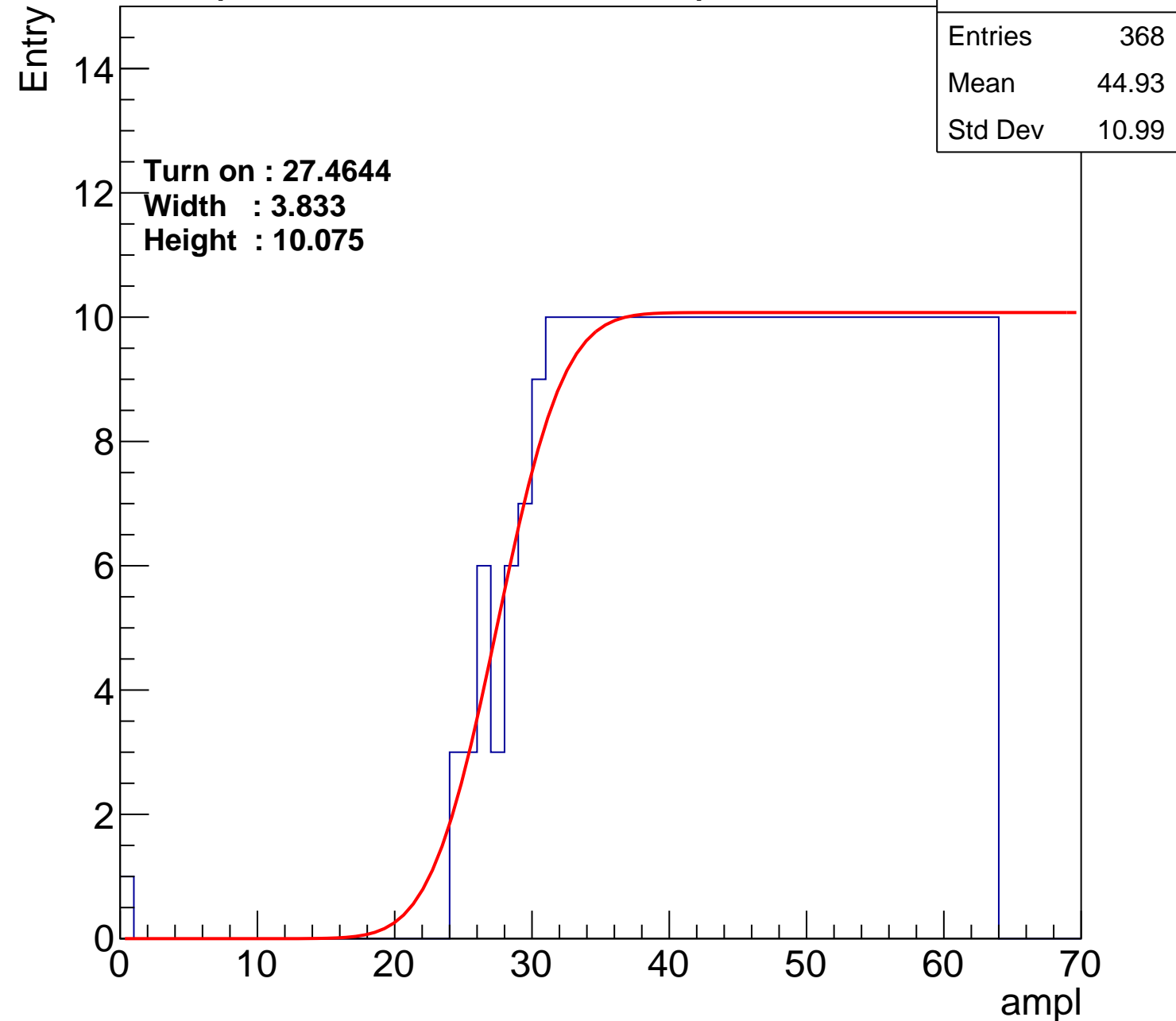
Width : 3.833

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch106

calib\_packv5\_042523\_0143.root, FC#4, port A2

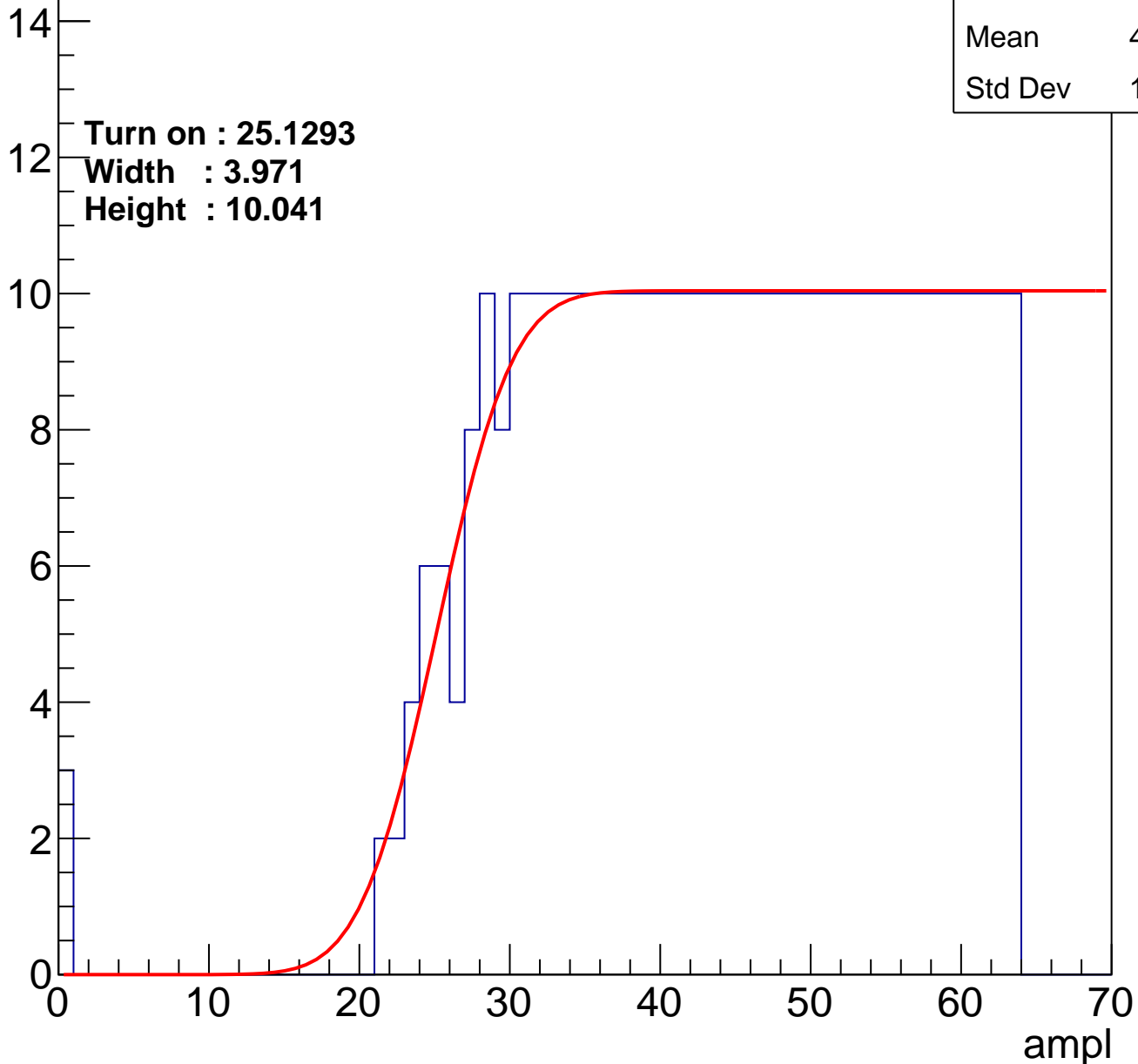
Entries	393
Mean	43.55
Std Dev	12.03

Turn on : 25.1293

Width : 3.971

Height : 10.041

Entry



# B1L100S, U24-ch107

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	350
Mean	45.66
Std Dev	10.99

**Turn on : 29.4045**

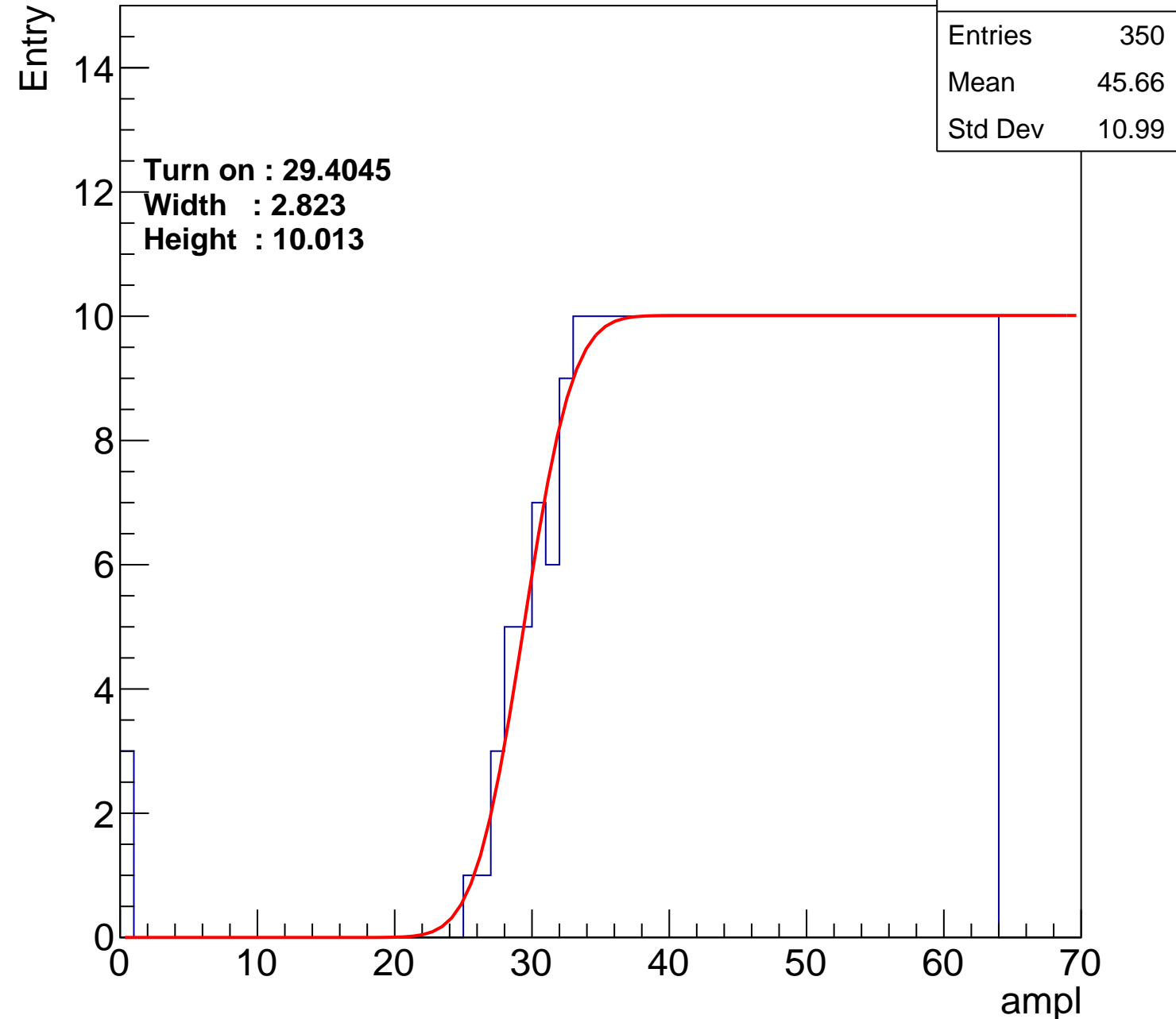
**Width : 2.823**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch108

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	43.98
Std Dev	11.77

Turn on : 26.0704

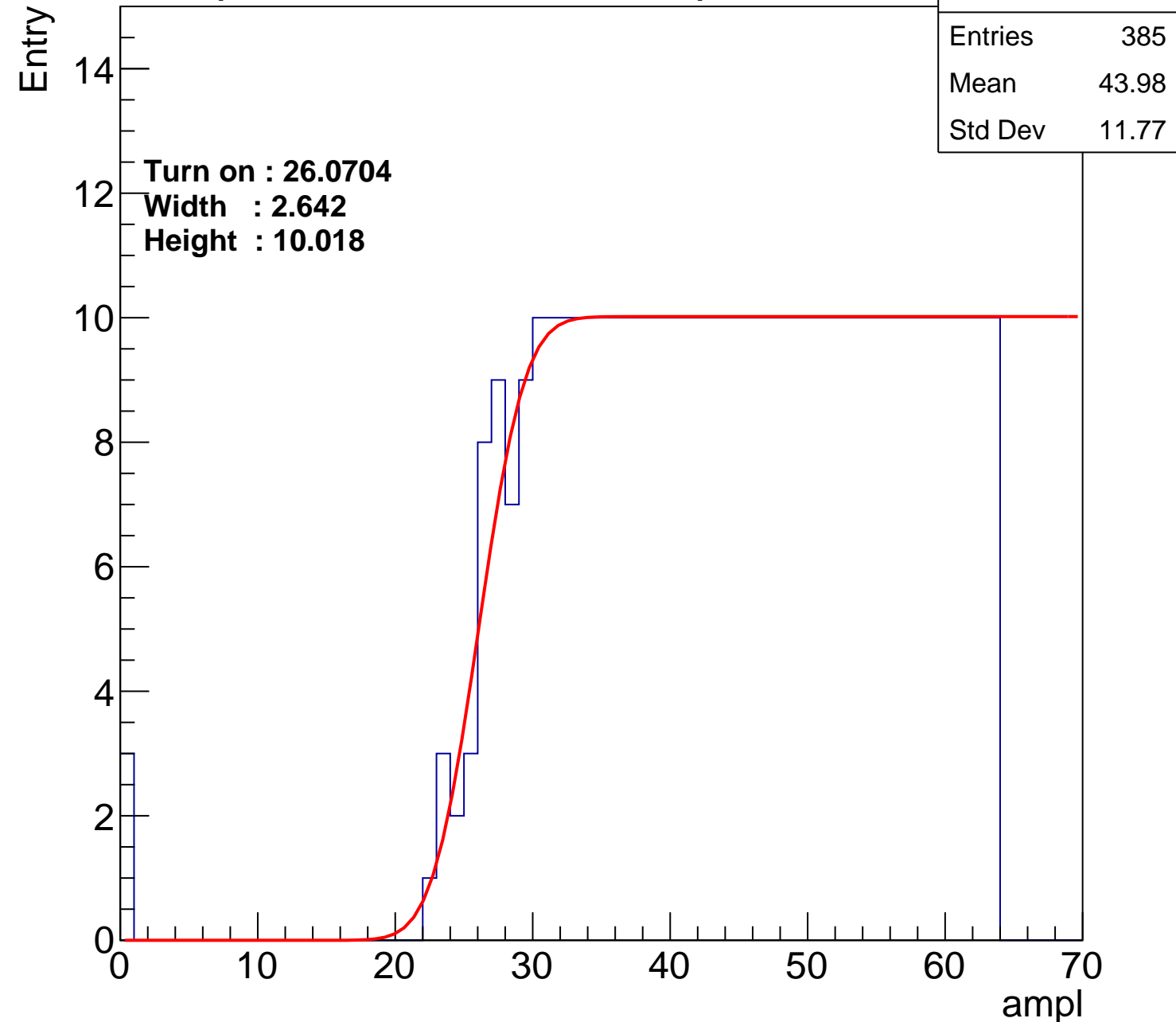
Width : 2.642

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch109

calib\_packv5\_042523\_0143.root, FC#4, port A2

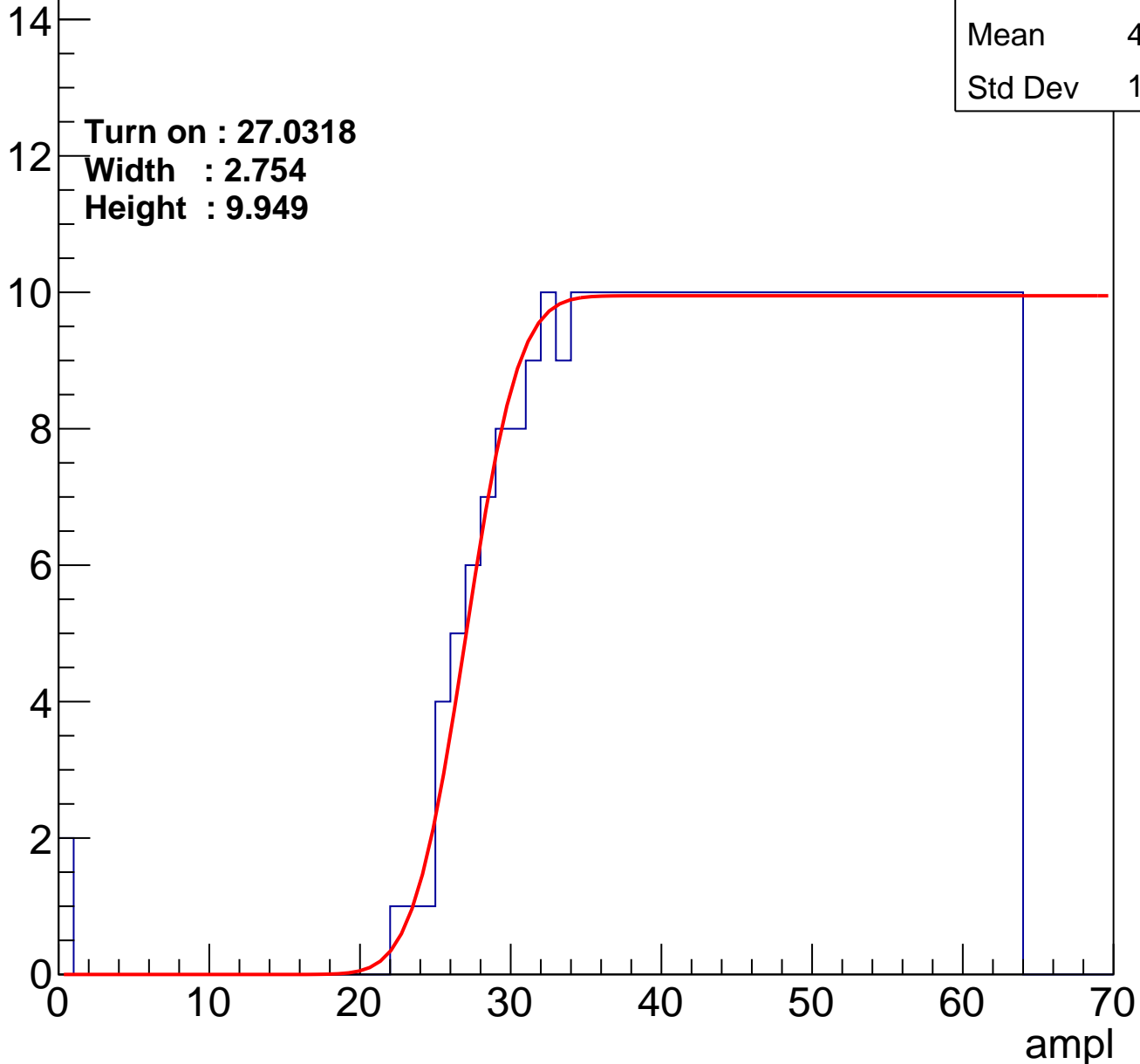
Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.0318

Width : 2.754

Height : 9.949

Entry



# B1L100S, U24-ch110

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.76
Std Dev	11.45

Turn on : 28.2023

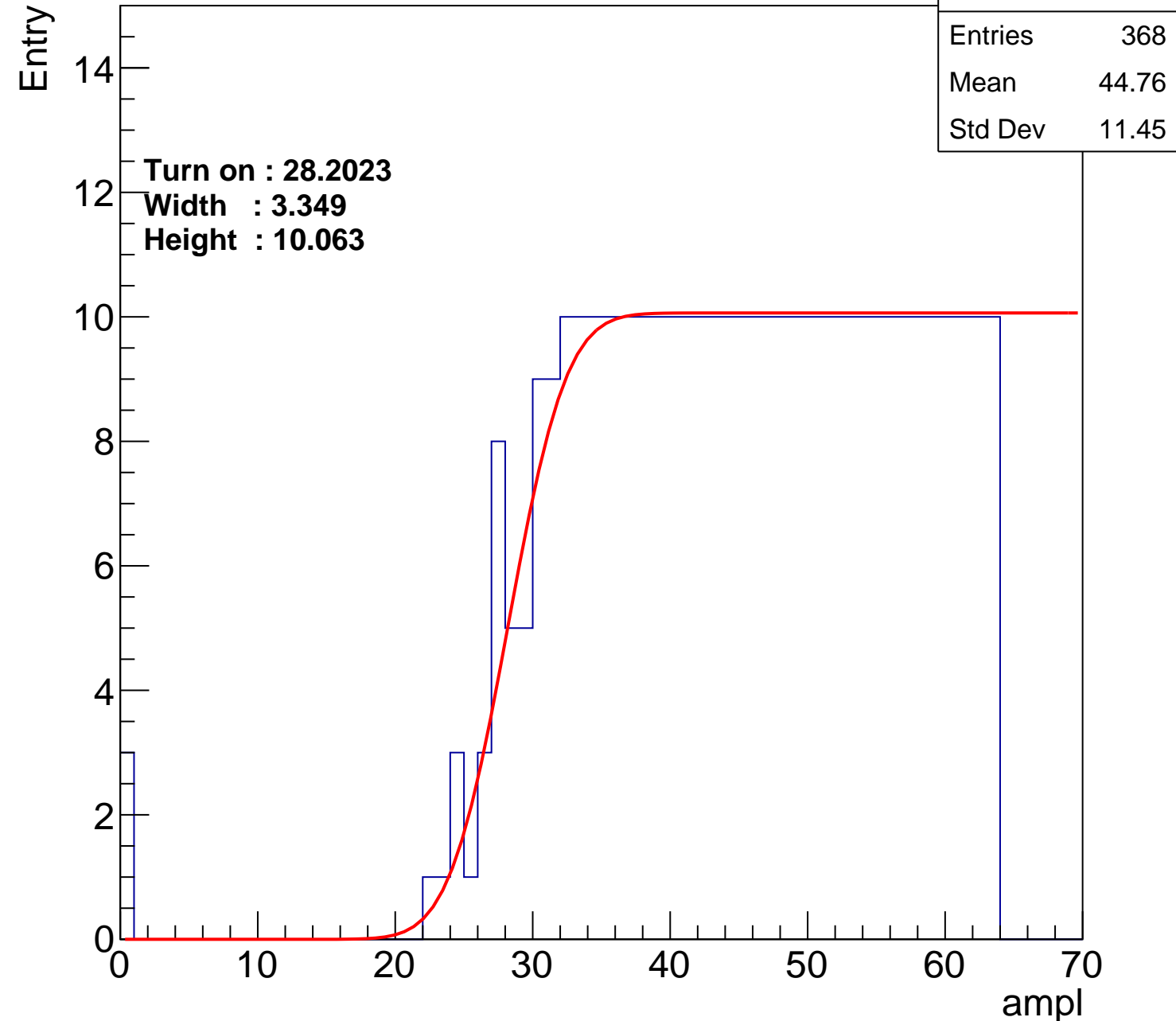
Width : 3.349

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch111

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.82
Std Dev	11.2

**Turn on : 27.5350**

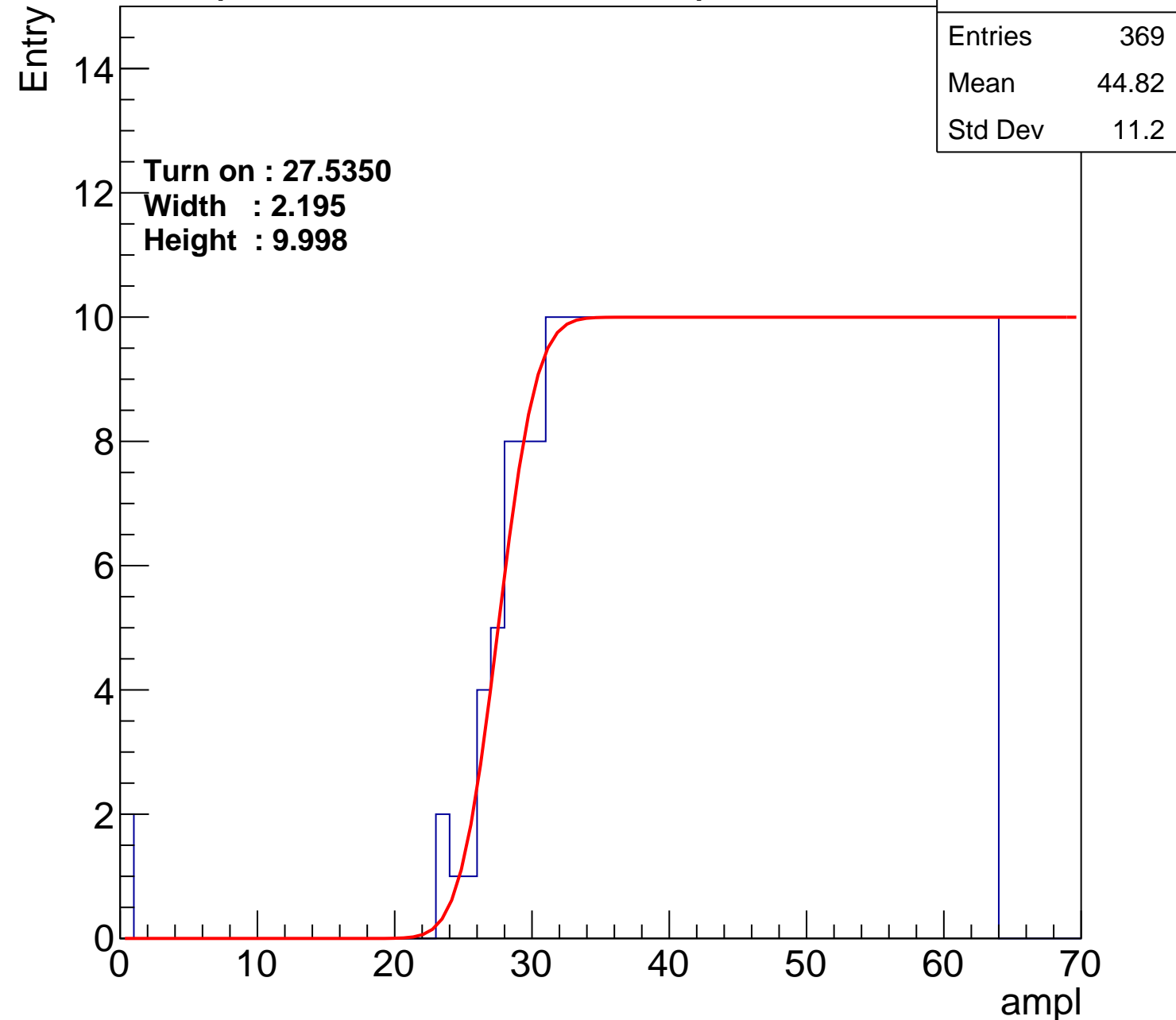
**Width : 2.195**

**Height : 9.998**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch112

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	401
Mean	43.04
Std Dev	12.45

**Turn on : 23.9752**

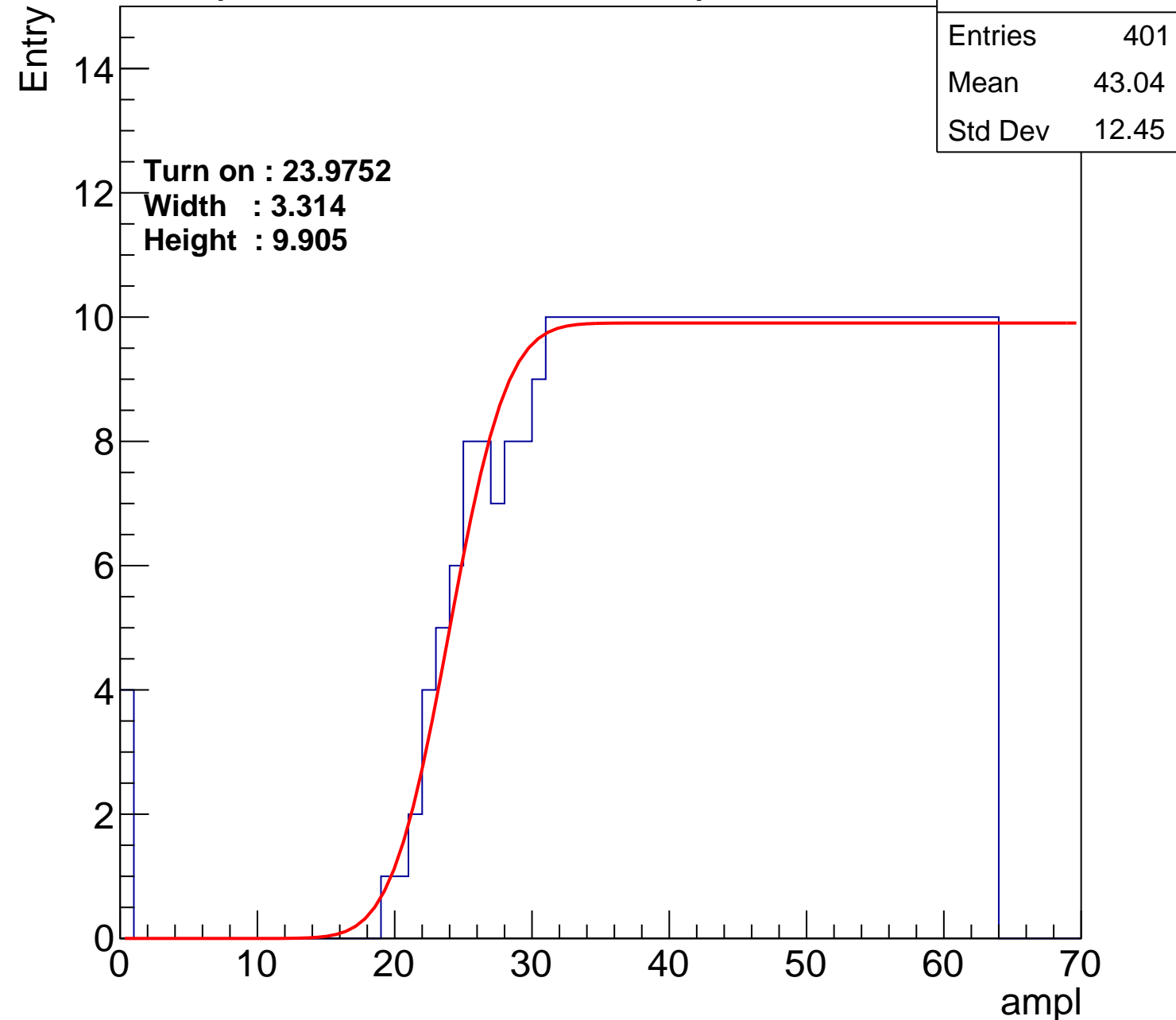
**Width : 3.314**

**Height : 9.905**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch113

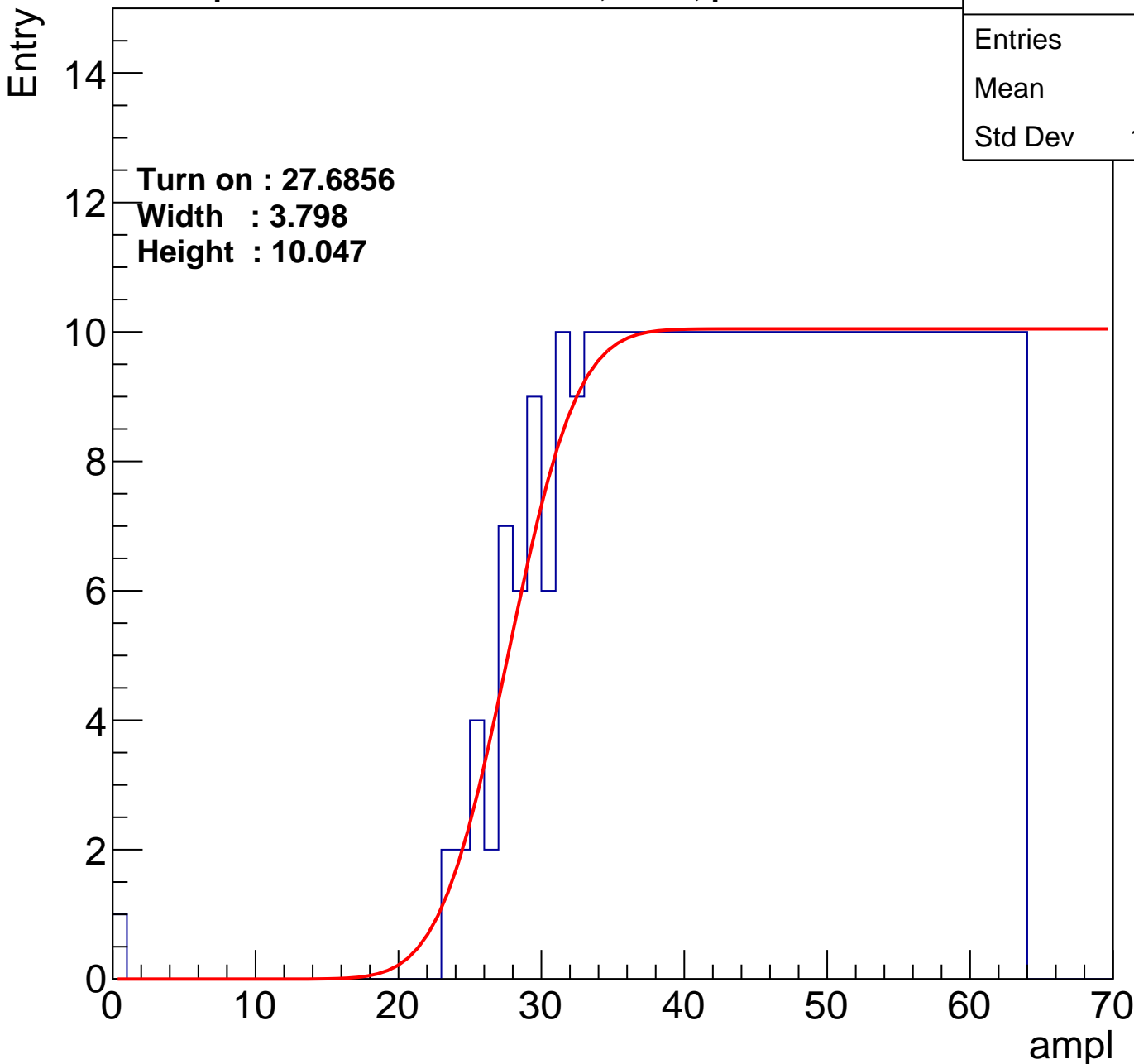
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	368
Mean	44.9
Std Dev	11.05

**Turn on : 27.6856**

**Width : 3.798**

**Height : 10.047**



# B1L100S, U24-ch114

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	397
Mean	43.26
Std Dev	12.34

Turn on : 25.2005

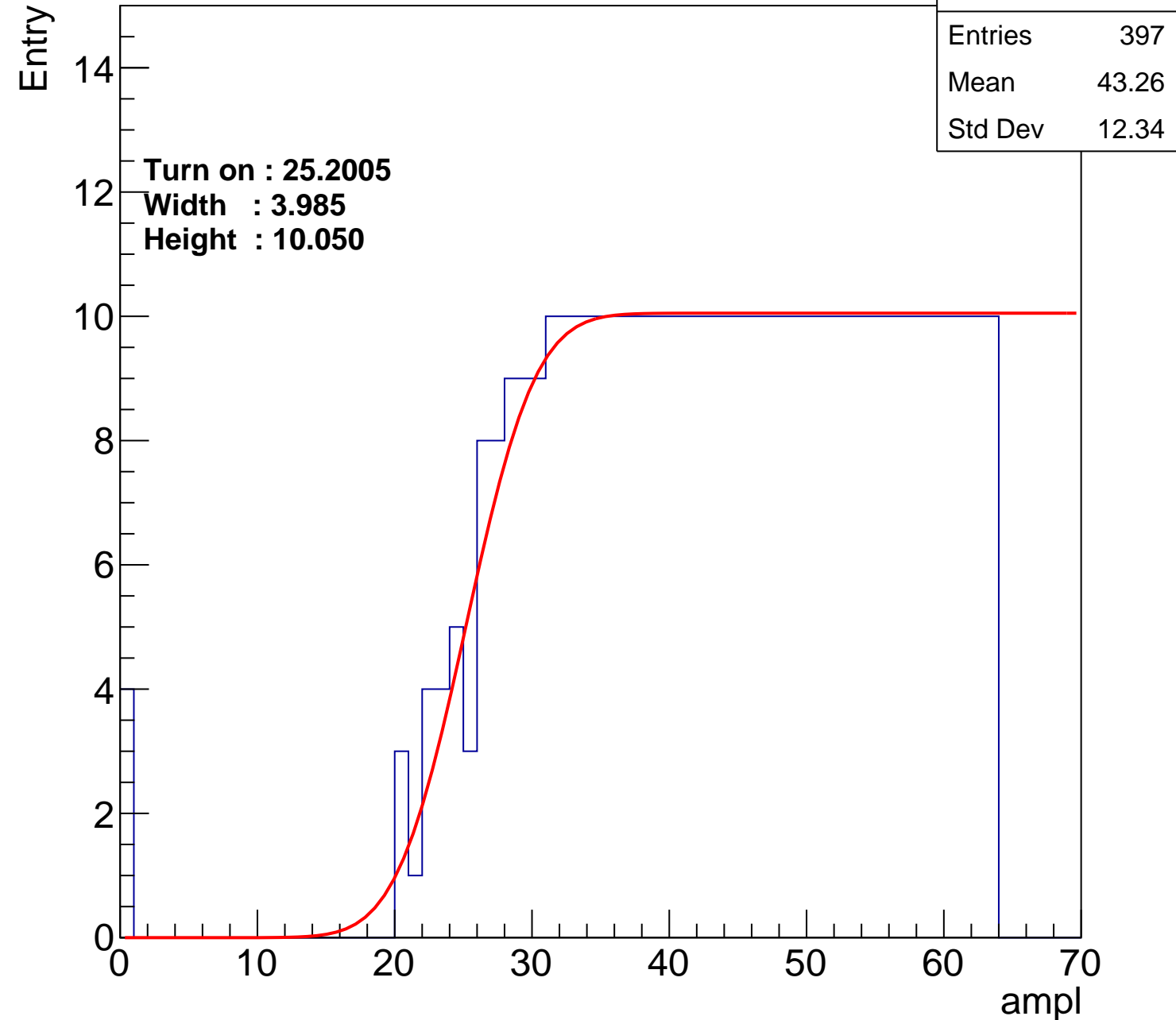
Width : 3.985

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch115

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.79
Std Dev	11.12

Turn on : 27.6920

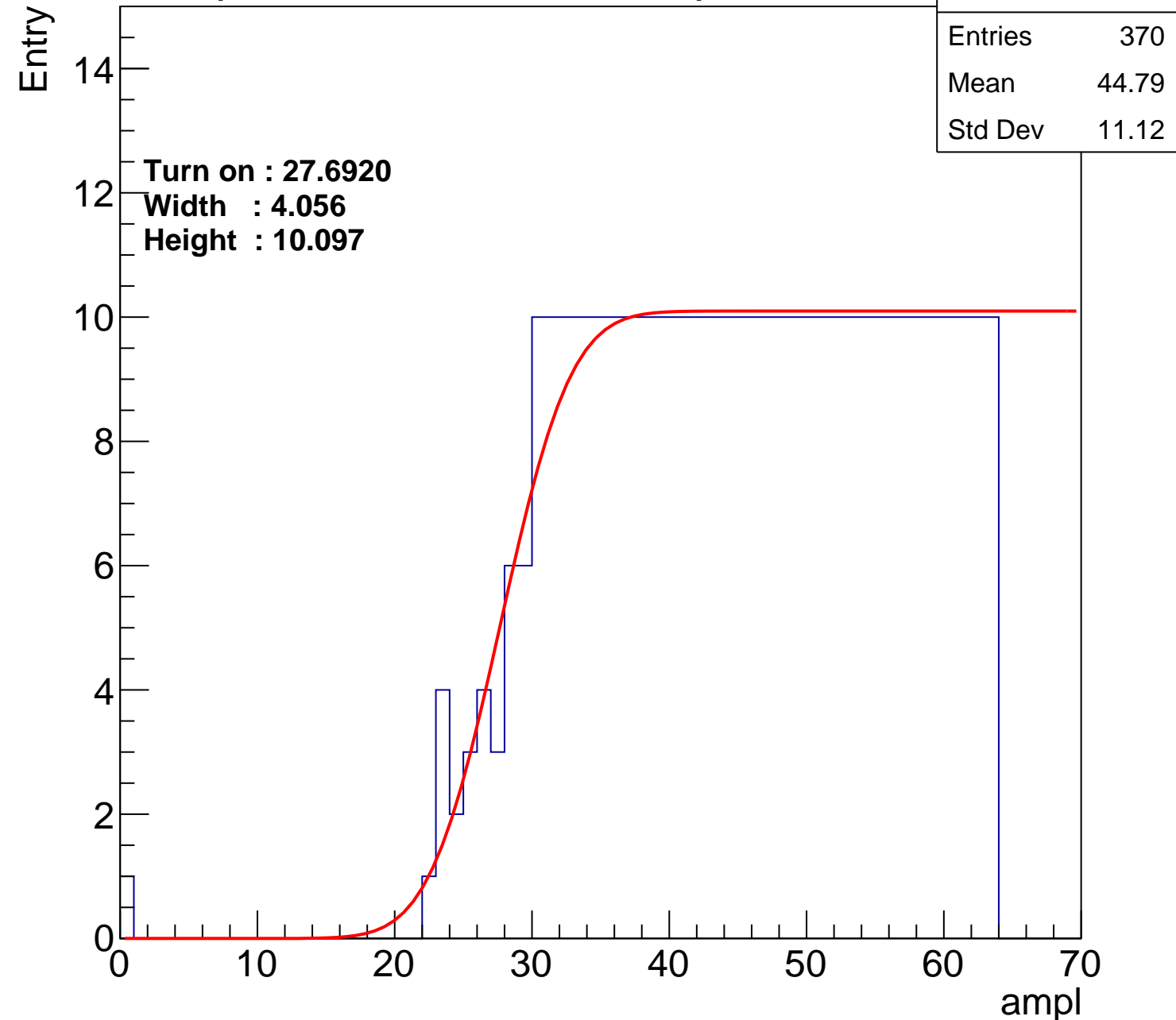
Width : 4.056

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch116

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.6681

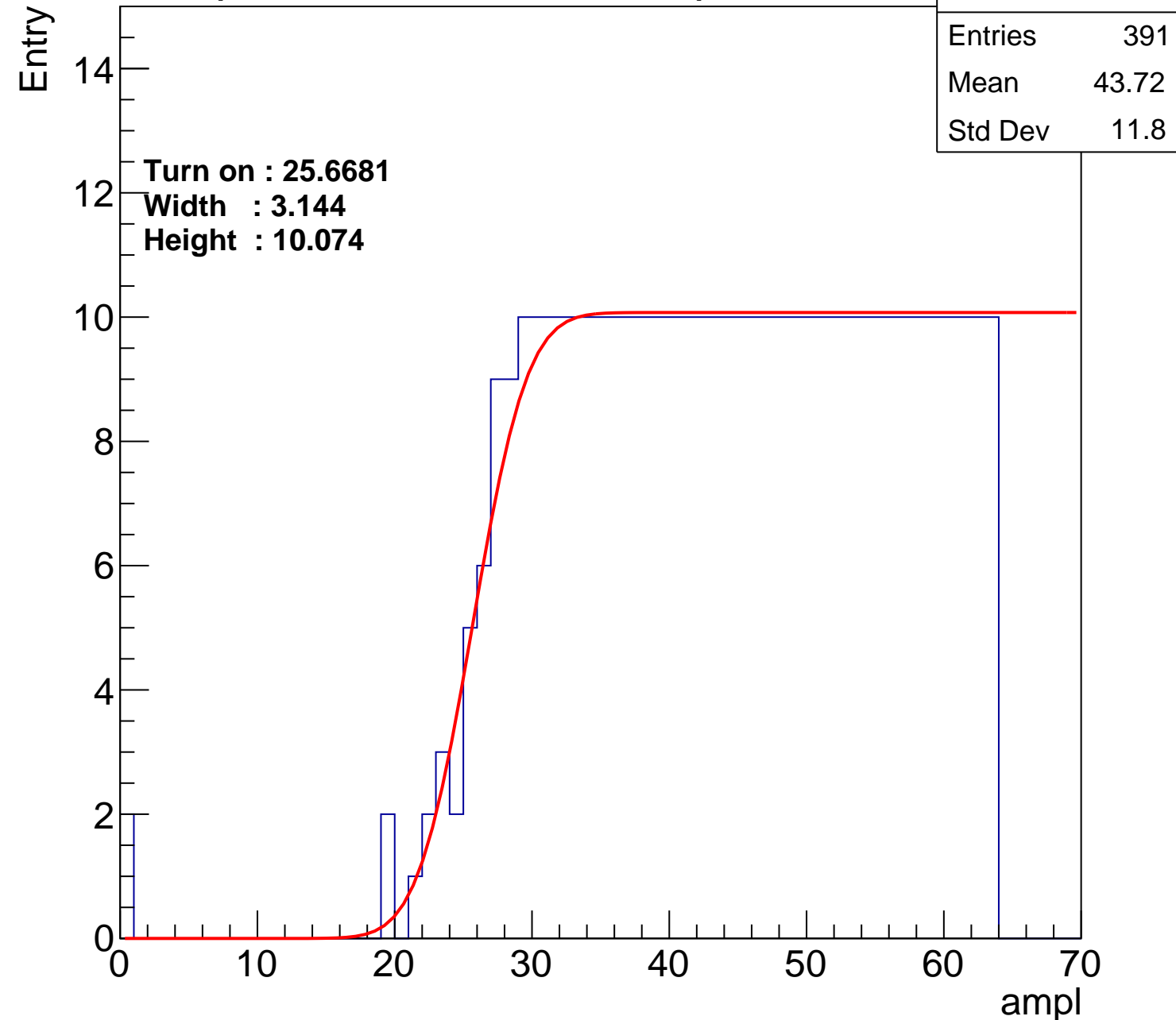
Width : 3.144

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch117

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.82
Std Dev	11.21

**Turn on : 26.5687**

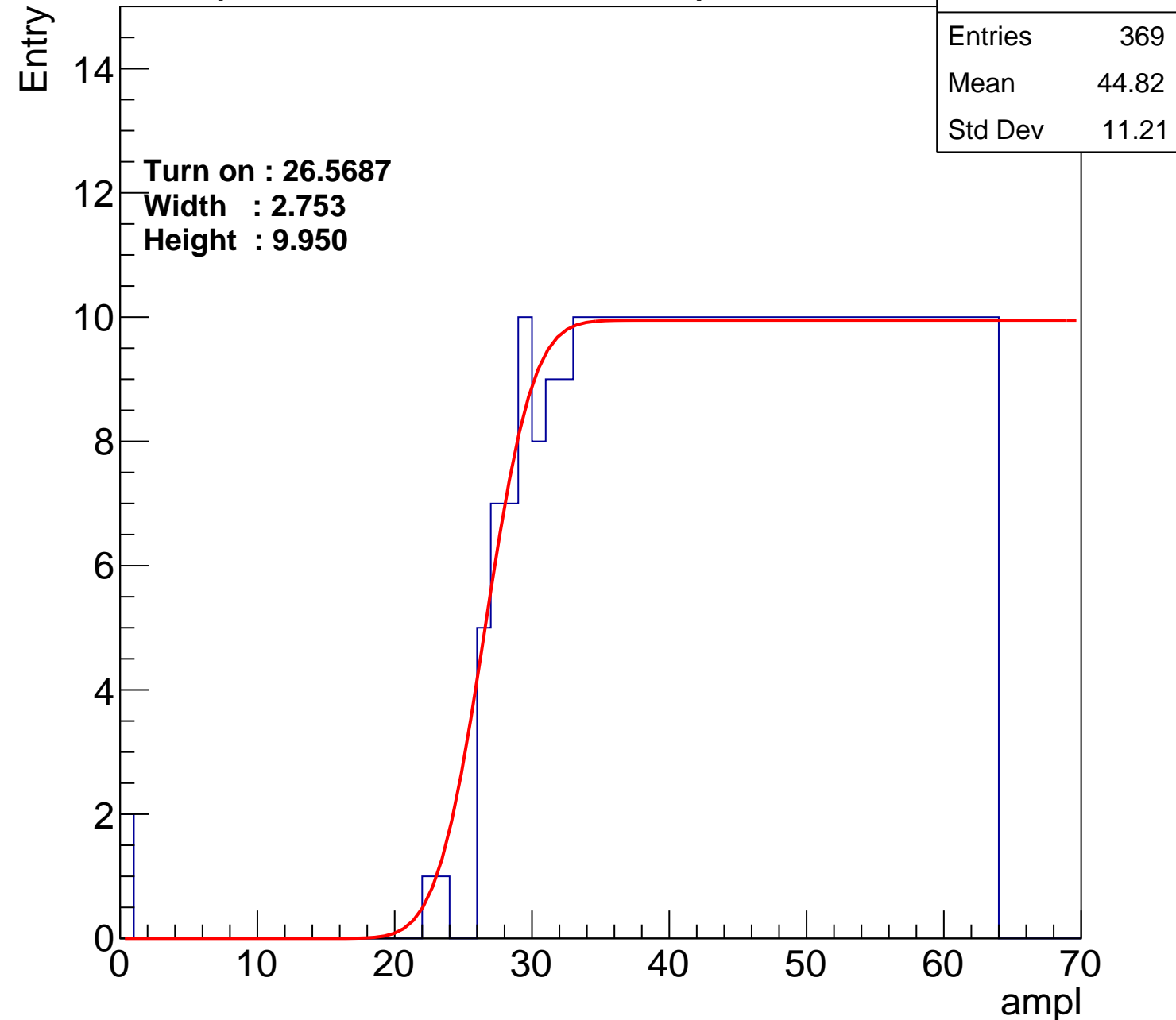
**Width : 2.753**

**Height : 9.950**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch118

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	361
Mean	45.16
Std Dev	11.1

**Turn on : 28.3310**

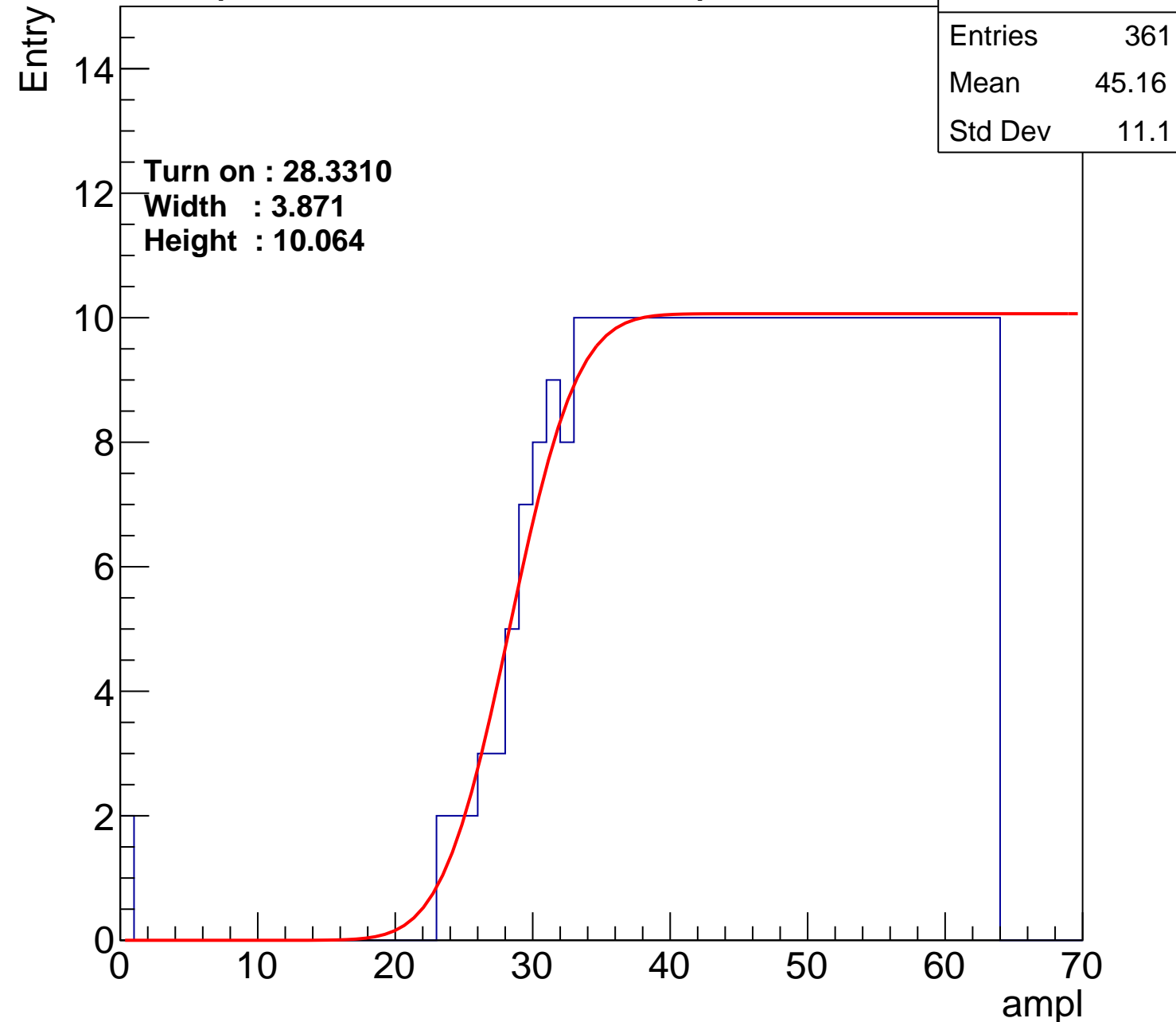
**Width : 3.871**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch119

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.1283

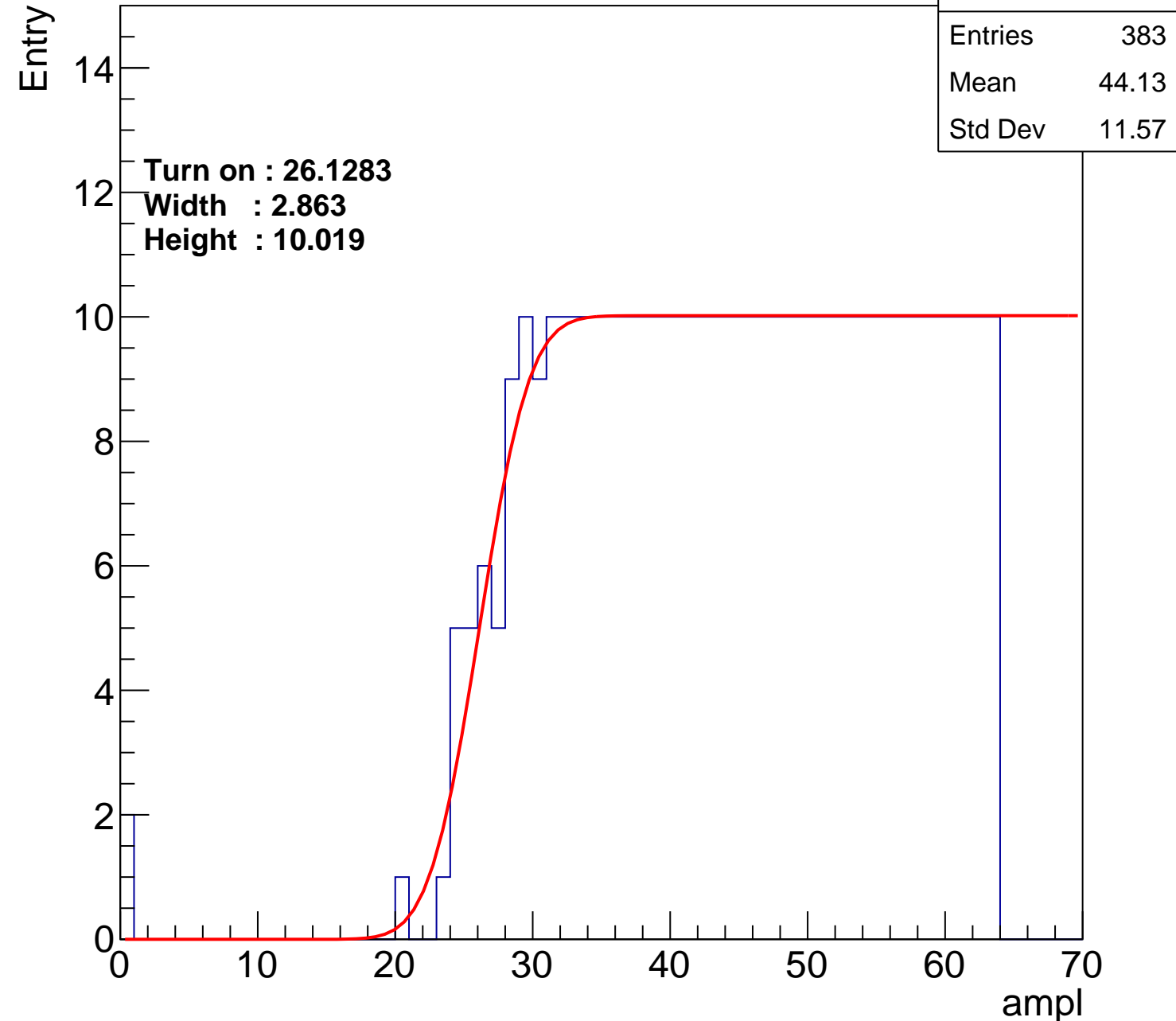
Width : 2.863

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch120

calib\_packv5\_042523\_0143.root, FC#4, port A2

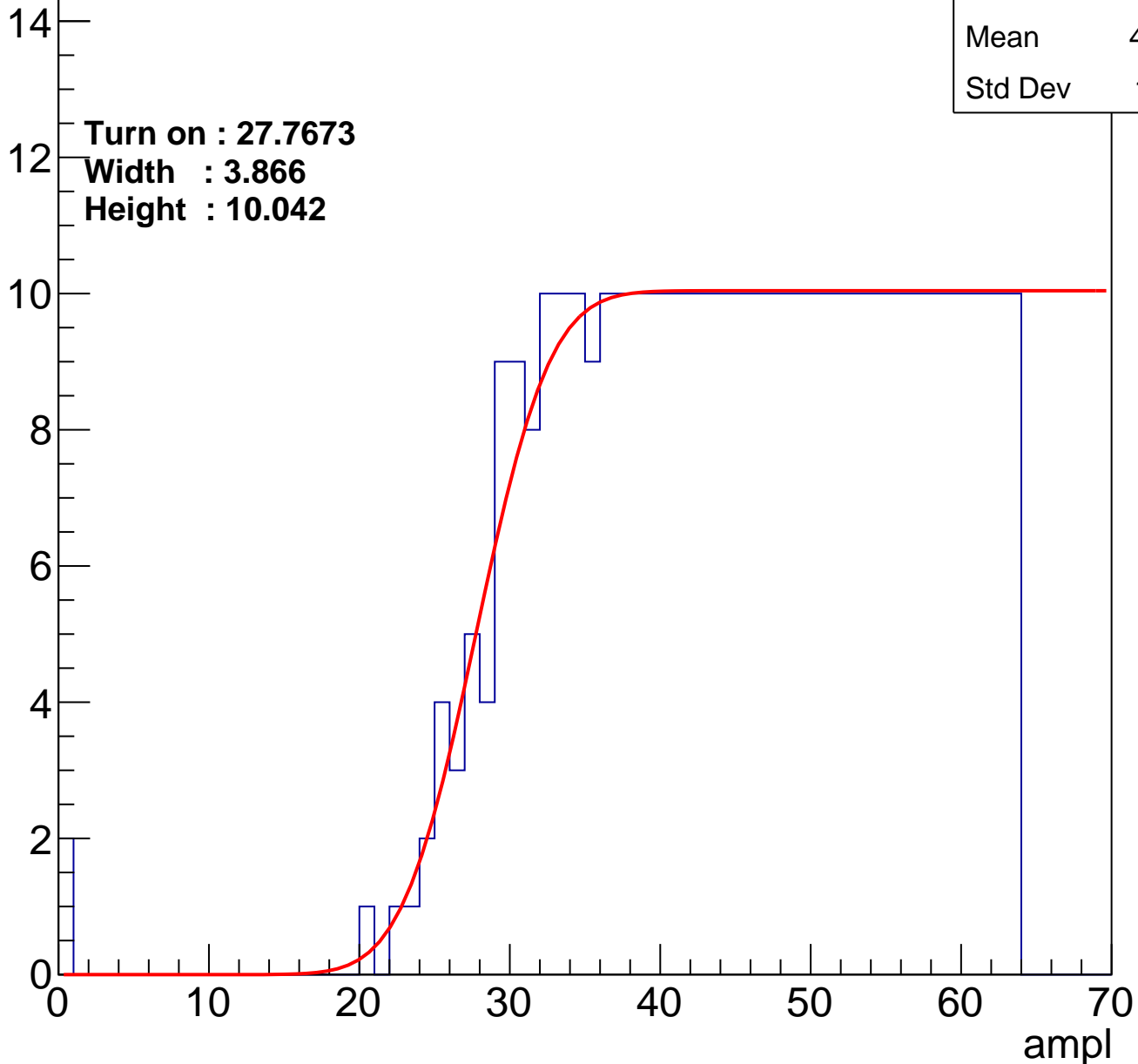
Entries	368
Mean	44.79
Std Dev	11.31

**Turn on : 27.7673**

**Width : 3.866**

**Height : 10.042**

Entry



# B1L100S, U24-ch121

calib\_packv5\_042523\_0143.root, FC#4, port A2

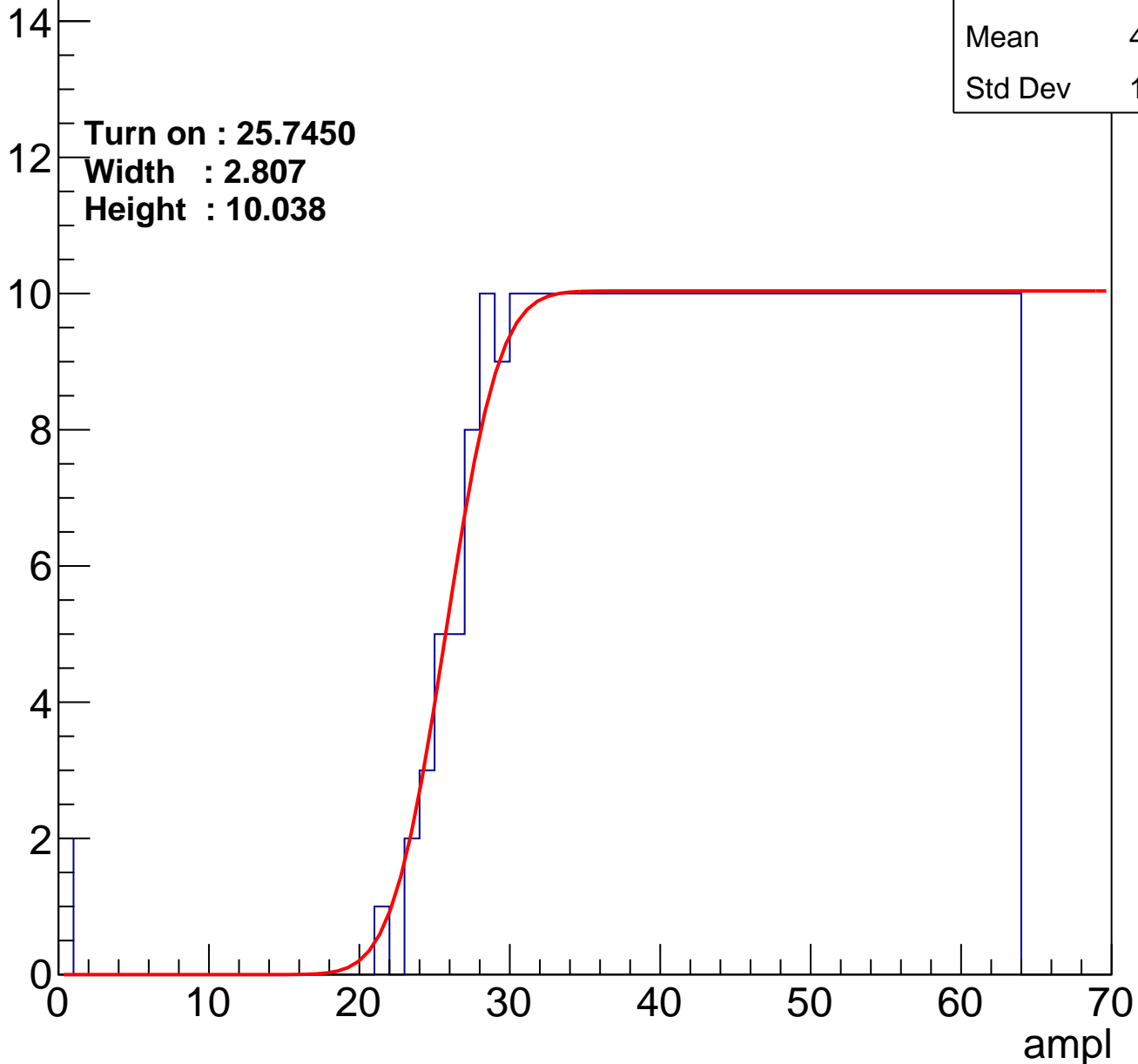
Entries	385
Mean	44.05
Std Dev	11.58

Turn on : 25.7450

Width : 2.807

Height : 10.038

Entry



# B1L100S, U24-ch122

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.43
Std Dev	11.59

**Turn on : 26.8778**

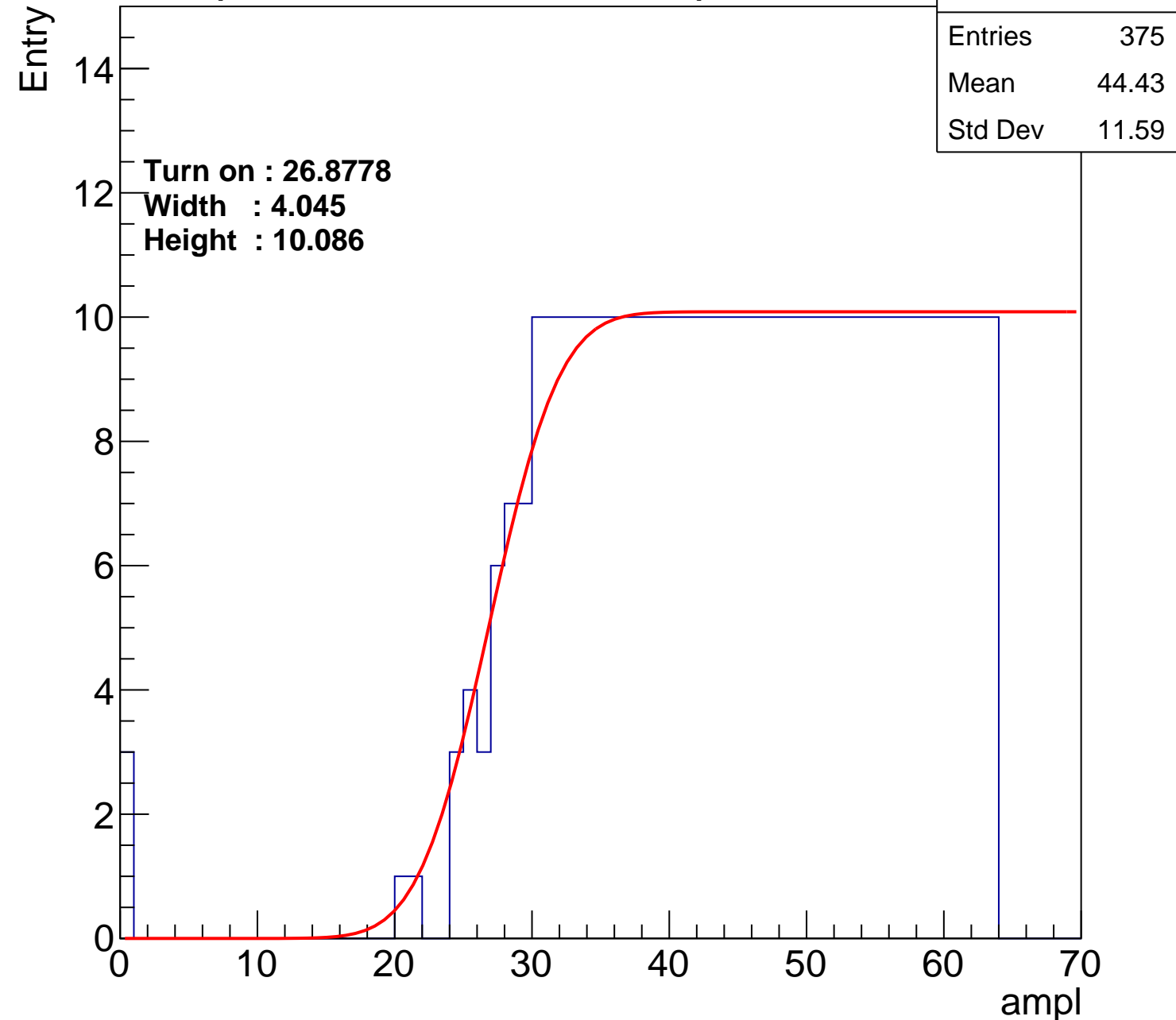
**Width : 4.045**

**Height : 10.086**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch123

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.39
Std Dev	12.04

Turn on : 28.0003

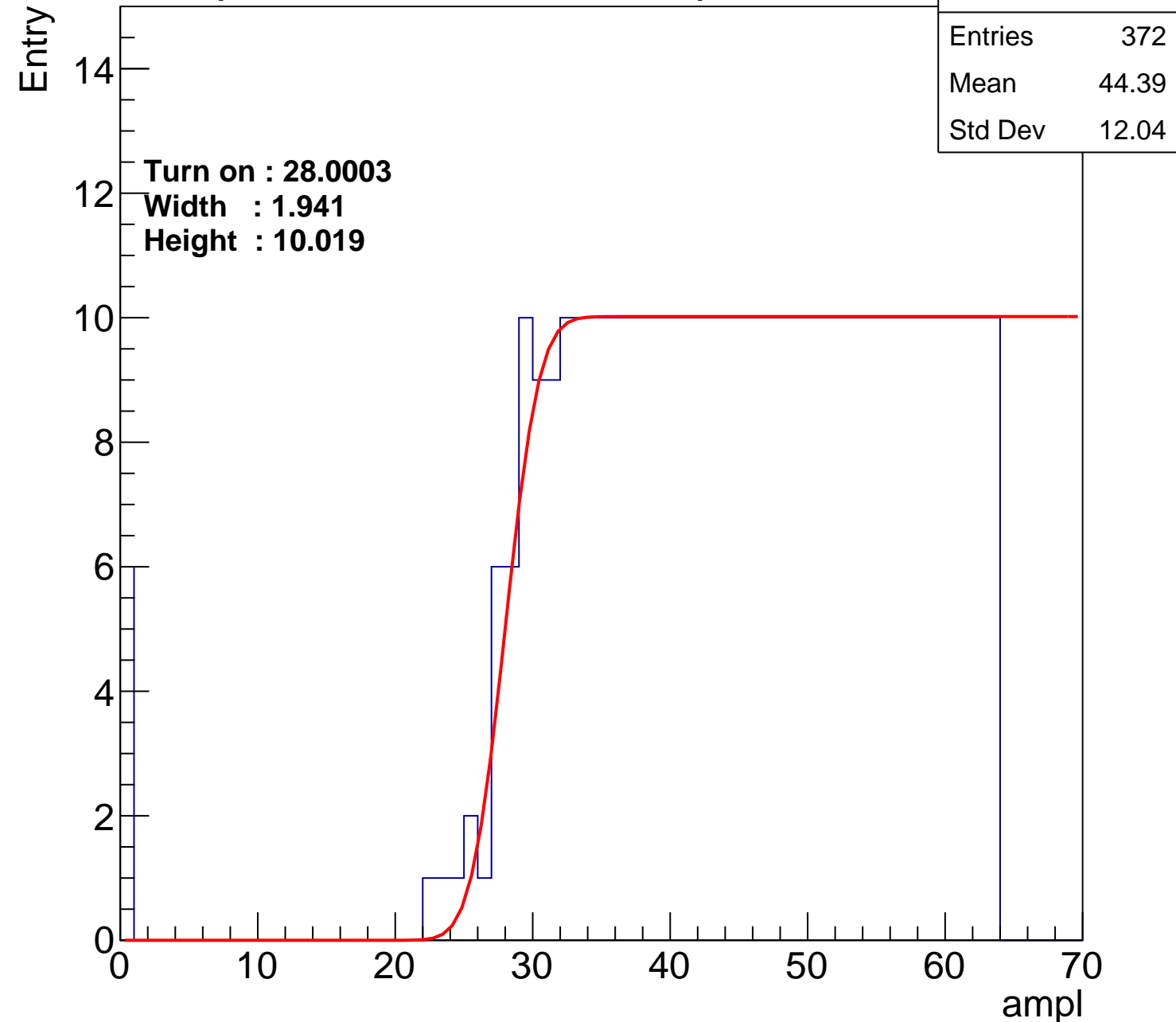
Width : 1.941

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U24-ch124

calib\_packv5\_042523\_0143.root, FC#4, port A2

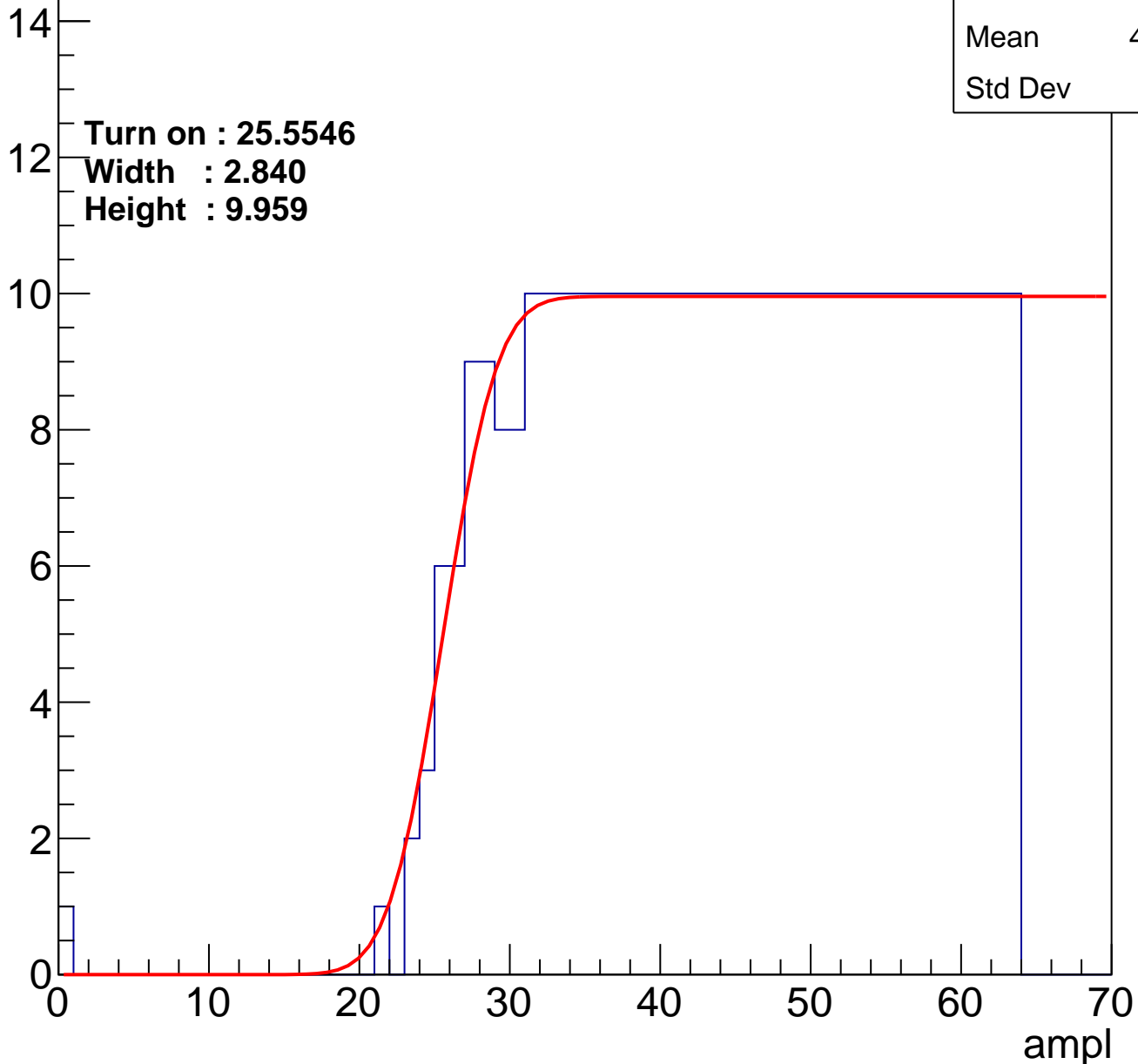
Entries	383
Mean	44.18
Std Dev	11.4

Turn on : 25.5546

Width : 2.840

Height : 9.959

Entry



# B1L100S, U24-ch125

calib\_packv5\_042523\_0143.root, FC#4, port A2

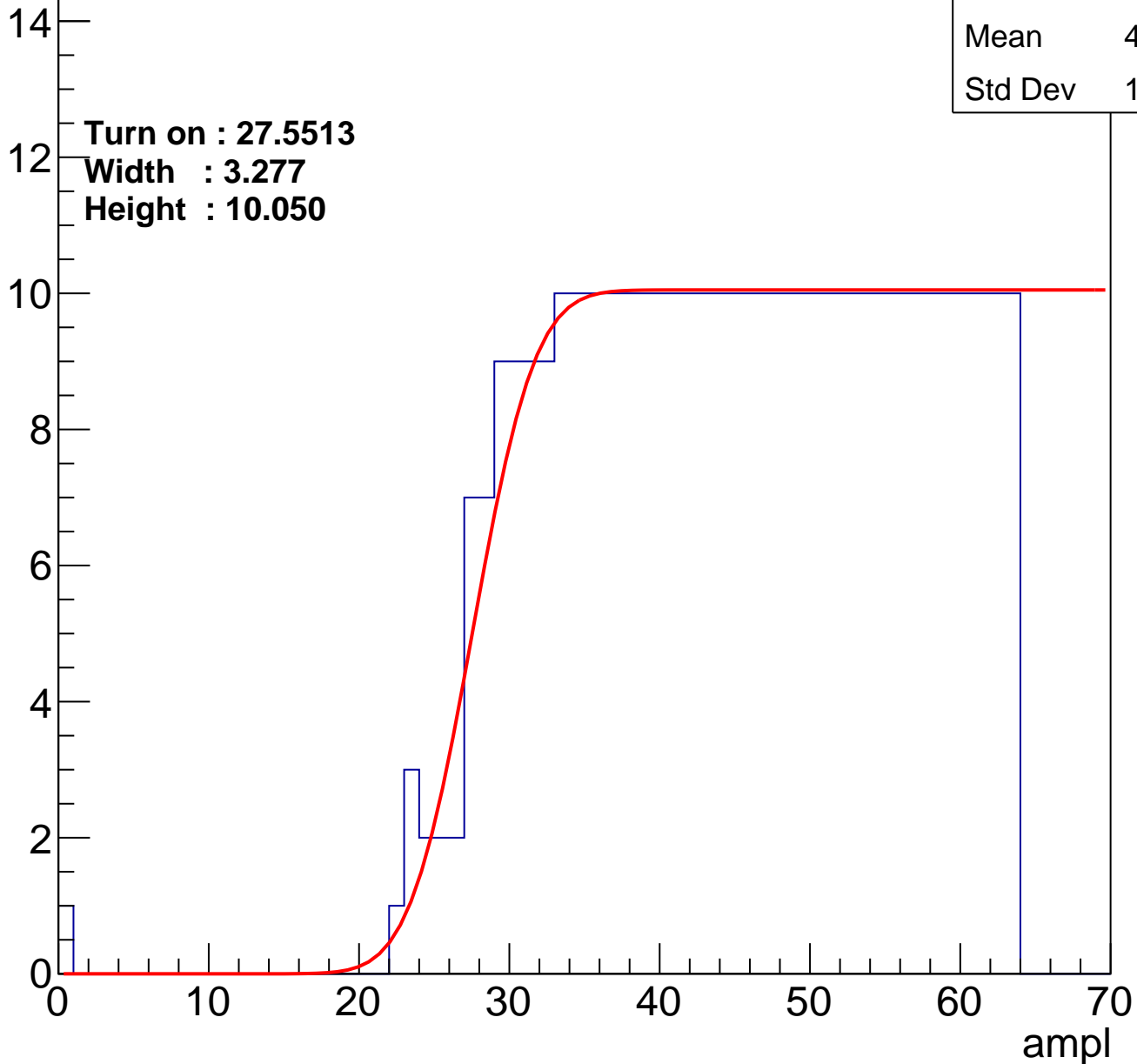
Entries	371
Mean	44.75
Std Dev	11.12

Turn on : 27.5513

Width : 3.277

Height : 10.050

Entry



# B1L100S, U24-ch126

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.86
Std Dev	11.05

Turn on : 27.2770

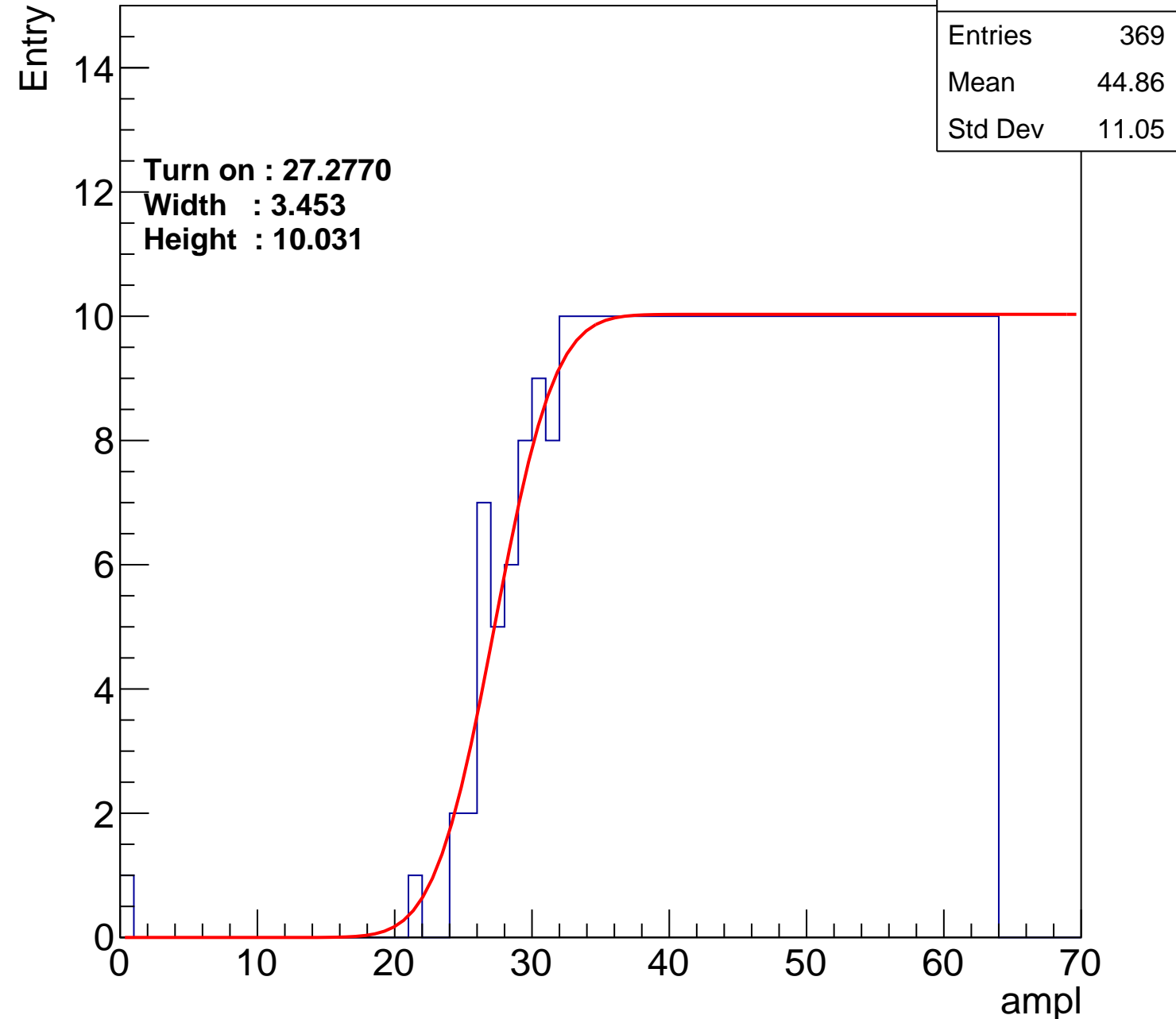
Width : 3.453

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U24-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

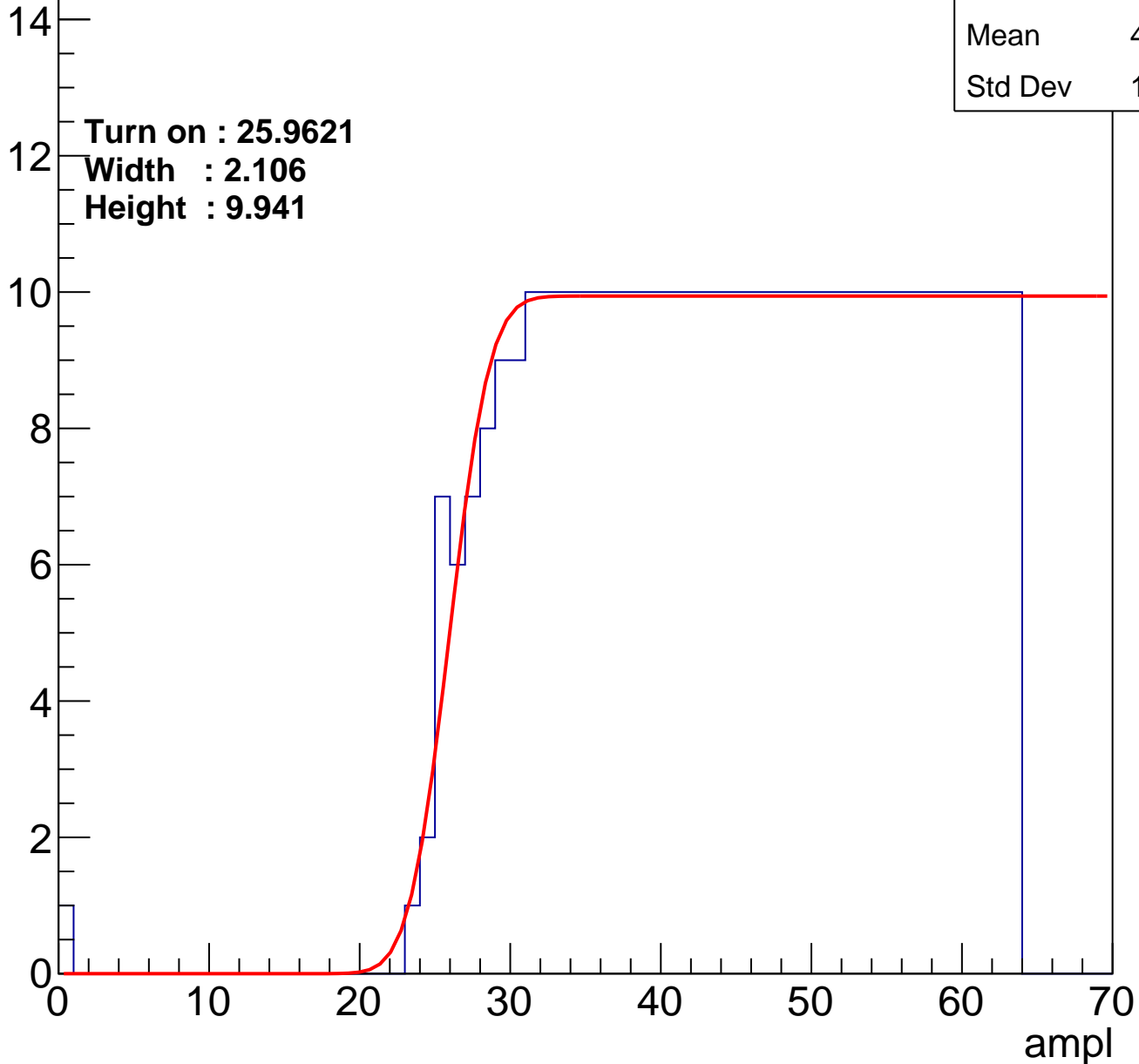
Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 25.9621

Width : 2.106

Height : 9.941

Entry



# B1L100S, U24-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 25.9621

Width : 2.106

Height : 9.941

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

