



# B0L101S, U26-ch0, adc0

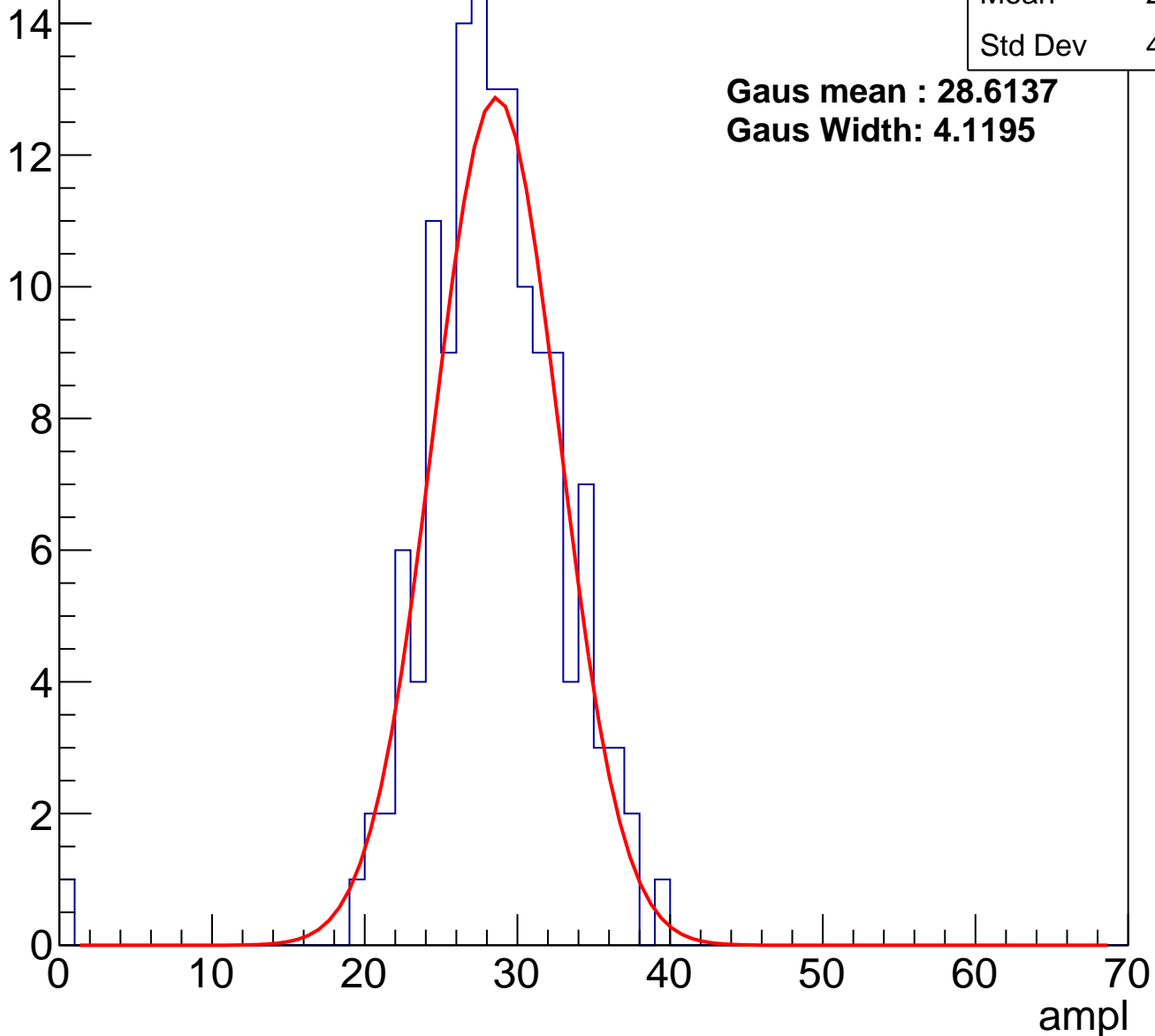
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	27.96
Std Dev	4.636

**Gaus mean : 28.6137**

**Gaus Width: 4.1195**

Entry



# B0L101S, U26-ch0, adc1

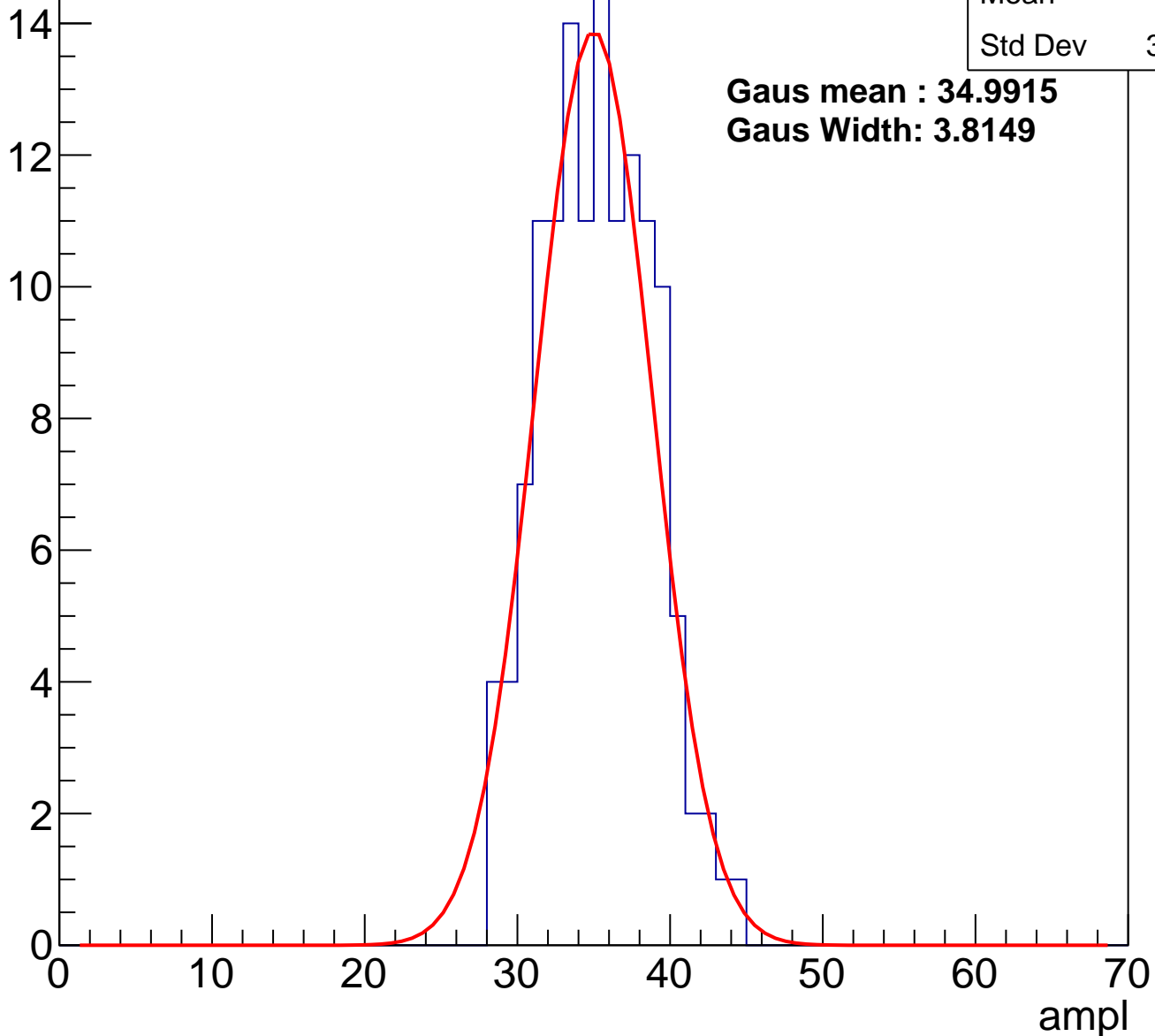
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	34.8
Std Dev	3.492

**Gaus mean : 34.9915**

**Gaus Width: 3.8149**

Entry



# B0L101S, U26-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

133

Mean

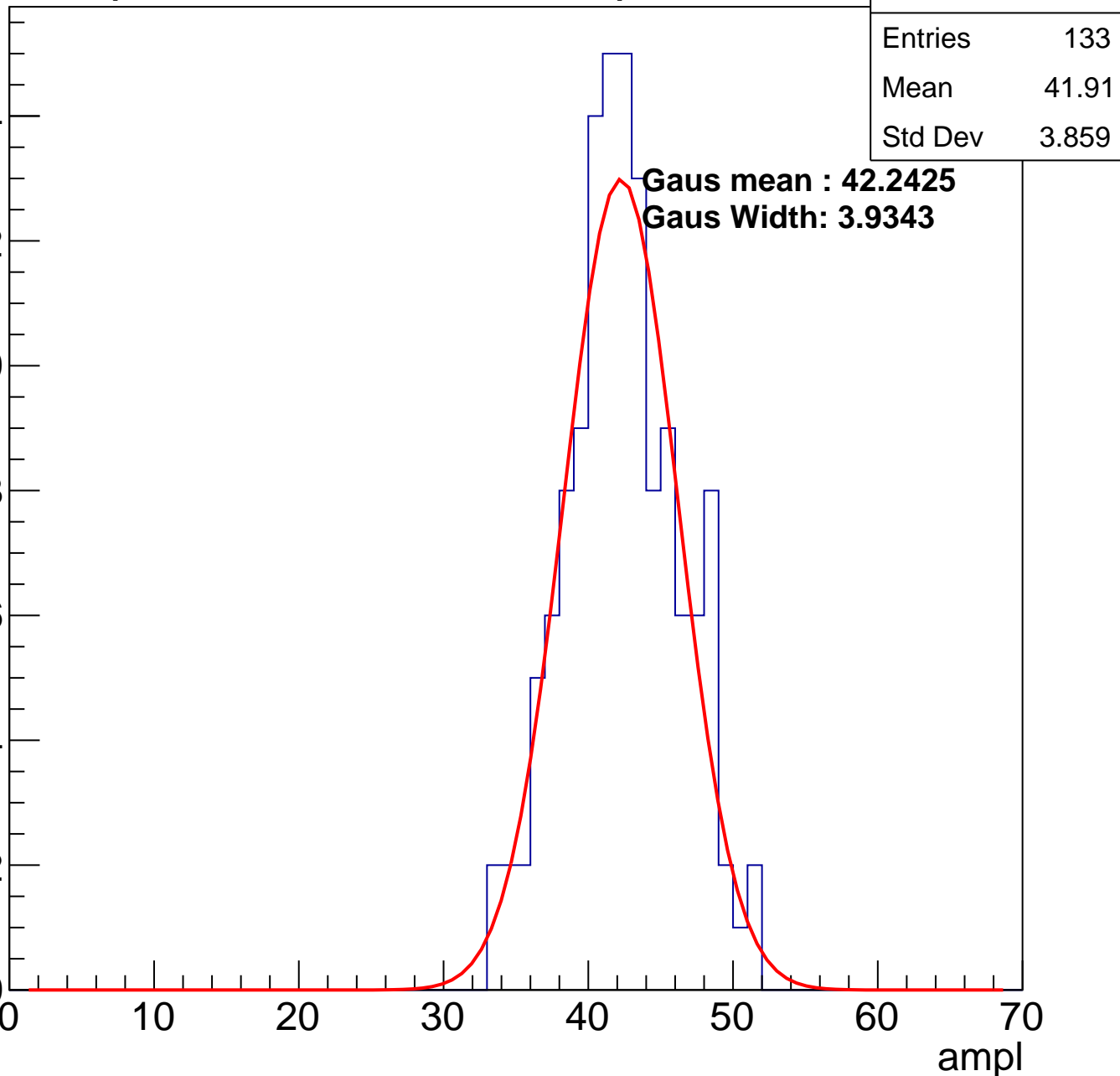
41.91

Std Dev

3.859

**Gaus mean : 42.2425**

**Gaus Width: 3.9343**



# B0L101S, U26-ch0, adc3

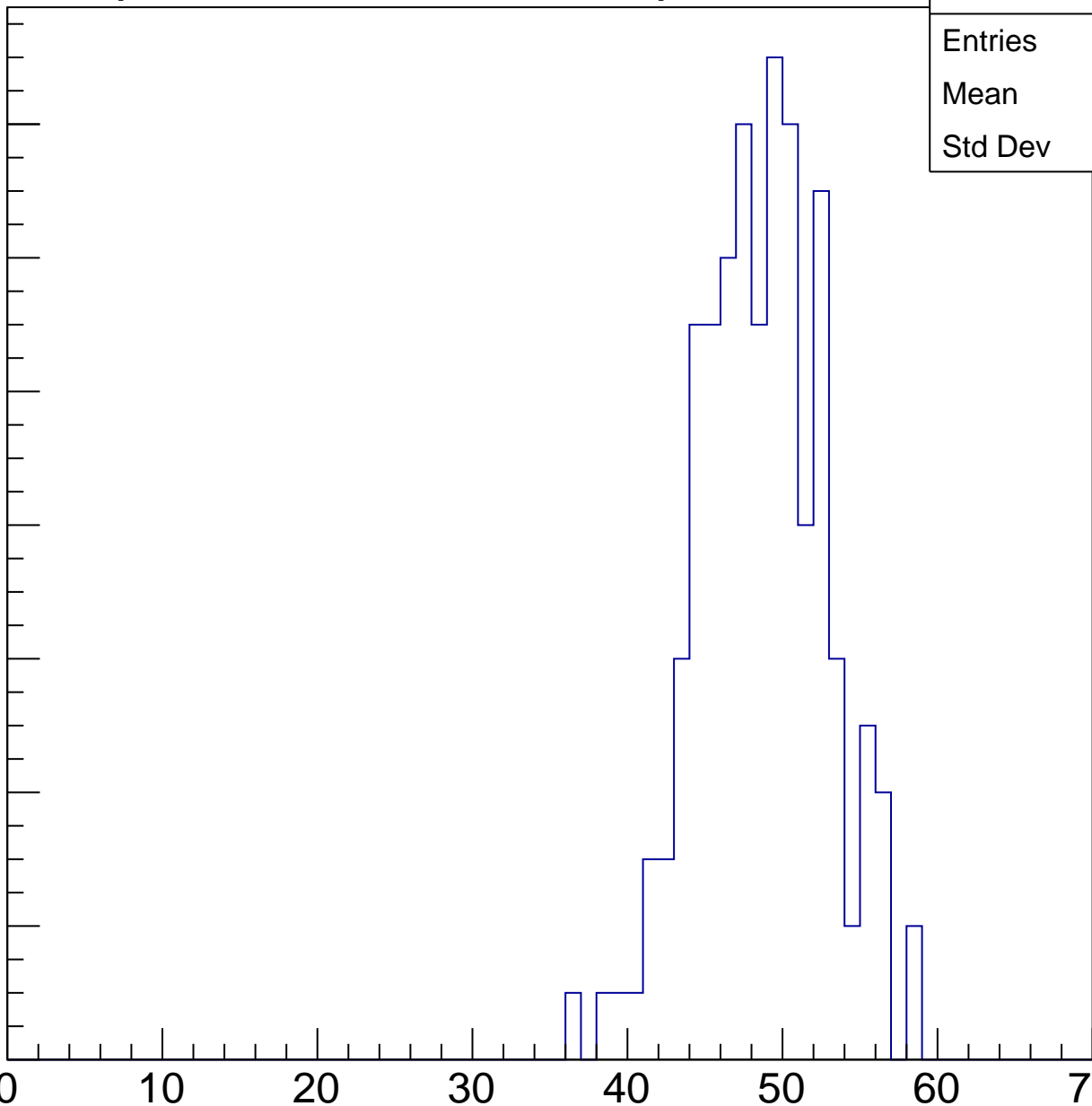
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	144
Mean	48.17
Std Dev	4.083

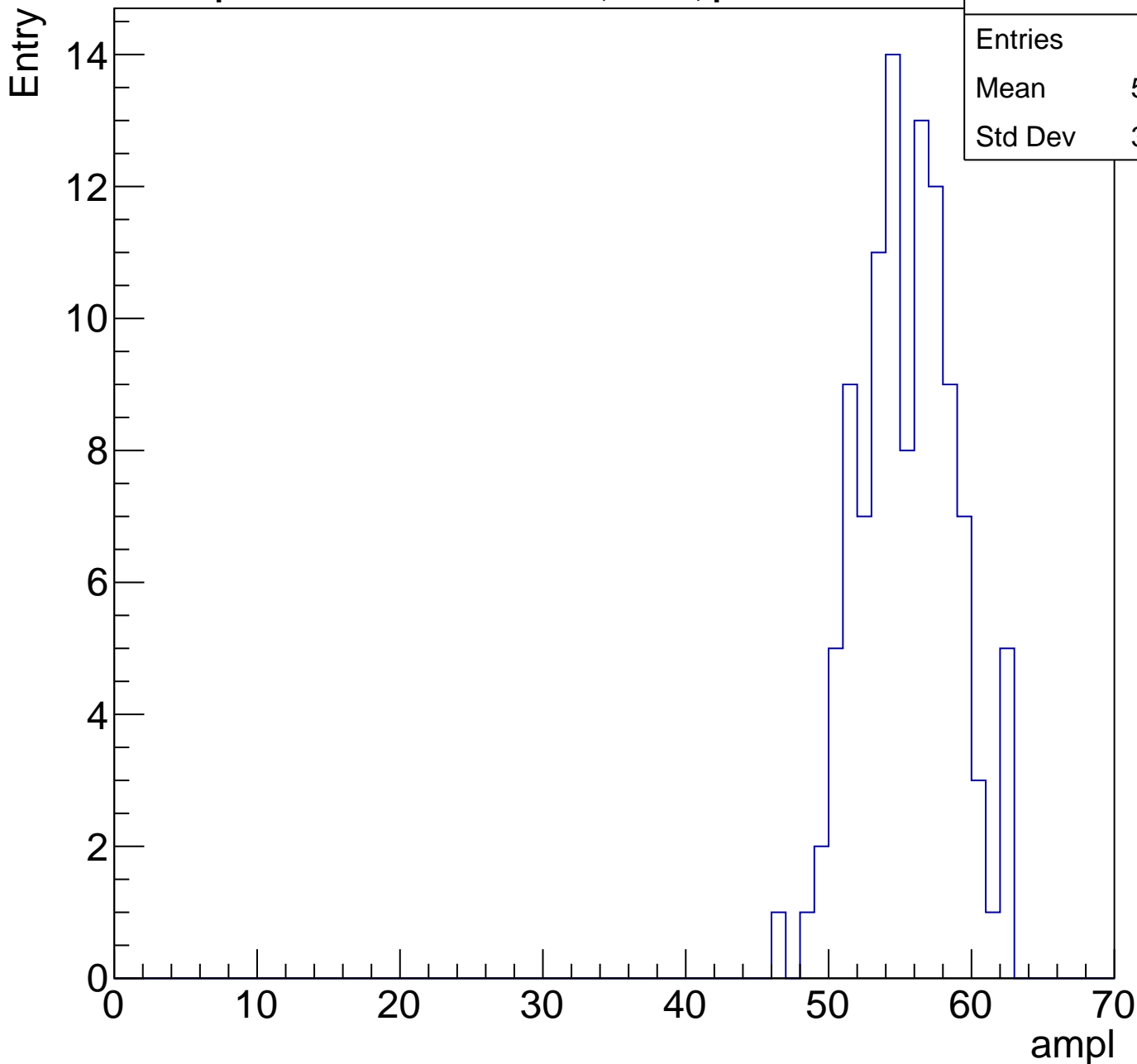
ampl



# B0L101S, U26-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	55.02
Std Dev	3.336



# B0L101S, U26-ch0, adc5

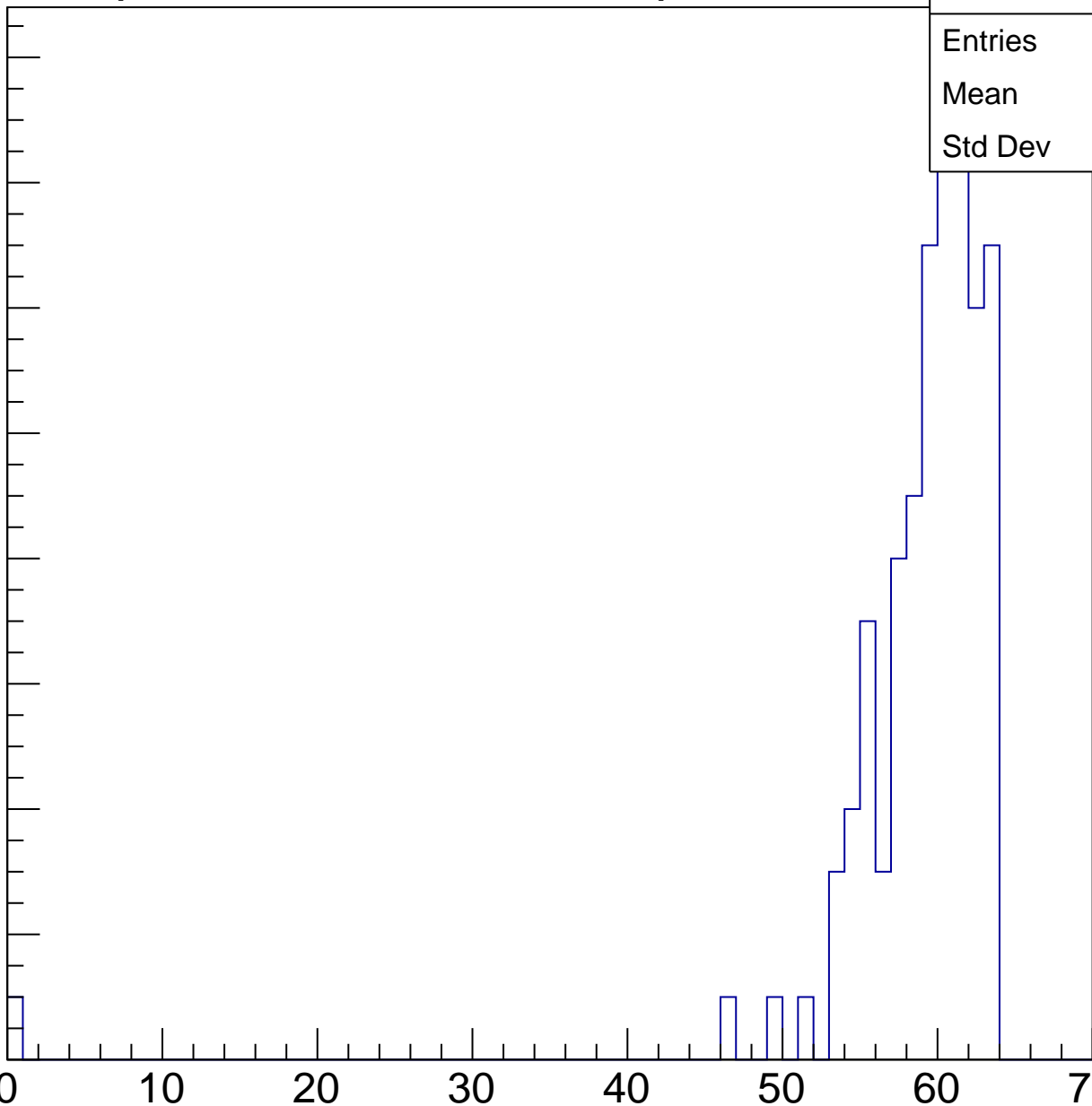
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	107
Mean	58.48
Std Dev	6.532

ampl

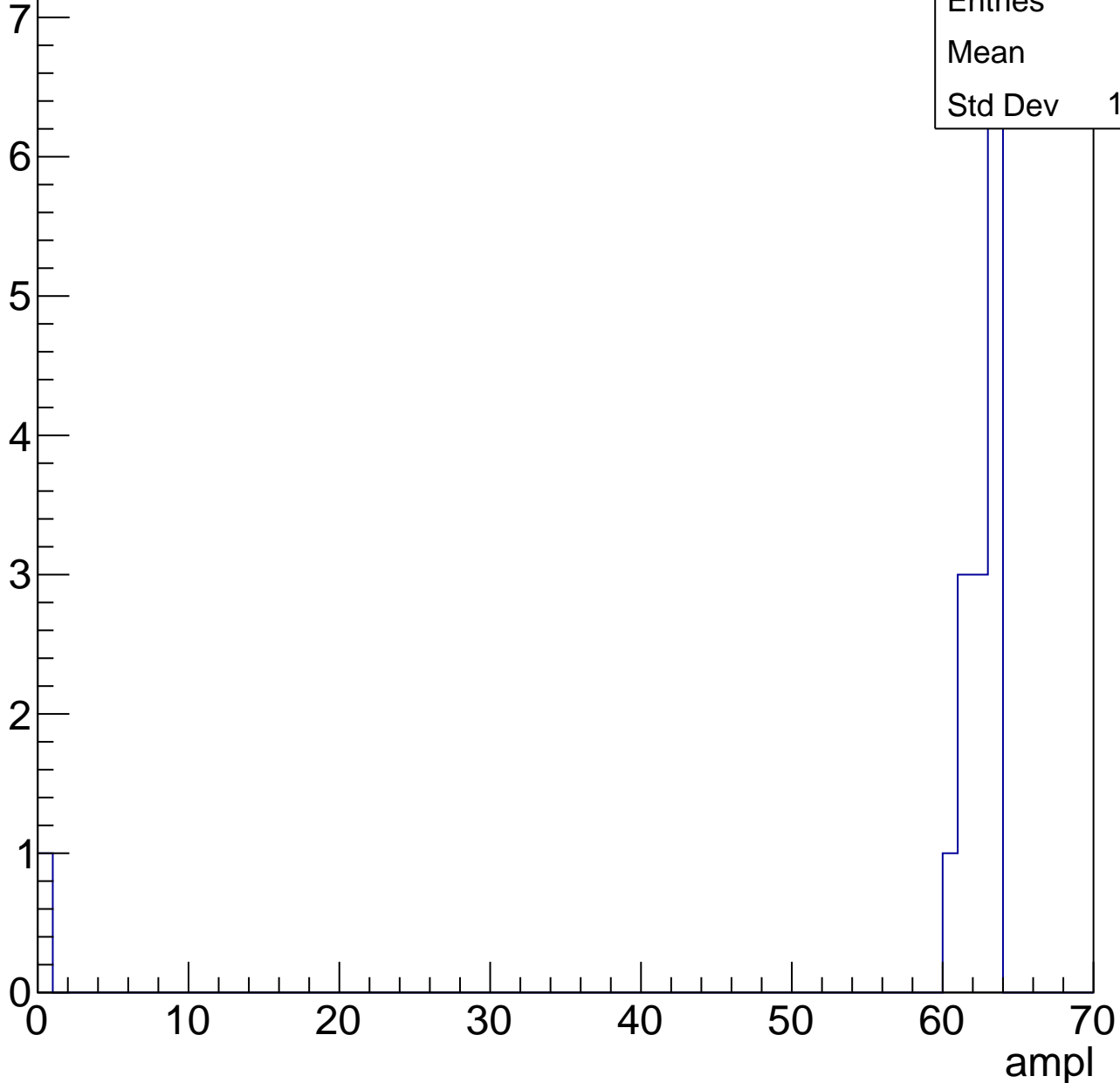


# B0L101S, U26-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	58
Std Dev	15.53





# B0L101S, U26-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	20.33
Std Dev	28.76

# B0L101S, U26-ch1, adc0

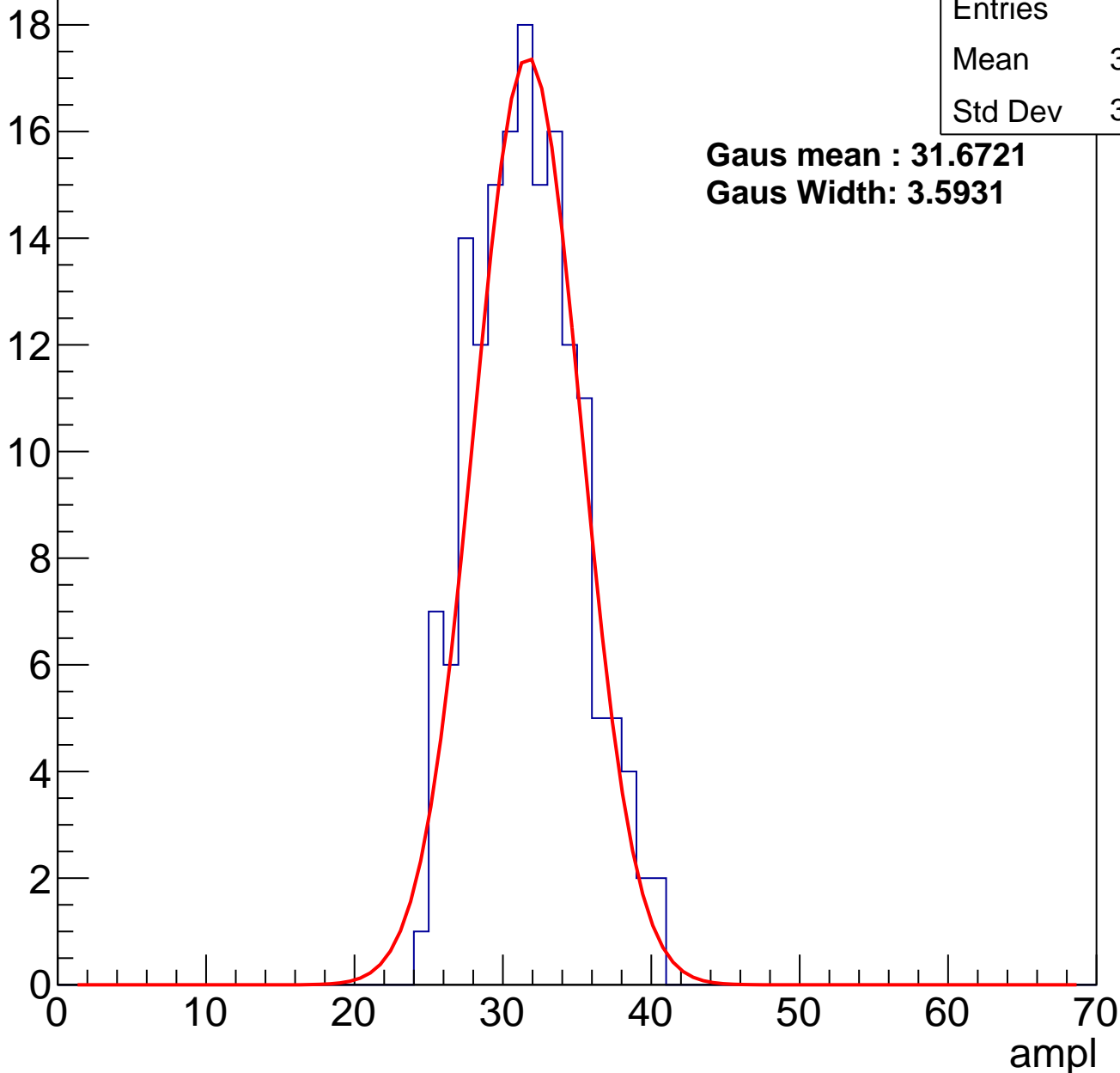
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	31.17
Std Dev	3.532

**Gaus mean : 31.6721**

**Gaus Width: 3.5931**

Entry



# B0L101S, U26-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	37.43
Std Dev	3.011

**Gaus mean : 37.8972**

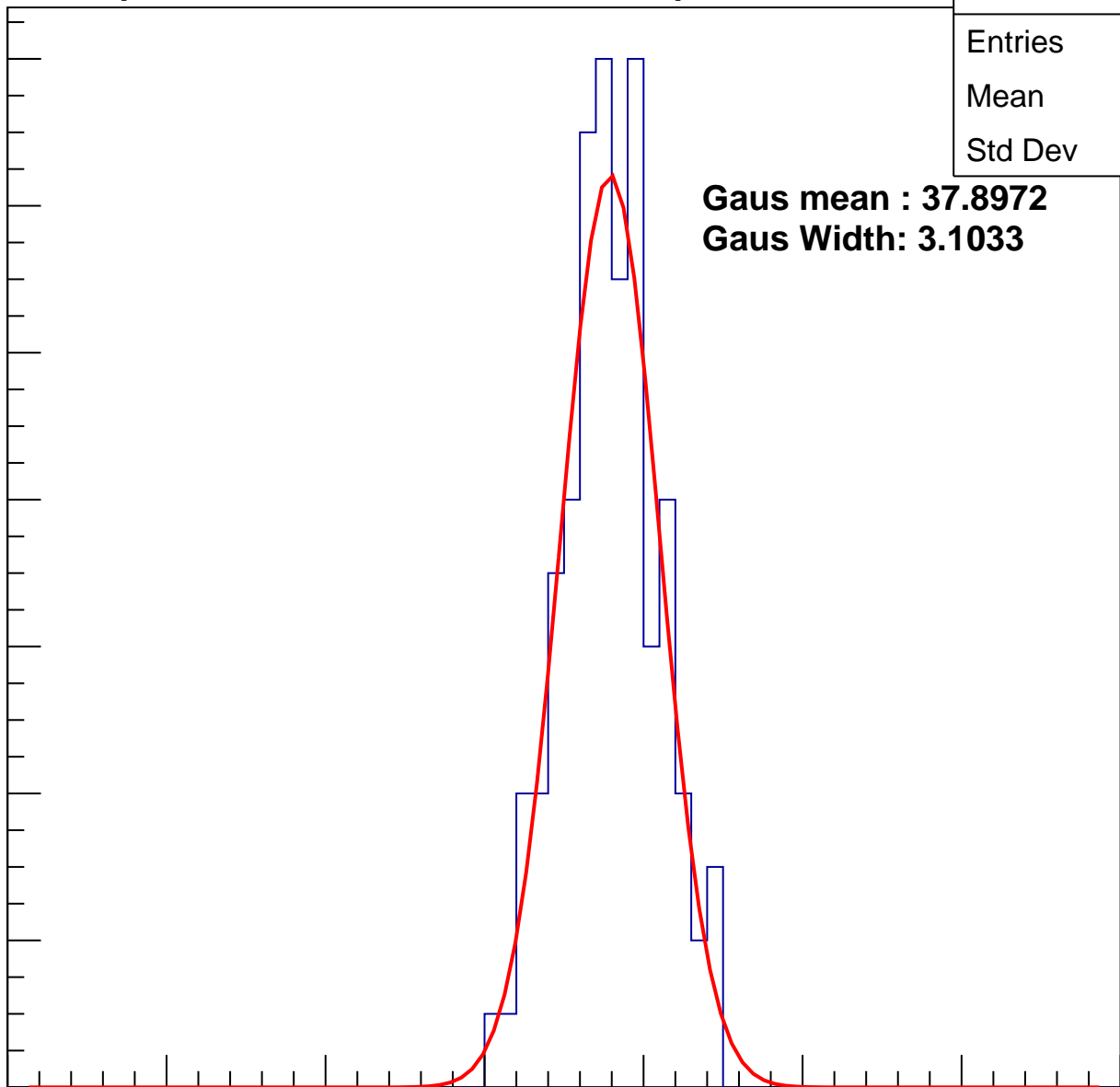
**Gaus Width: 3.1033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

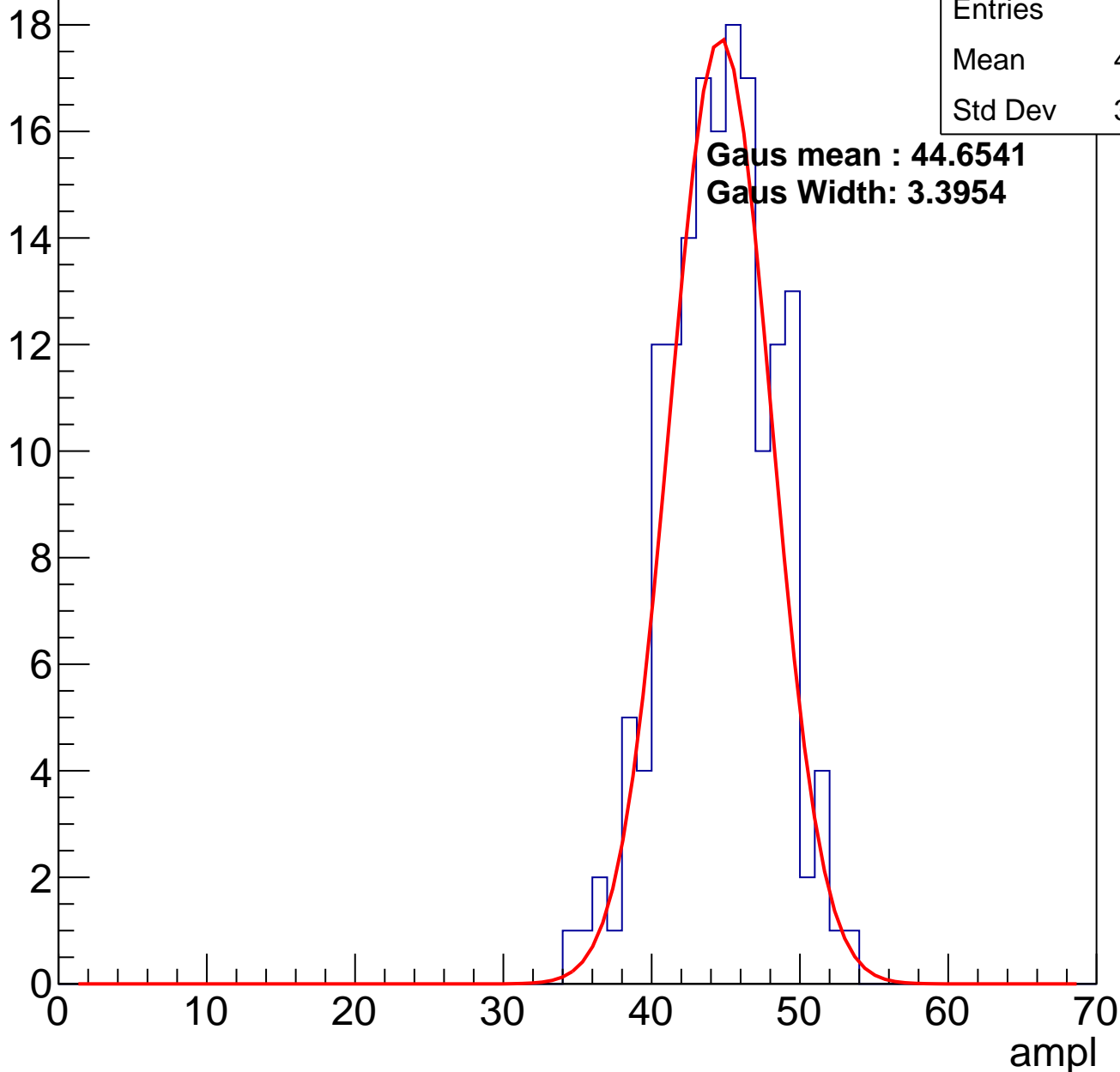
0 10 20 30 40 50 60 70



# B0L101S, U26-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

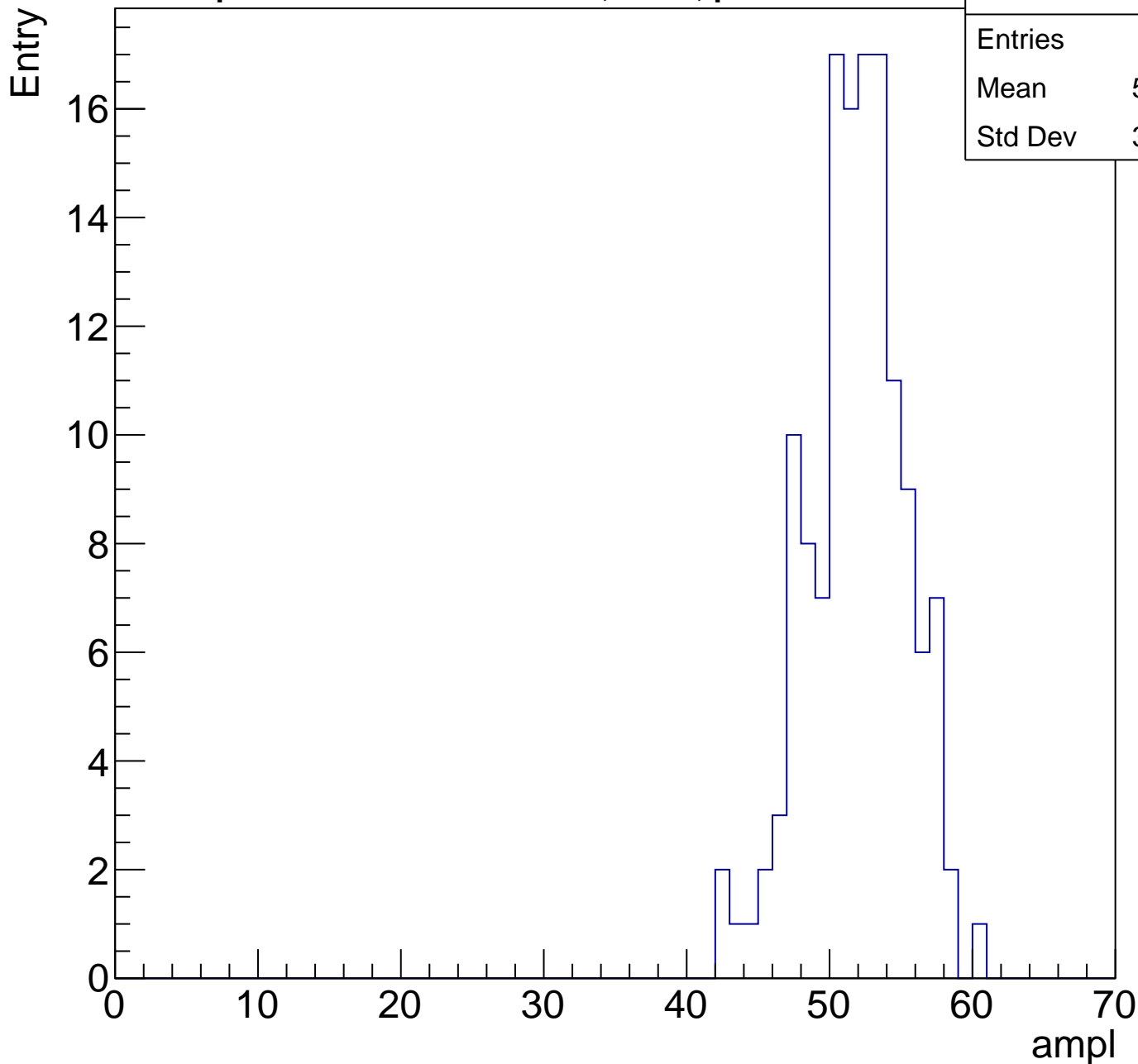
Entry



# B0L101S, U26-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	51.44
Std Dev	3.398



# B0L101S, U26-ch1, adc4

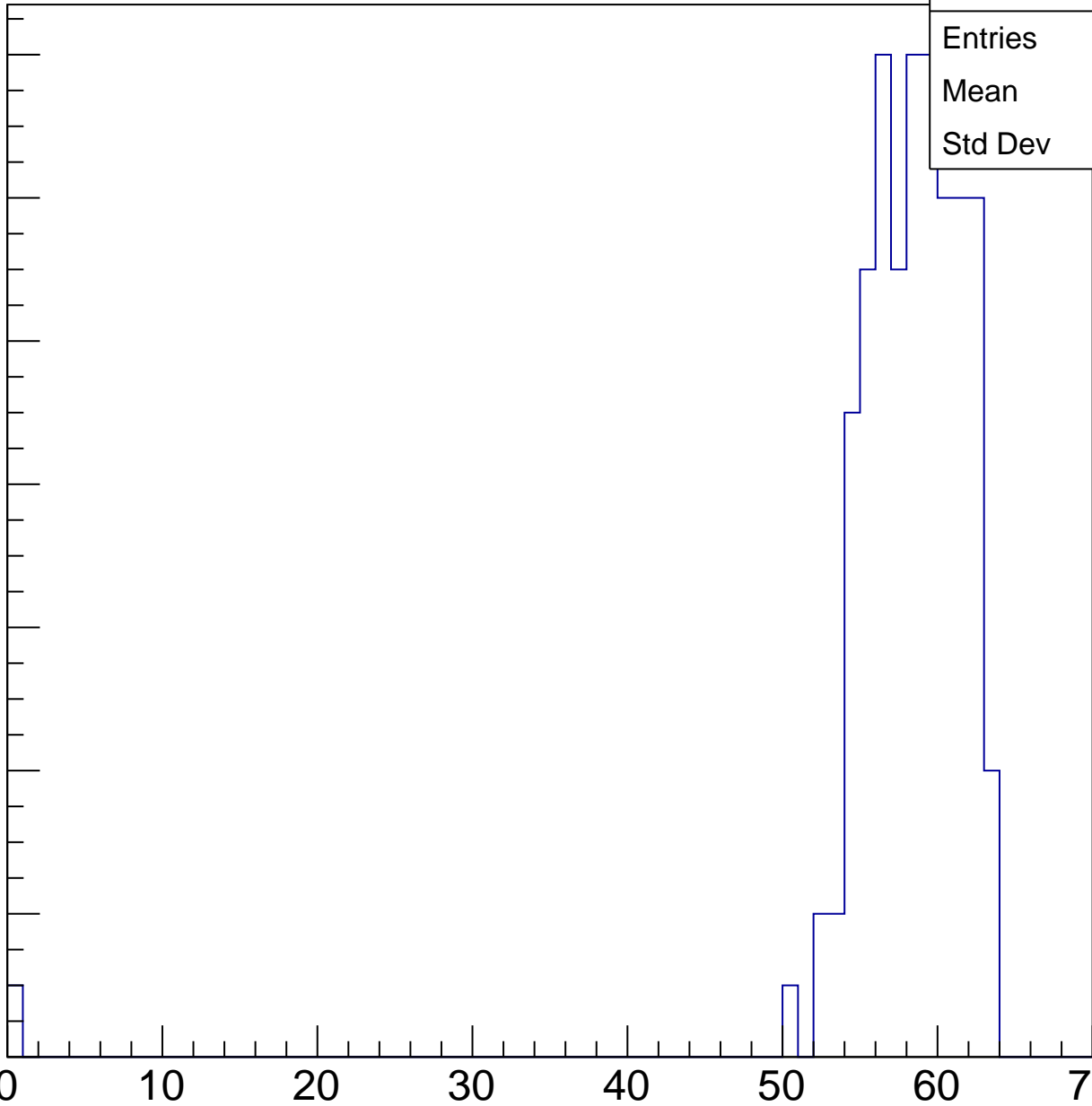
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	57.55
Std Dev	6.009

ampl



# B0L101S, U26-ch1, adc5

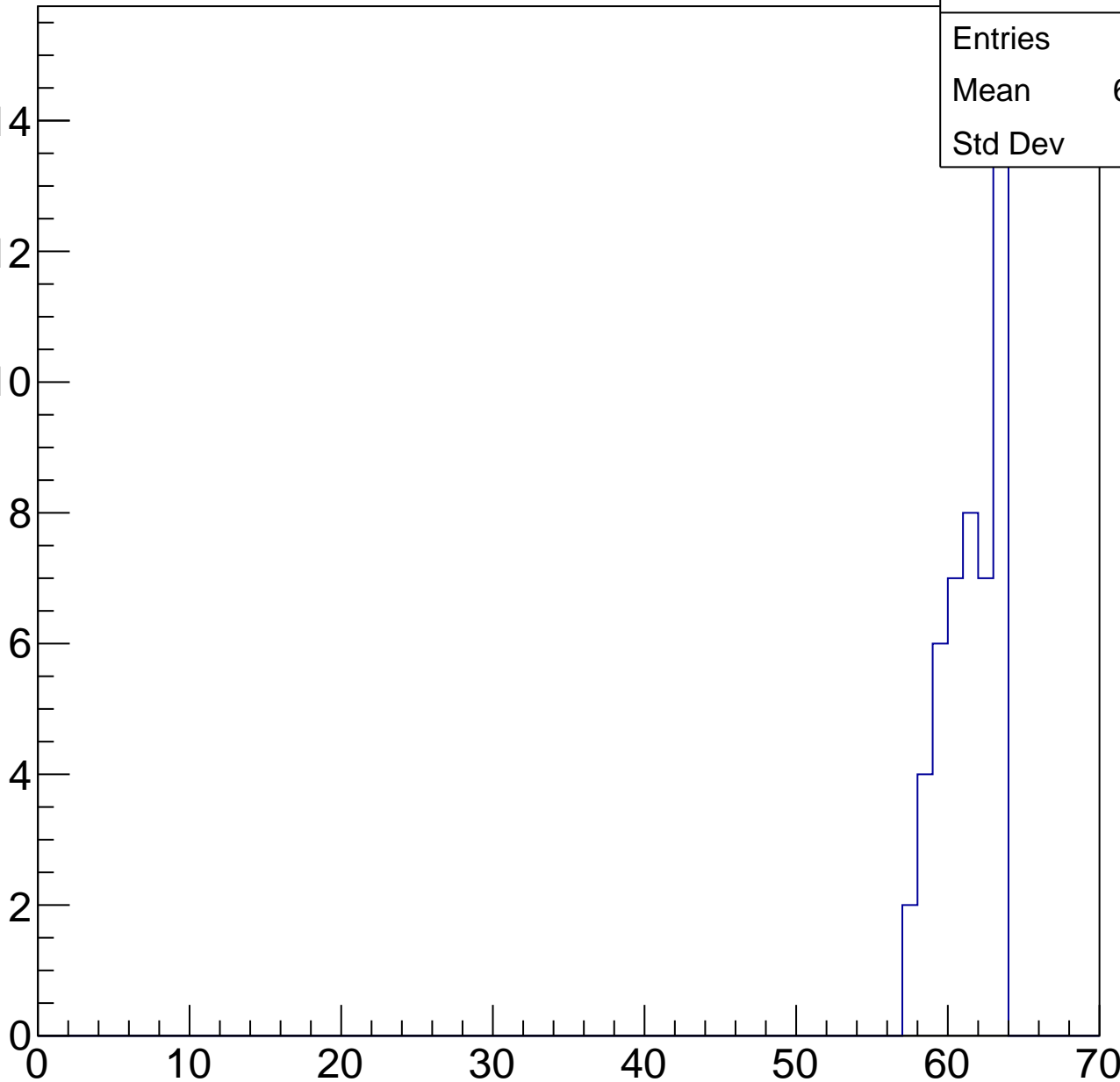
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	49
Mean	60.96
Std Dev	1.84

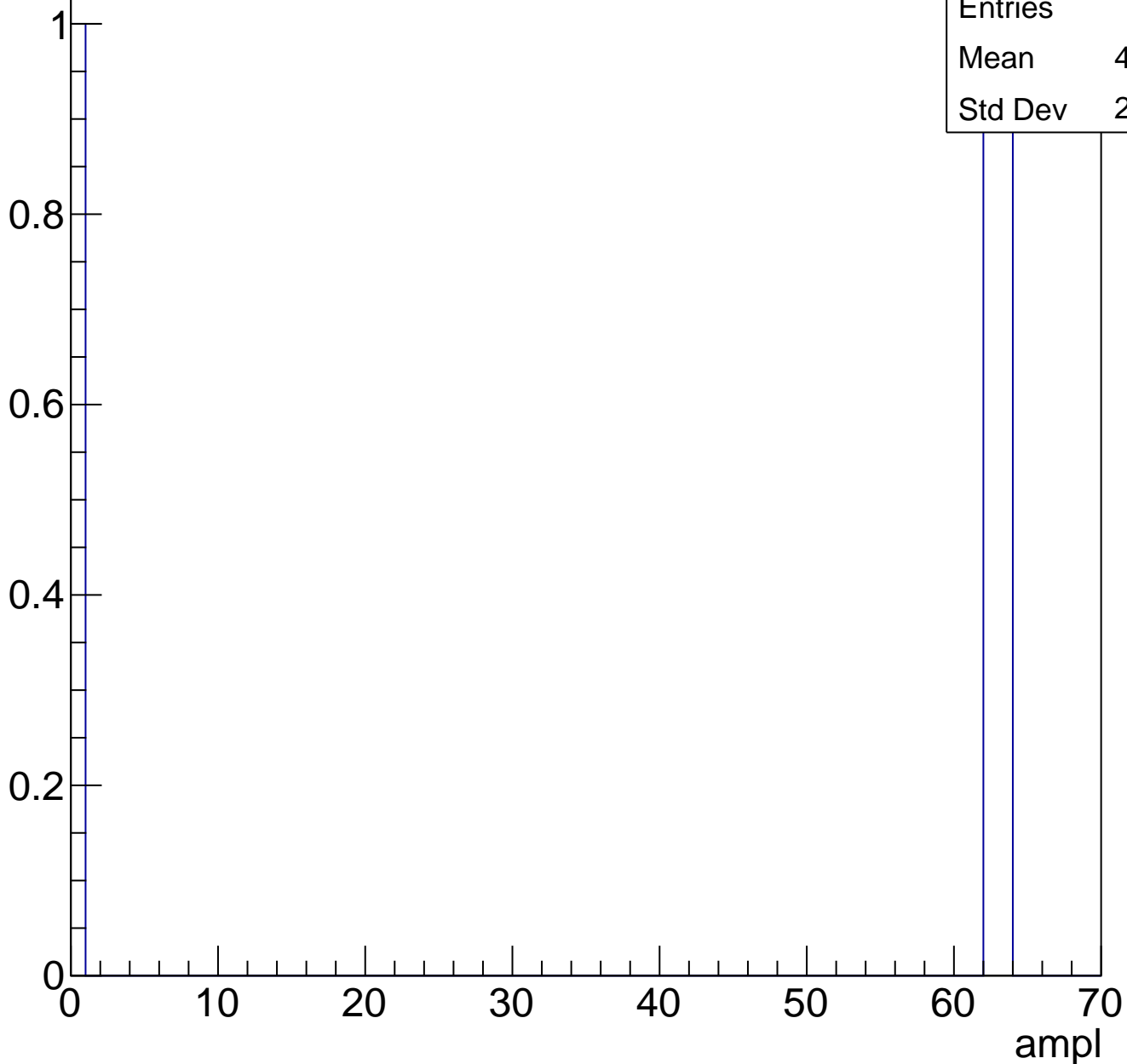
ampl



# B0L101S, U26-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch2, adc0

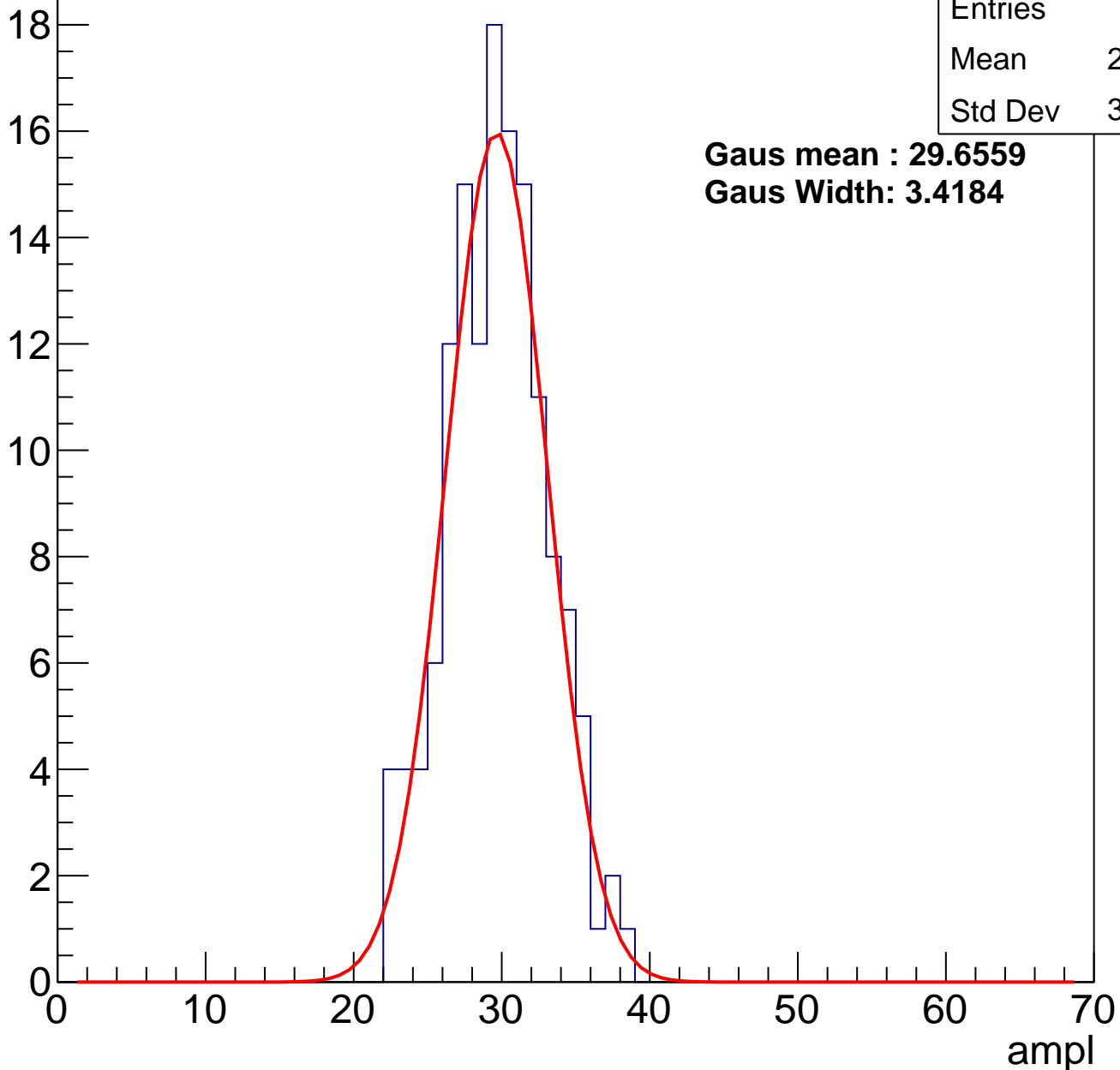
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	29.24
Std Dev	3.394

**Gaus mean : 29.6559**

**Gaus Width: 3.4184**

Entry



# B0L101S, U26-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

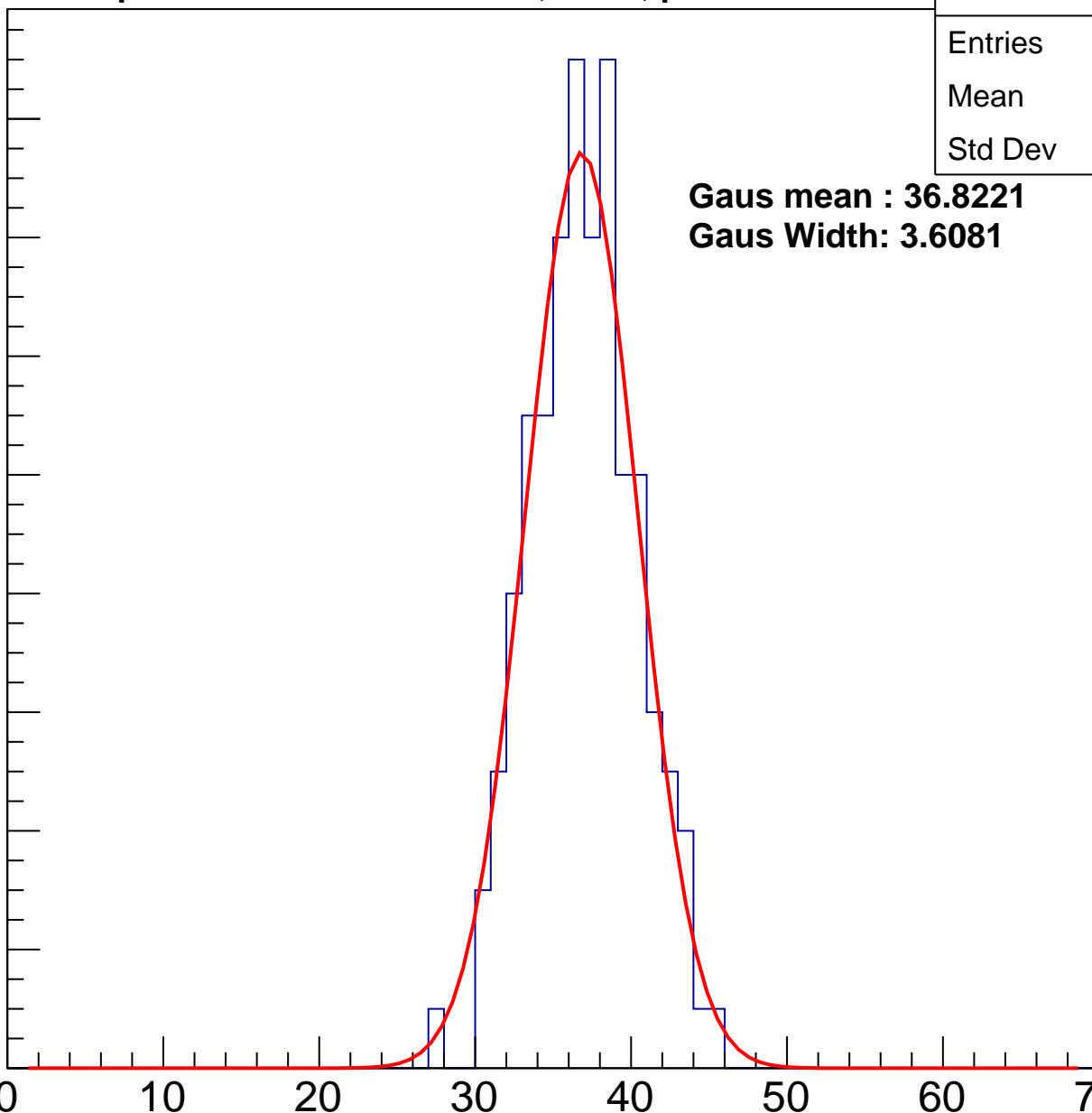
70

ampl

Entries	138
Mean	36.51
Std Dev	3.369

**Gaus mean : 36.8221**

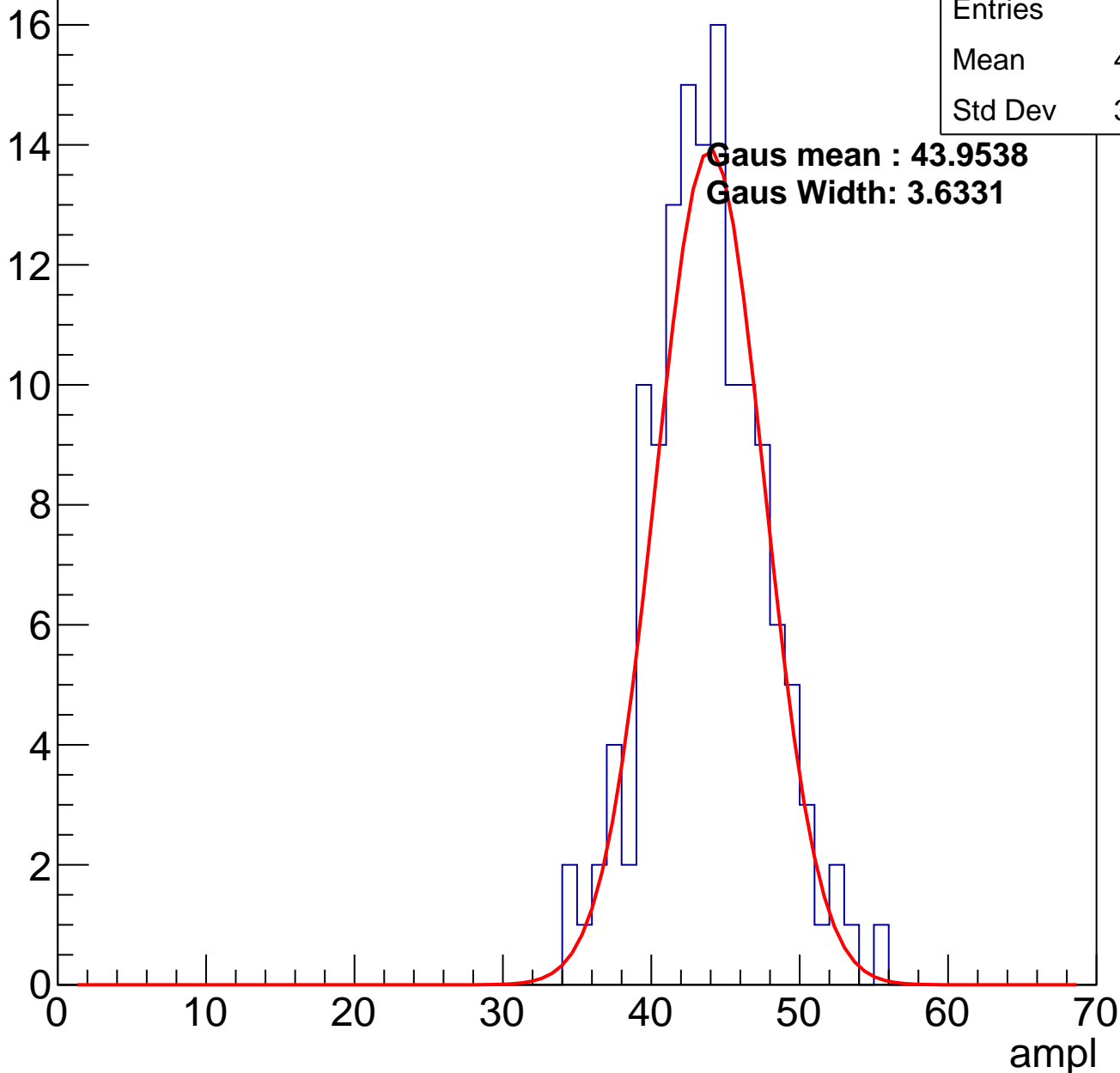
**Gaus Width: 3.6081**



# B0L101S, U26-ch2, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

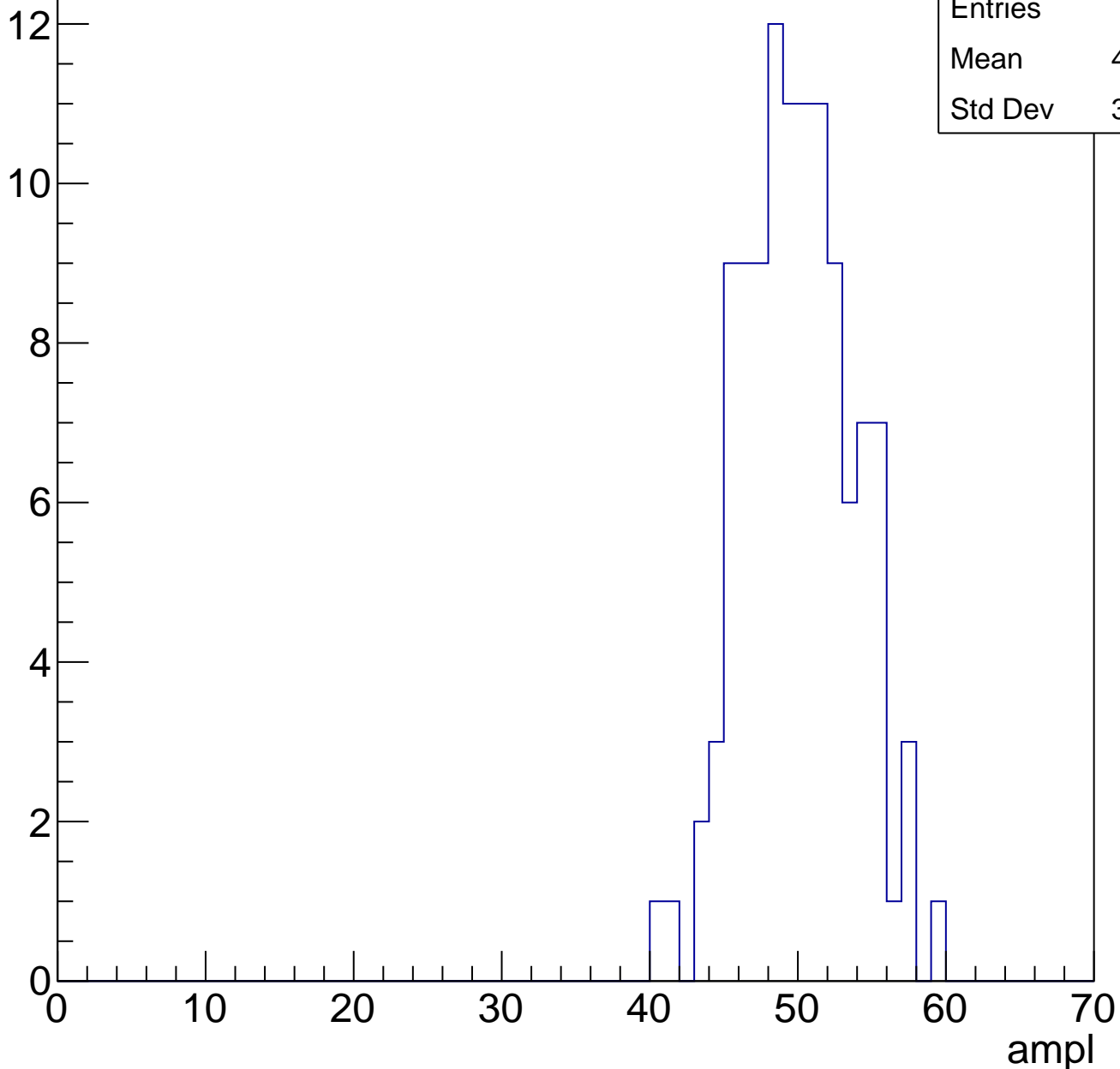


# B0L101S, U26-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	49.58
Std Dev	3.677

Entry

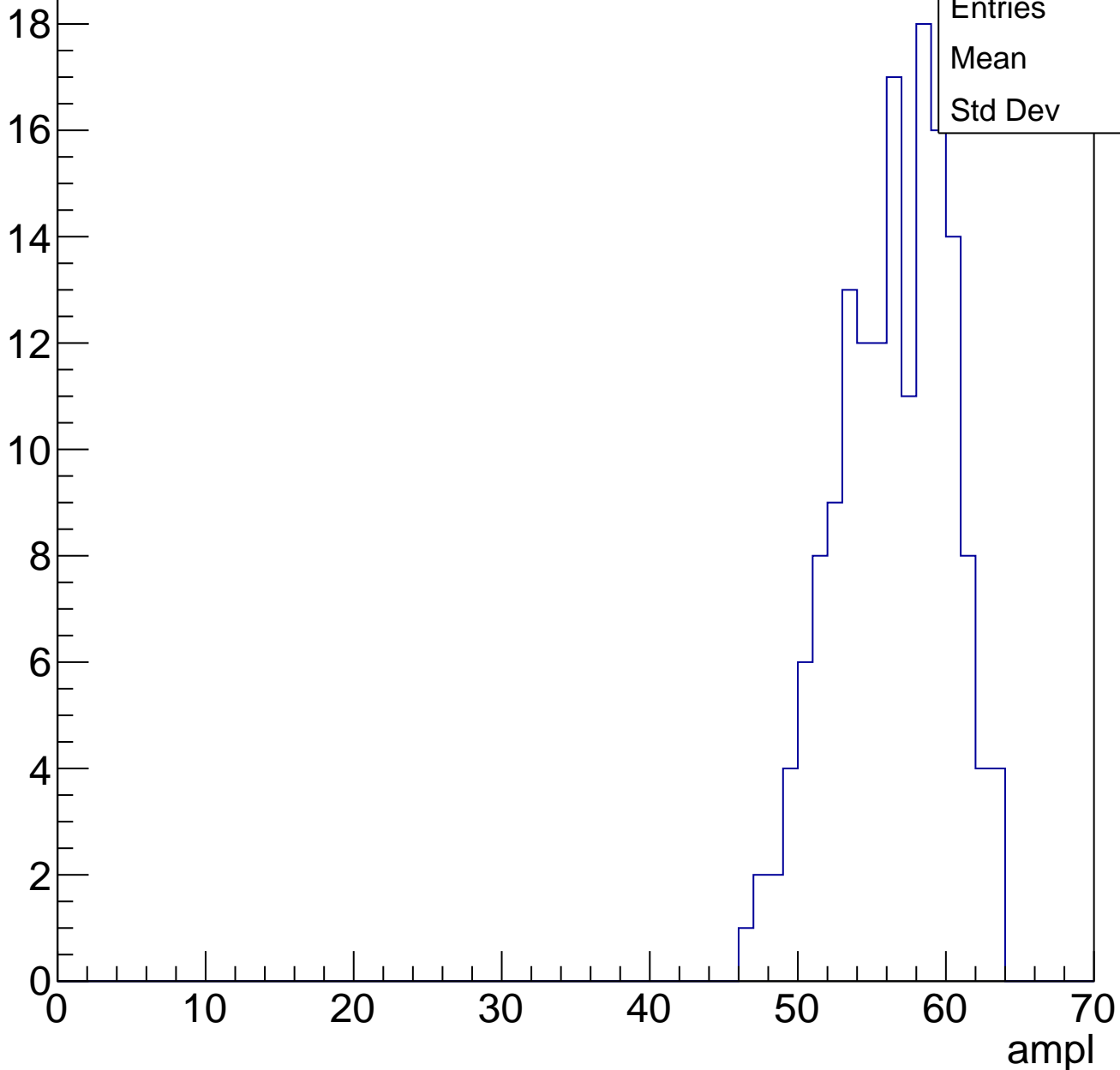


# B0L101S, U26-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	55.9
Std Dev	3.79

Entry



# B0L101S, U26-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	59
Mean	58.56
Std Dev	11.19

ampl

0

10

20

30

40

50

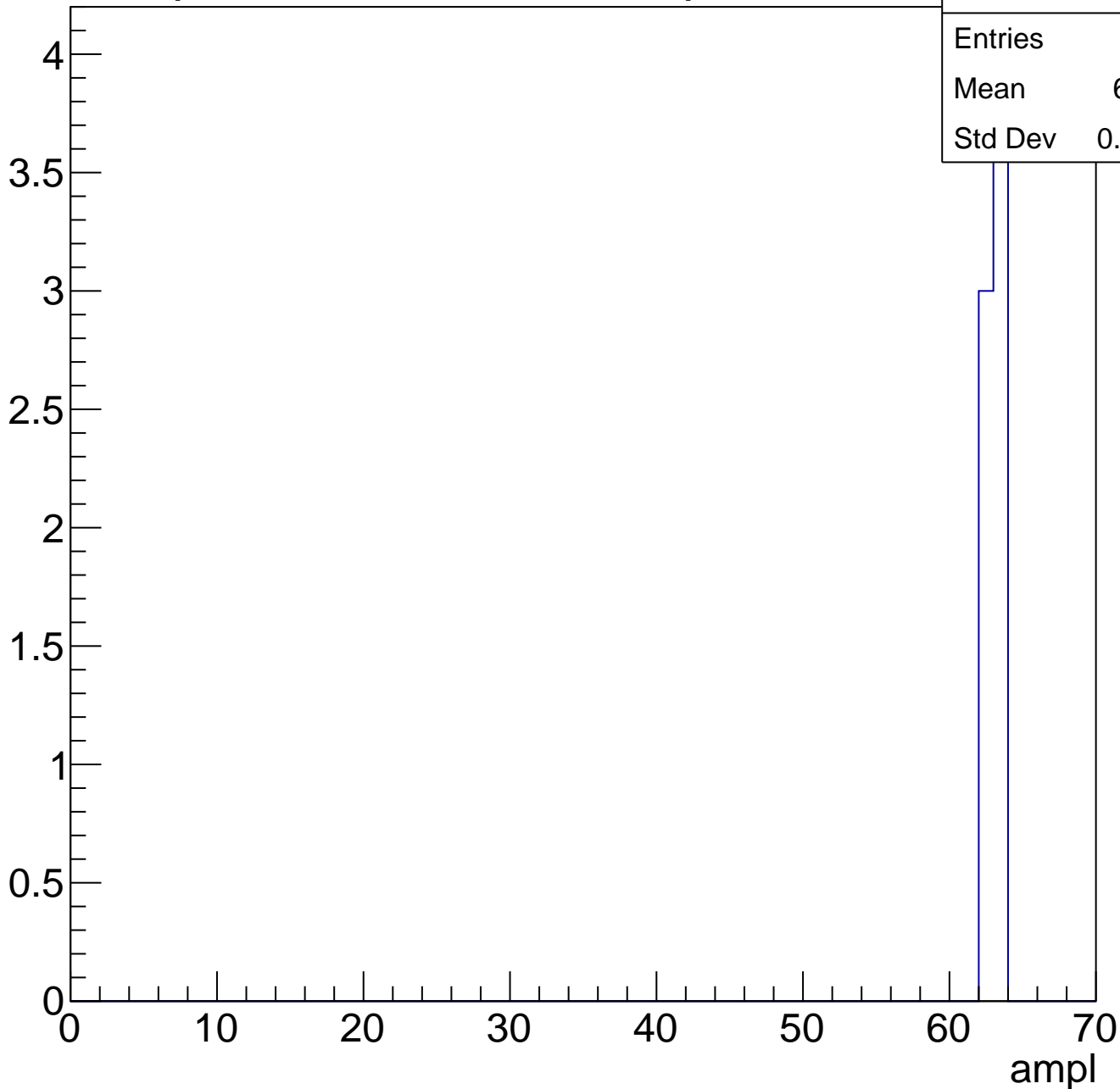
60

70

# B0L101S, U26-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch3, adc0

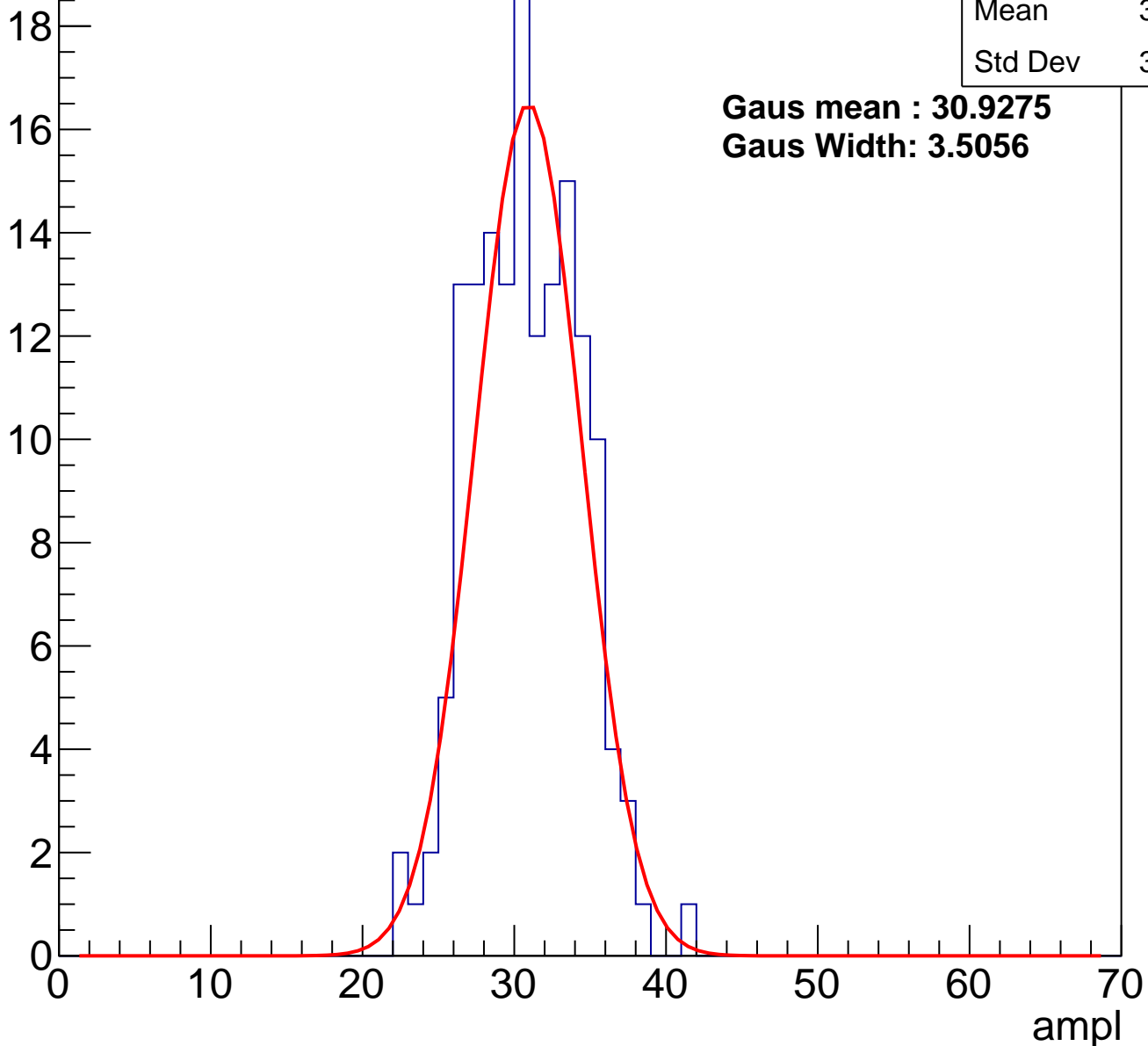
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	30.35
Std Dev	3.493

**Gaus mean : 30.9275**

**Gaus Width: 3.5056**

Entry



# B0L101S, U26-ch3, adc1

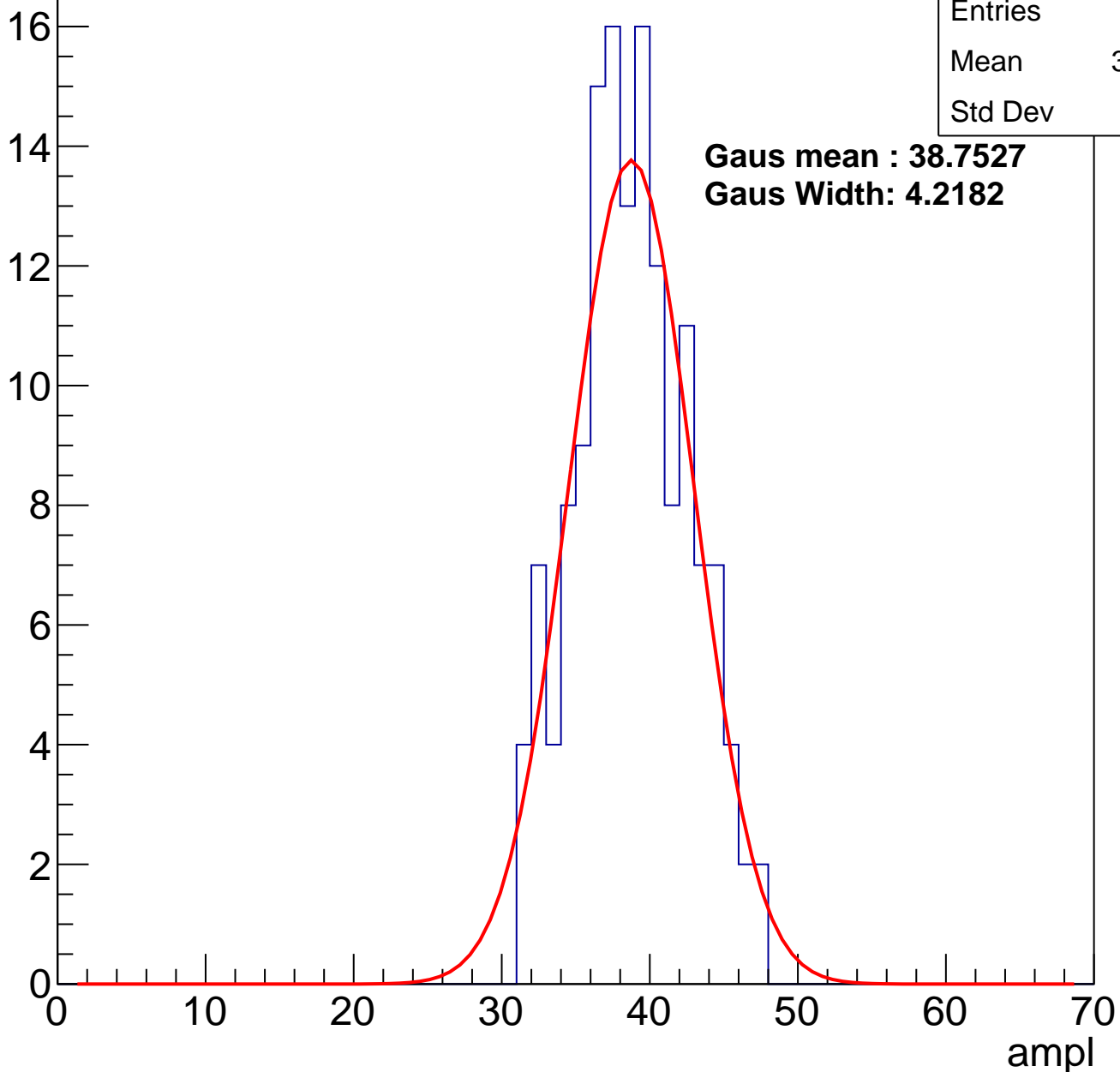
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	38.36
Std Dev	3.75

**Gaus mean : 38.7527**

**Gaus Width: 4.2182**

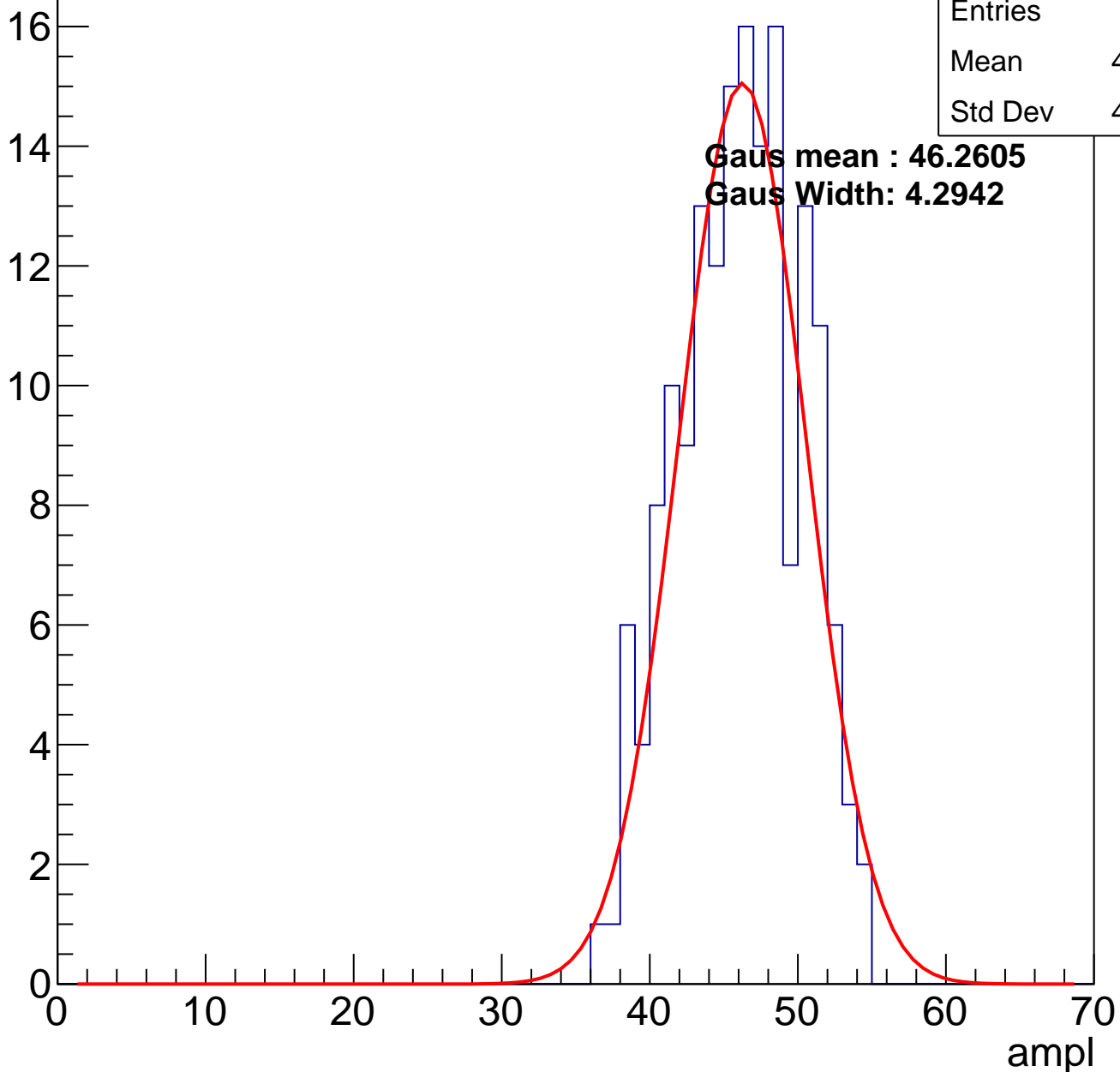
Entry



# B0L101S, U26-ch3, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

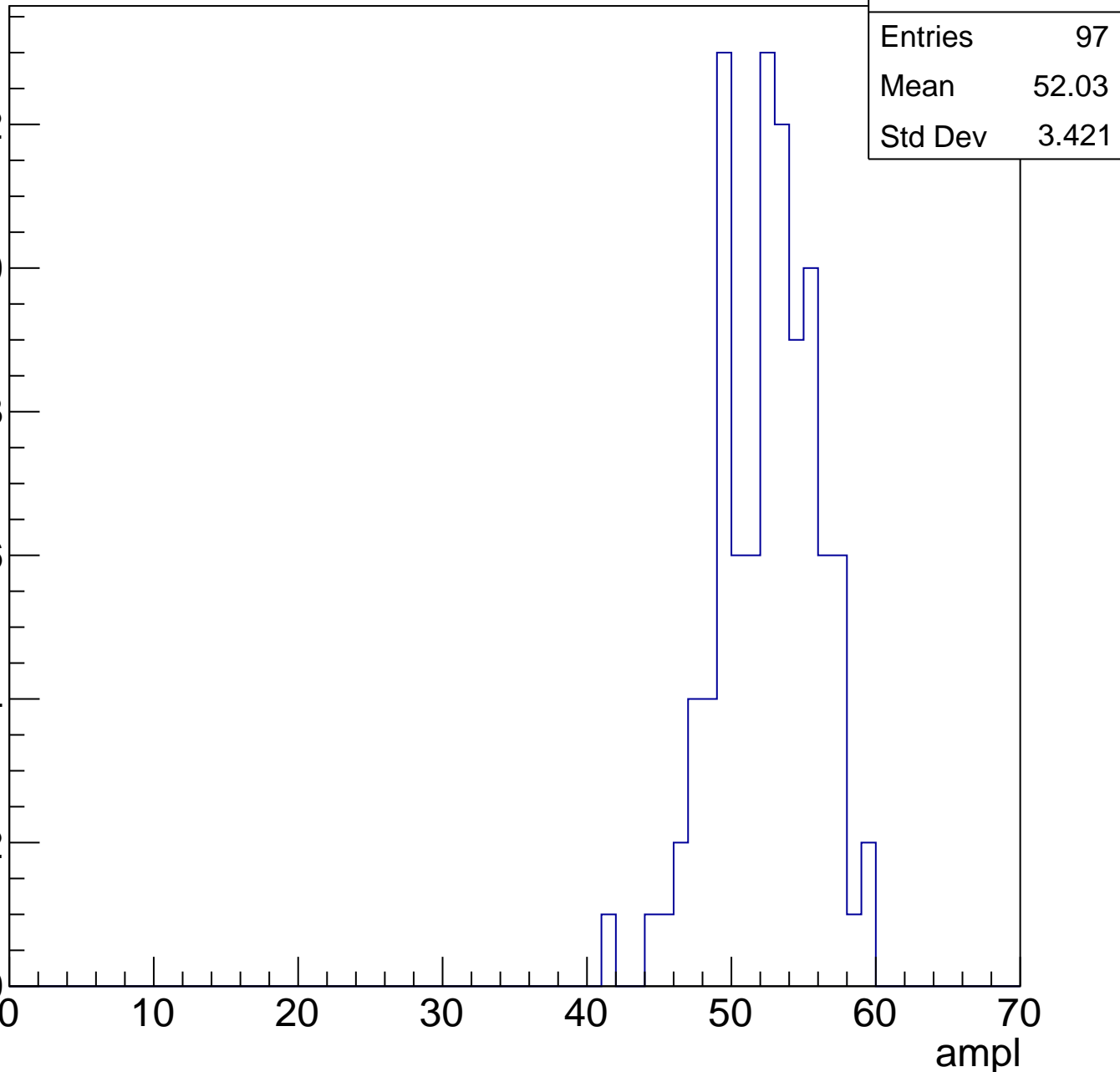
40

50

60

ampl

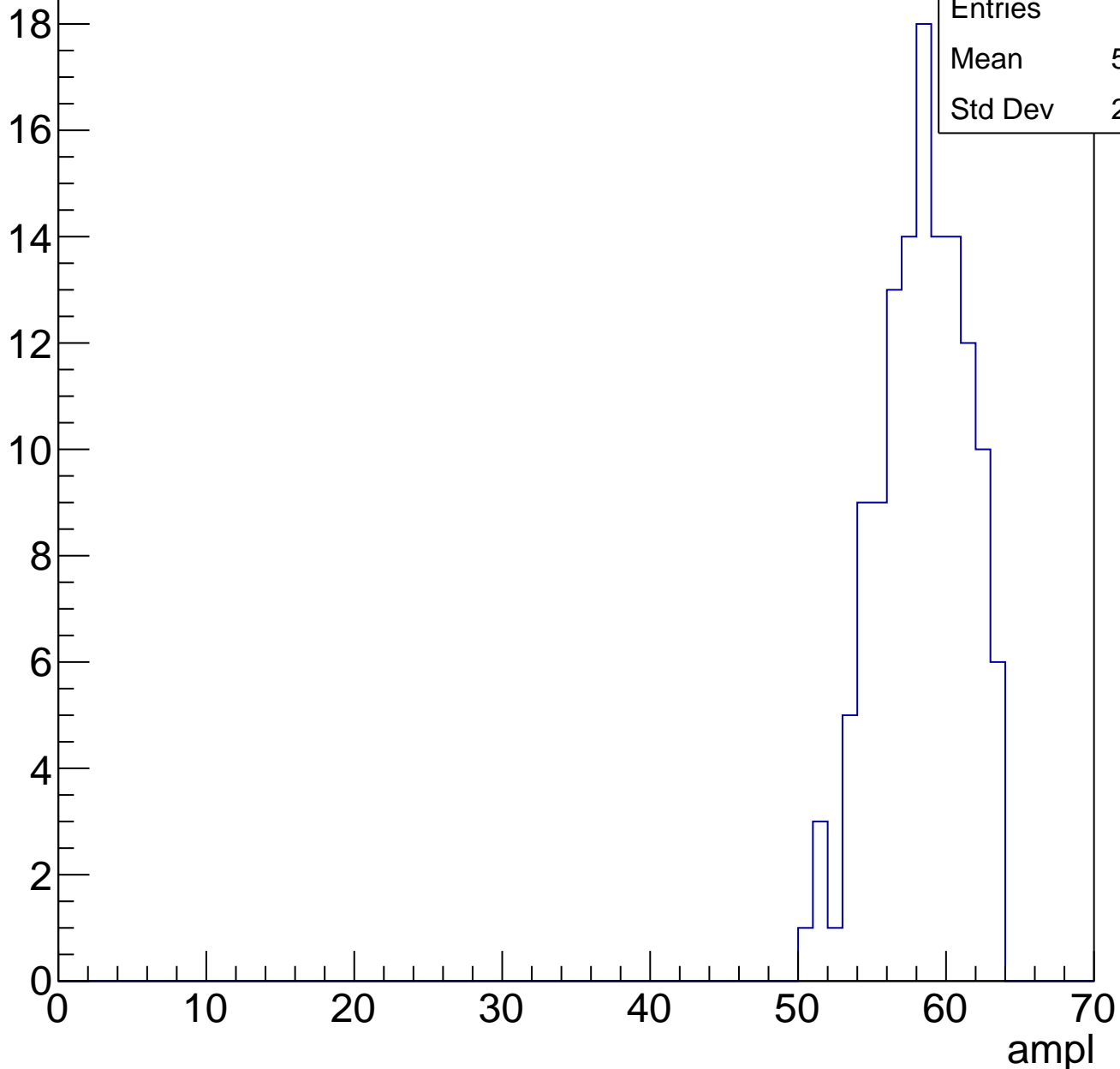
Entries	97
Mean	52.03
Std Dev	3.421



# B0L101S, U26-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

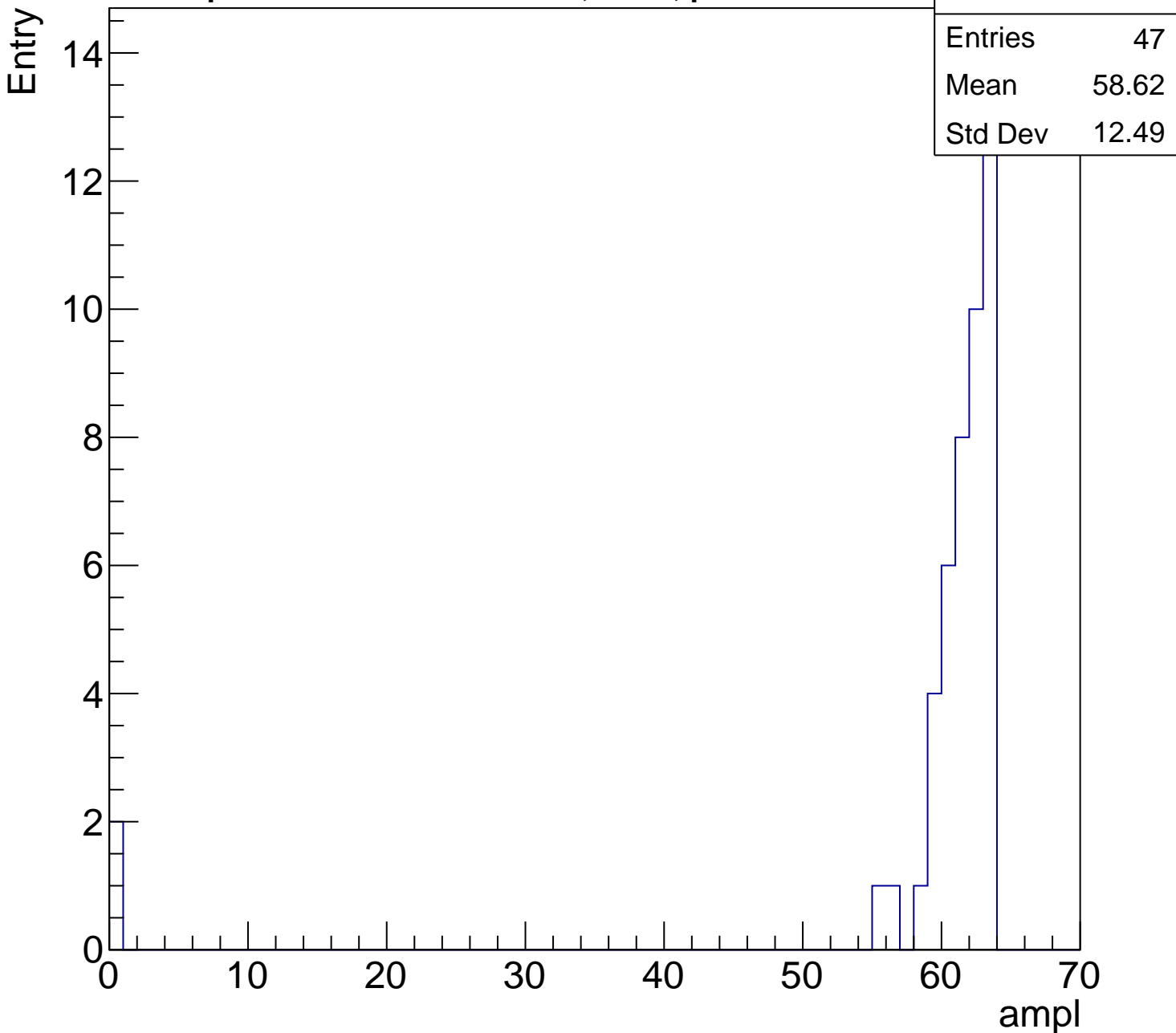
Entries	47
Mean	58.62
Std Dev	12.49

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U26-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

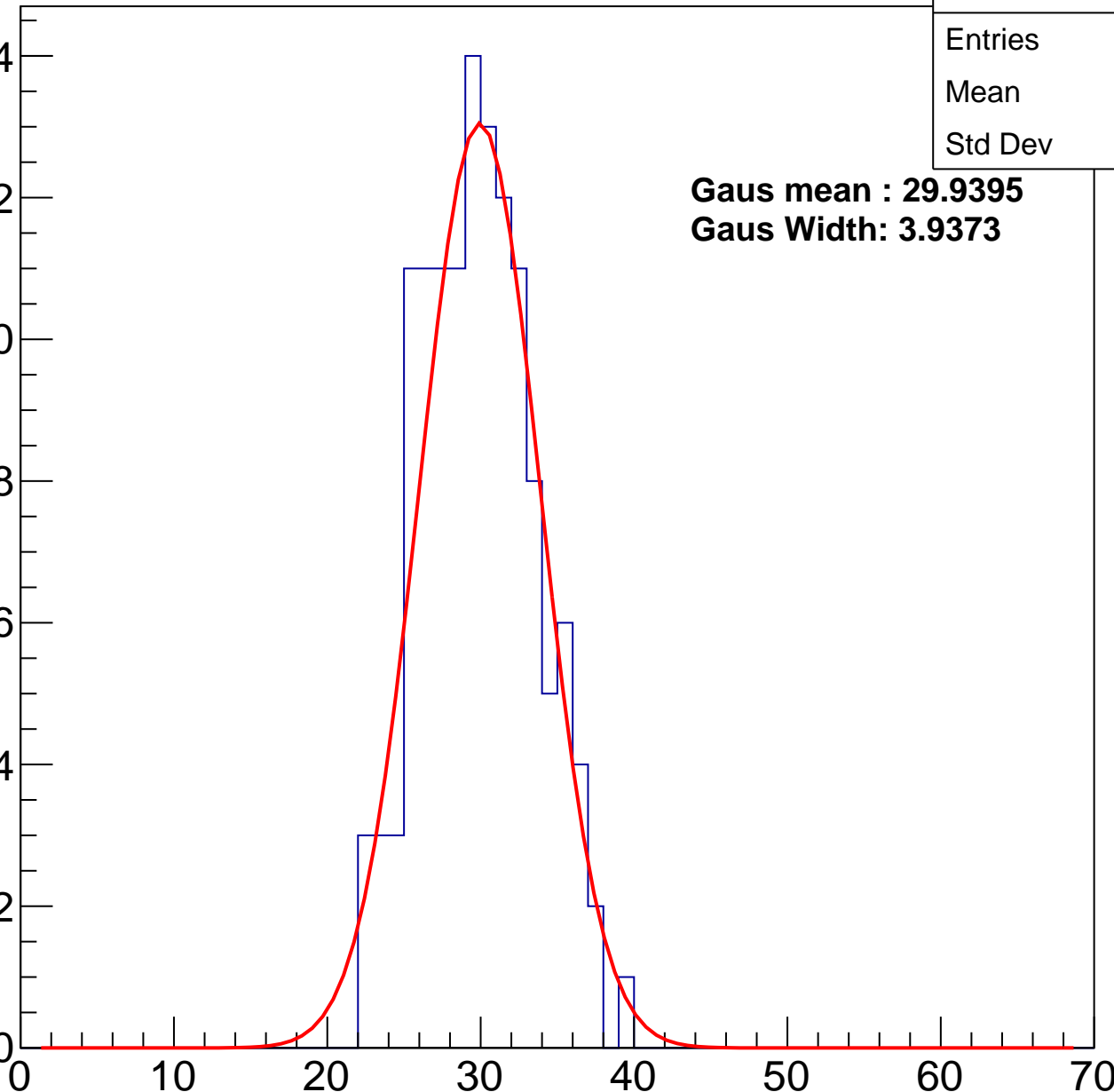
14  
12  
10  
8  
6  
4  
2  
0

Entries	129
Mean	29.41
Std Dev	3.621

**Gaus mean : 29.9395**

**Gaus Width: 3.9373**

ampl

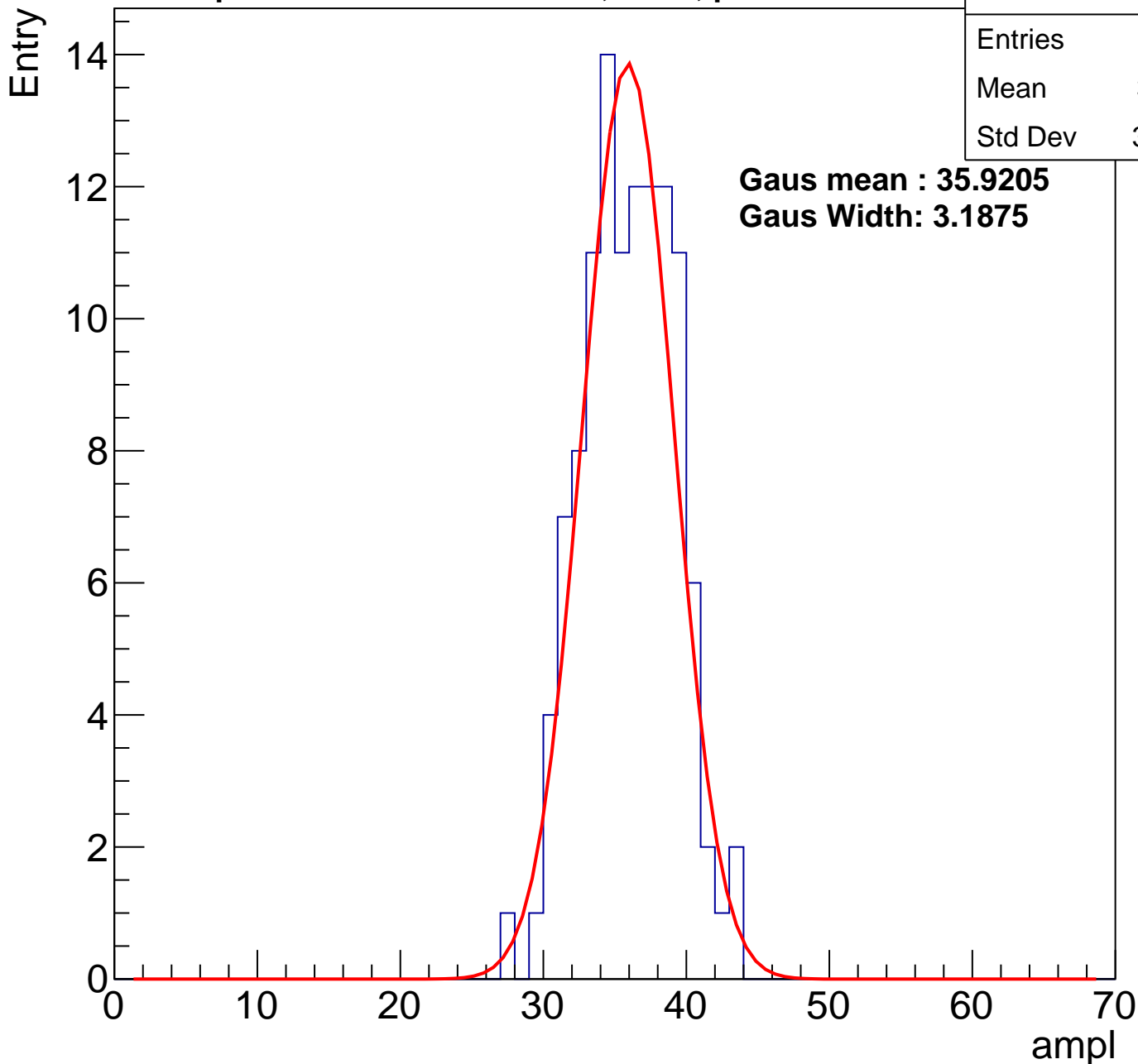


# B0L101S, U26-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	35.51
Std Dev	3.169

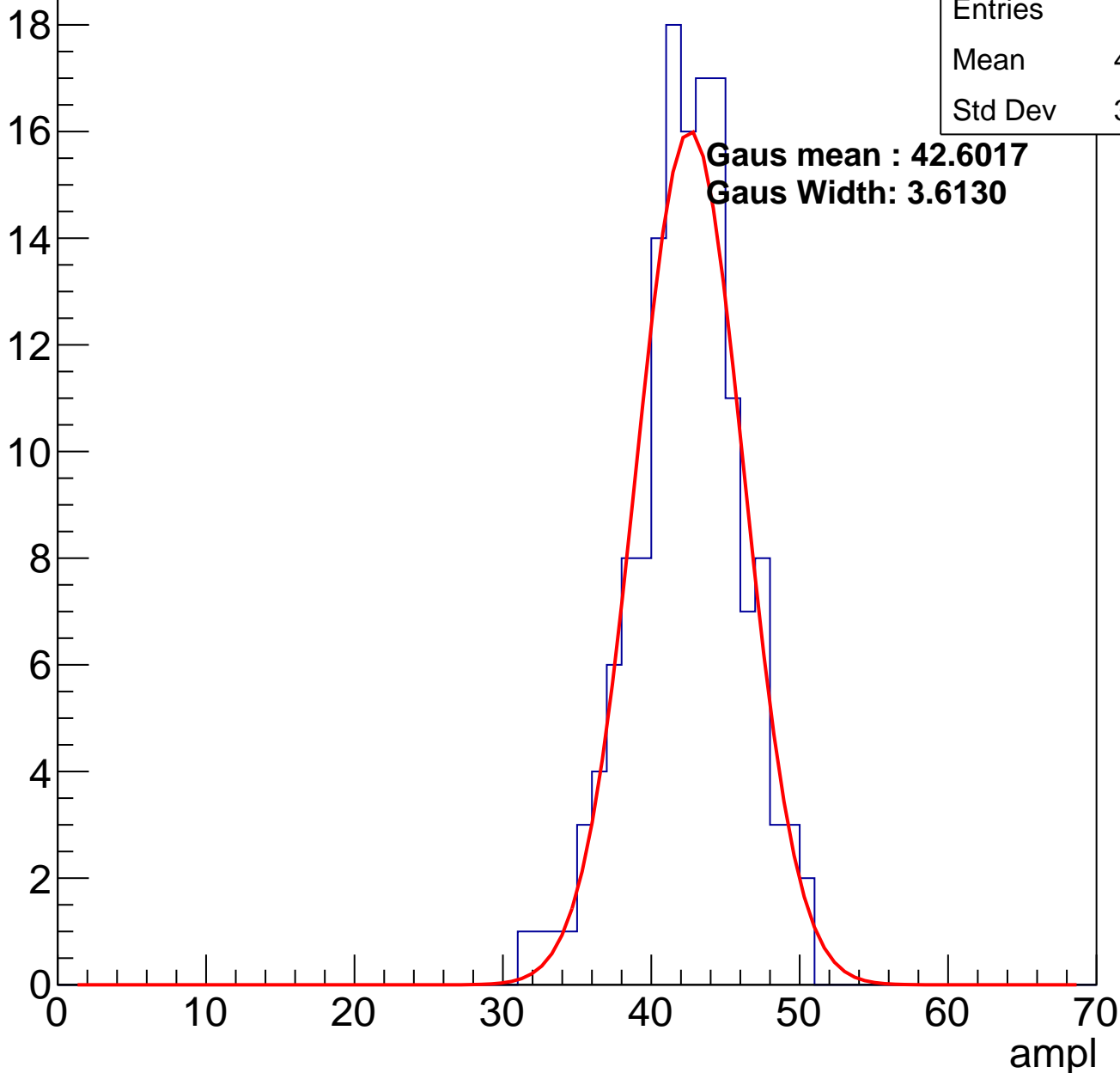
**Gaus mean : 35.9205**  
**Gaus Width: 3.1875**



# B0L101S, U26-ch4, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	49.73
Std Dev	3.442

Entry

16

14

12

10

8

6

4

2

0

0

10

20

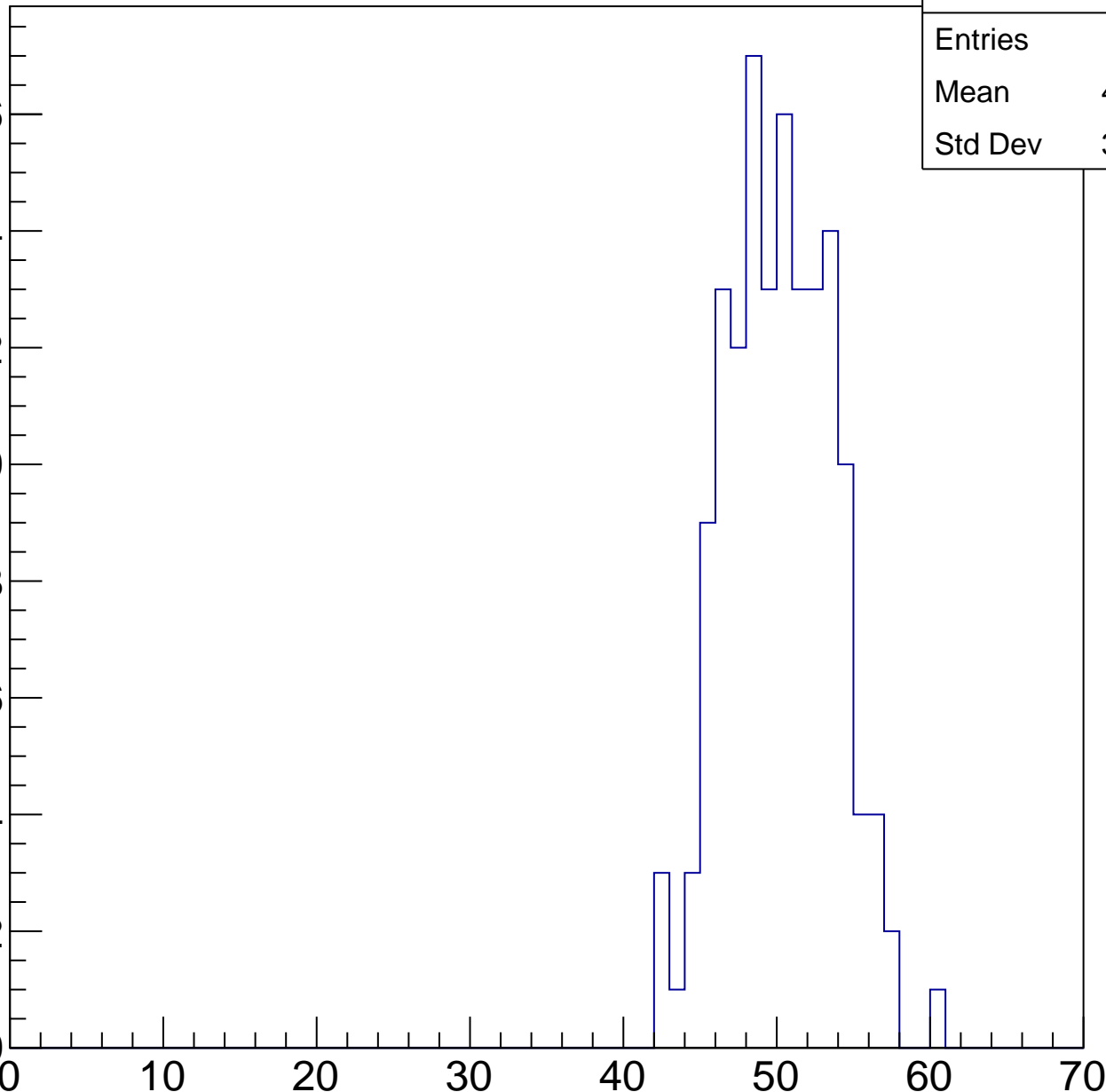
30

40

50

60

ampl



# B0L101S, U26-ch4, adc4

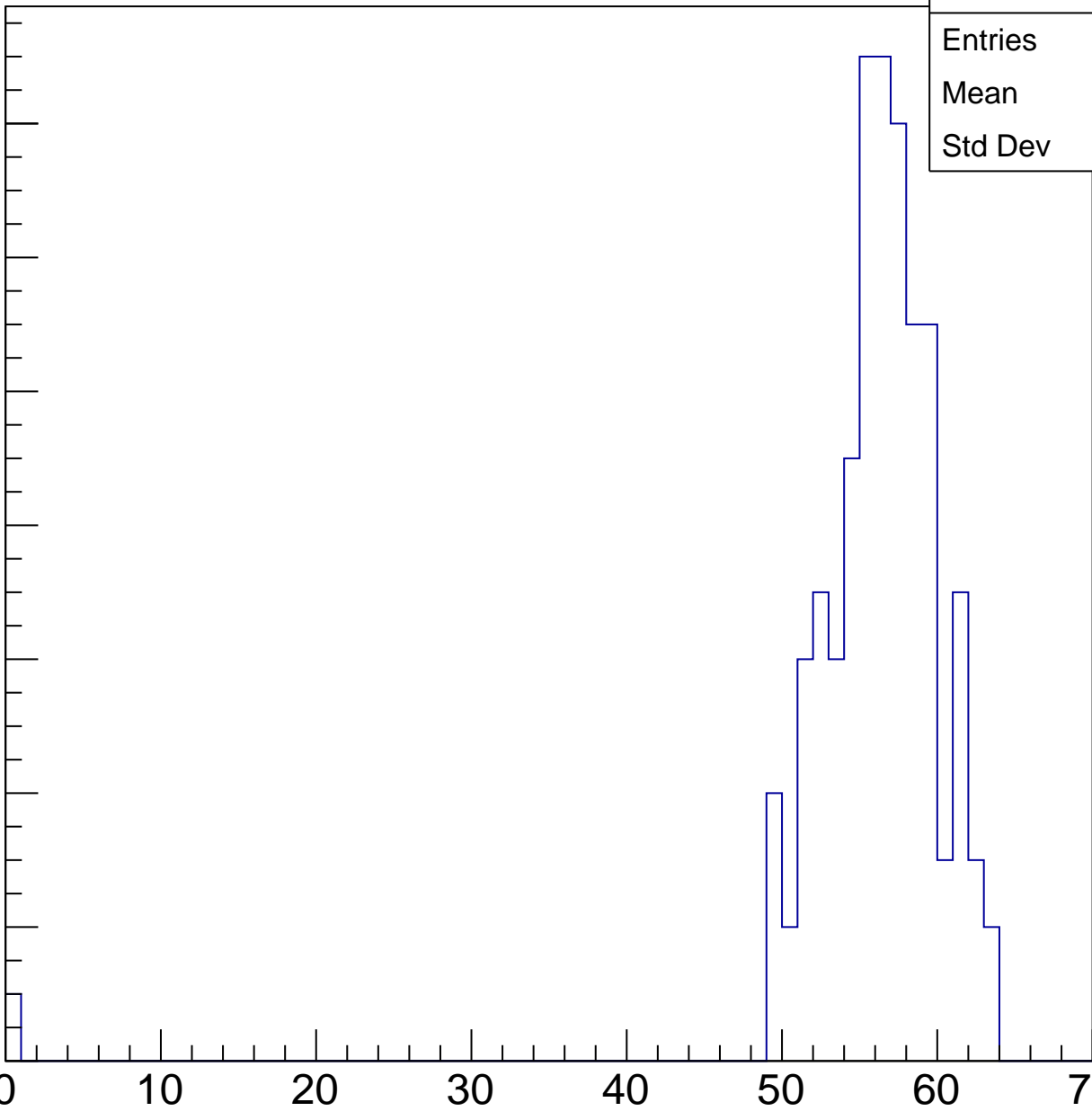
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	55.51
Std Dev	6.109

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch4, adc5

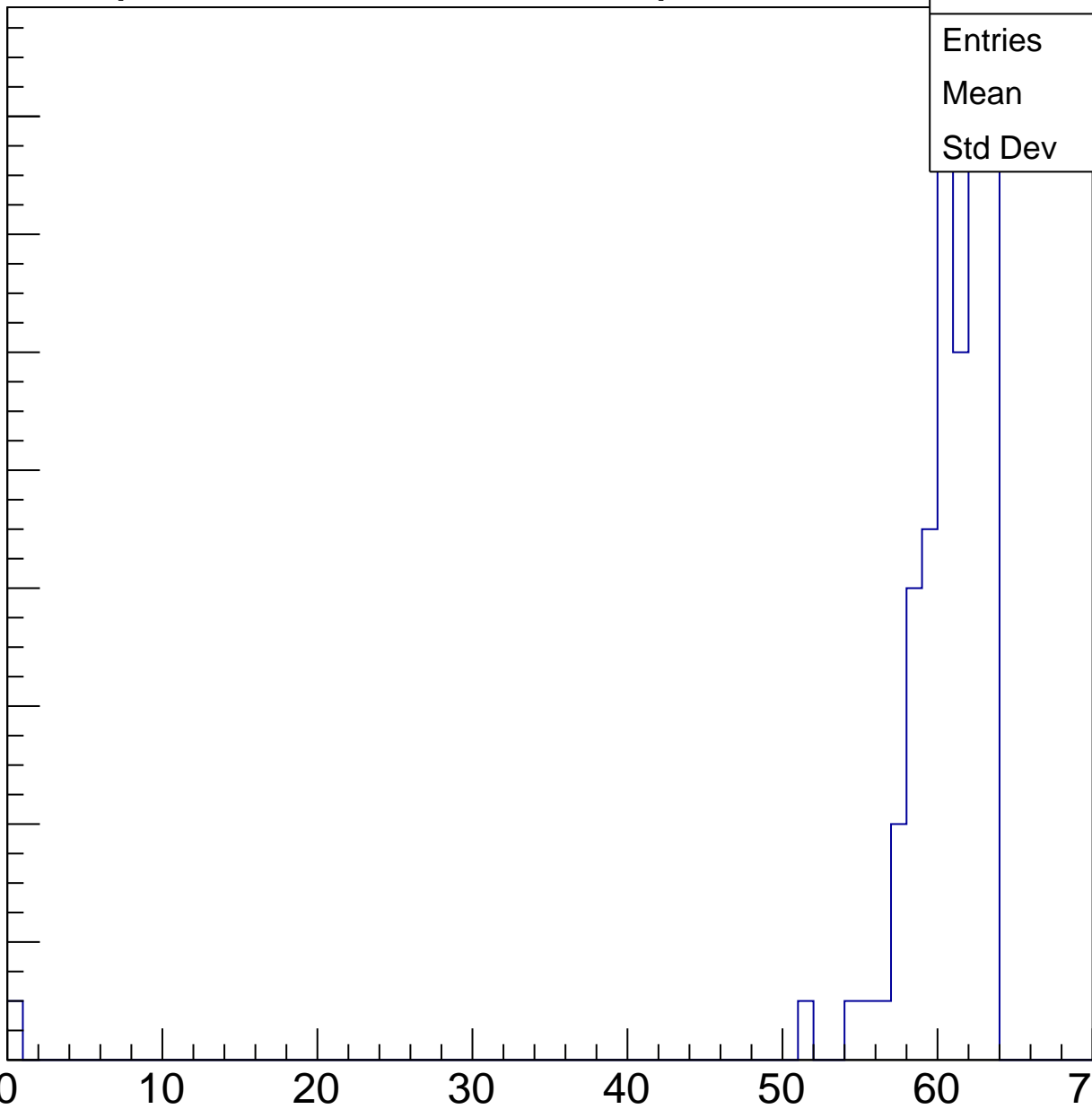
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	59.73
Std Dev	6.794

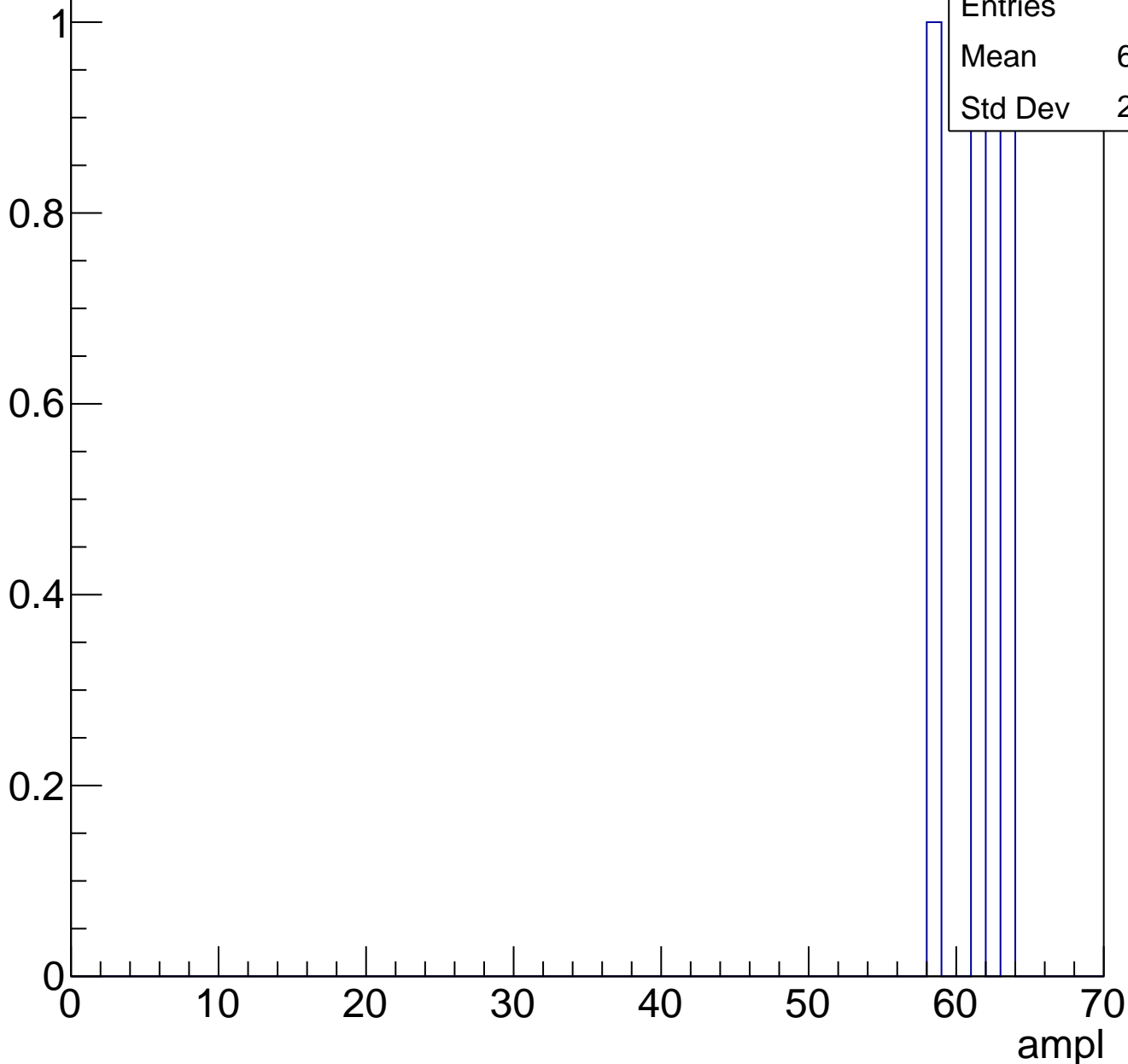
ampl



# B0L101S, U26-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	30.2
Std Dev	3.413

**Gaus mean : 30.4494**

**Gaus Width: 3.6121**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

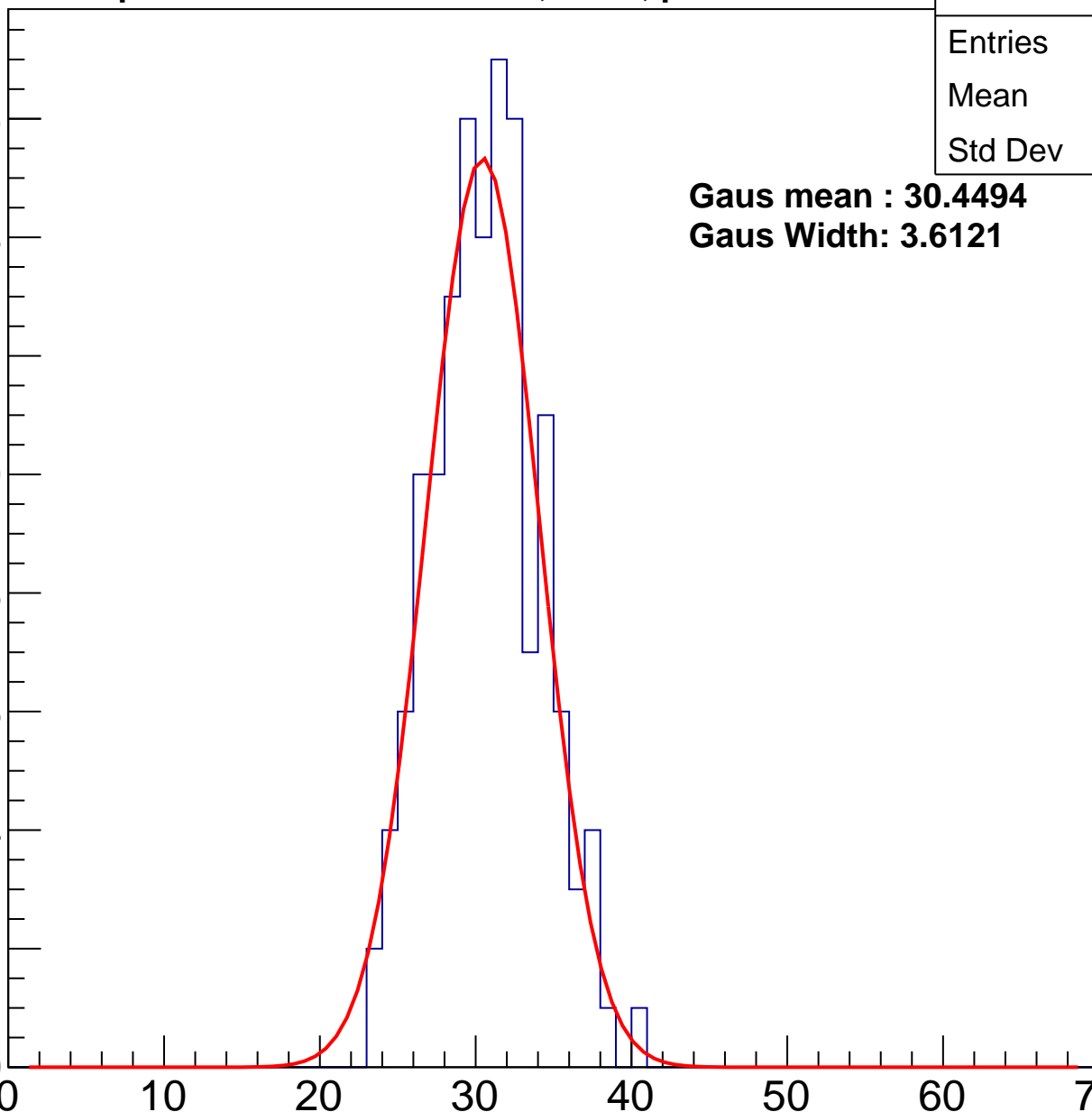
40

50

60

70

ampl



# B0L101S, U26-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	37.74
Std Dev	3.813

**Gaus mean : 38.4845**

**Gaus Width: 3.7578**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

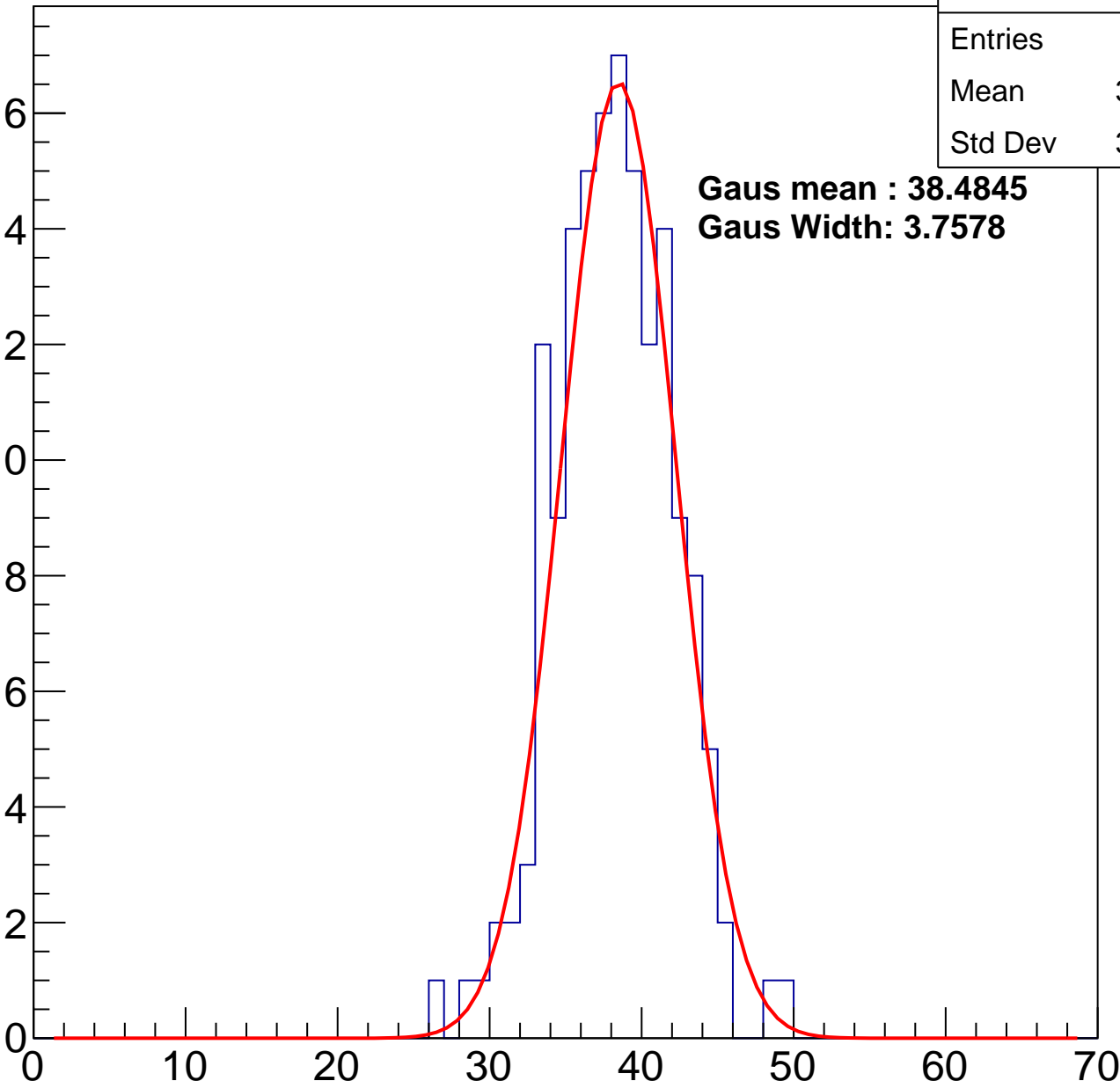
40

50

60

70

ampl



# B0L101S, U26-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	120
Mean	44.67
Std Dev	3.494

**Gaus mean : 45.1323**

**Gaus Width: 3.6484**

0

10

20

30

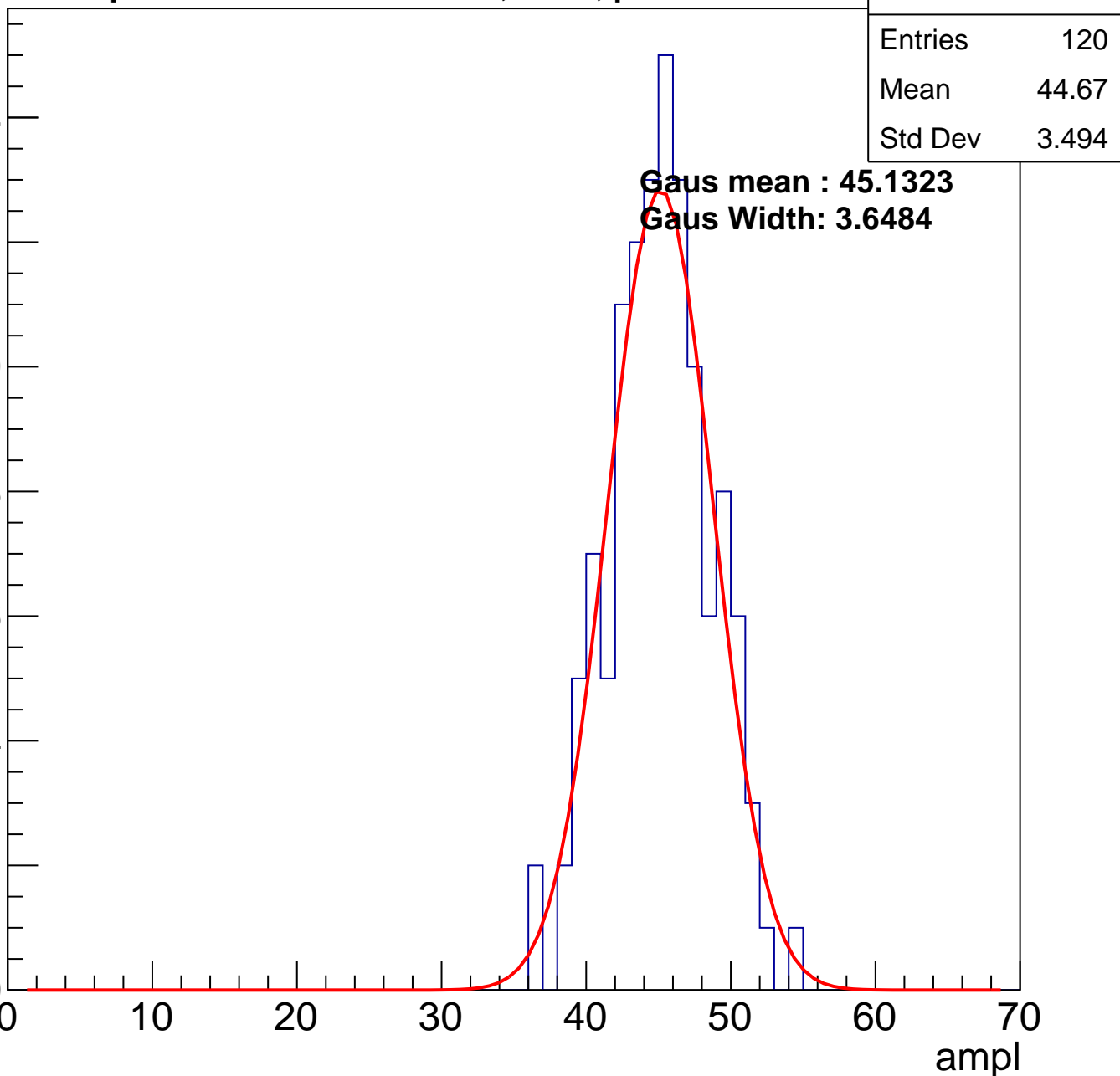
40

50

60

70

ampl



# B0L101S, U26-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

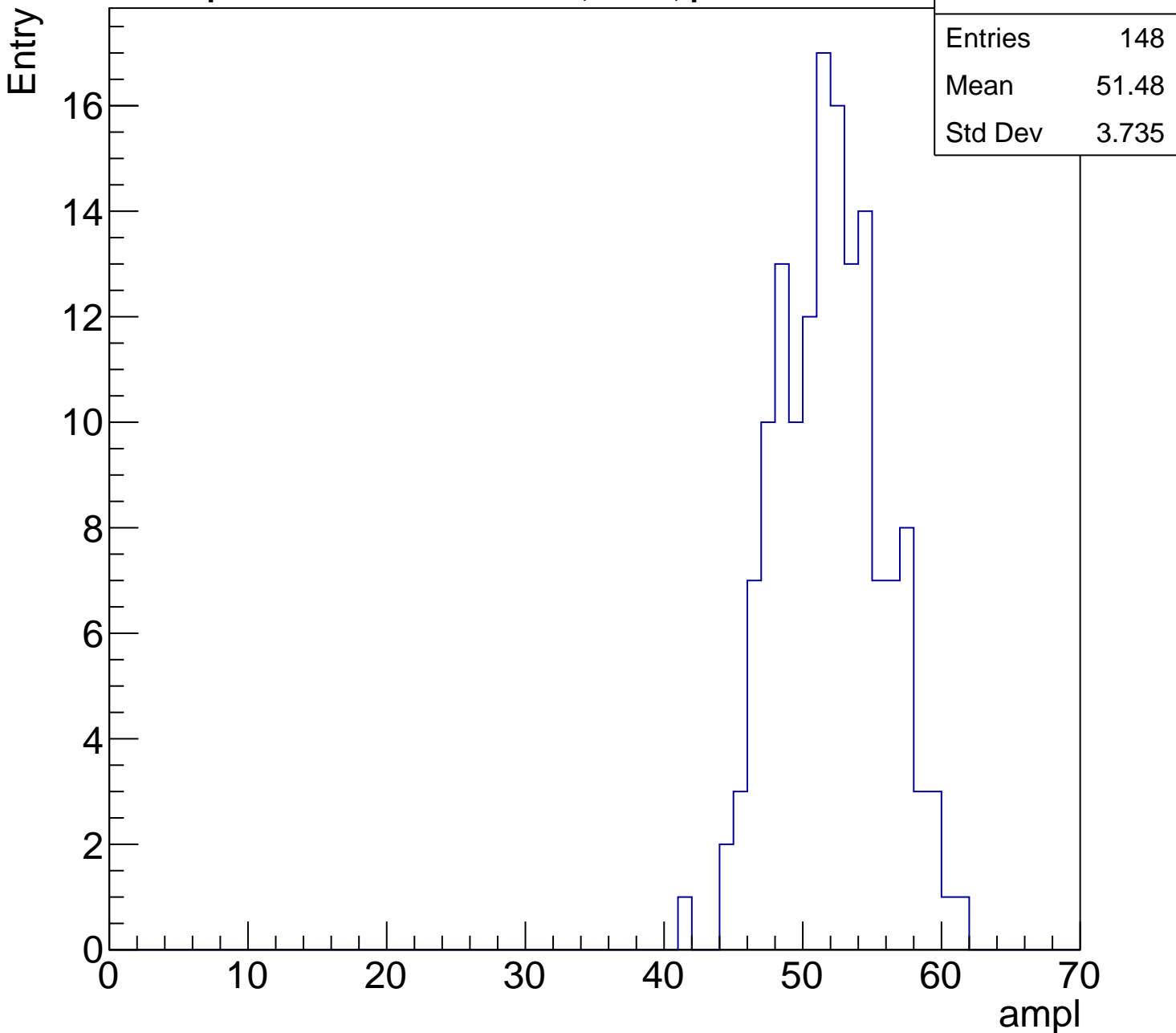
Entries	148
Mean	51.48
Std Dev	3.735

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

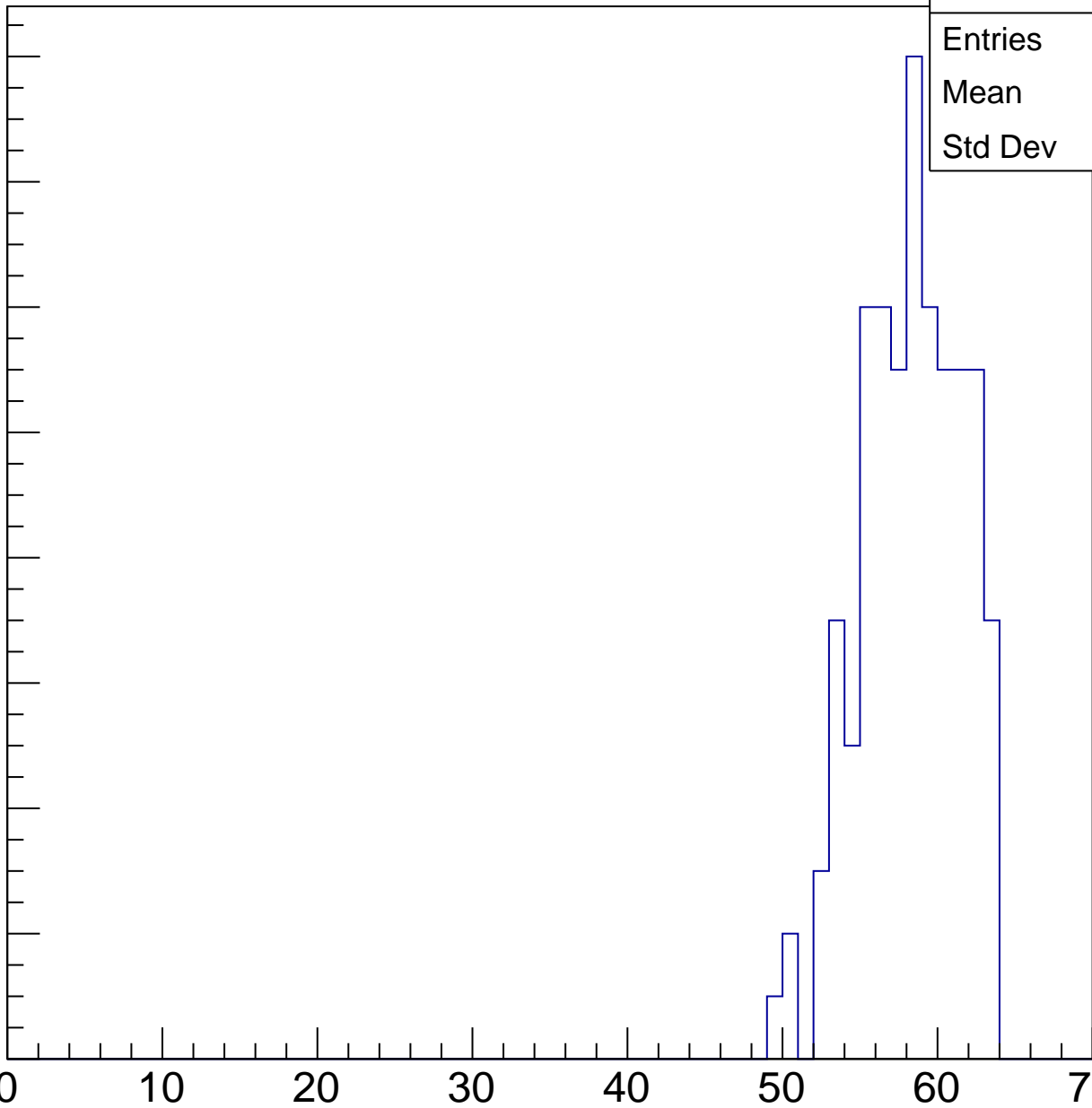
Entries	121
Mean	57.81
Std Dev	3.202

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

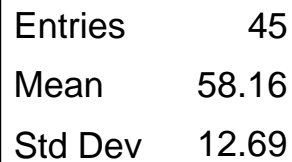
40

50

60

ampl

Entries	45
Mean	58.16
Std Dev	12.69



# B0L101S, U26-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

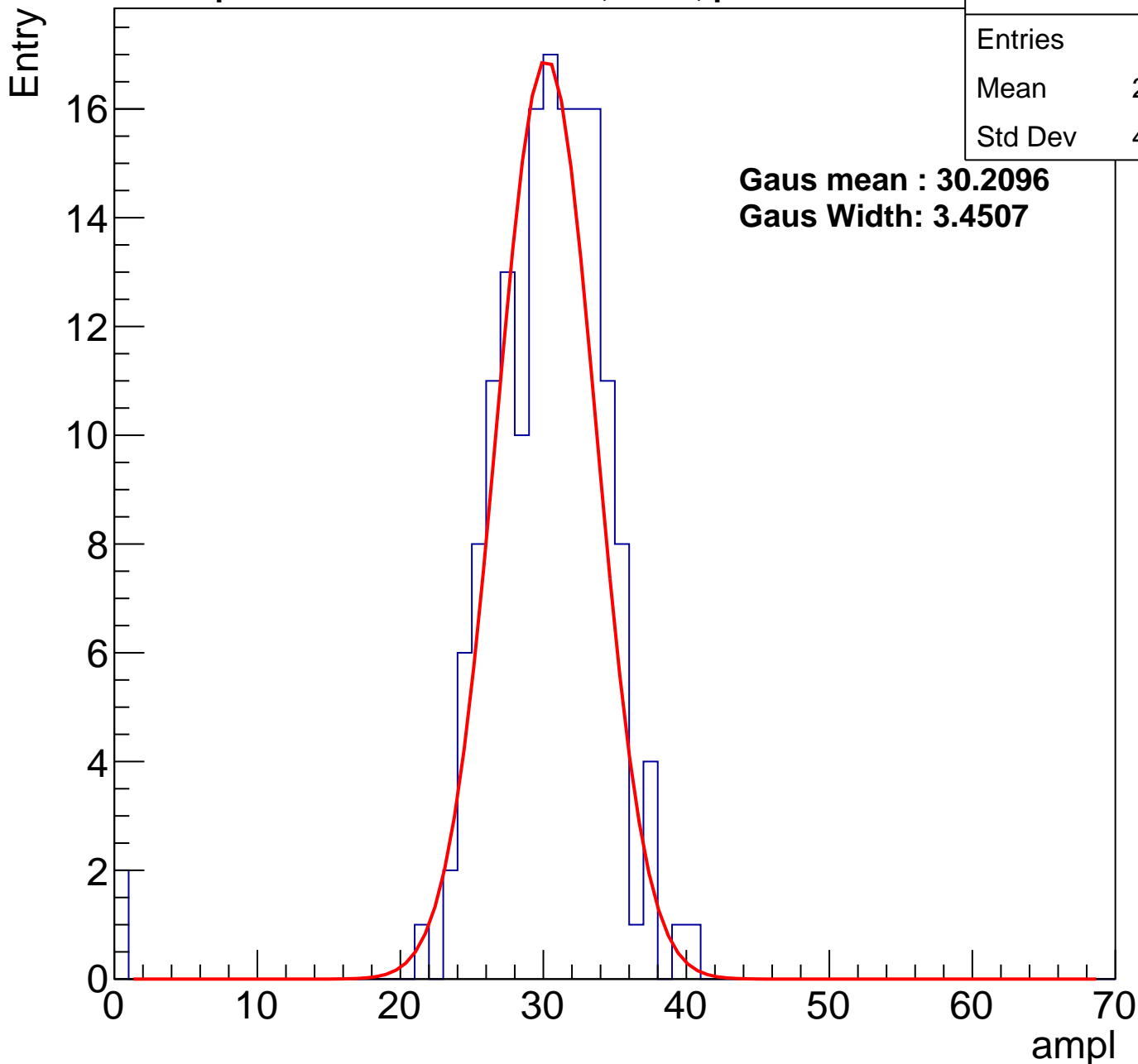
# B0L101S, U26-ch6, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	29.72
Std Dev	4.838

**Gaus mean : 30.2096**

**Gaus Width: 3.4507**



# B0L101S, U26-ch6, adc1

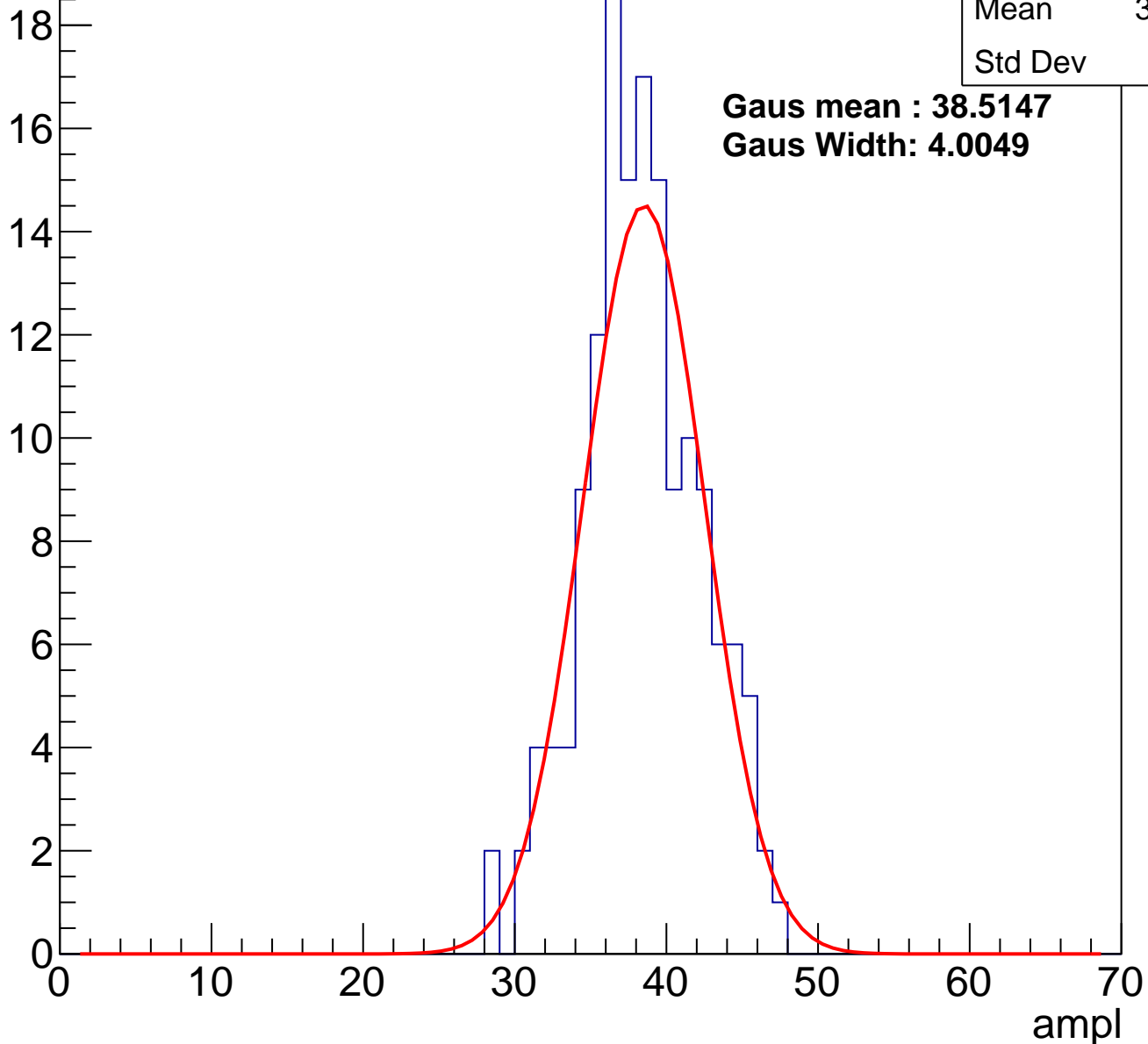
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	37.95
Std Dev	3.83

**Gaus mean : 38.5147**

**Gaus Width: 4.0049**

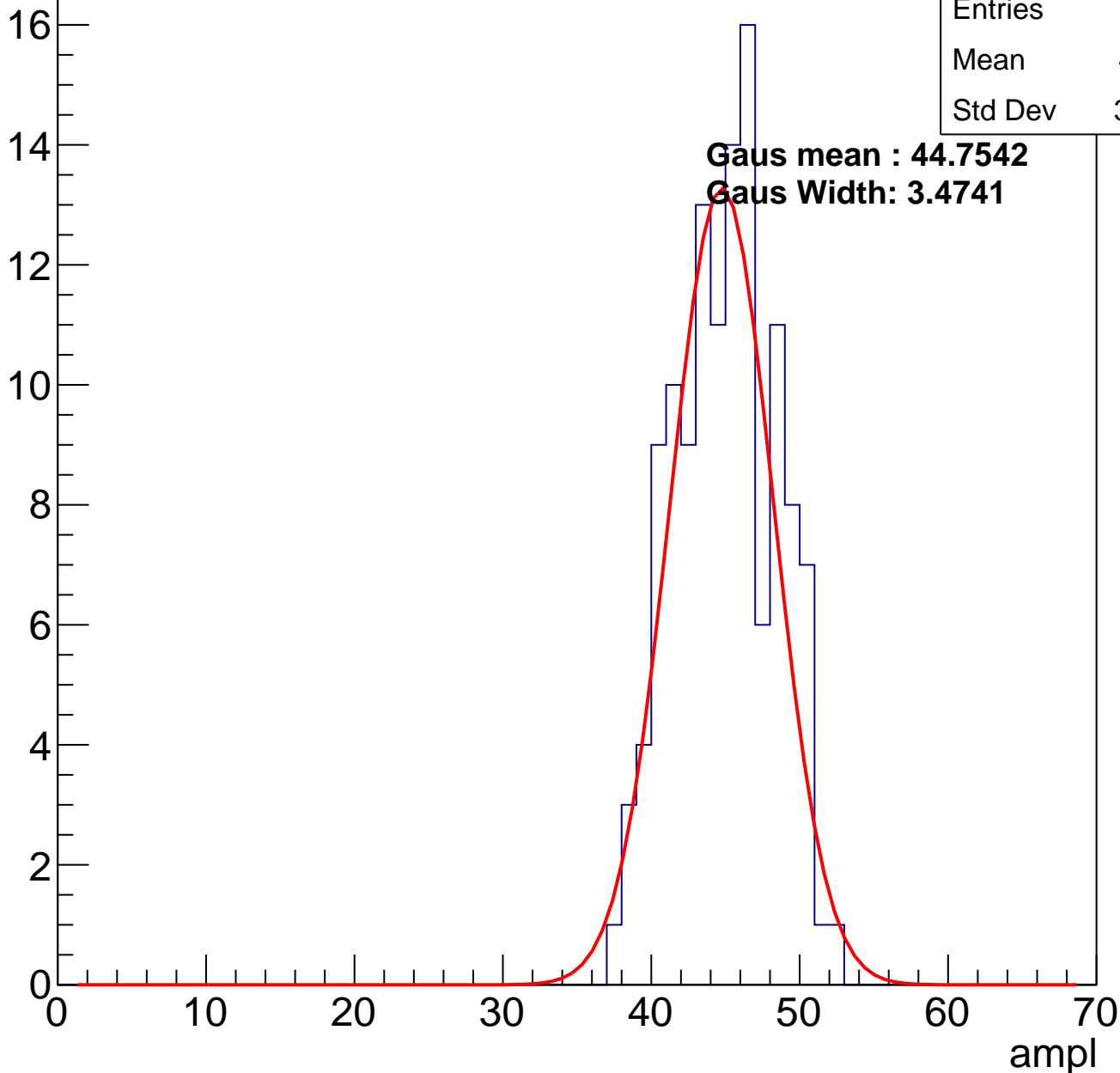
Entry



# B0L101S, U26-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

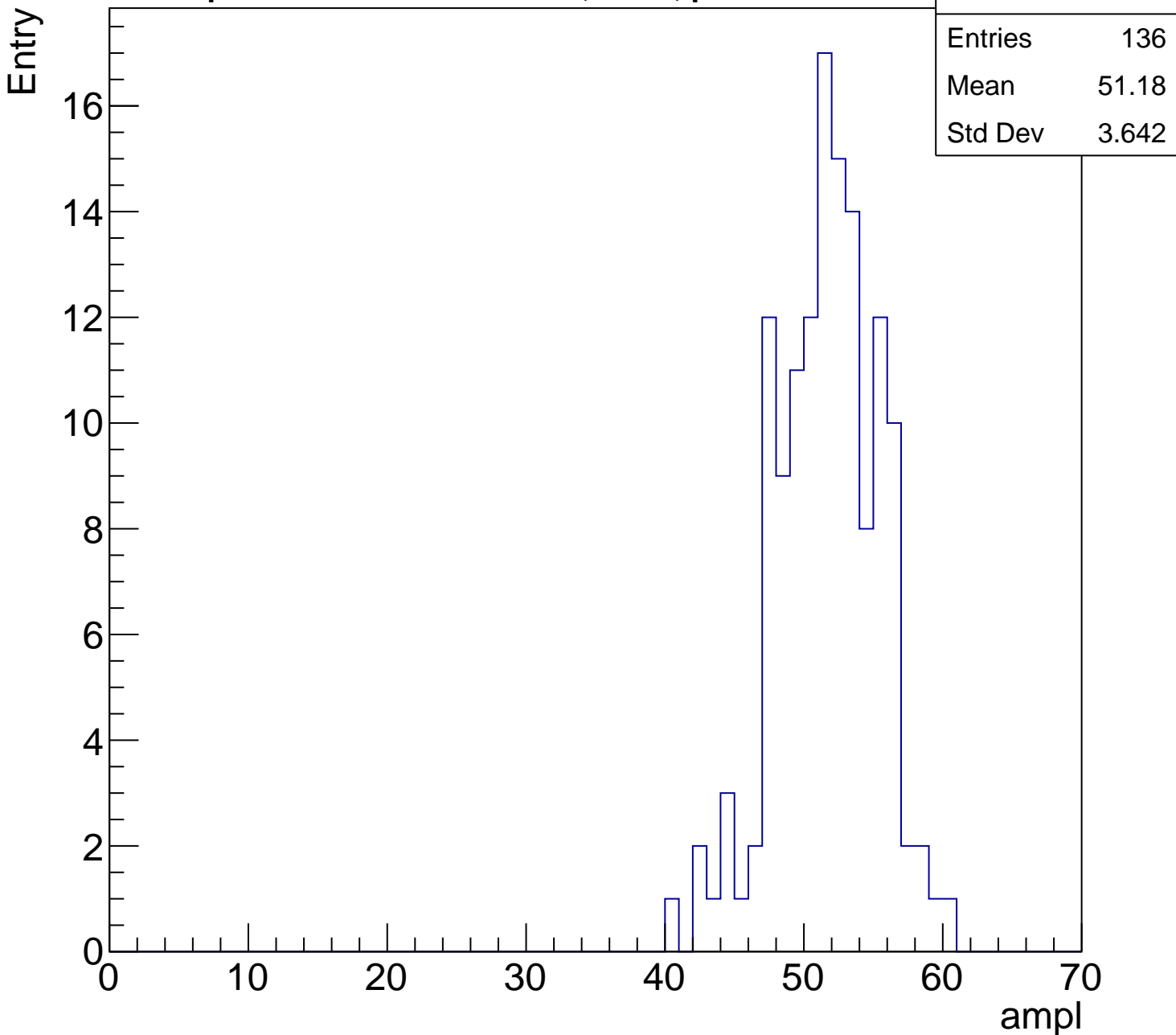
Entries	136
Mean	51.18
Std Dev	3.642

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

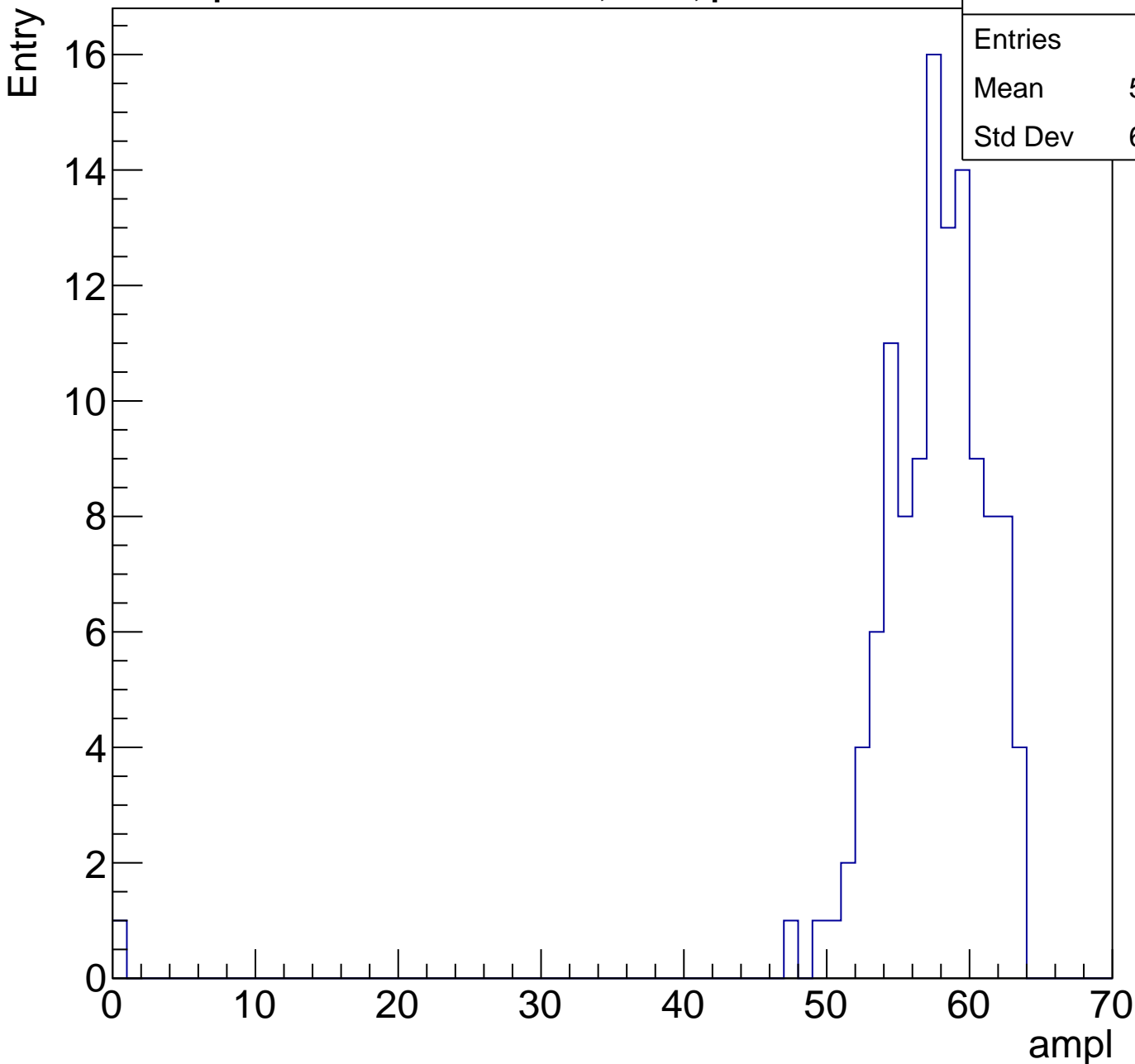
ampl



# B0L101S, U26-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	56.72
Std Dev	6.217



# B0L101S, U26-ch6, adc5

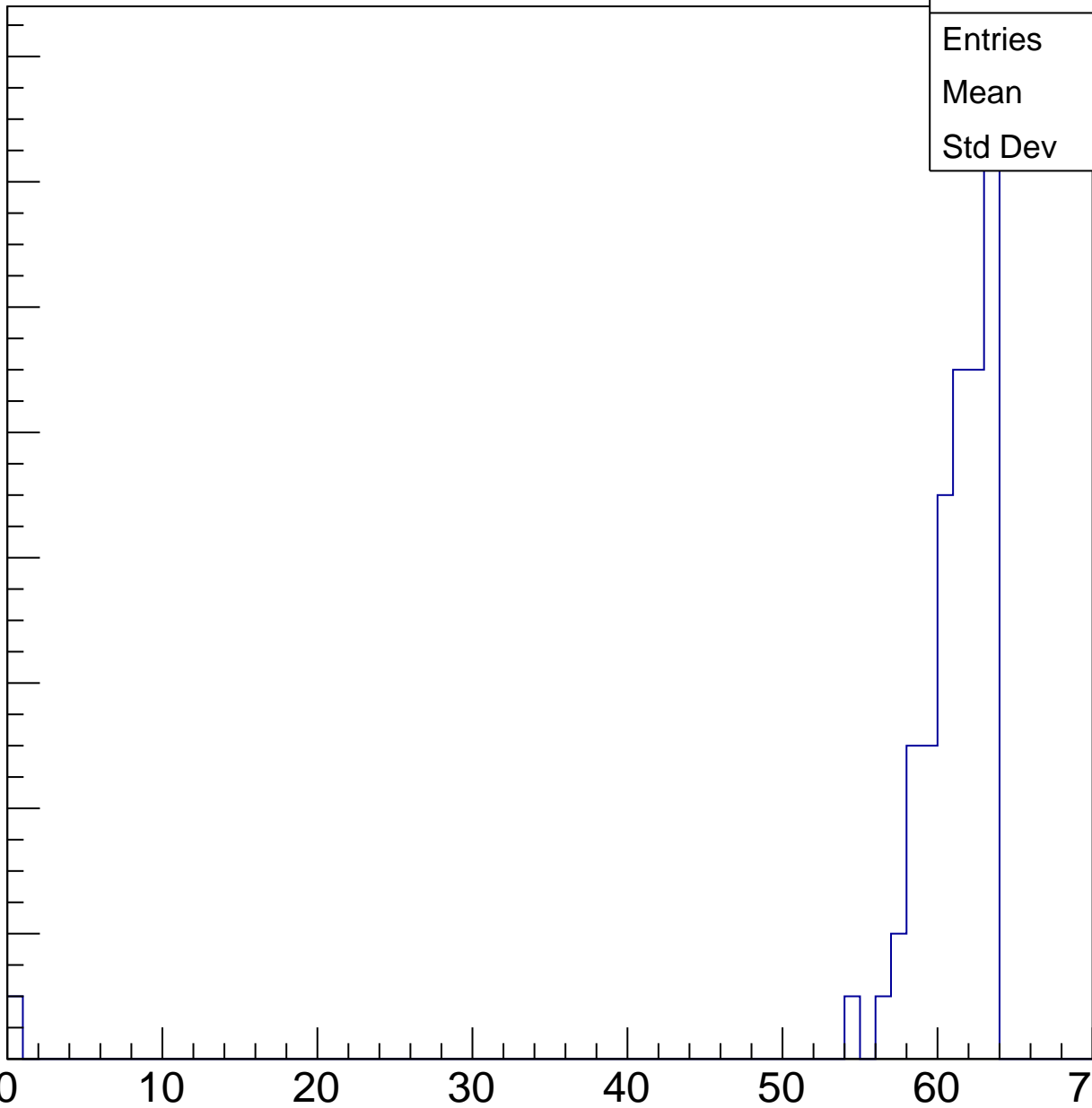
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	62
Mean	59.84
Std Dev	7.923

ampl



# B0L101S, U26-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L101S, U26-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

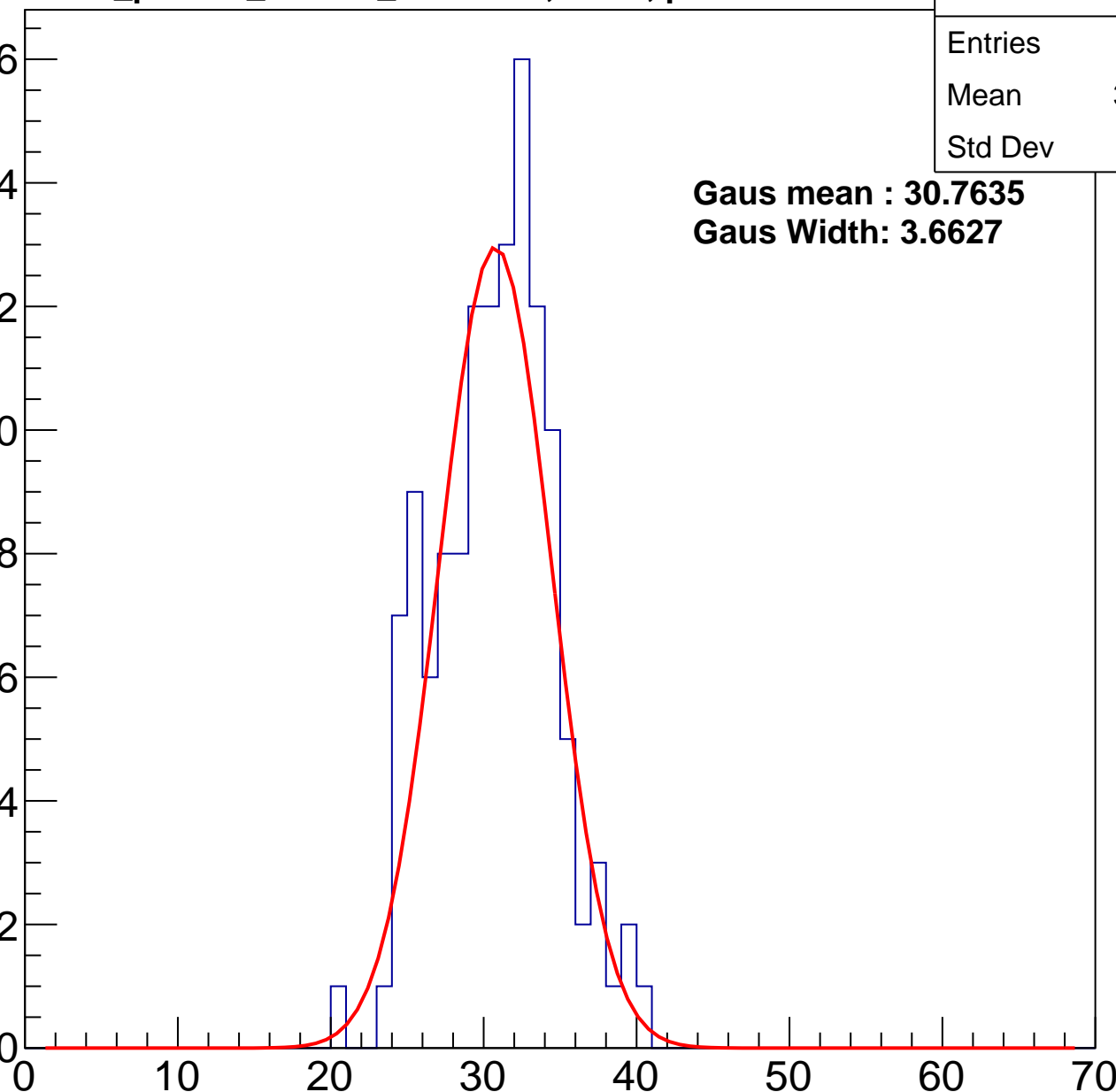
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	129
Mean	30.27
Std Dev	3.78

**Gaus mean : 30.7635**

**Gaus Width: 3.6627**



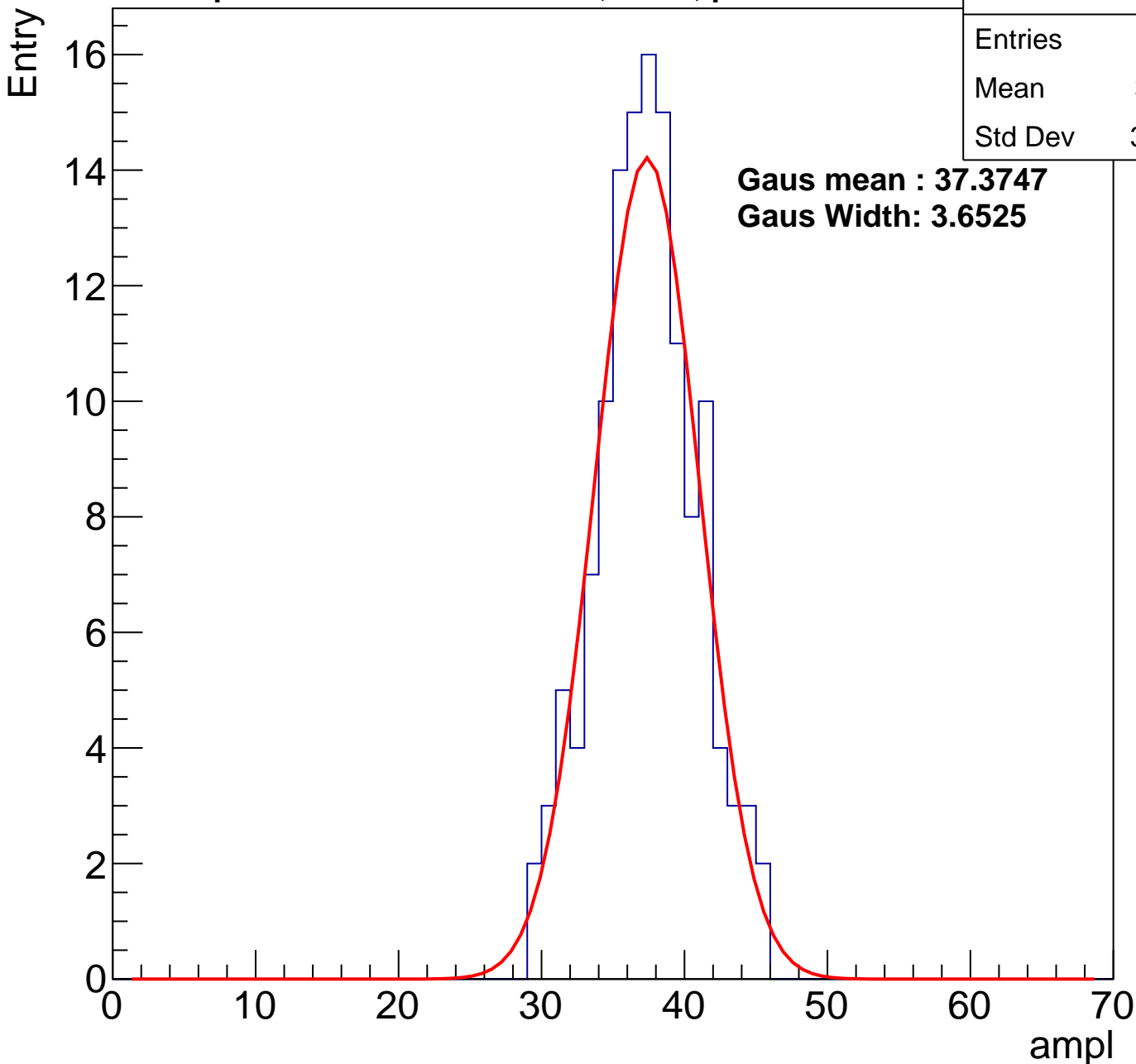
ampl

# B0L101S, U26-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	36.91
Std Dev	3.483

**Gaus mean : 37.3747**  
**Gaus Width: 3.6525**



# B0L101S, U26-ch7, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

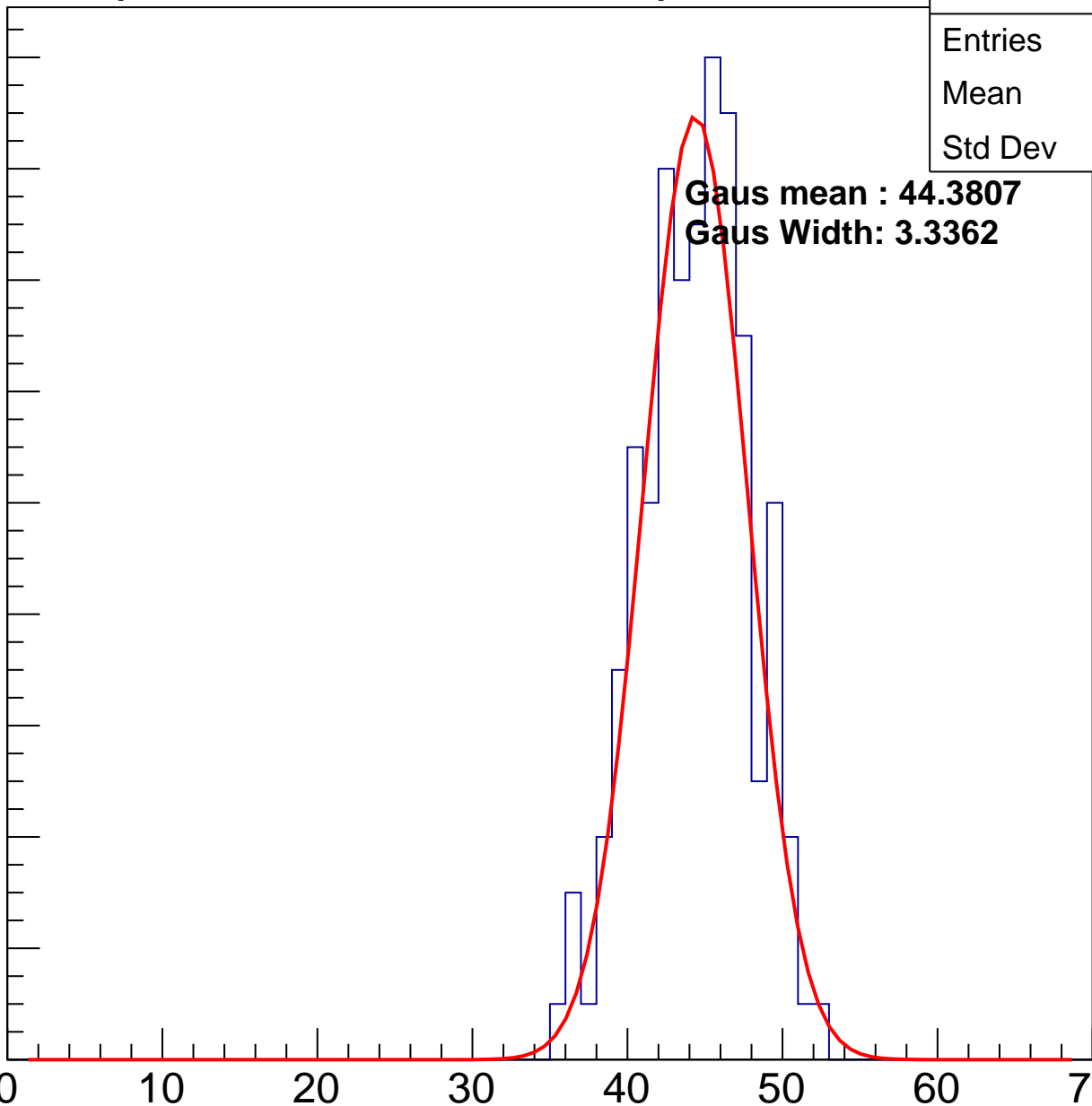
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	151
Mean	43.87
Std Dev	3.426

**Gaus mean : 44.3807**  
**Gaus Width: 3.3362**

0 10 20 30 40 50 60 70  
ampl



# B0L101S, U26-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	51.04
Std Dev	3.244

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

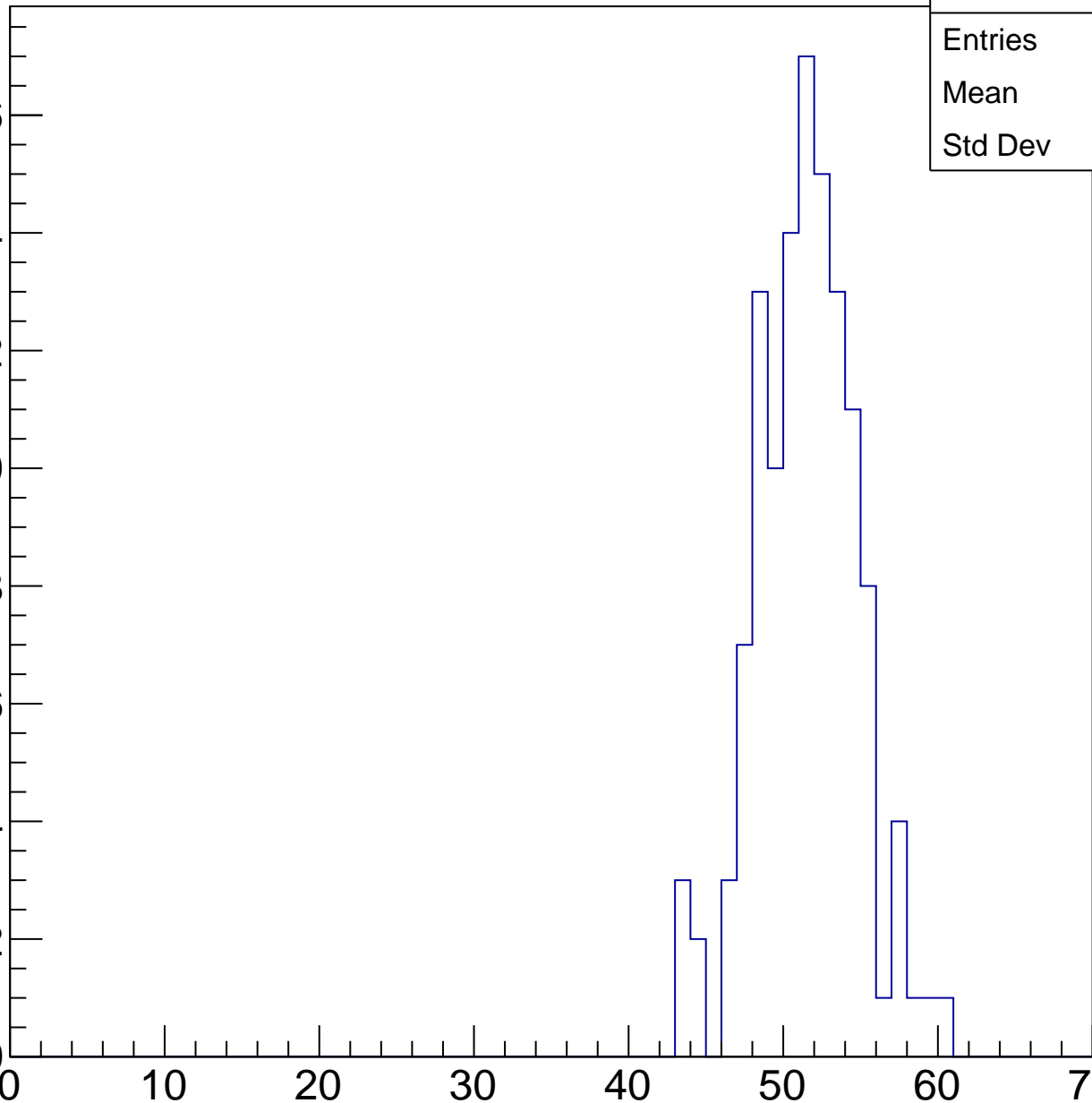
30

40

50

60

ampl



# B0L101S, U26-ch7, adc4

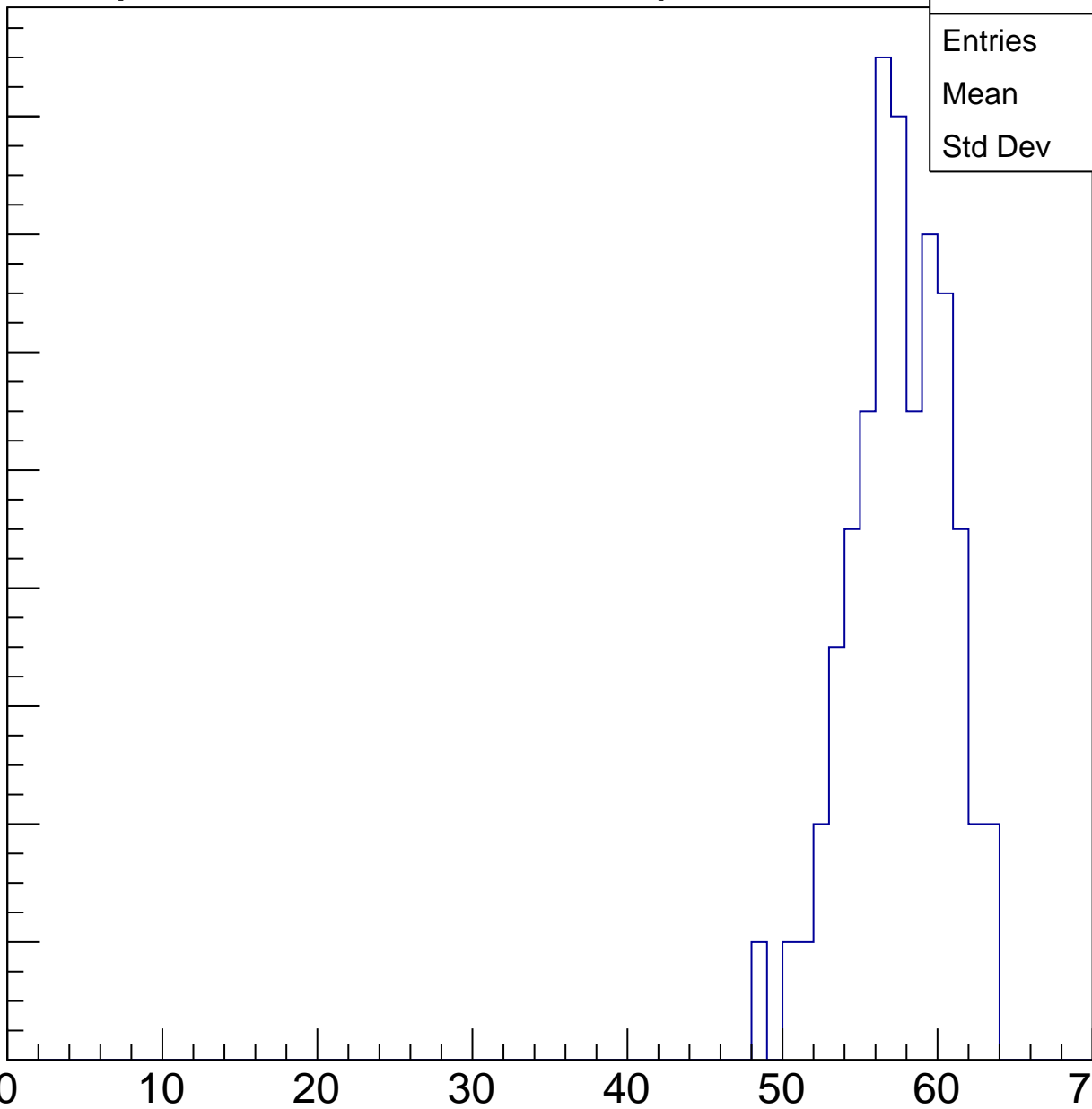
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	125
Mean	57
Std Dev	3.172

ampl



# B0L101S, U26-ch7, adc5

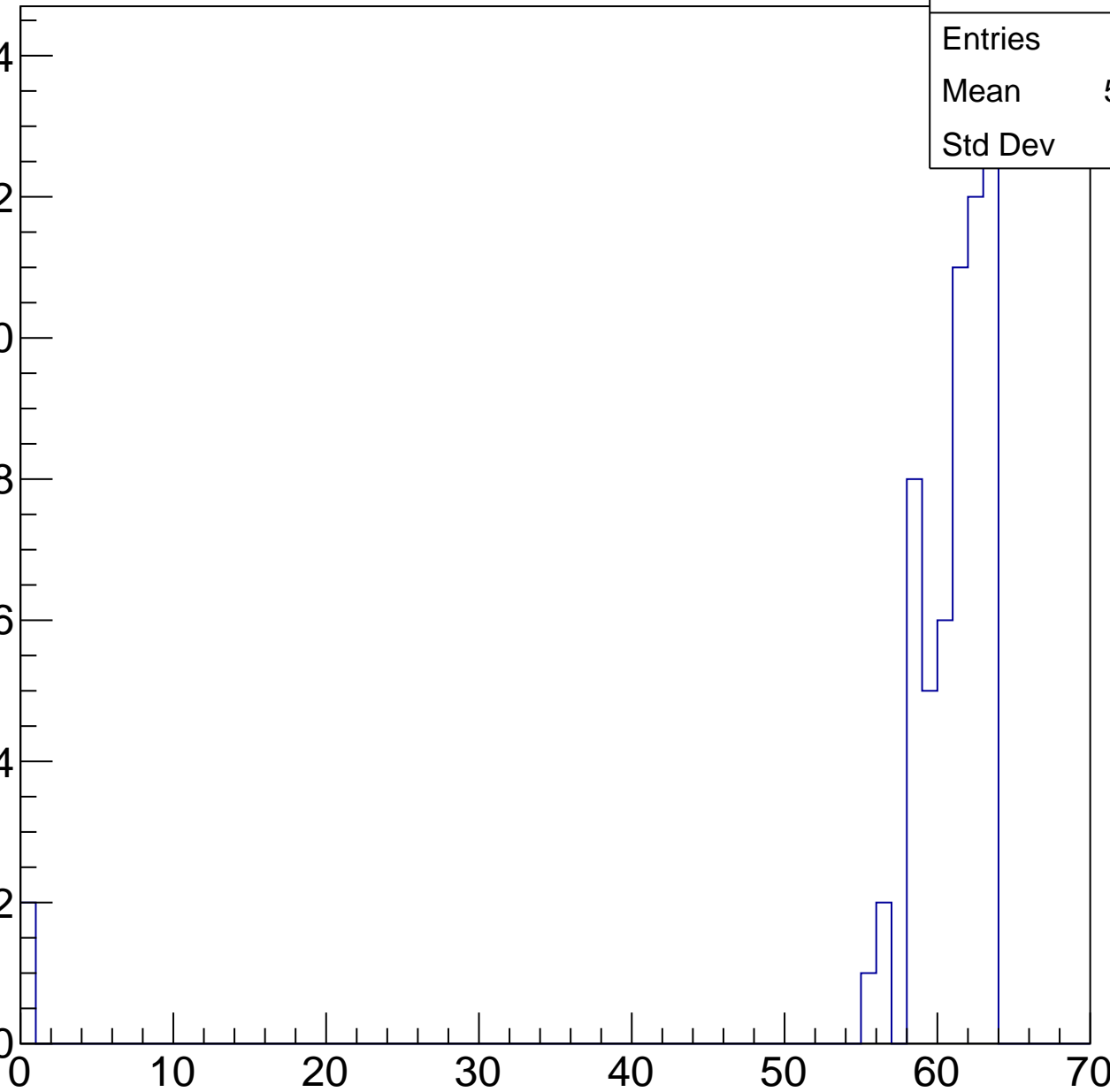
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	61
Mean	58.74
Std Dev	11

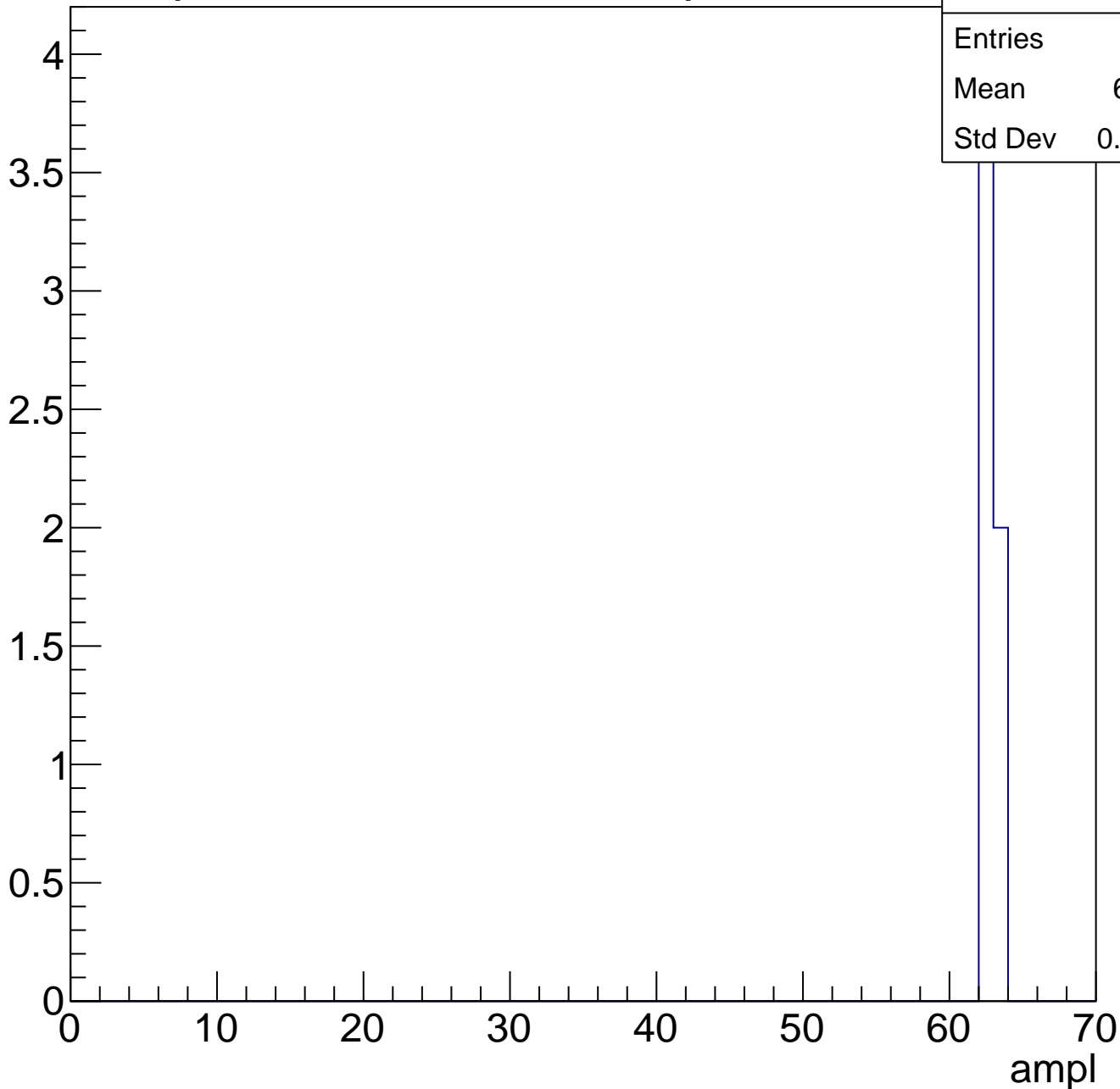
ampl



# B0L101S, U26-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

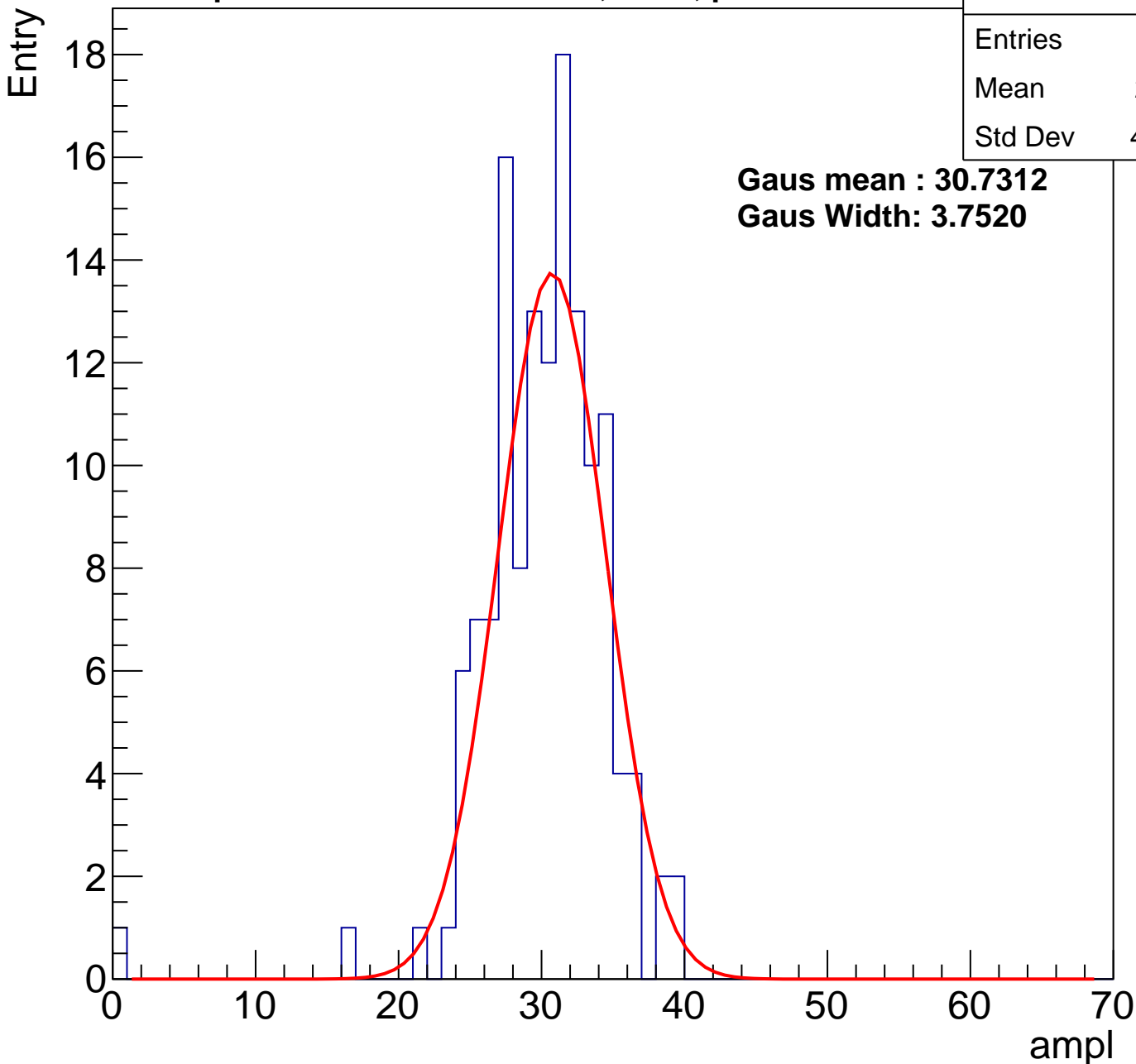
# B0L101S, U26-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	29.71
Std Dev	4.502

**Gaus mean : 30.7312**

**Gaus Width: 3.7520**



# B0L101S, U26-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

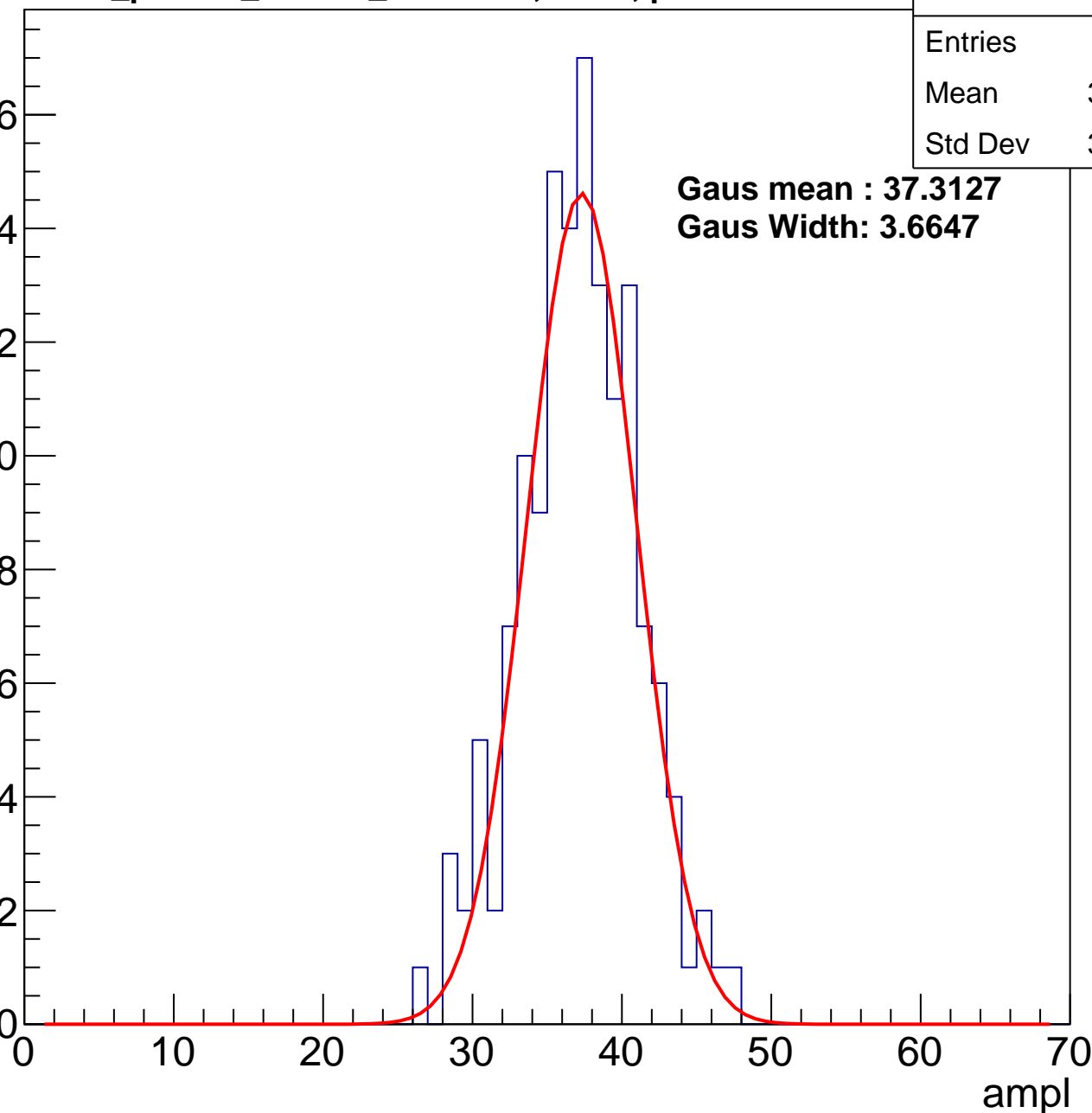
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	144
Mean	36.66
Std Dev	3.927

**Gaus mean : 37.3127**

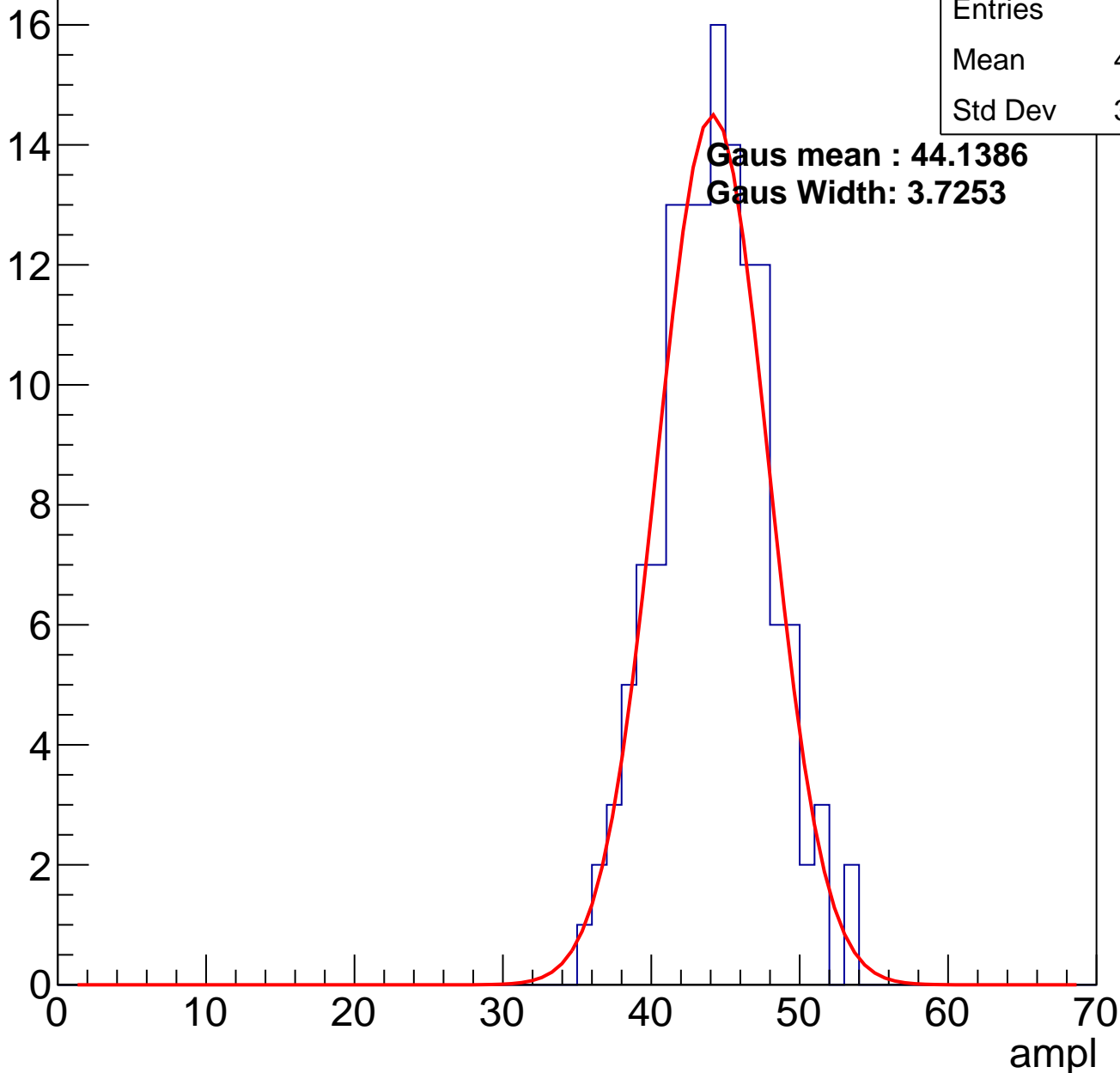
**Gaus Width: 3.6647**



# B0L101S, U26-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

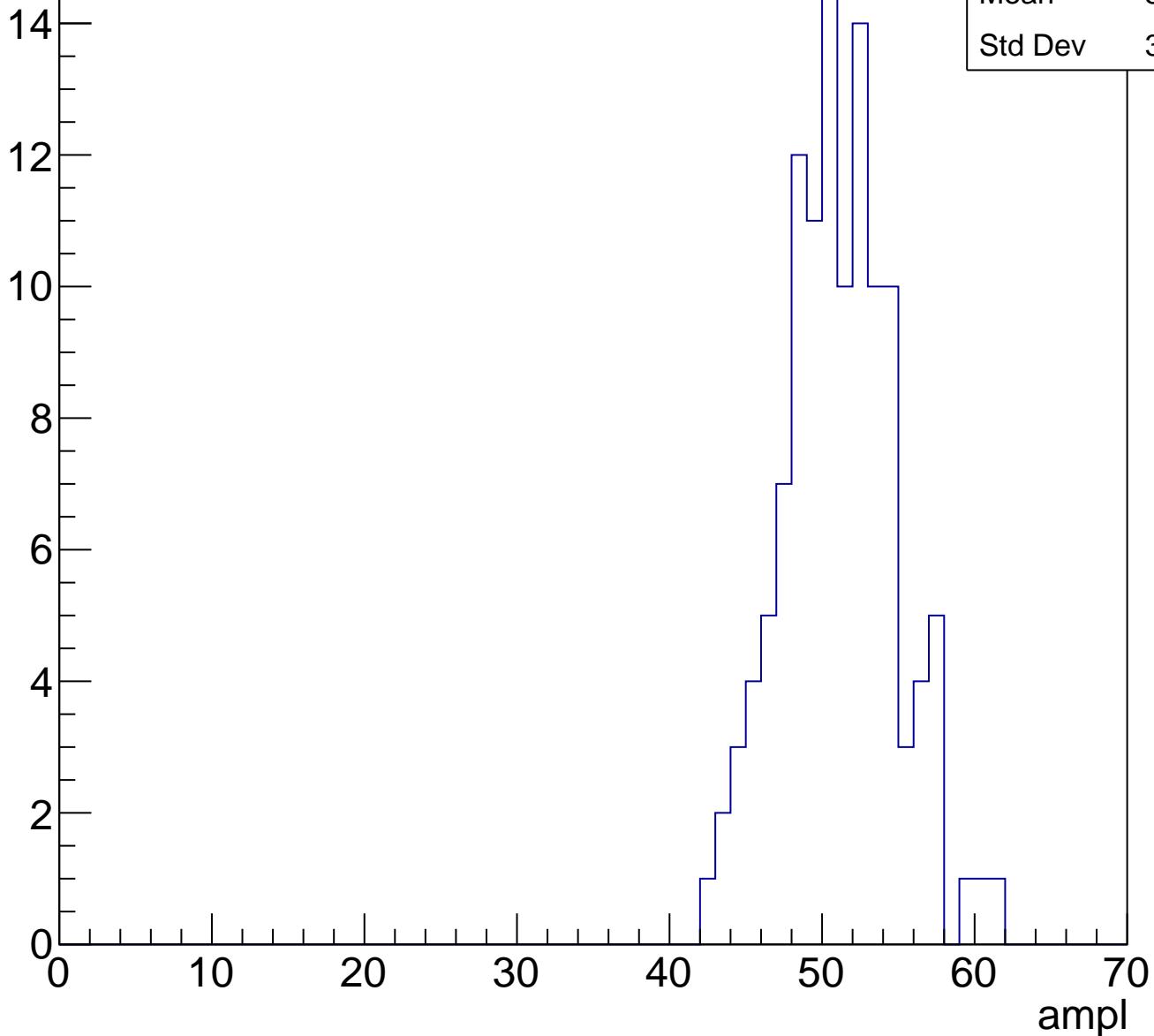
Entry



# B0L101S, U26-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

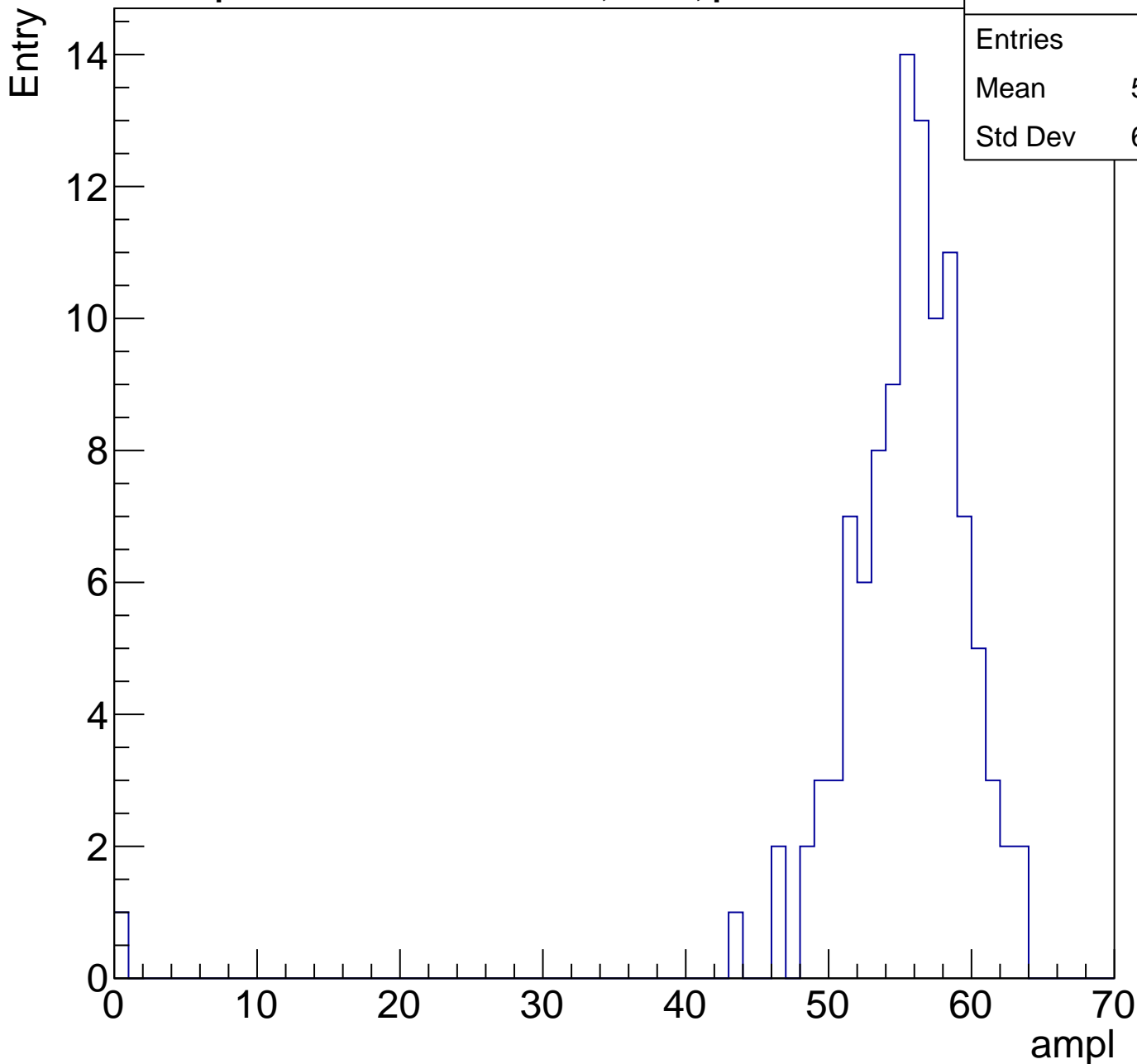


Entries	119
Mean	50.64
Std Dev	3.673

# B0L101S, U26-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	54.67
Std Dev	6.447



# B0L101S, U26-ch8, adc5

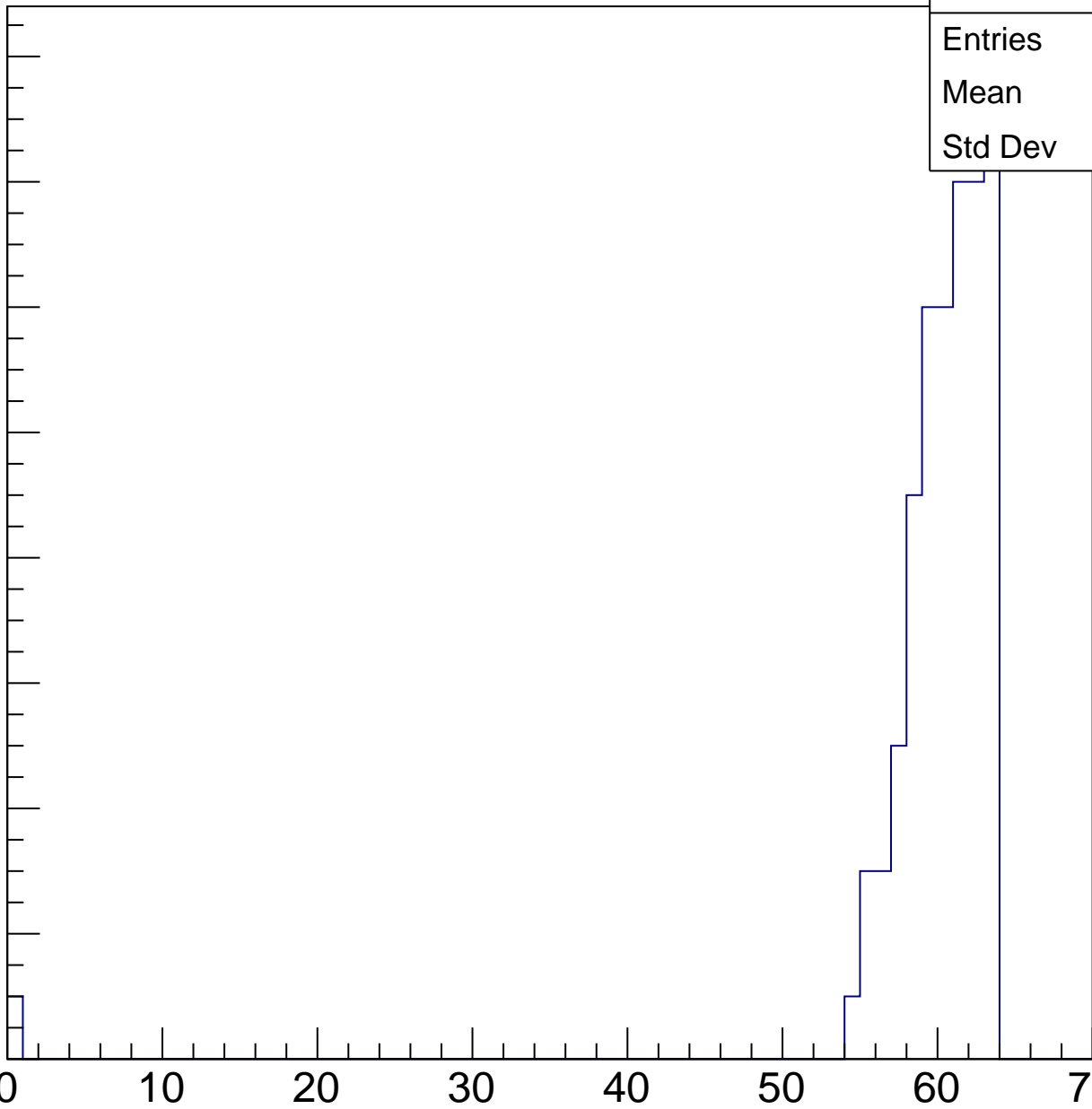
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	90
Mean	59.47
Std Dev	6.7

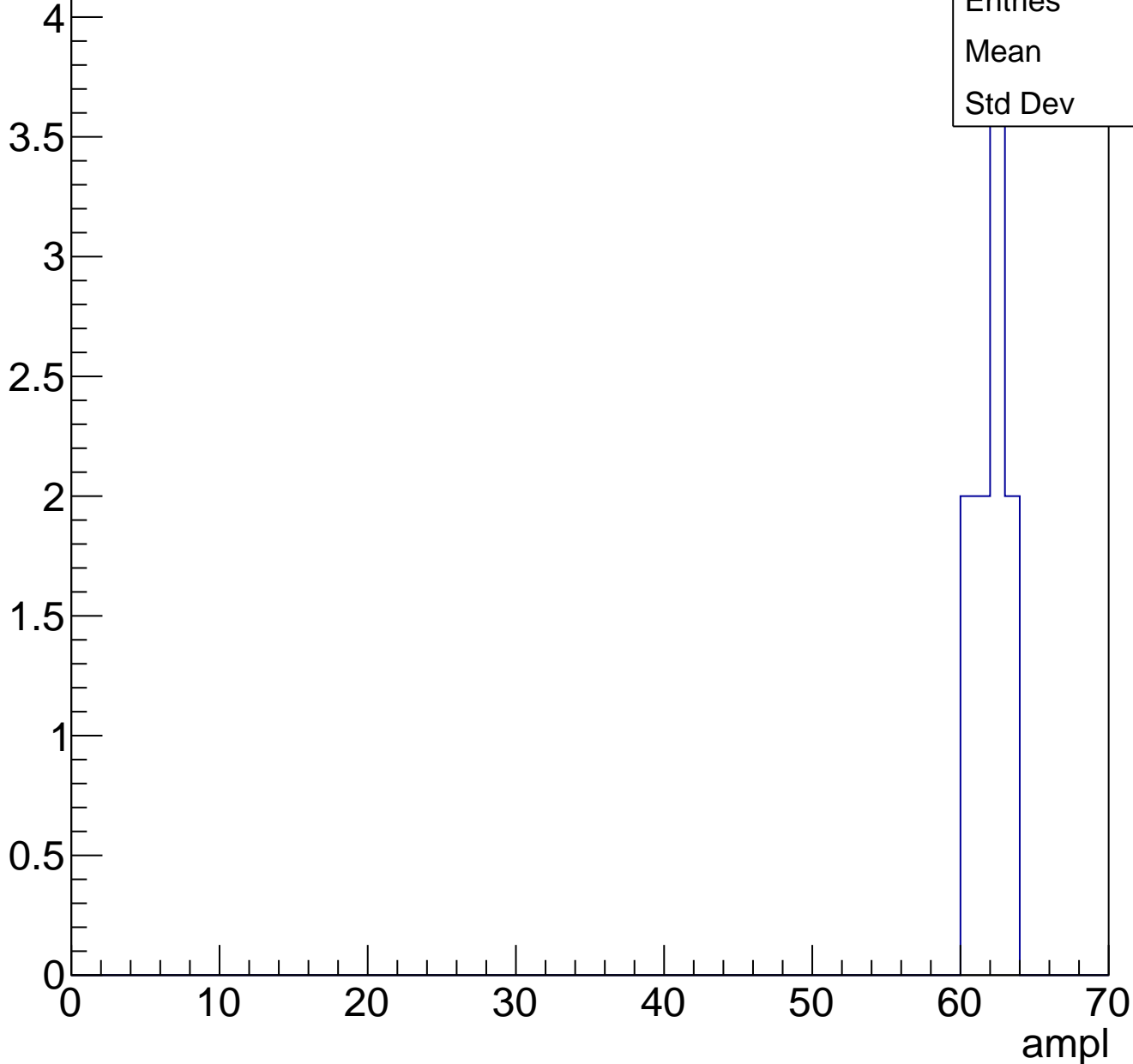
ampl



# B0L101S, U26-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

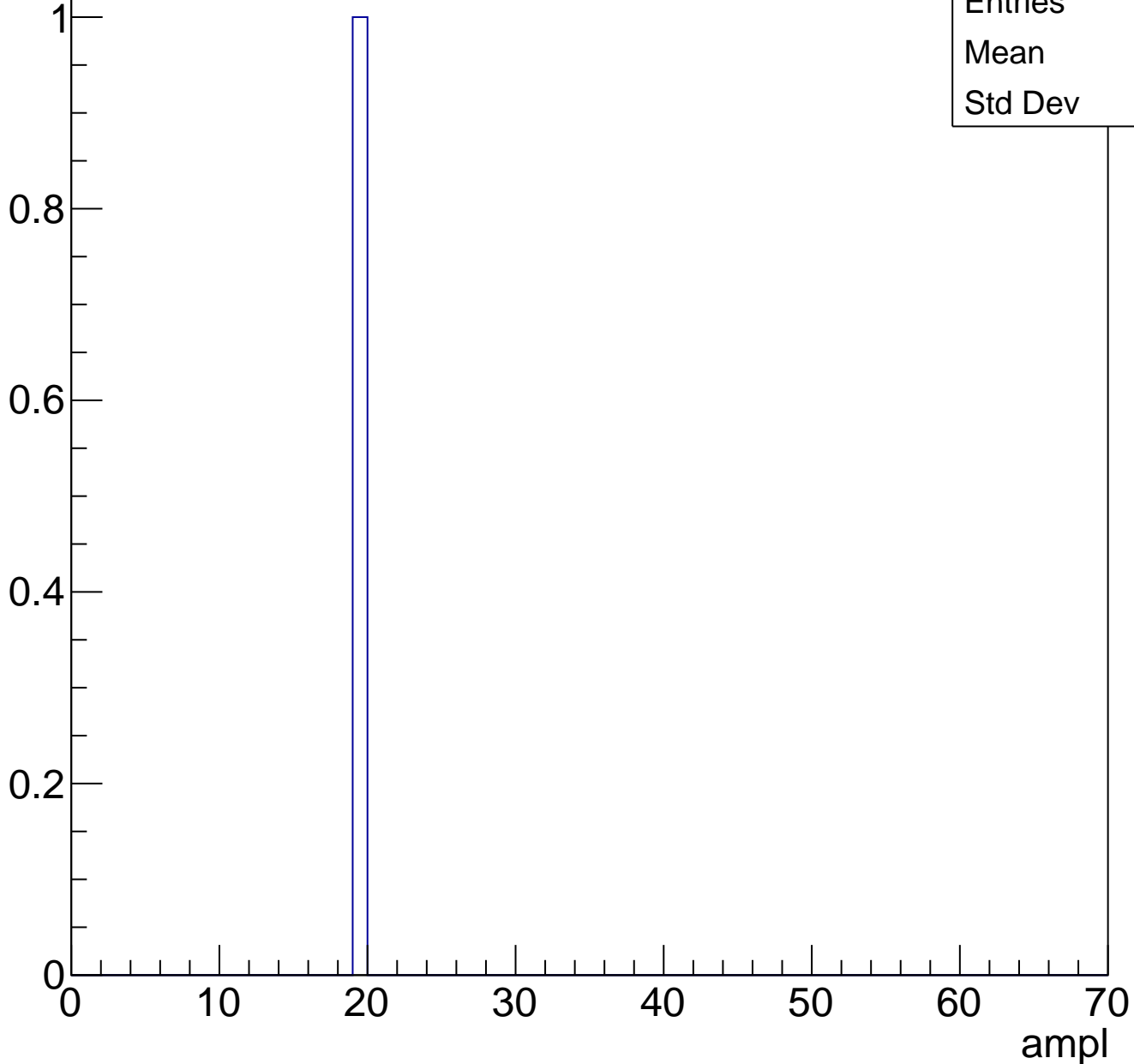




# B0L101S, U26-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	29.75
Std Dev	3.847

**Gaus mean : 30.1588**

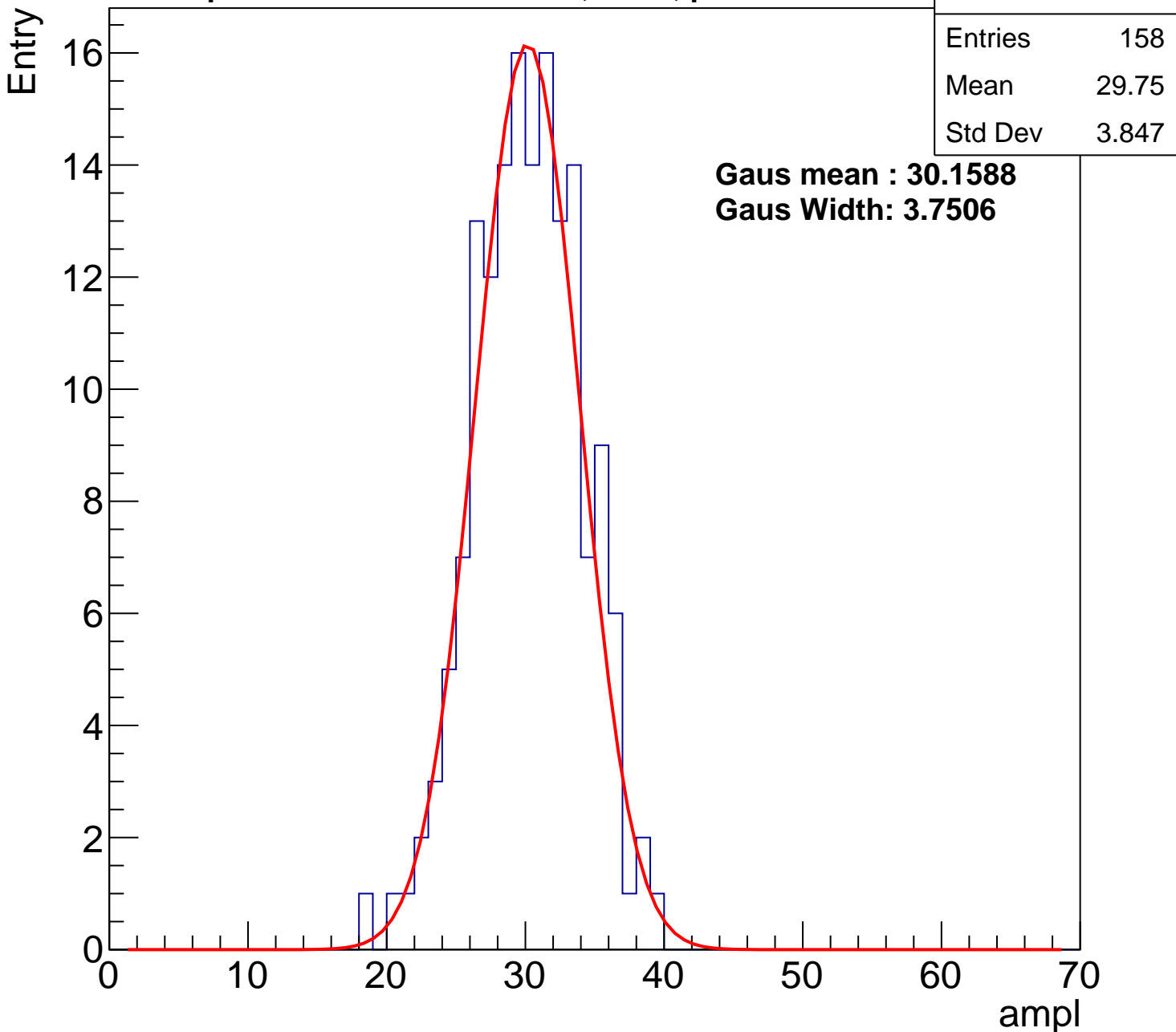
**Gaus Width: 3.7506**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	37.11
Std Dev	3.417

**Gaus mean : 37.6106**

**Gaus Width: 3.4432**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

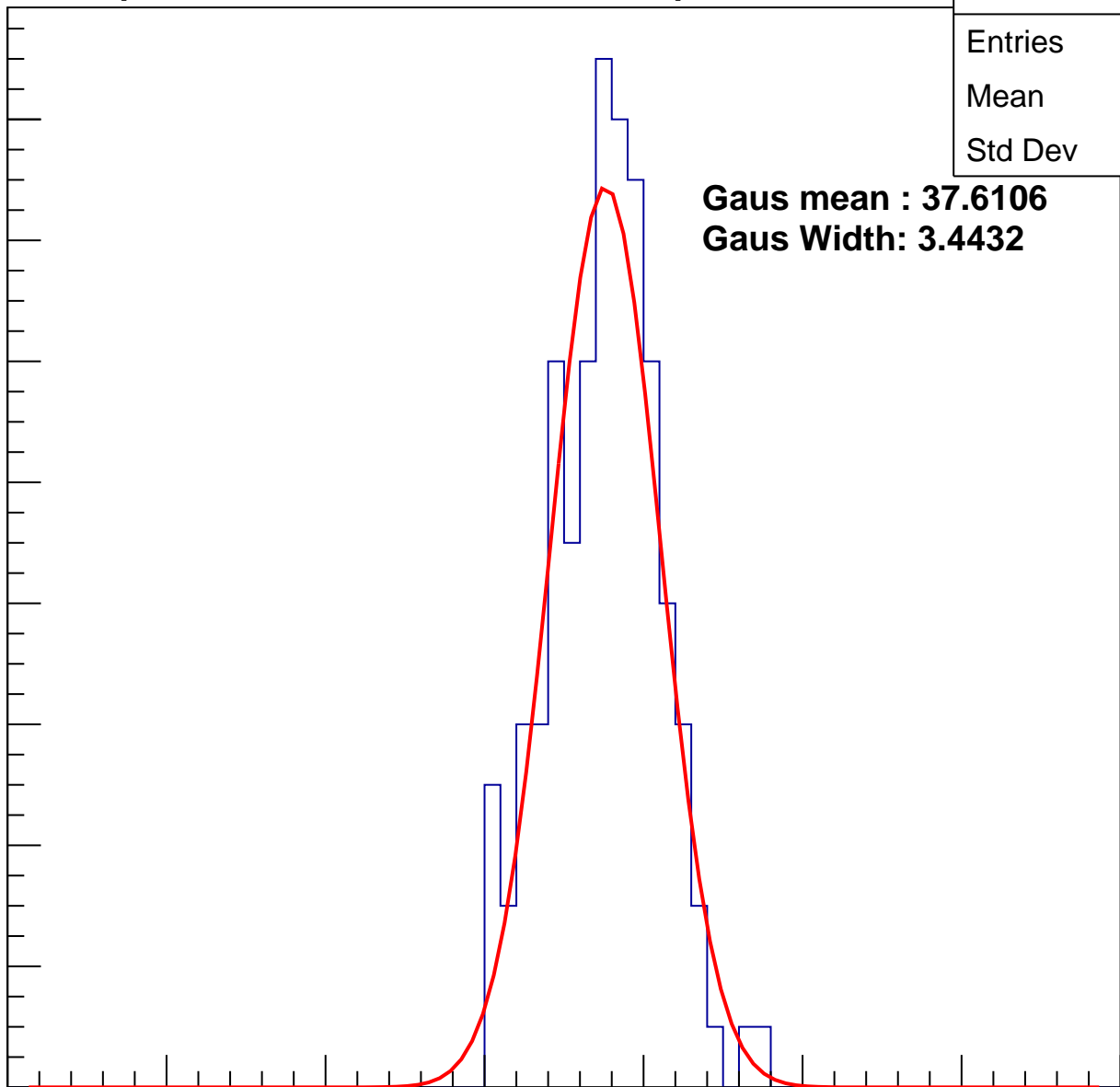
40

50

60

70

ampl



# B0L101S, U26-ch9, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

43.61

Std Dev

3.326

**Gaus mean : 44.2119**

**Gaus Width: 3.2017**

0

10

20

30

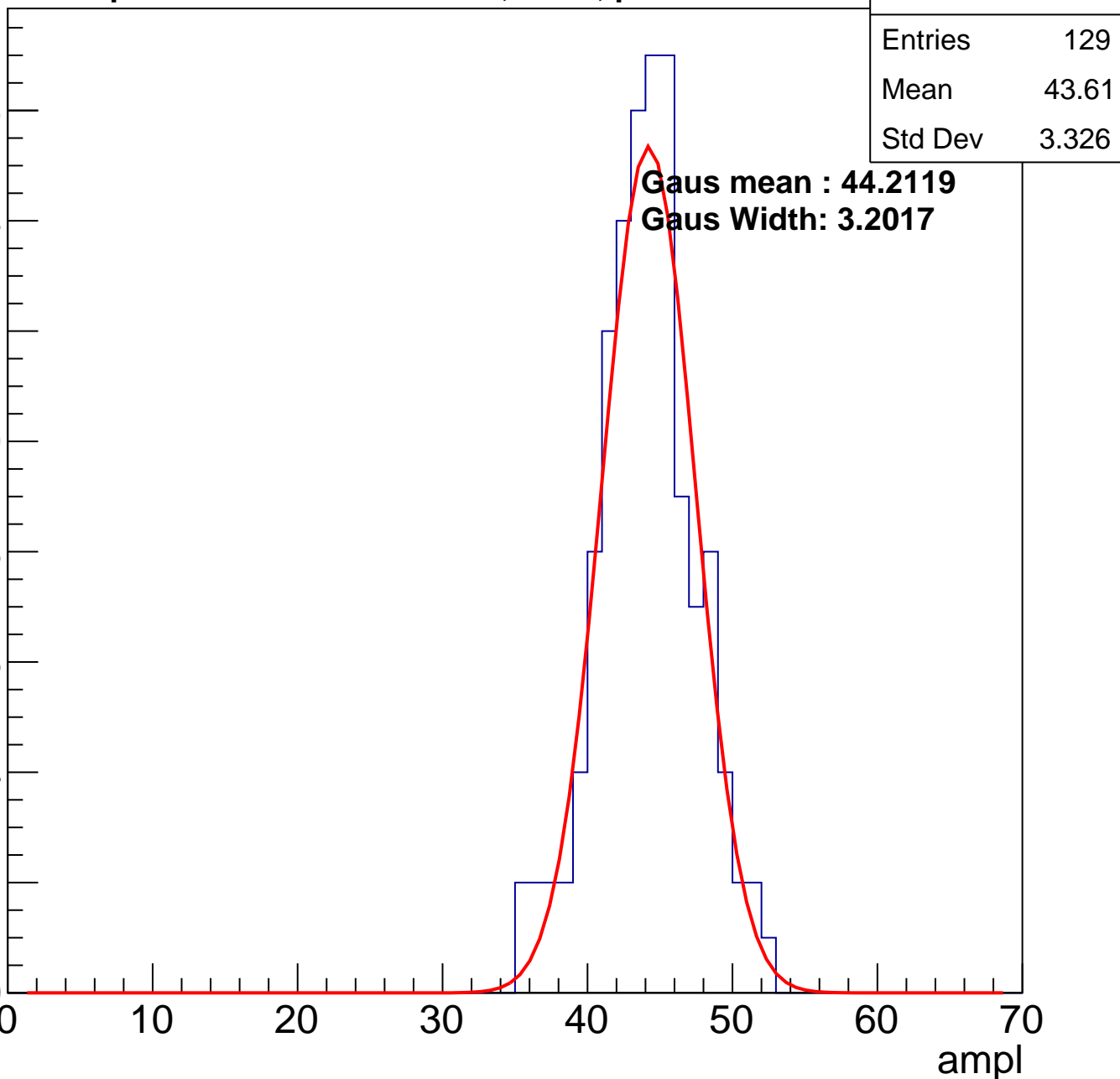
40

50

60

70

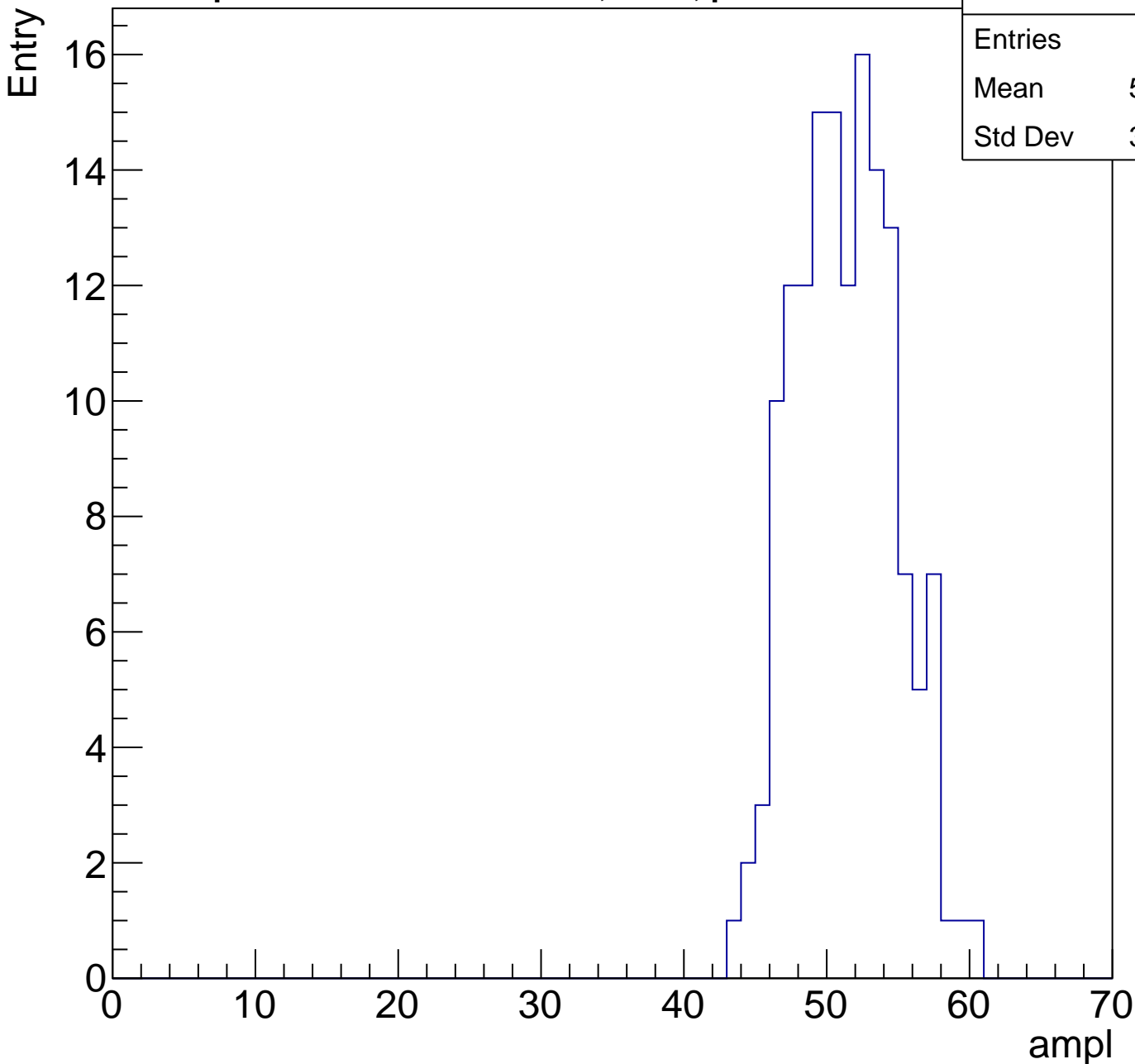
ampl



# B0L101S, U26-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	50.88
Std Dev	3.465



# B0L101S, U26-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

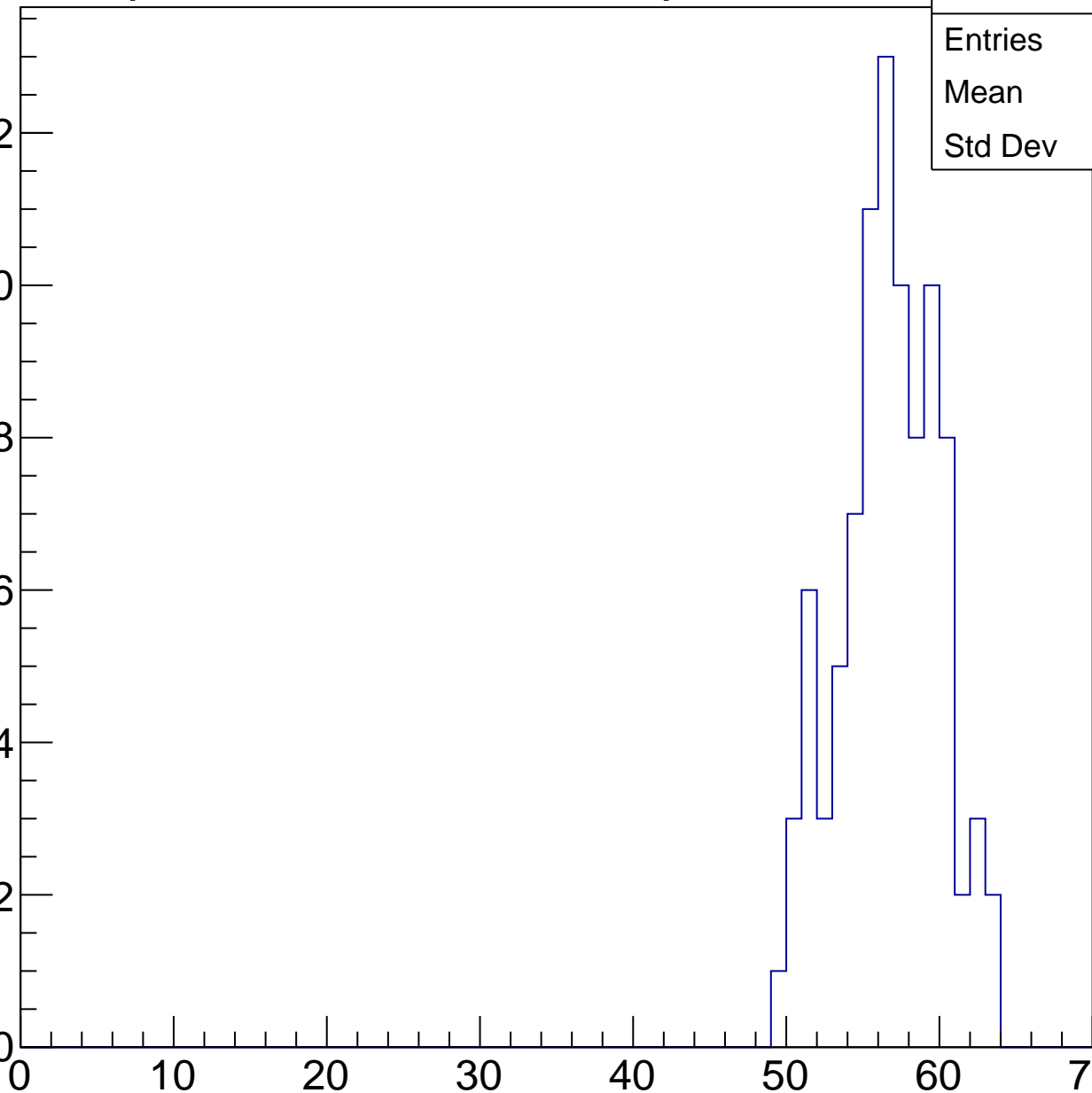
4

2

0

Entries	92
Mean	56.25
Std Dev	3.212

ampl



# B0L101S, U26-ch9, adc5

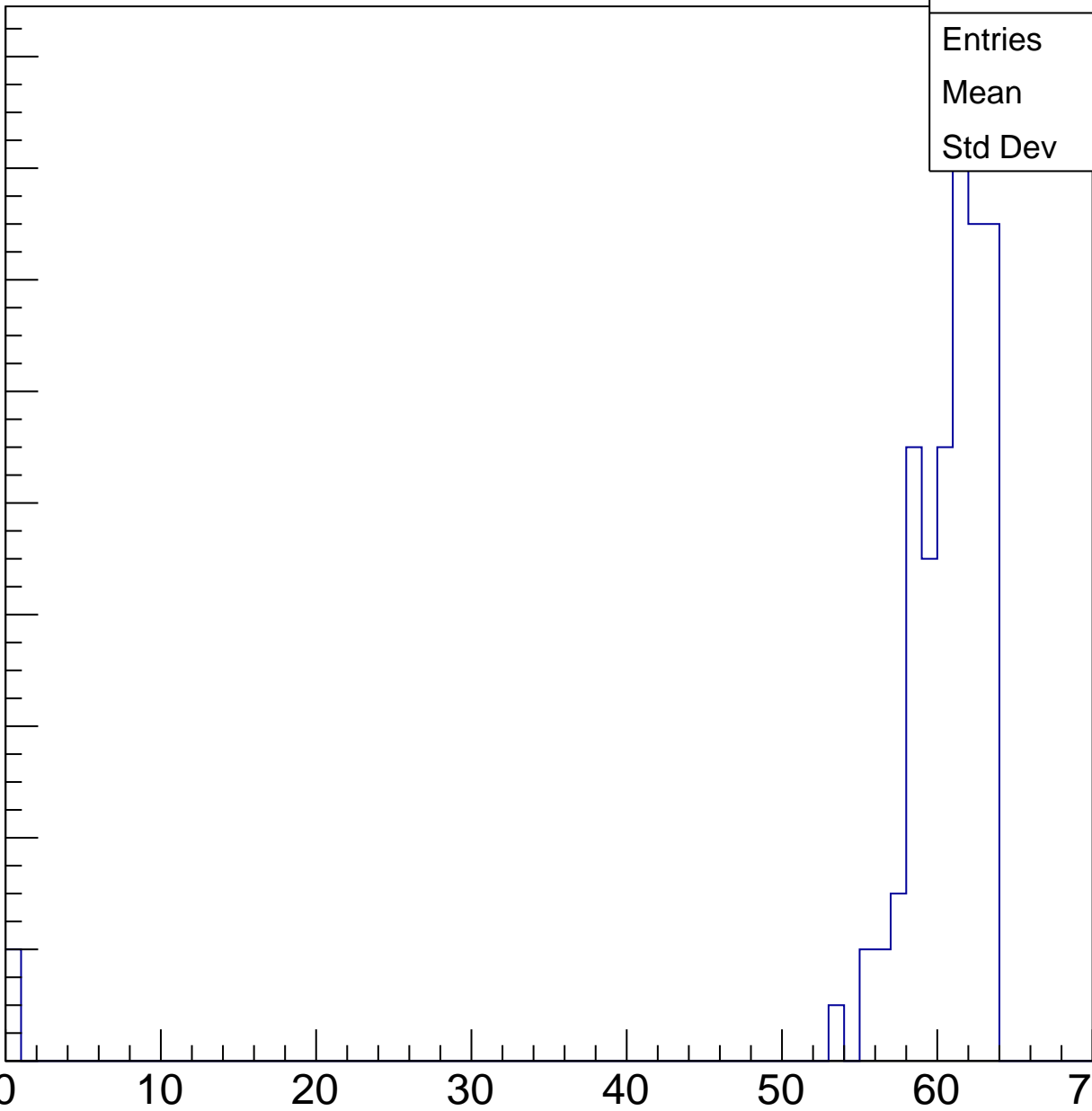
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	89
Mean	58.97
Std Dev	9.198

ampl



# B0L101S, U26-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	29.95
Std Dev	4.256

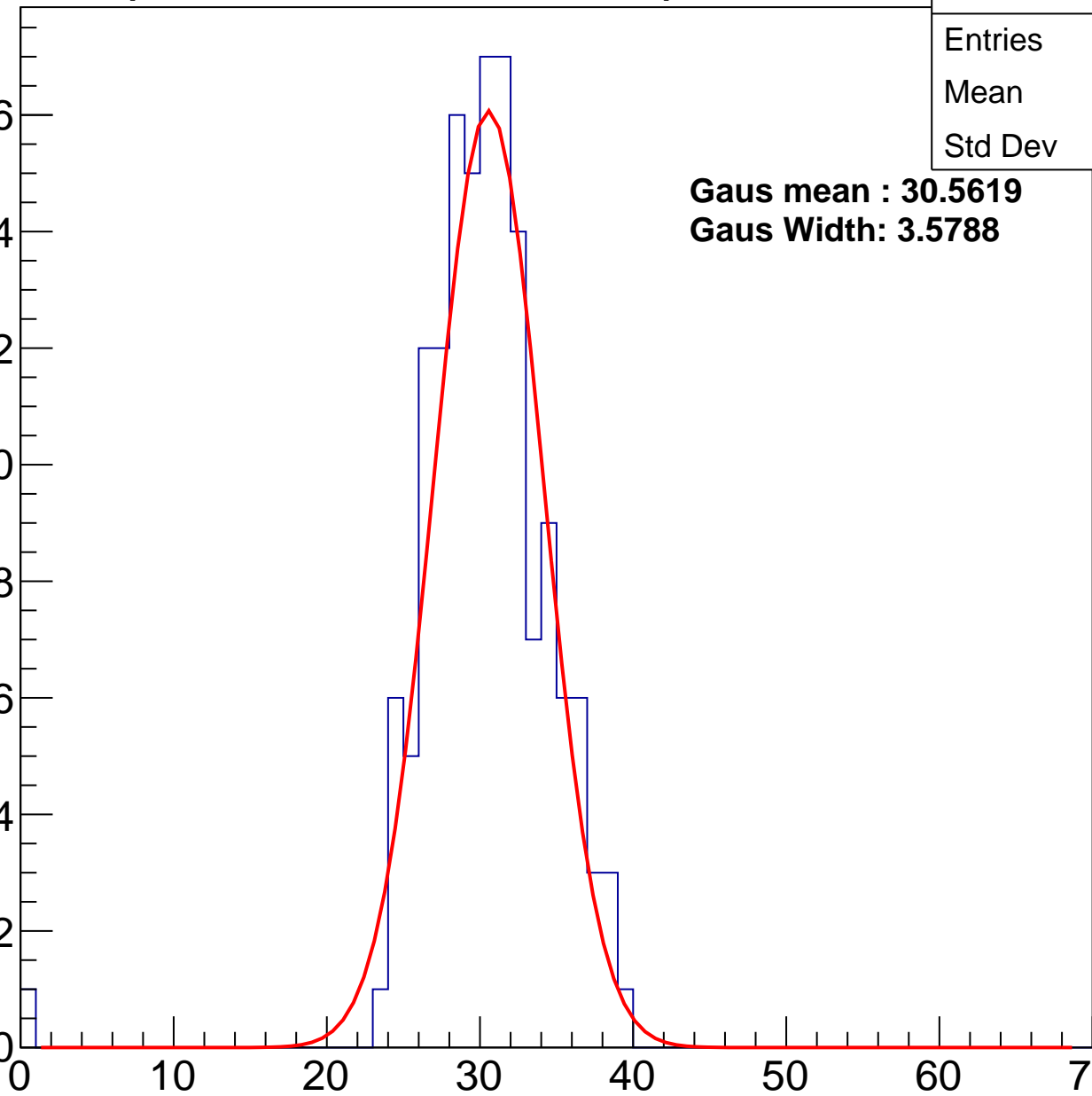
**Gaus mean : 30.5619**

**Gaus Width: 3.5788**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch10, adc1

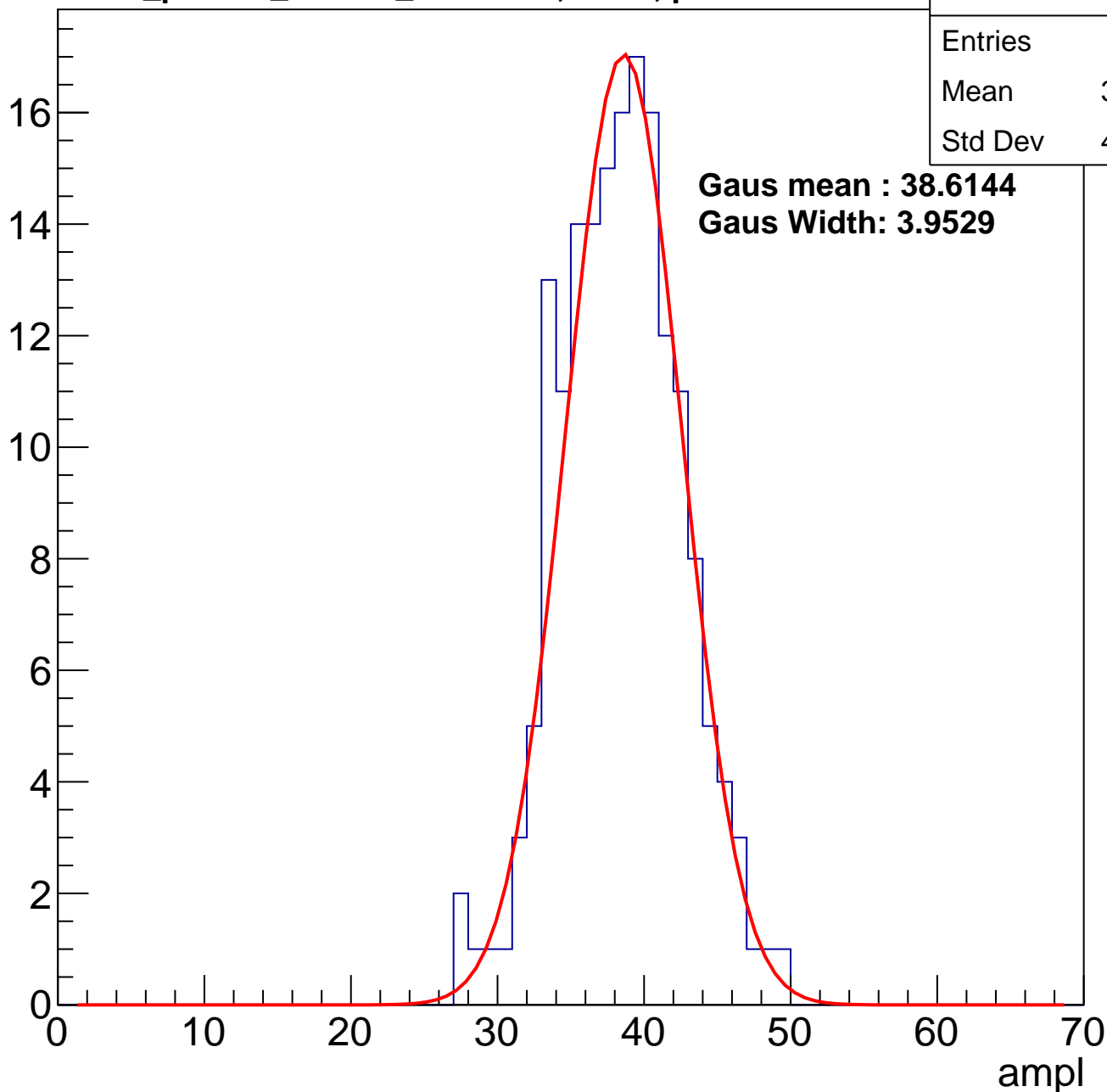
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	37.93
Std Dev	4.103

**Gaus mean : 38.6144**

**Gaus Width: 3.9529**

Entry



# B0L101S, U26-ch10, adc2

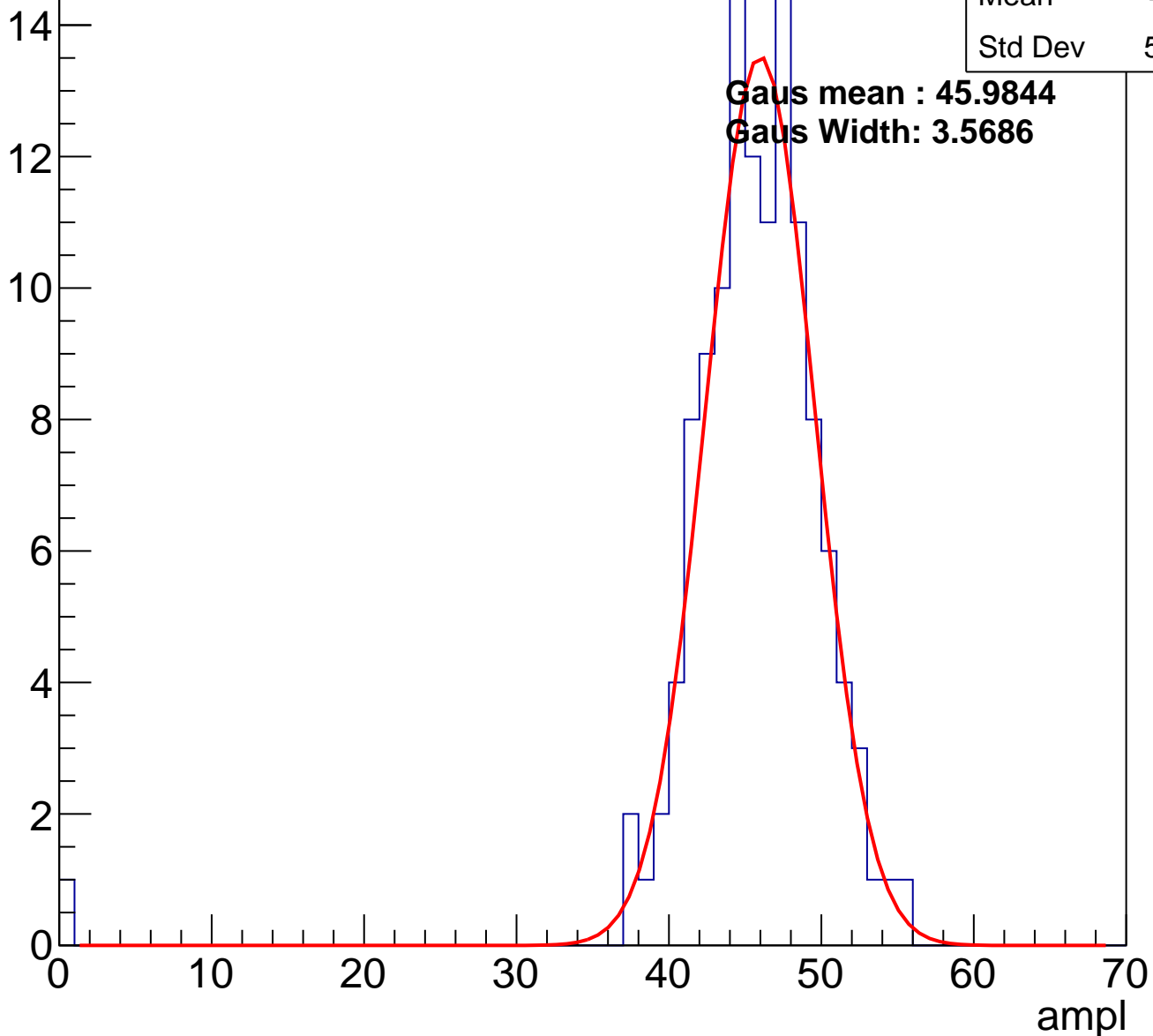
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	45.11
Std Dev	5.382

**Gaus mean : 45.9844**

**Gaus Width: 3.5686**

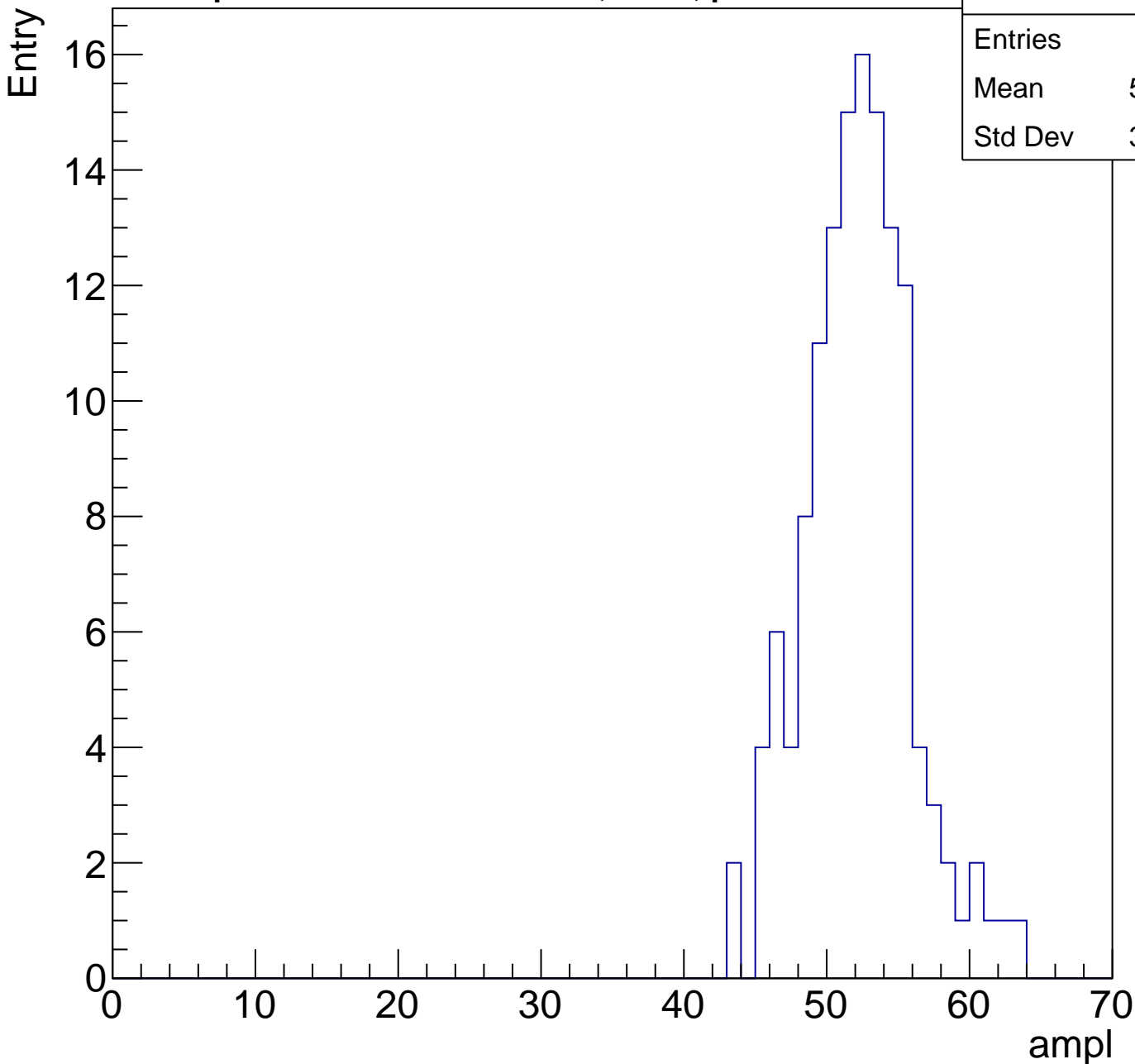
Entry



# B0L101S, U26-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	51.74
Std Dev	3.689



# B0L101S, U26-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	101
Mean	57.72
Std Dev	2.736

Entry

14

12

10

8

6

4

2

0

0

10

20

30

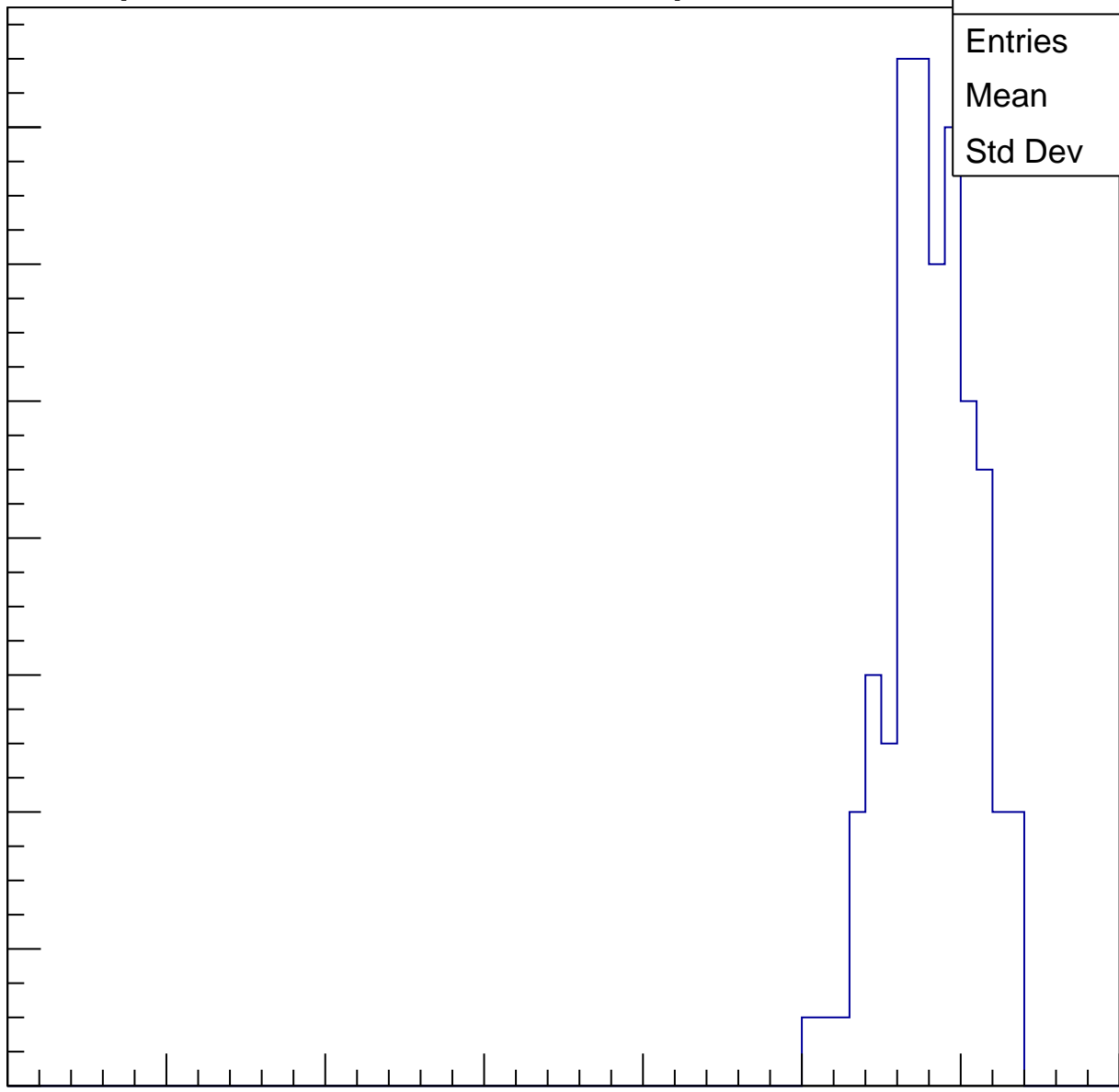
40

50

60

70

ampl



# B0L101S, U26-ch10, adc5

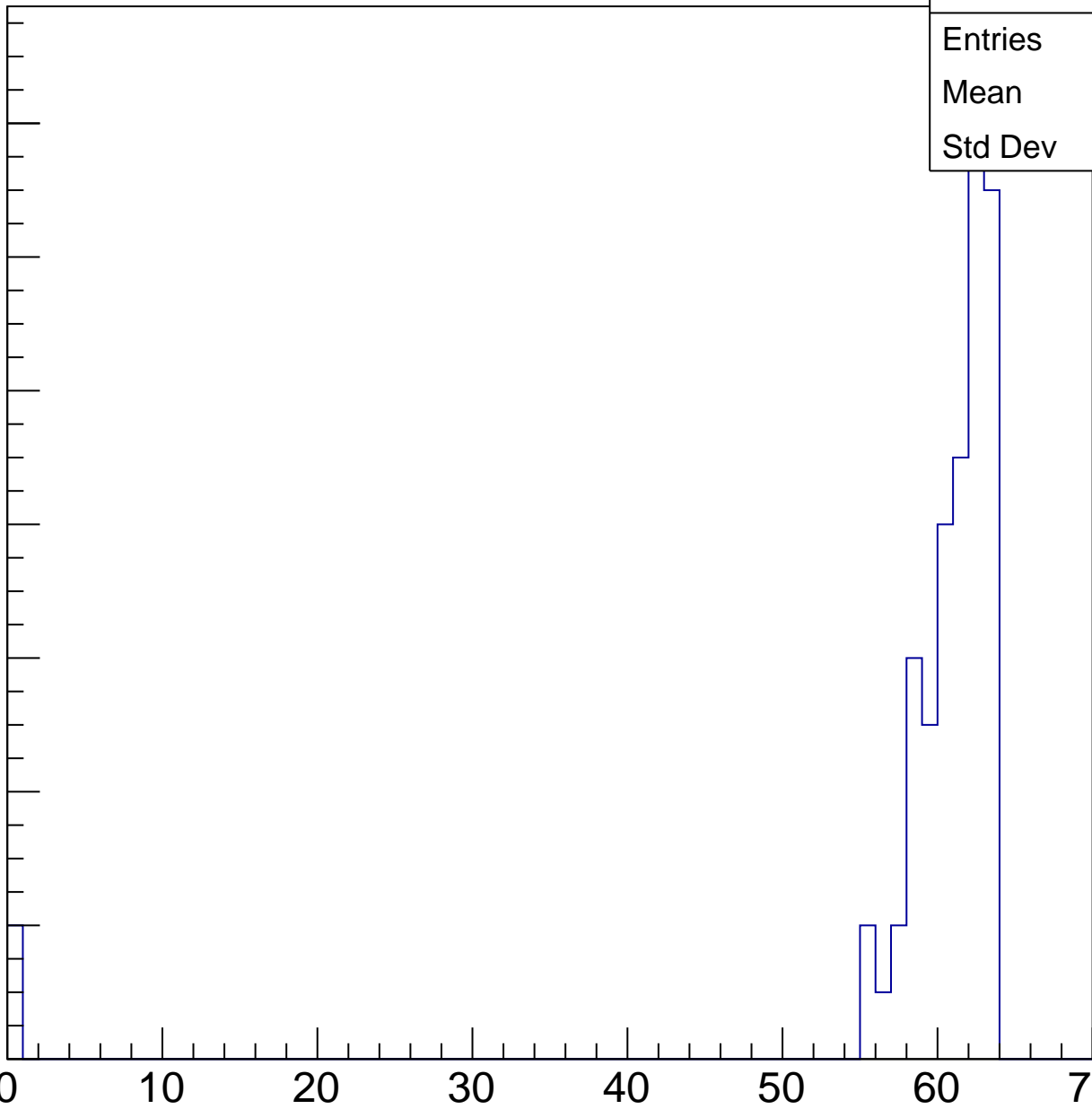
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	63
Mean	58.75
Std Dev	10.84

ampl



# B0L101S, U26-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch11, adc0

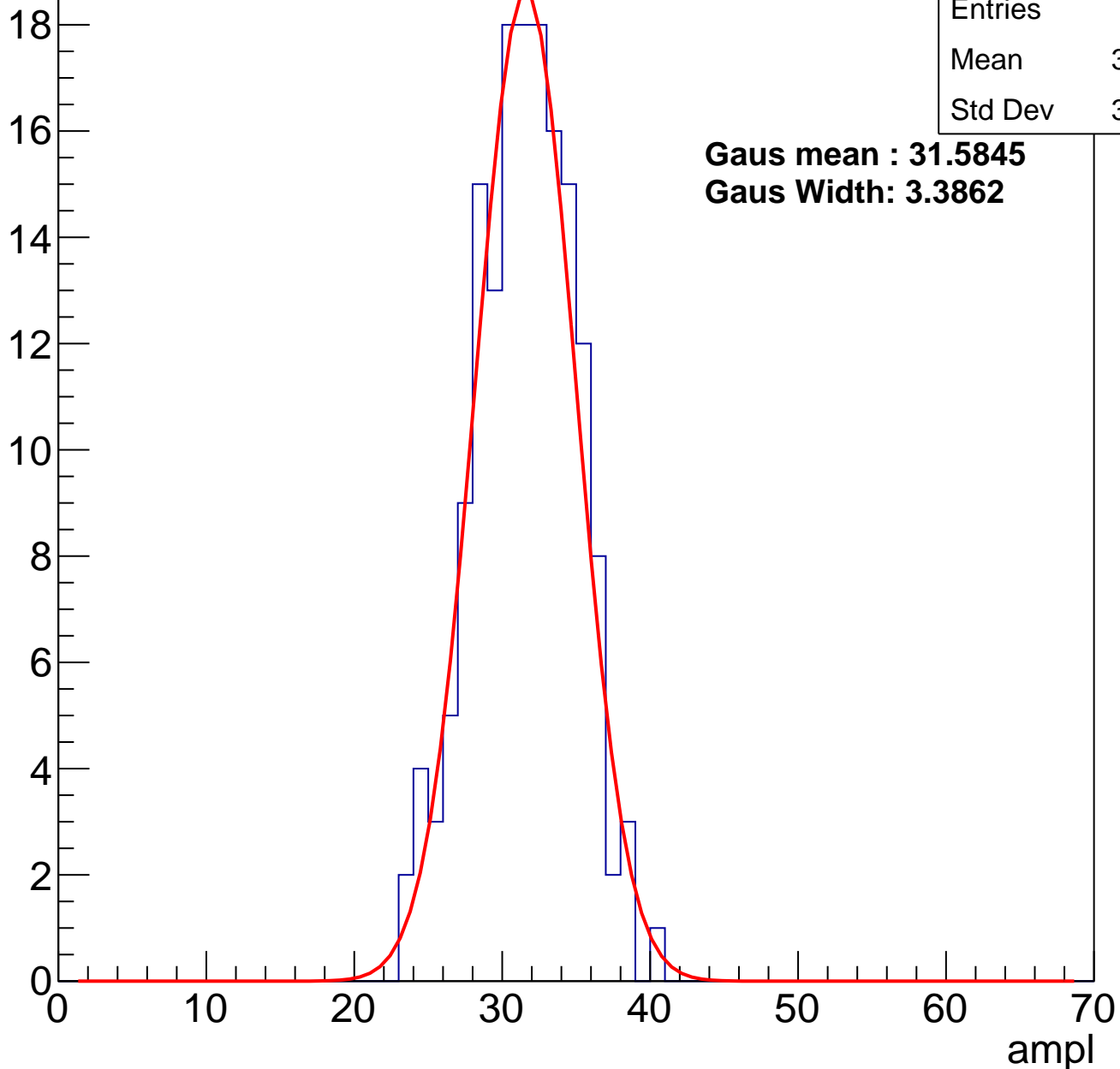
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	31.08
Std Dev	3.346

**Gaus mean : 31.5845**

**Gaus Width: 3.3862**

Entry

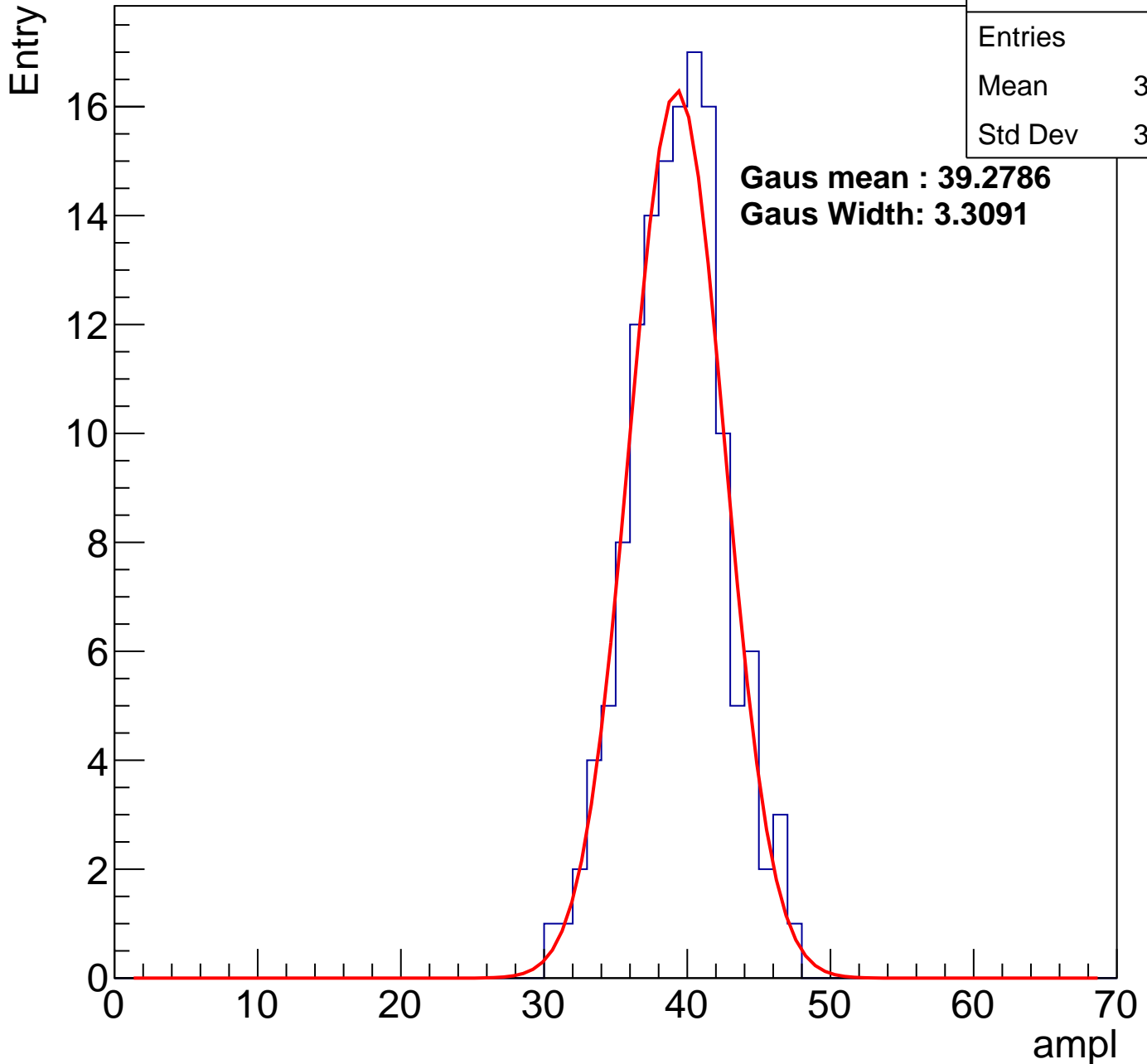


# B0L101S, U26-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	38.85
Std Dev	3.297

**Gaus mean : 39.2786**  
**Gaus Width: 3.3091**



# B0L101S, U26-ch11, adc2

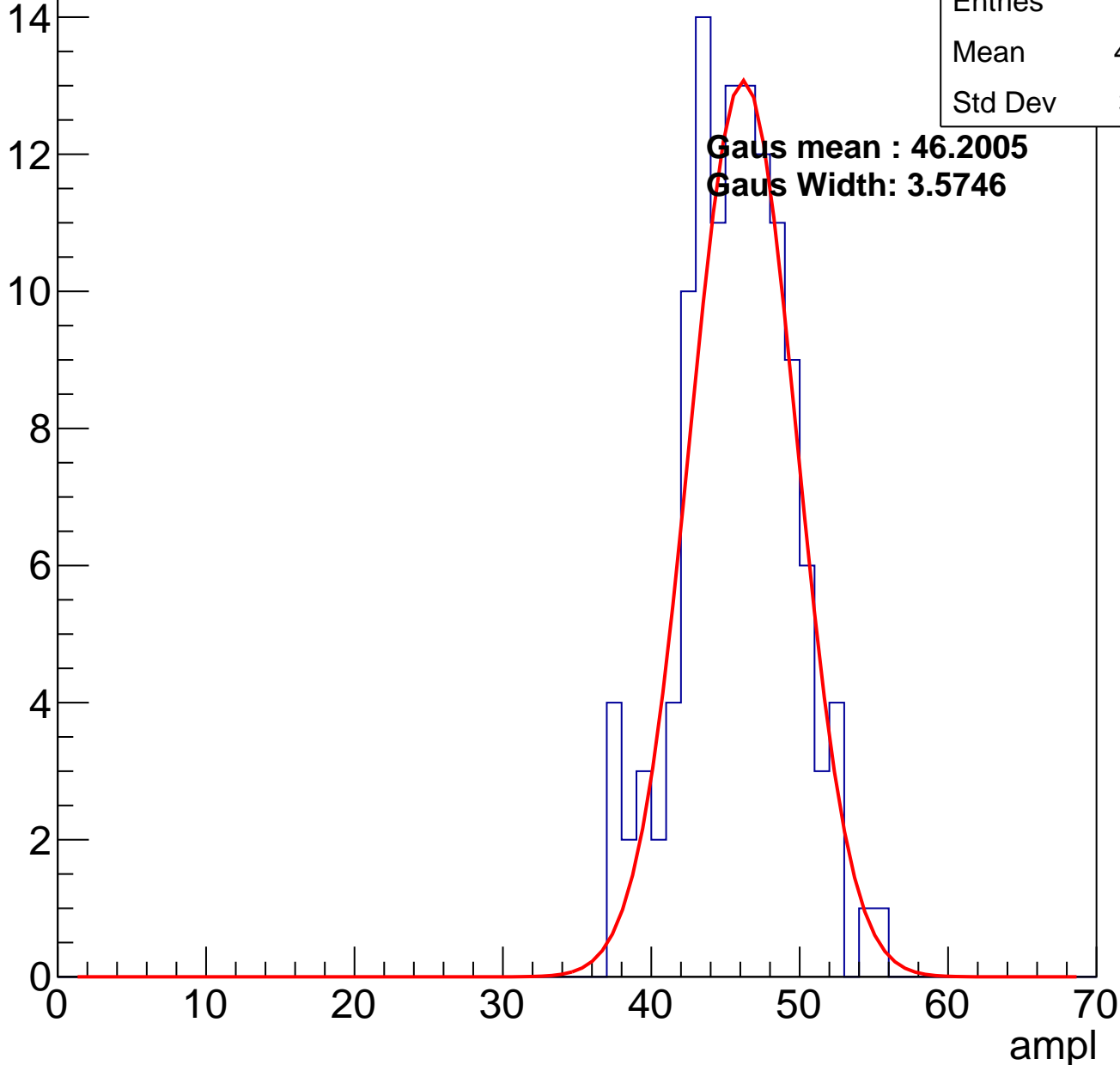
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	45.34
Std Dev	3.671

**Gaus mean : 46.2005**

**Gaus Width: 3.5746**

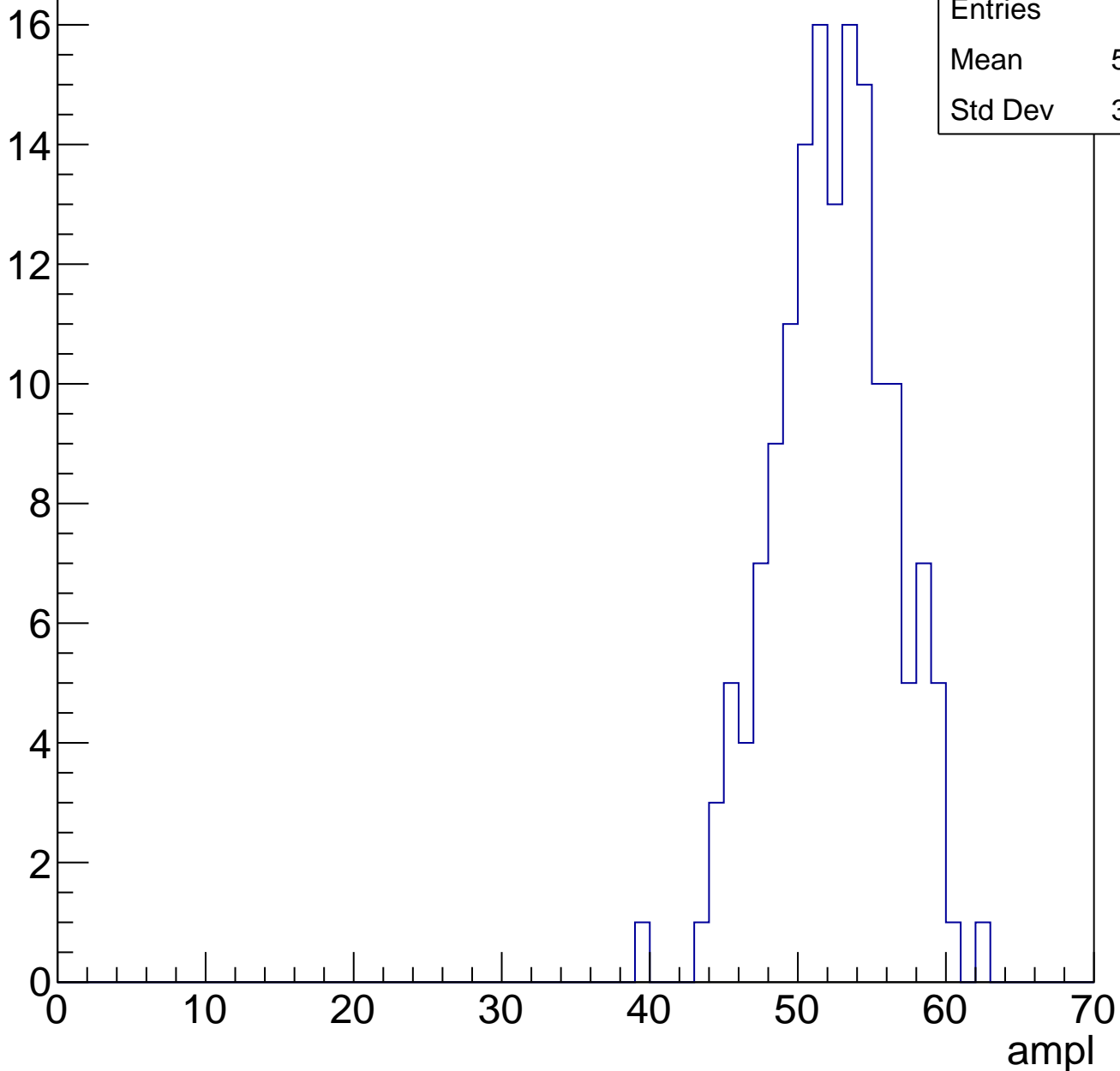
Entry



# B0L101S, U26-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

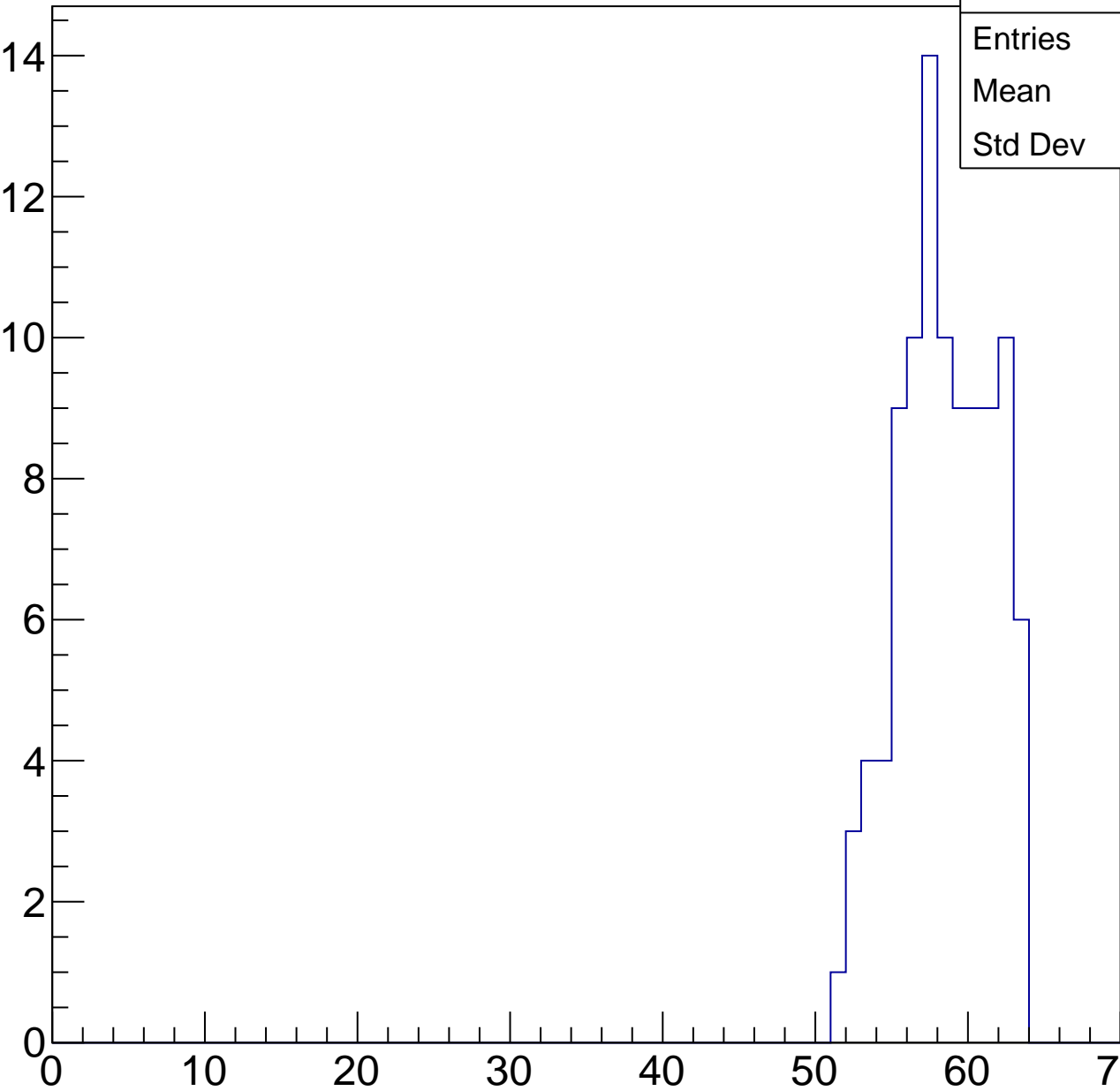
Entries	98
Mean	58.02
Std Dev	3.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

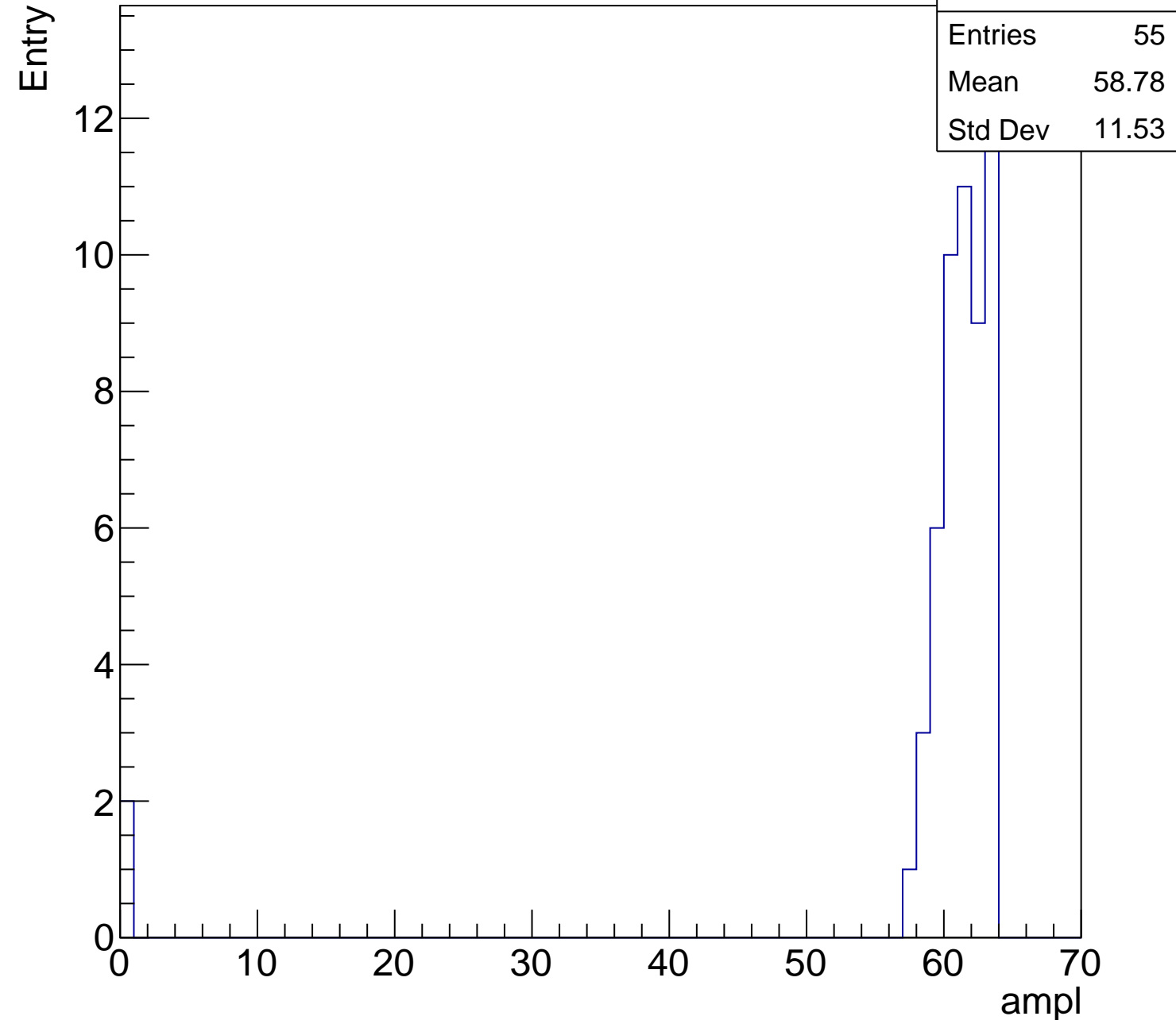
2

0

0 10 20 30 40 50 60 70

ampl

Entries	55
Mean	58.78
Std Dev	11.53



# B0L101S, U26-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



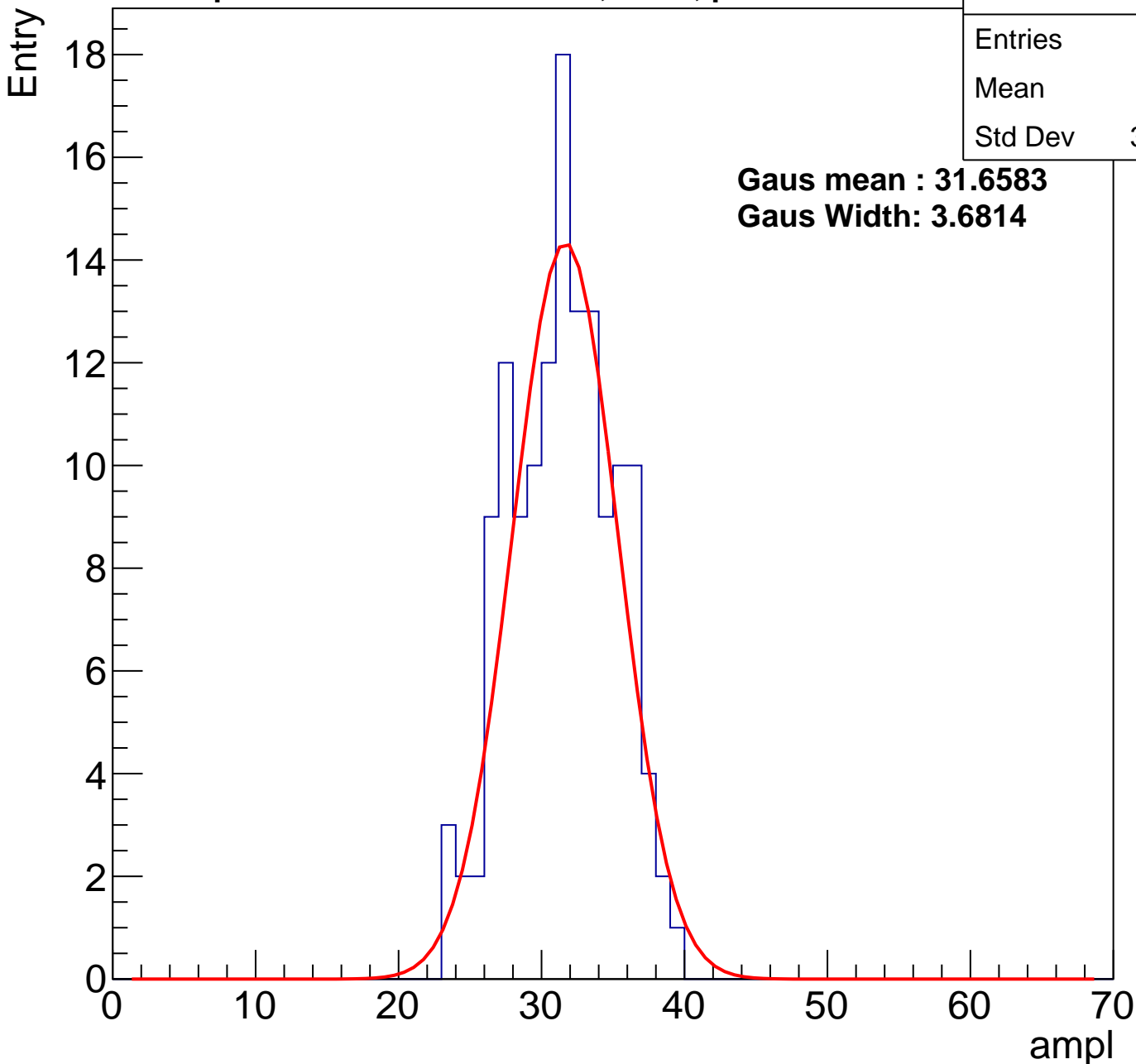
# B0L101S, U26-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	31
Std Dev	3.572

**Gaus mean : 31.6583**

**Gaus Width: 3.6814**



# B0L101S, U26-ch12, adc1

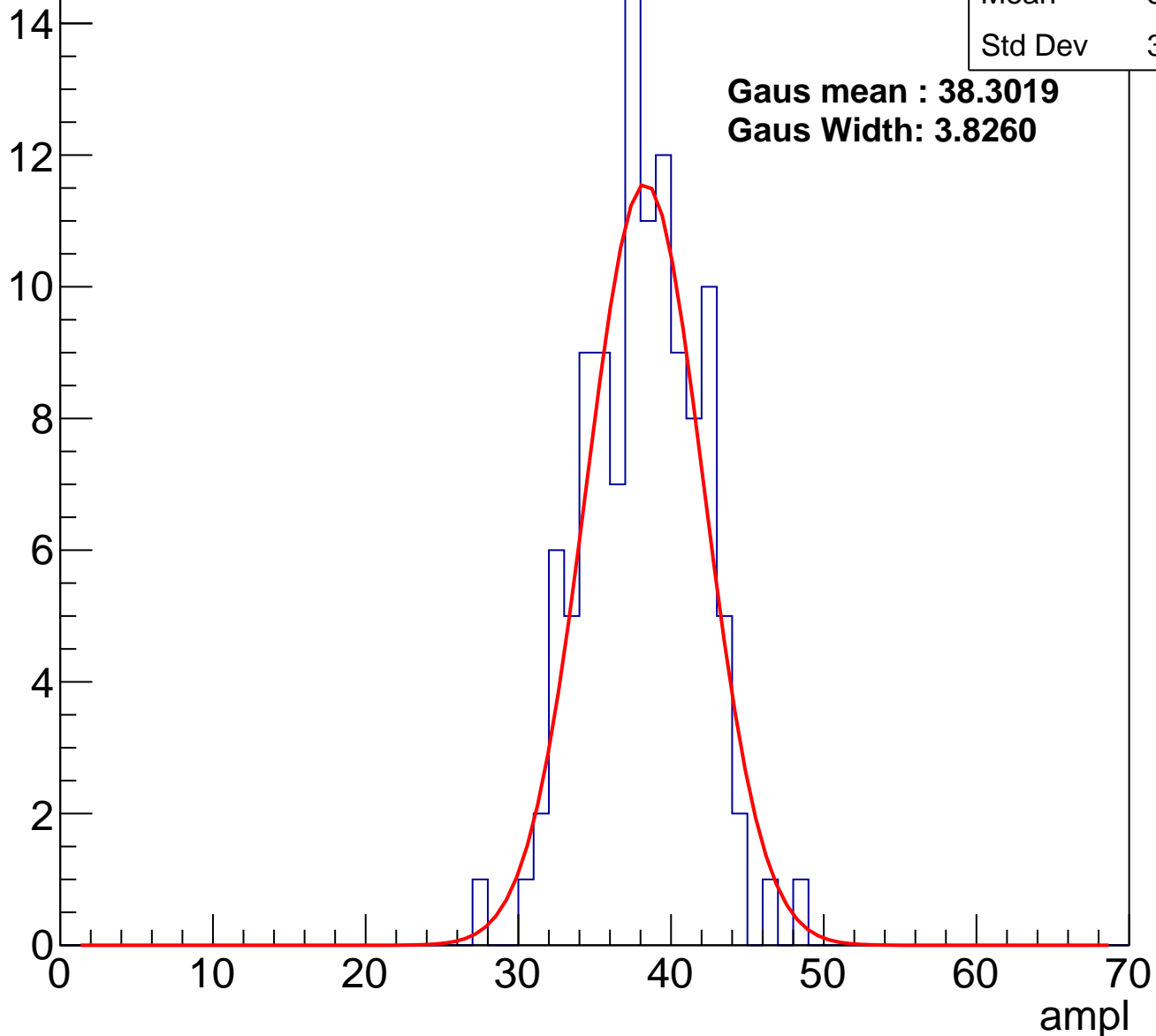
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	37.68
Std Dev	3.653

**Gaus mean : 38.3019**

**Gaus Width: 3.8260**

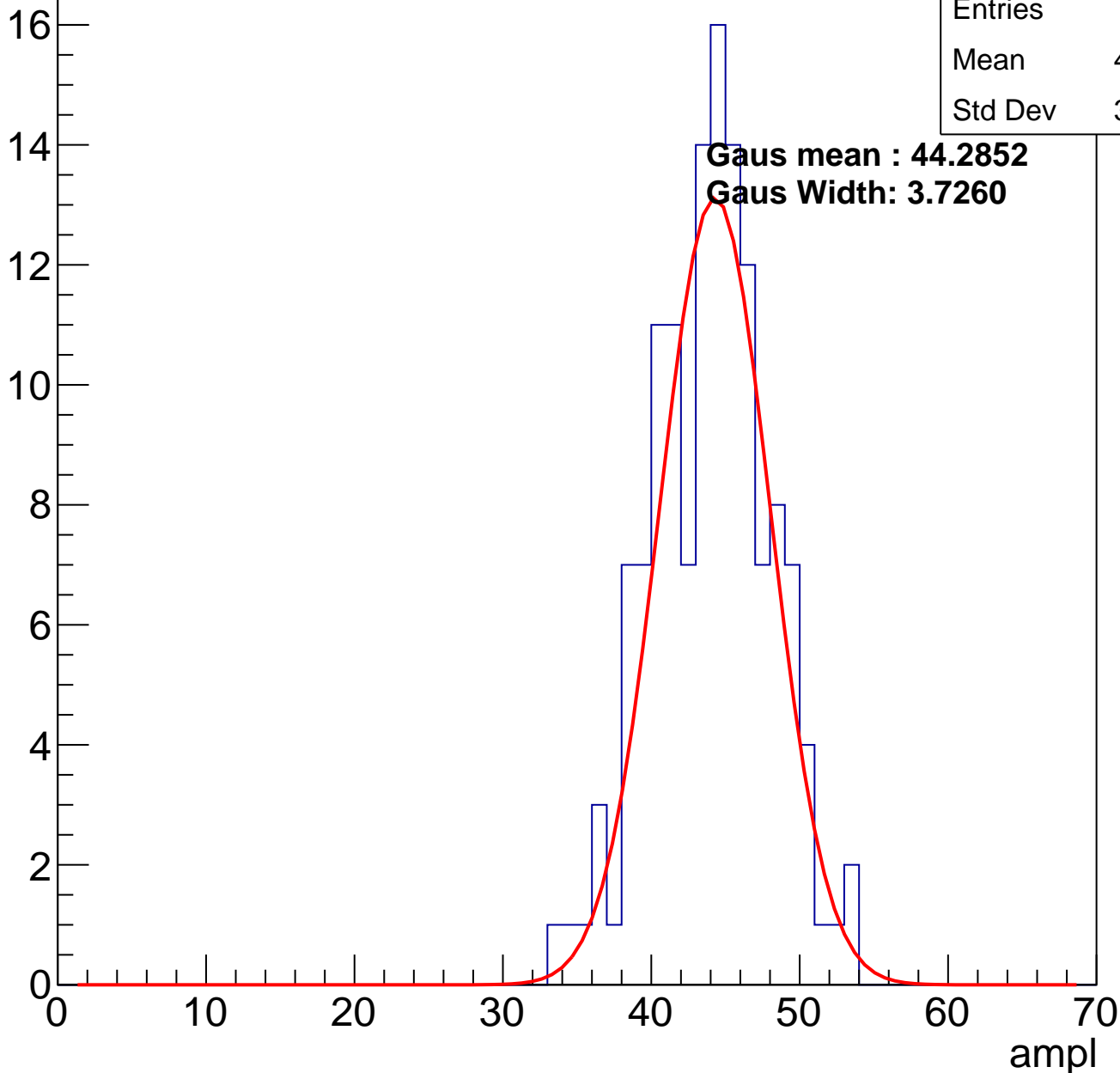
Entry



# B0L101S, U26-ch12, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

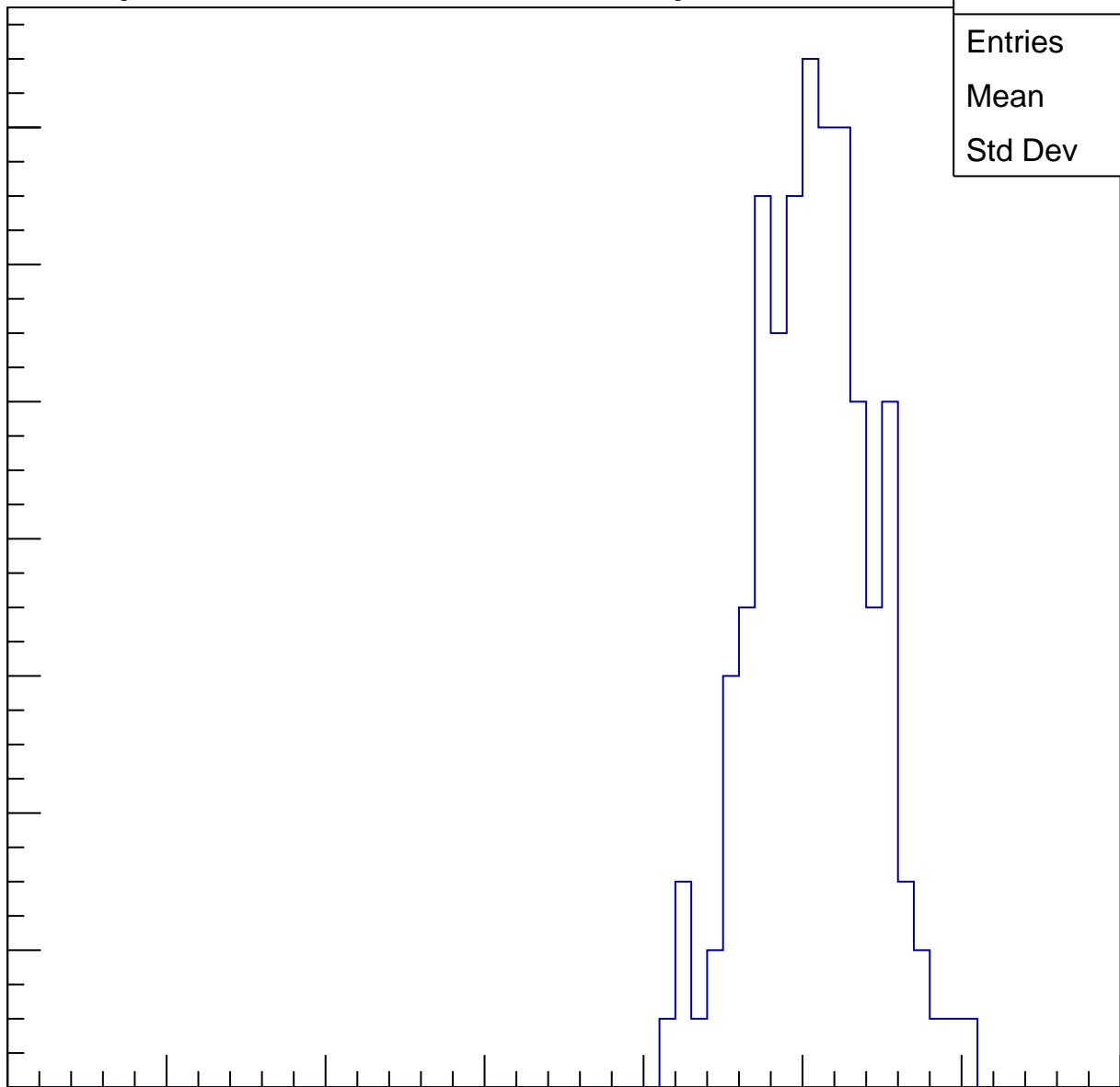
Entries	135
Mean	50.19
Std Dev	3.65

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch12, adc4

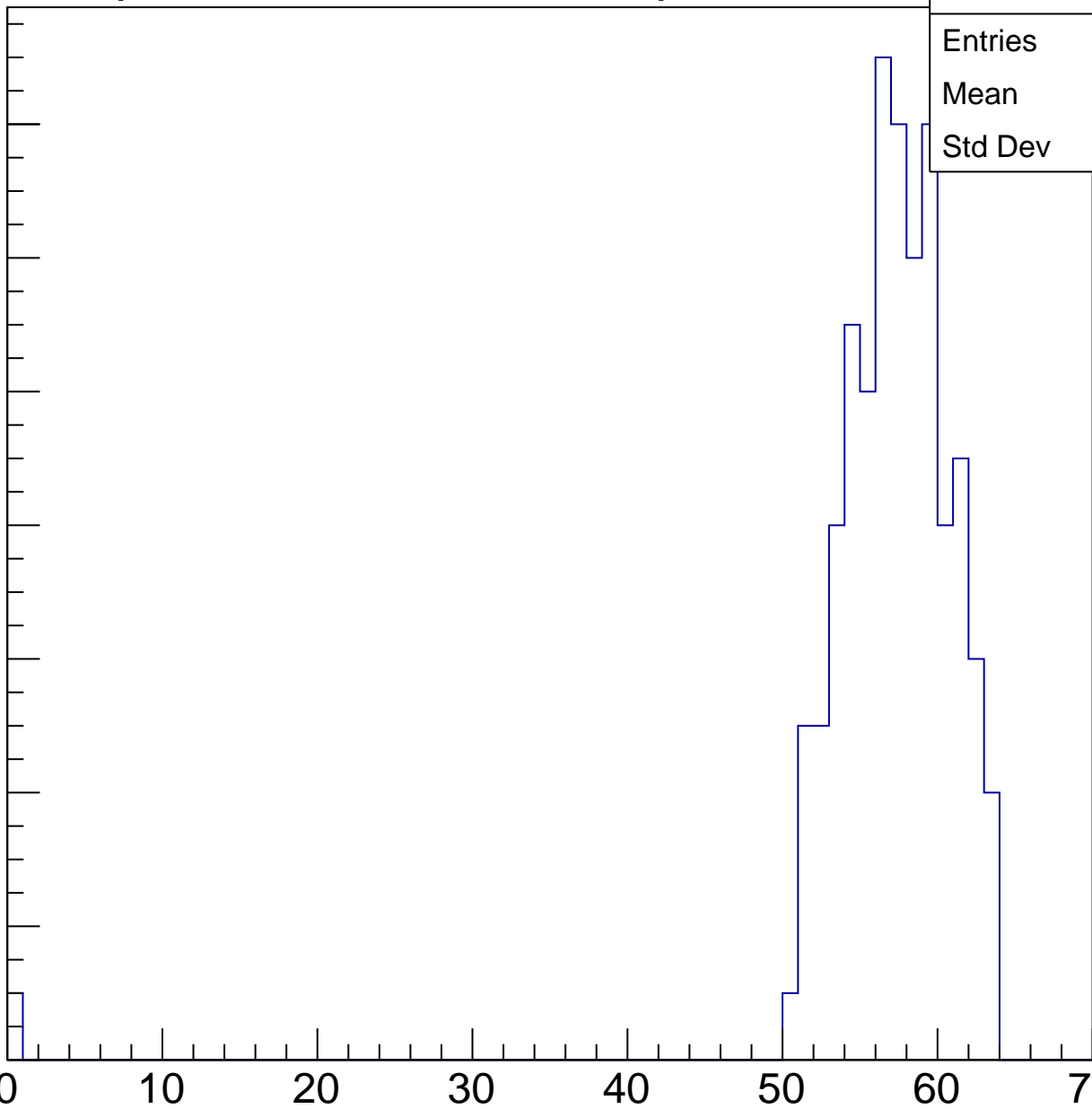
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	56.47
Std Dev	6

ampl



# B0L101S, U26-ch12, adc5

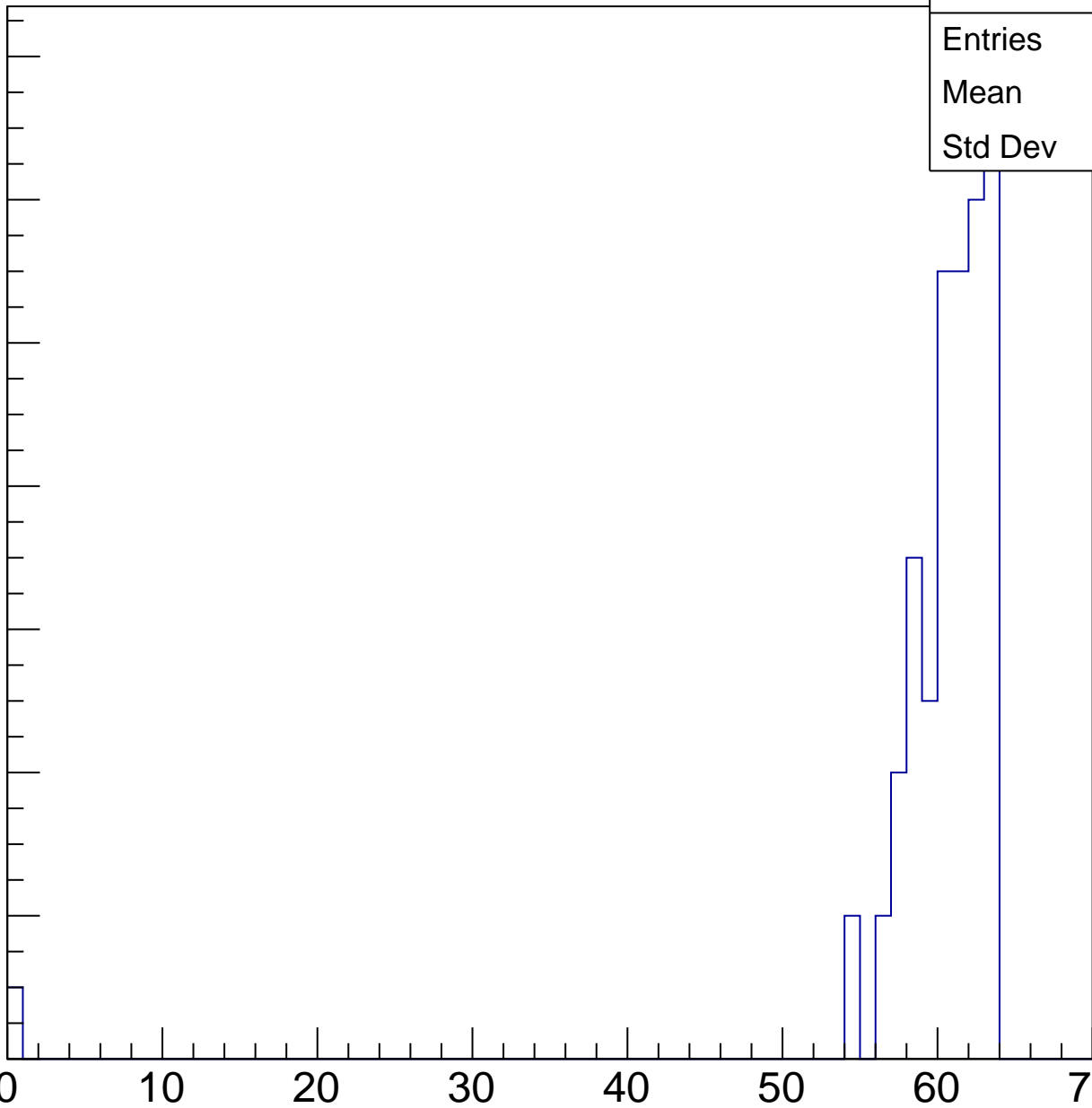
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	59.51
Std Dev	7.558

ampl



# B0L101S, U26-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L101S, U26-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	31.6
Std Dev	3.365

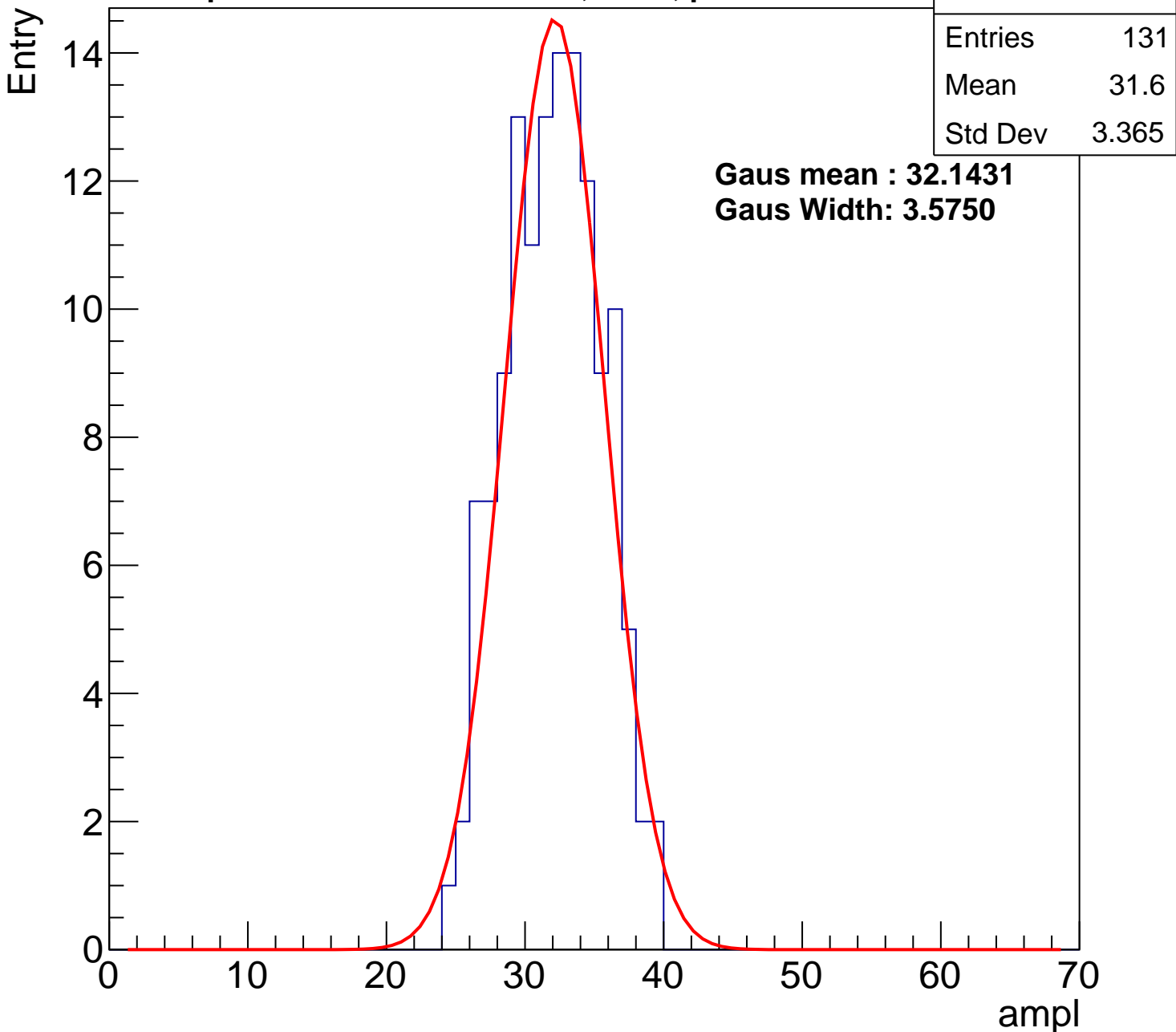
**Gaus mean : 32.1431**  
**Gaus Width: 3.5750**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch13, adc1

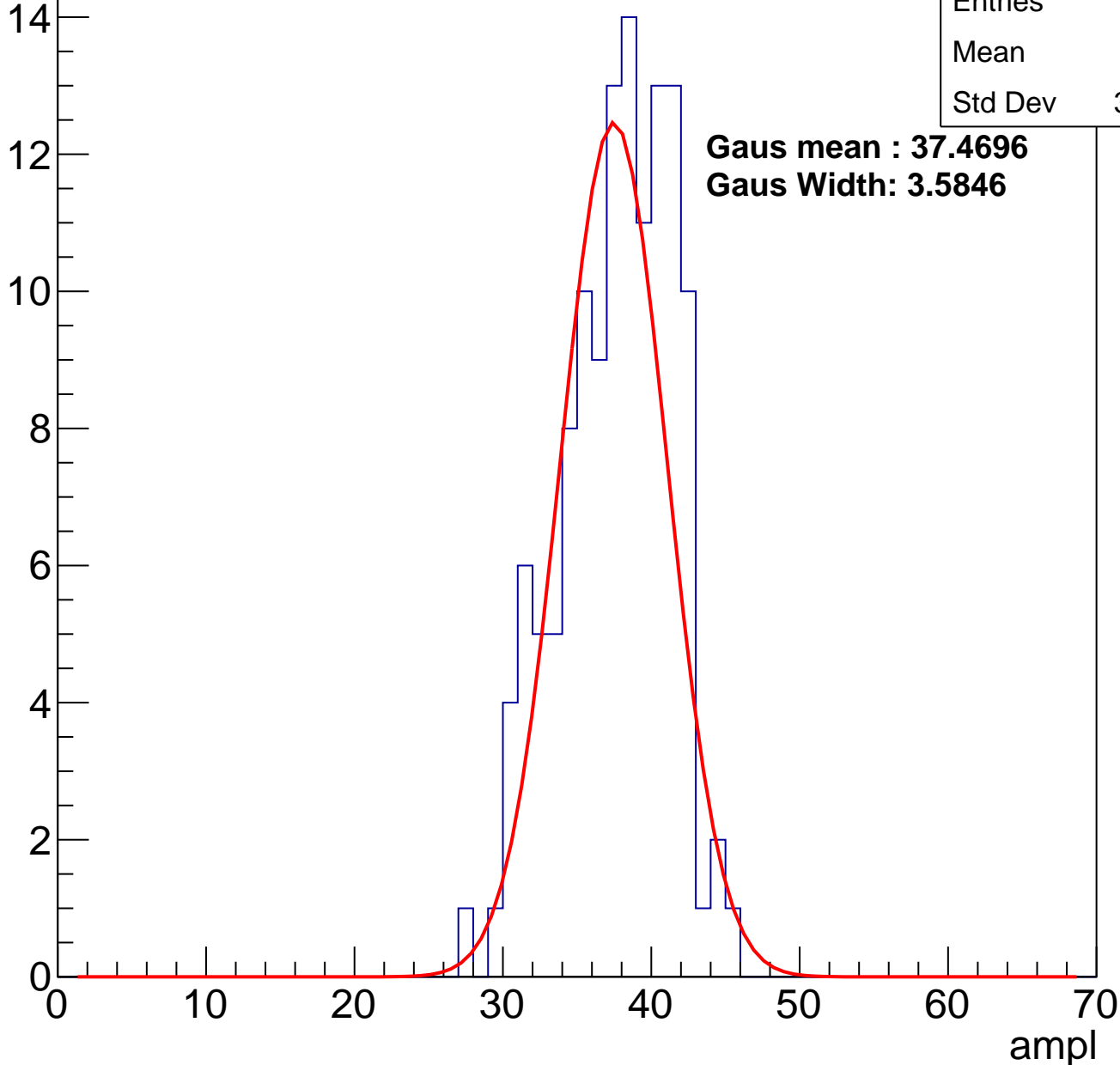
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	37.2
Std Dev	3.689

**Gaus mean : 37.4696**

**Gaus Width: 3.5846**

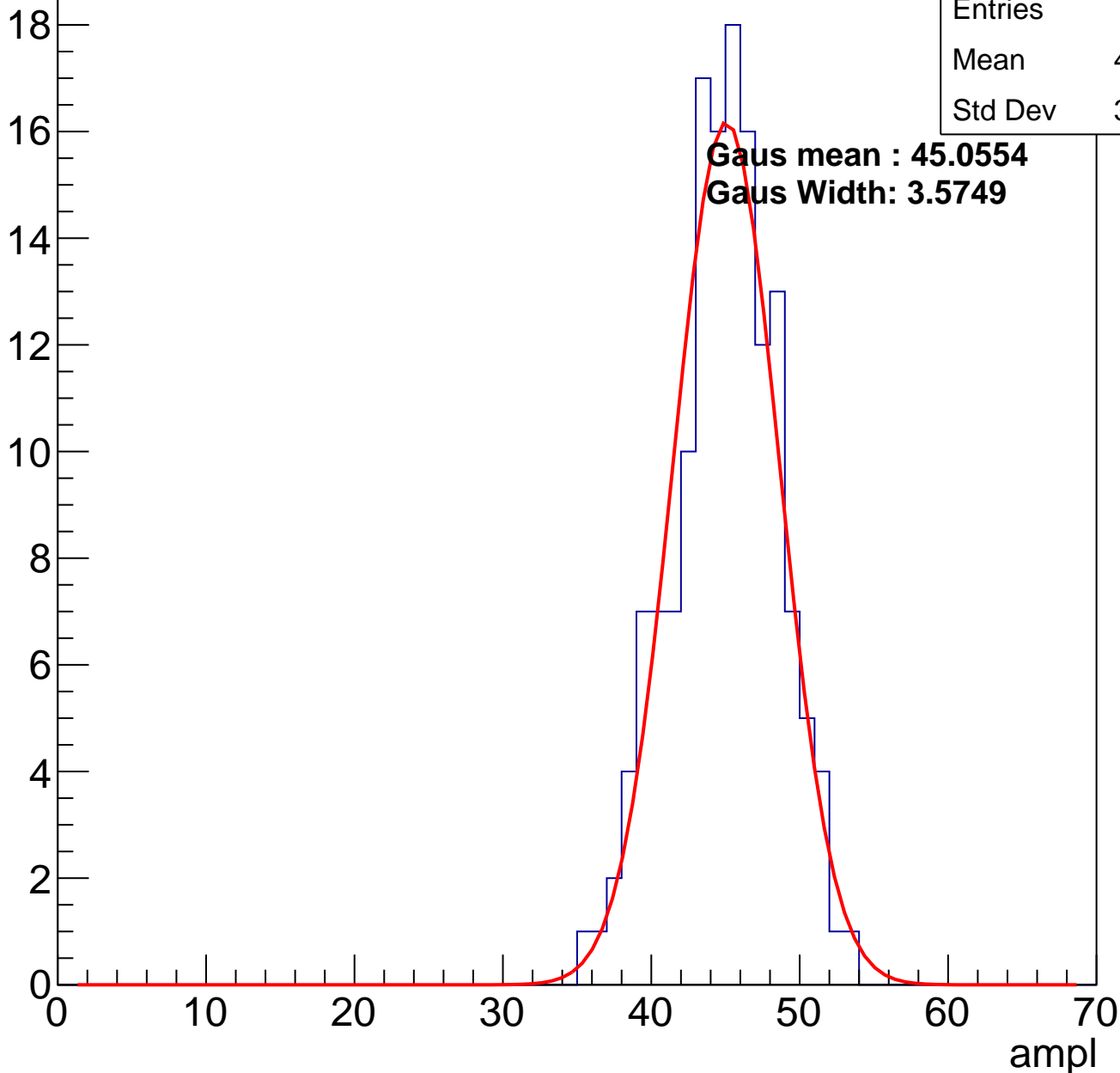
Entry



# B0L101S, U26-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

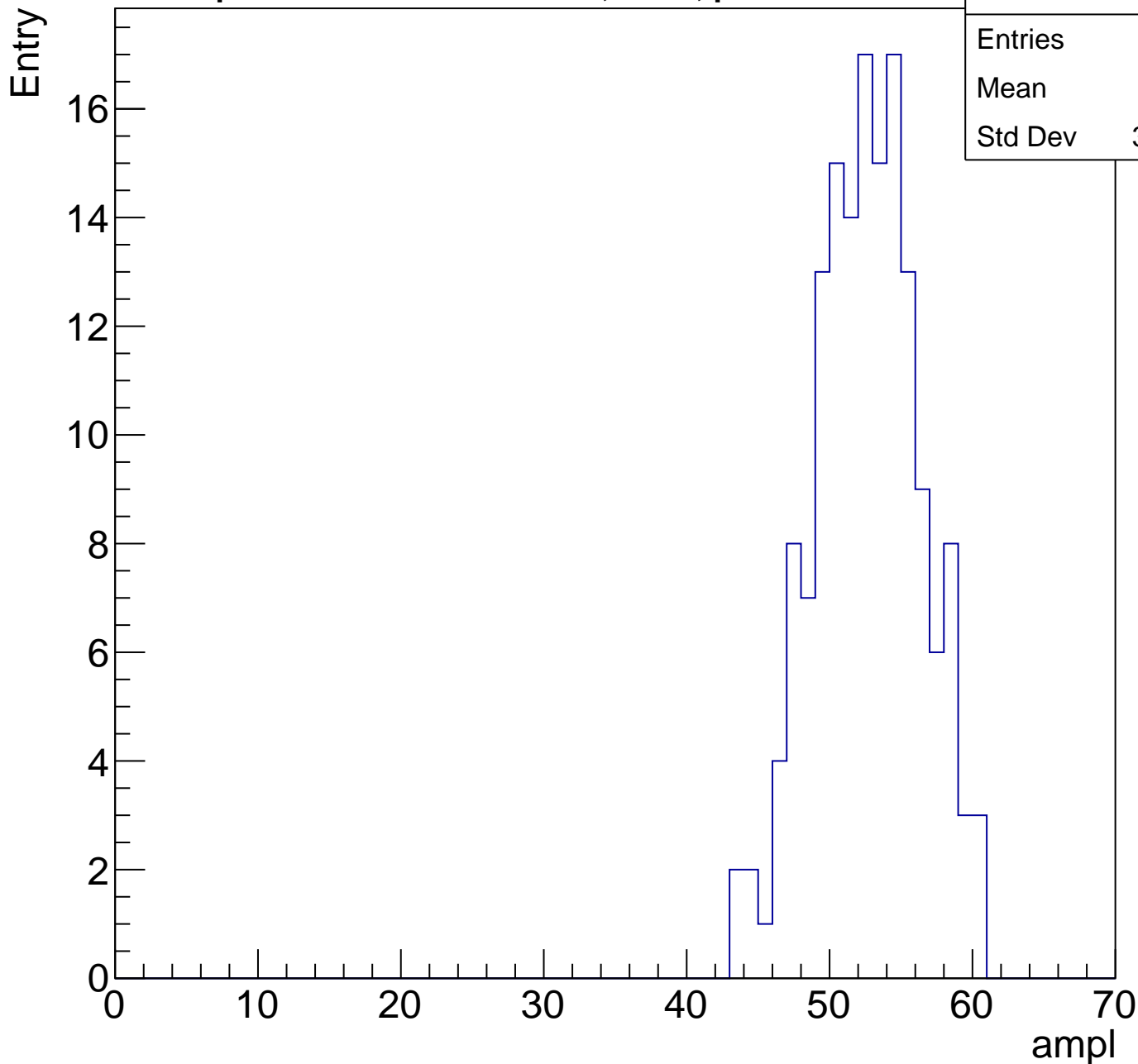
Entry



# B0L101S, U26-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	52.2
Std Dev	3.658



# B0L101S, U26-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	87
Mean	56.99
Std Dev	6.759

Entry

12

10

8

6

4

2

0

0

10

20

30

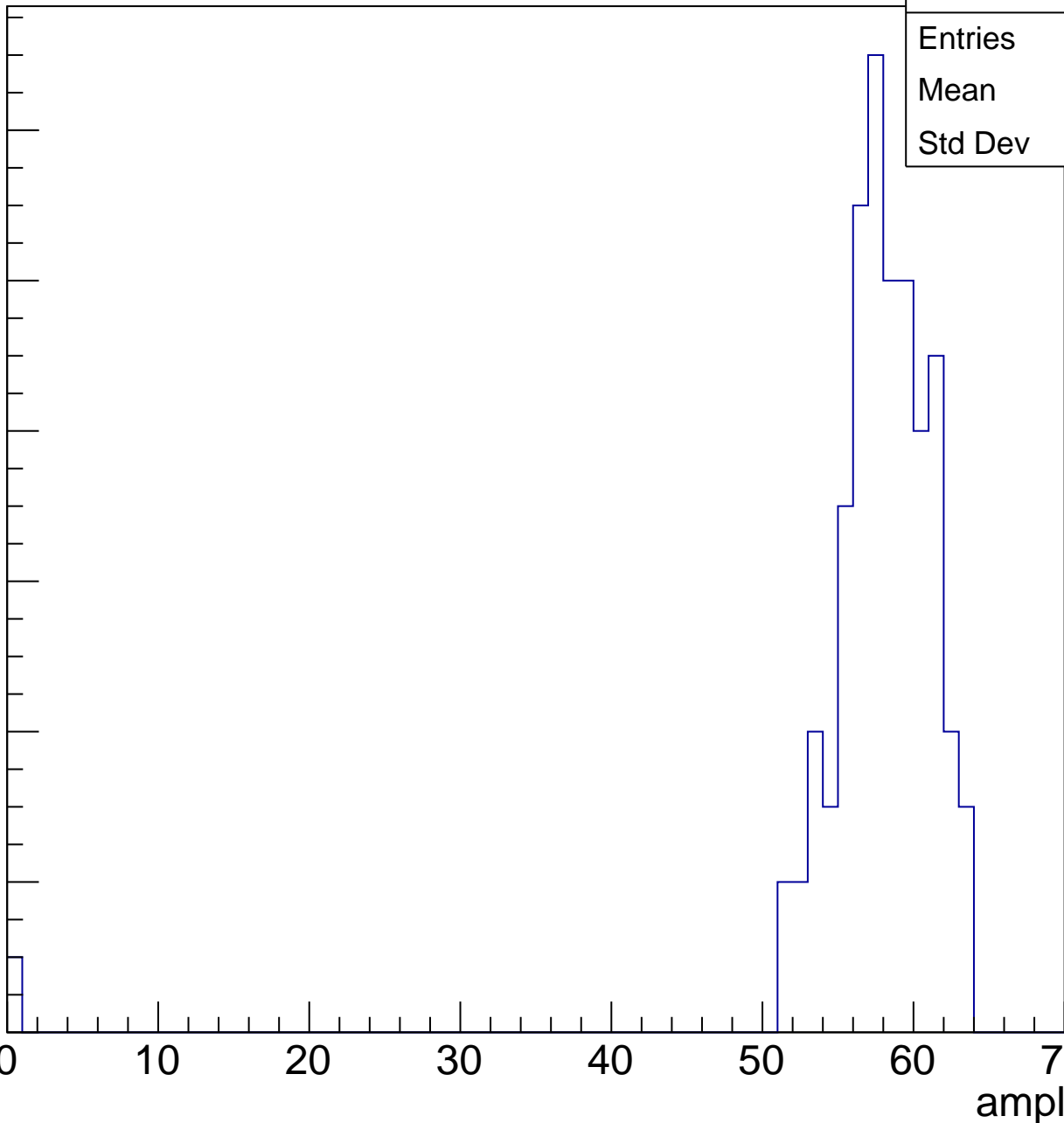
40

50

60

ampl

70



# B0L101S, U26-ch13, adc5

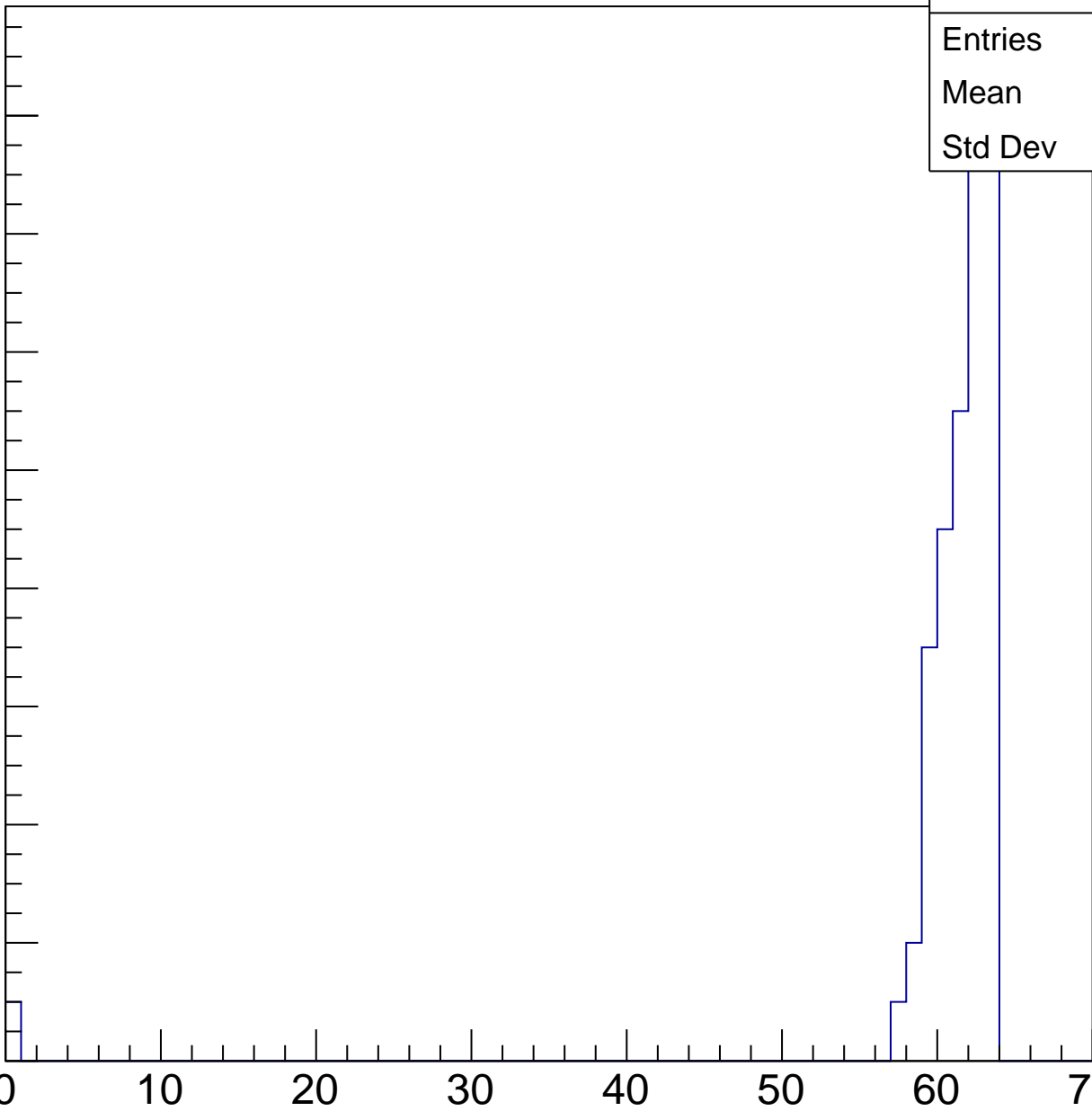
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	64
Mean	60.31
Std Dev	7.752

ampl



# B0L101S, U26-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U26-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	28.1
Std Dev	3.108

**Gaus mean : 28.4346**

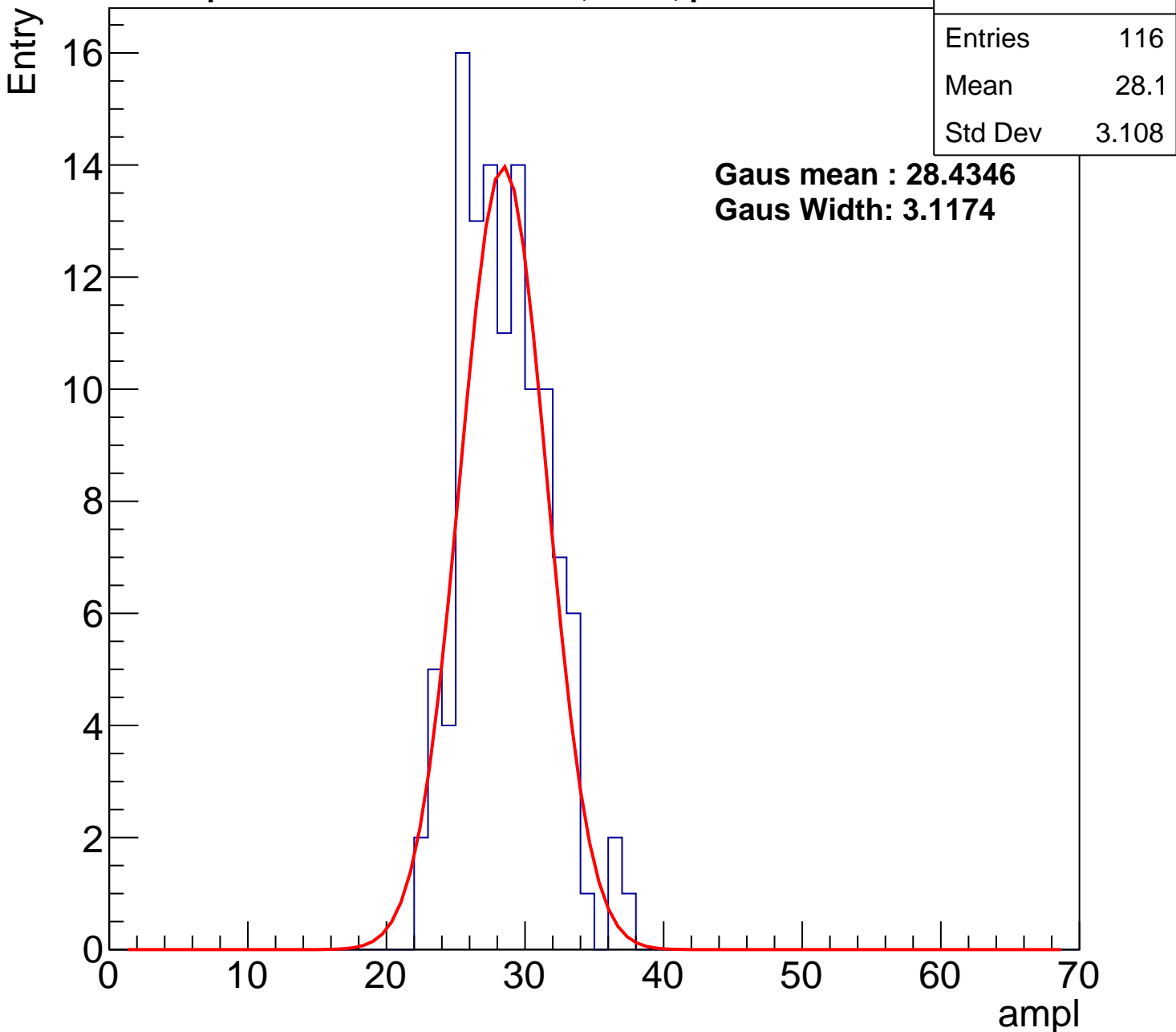
**Gaus Width: 3.1174**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



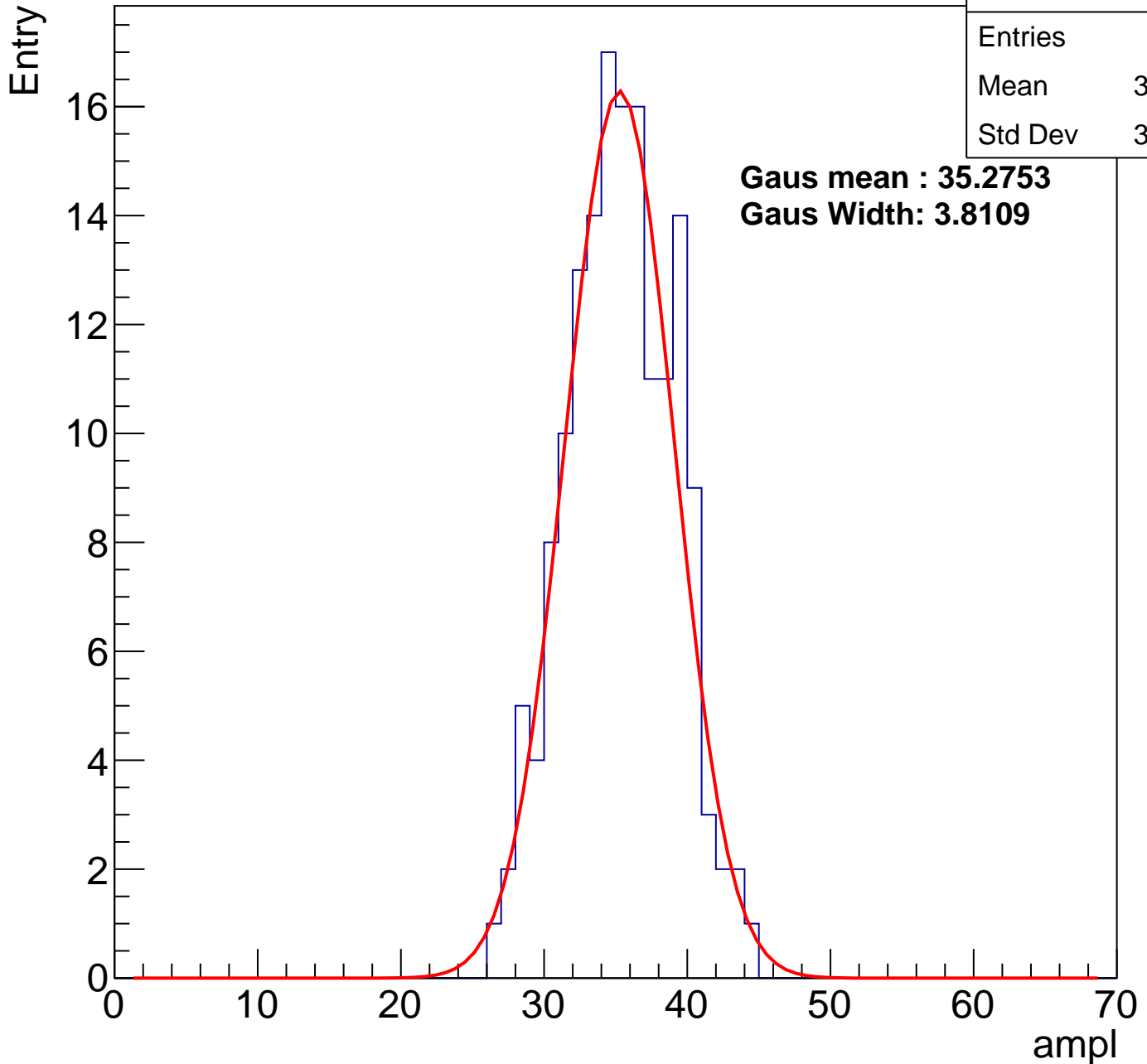
# B0L101S, U26-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	34.88
Std Dev	3.693

**Gaus mean : 35.2753**

**Gaus Width: 3.8109**



# B0L101S, U26-ch14, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

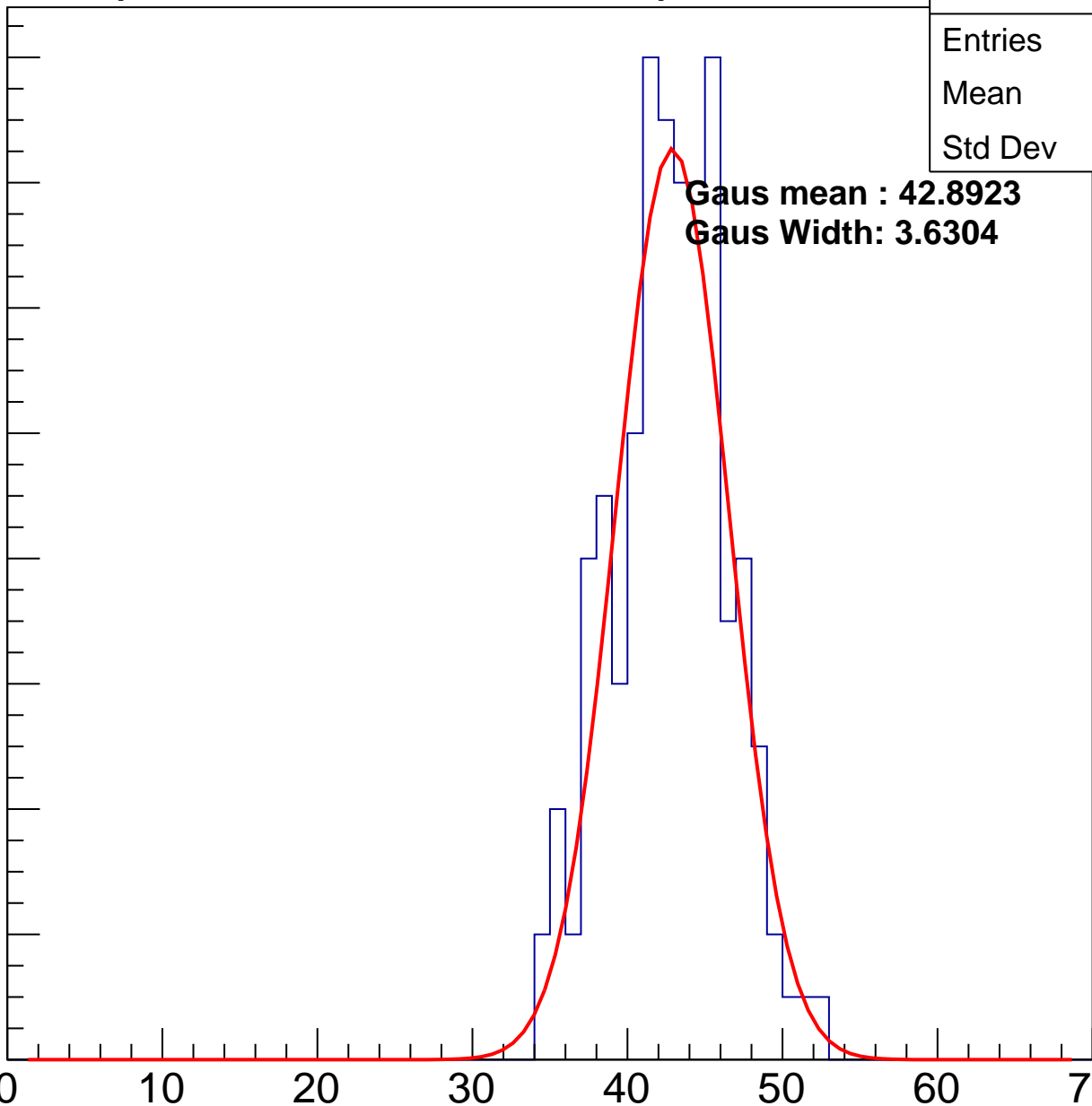
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	141
Mean	42.3
Std Dev	3.657

**Gaus mean : 42.8923**

**Gaus Width: 3.6304**

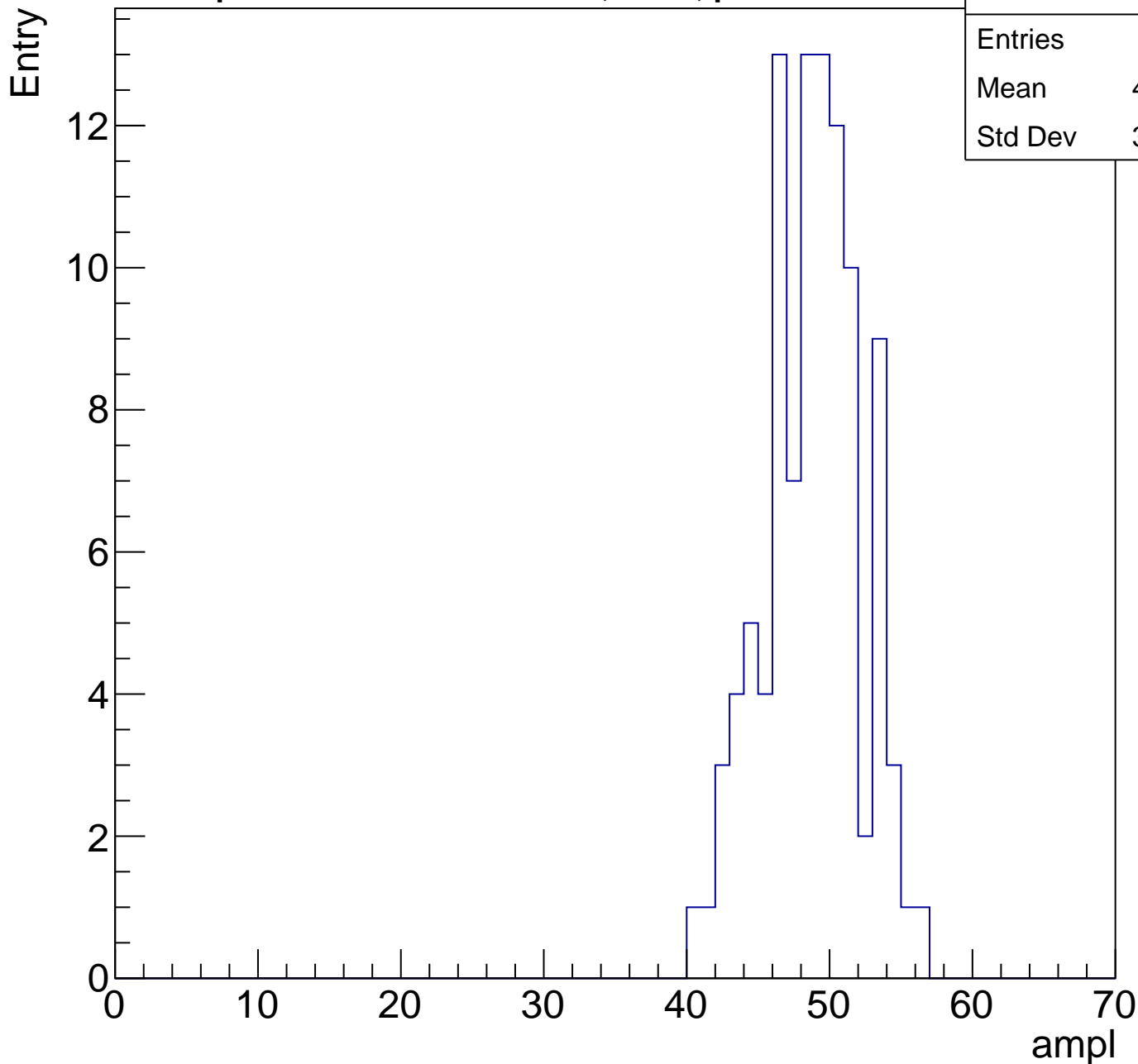
ampl



# B0L101S, U26-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

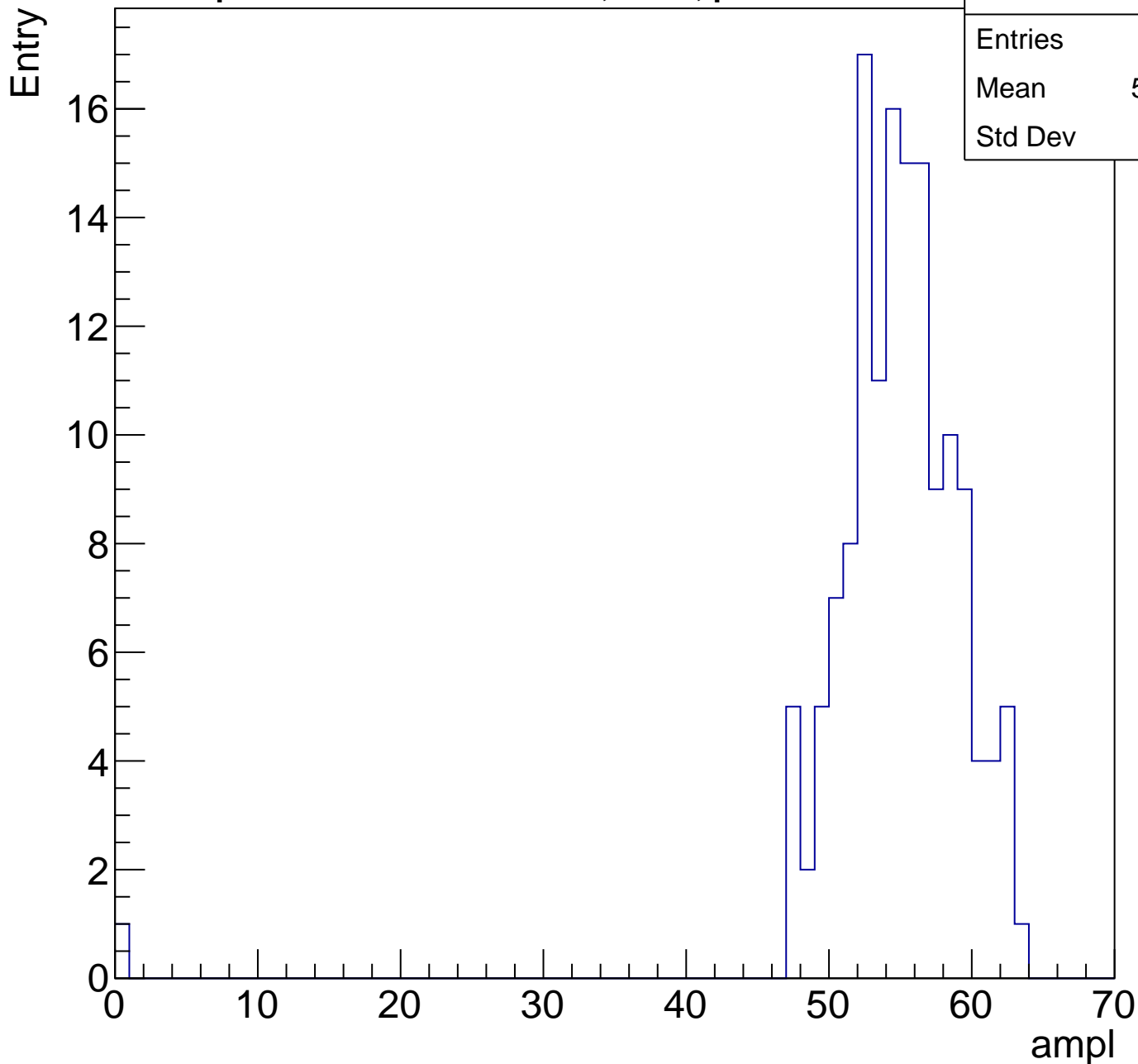
Entries	102
Mean	48.34
Std Dev	3.303



# B0L101S, U26-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	54.24
Std Dev	5.83



# B0L101S, U26-ch14, adc5

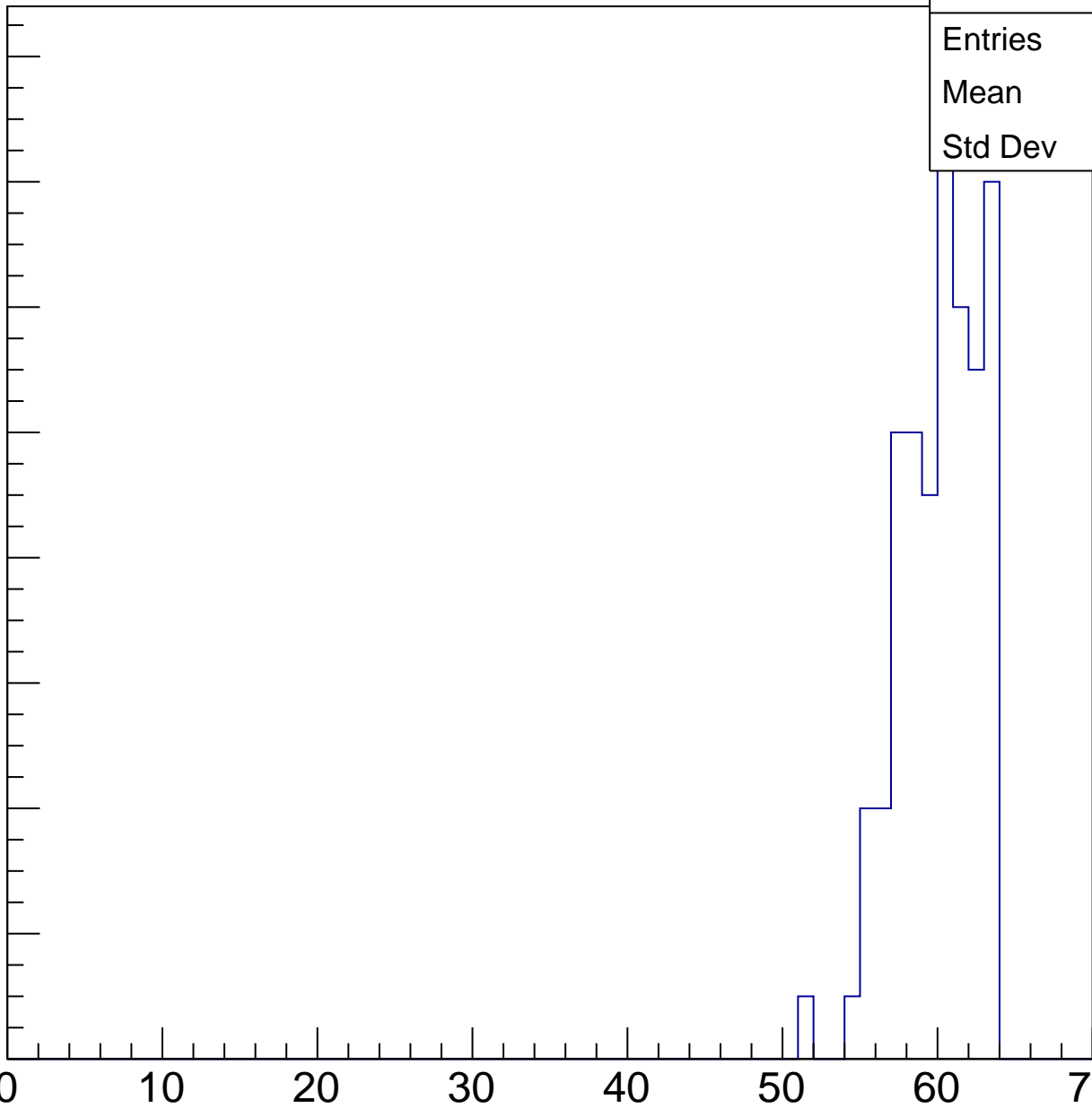
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	92
Mean	59.63
Std Dev	2.531

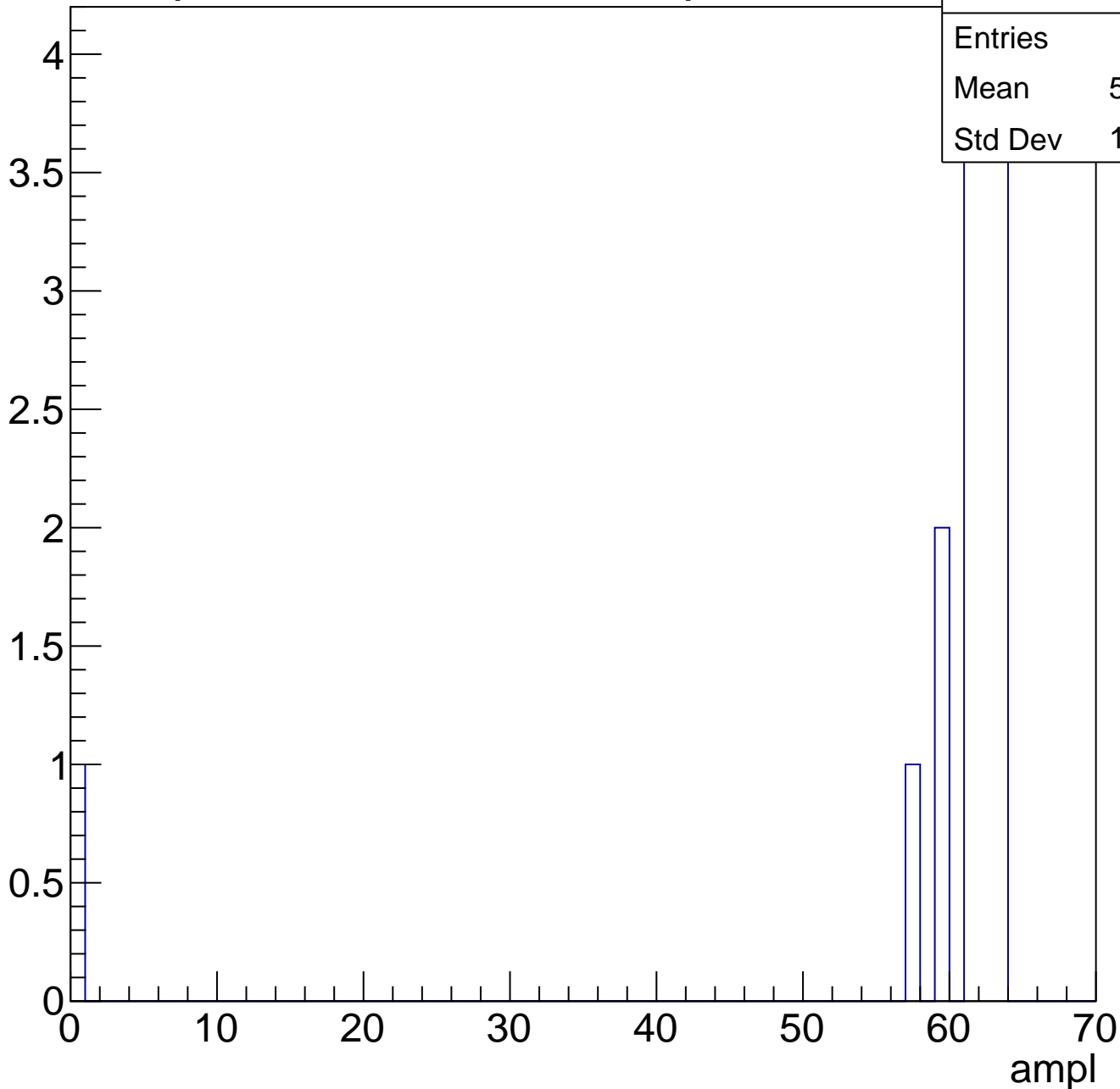
ampl



# B0L101S, U26-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42
Std Dev	21

ampl

# B0L101S, U26-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	93
Mean	30.34
Std Dev	3.171

**Gaus mean : 31.2812**

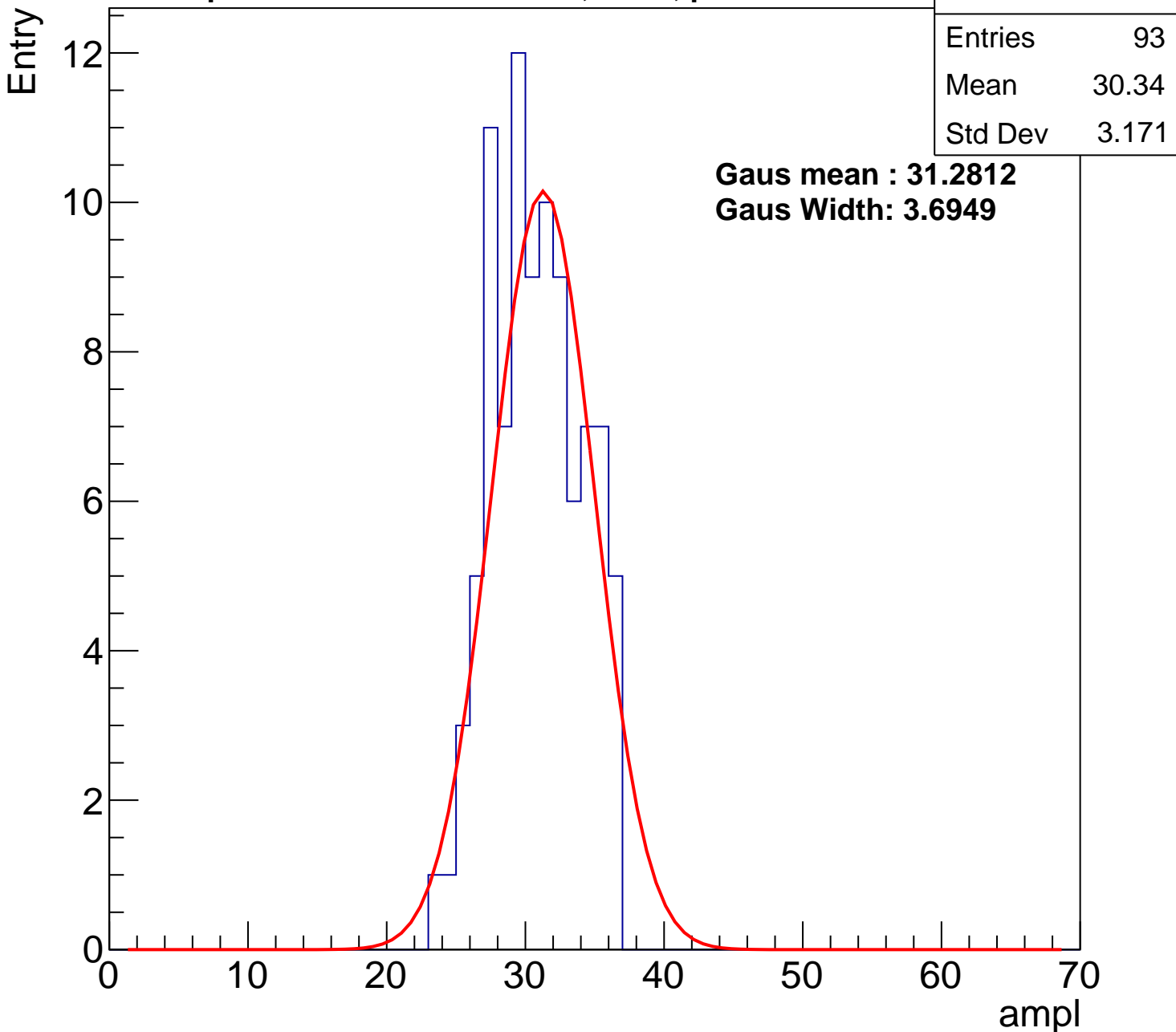
**Gaus Width: 3.6949**

Entry

12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



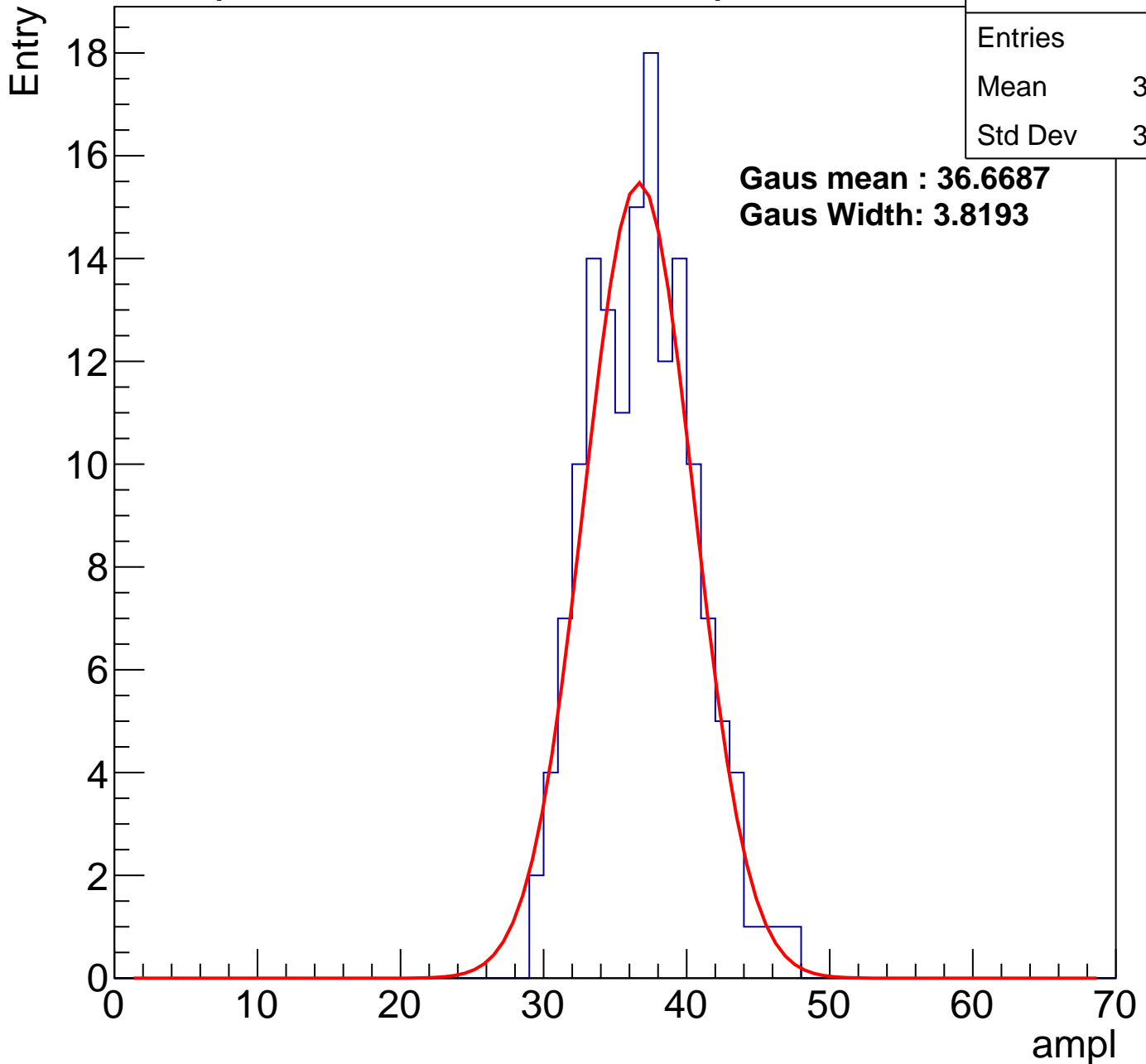
# B0L101S, U26-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	36.42
Std Dev	3.659

**Gaus mean : 36.6687**

**Gaus Width: 3.8193**



# B0L101S, U26-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

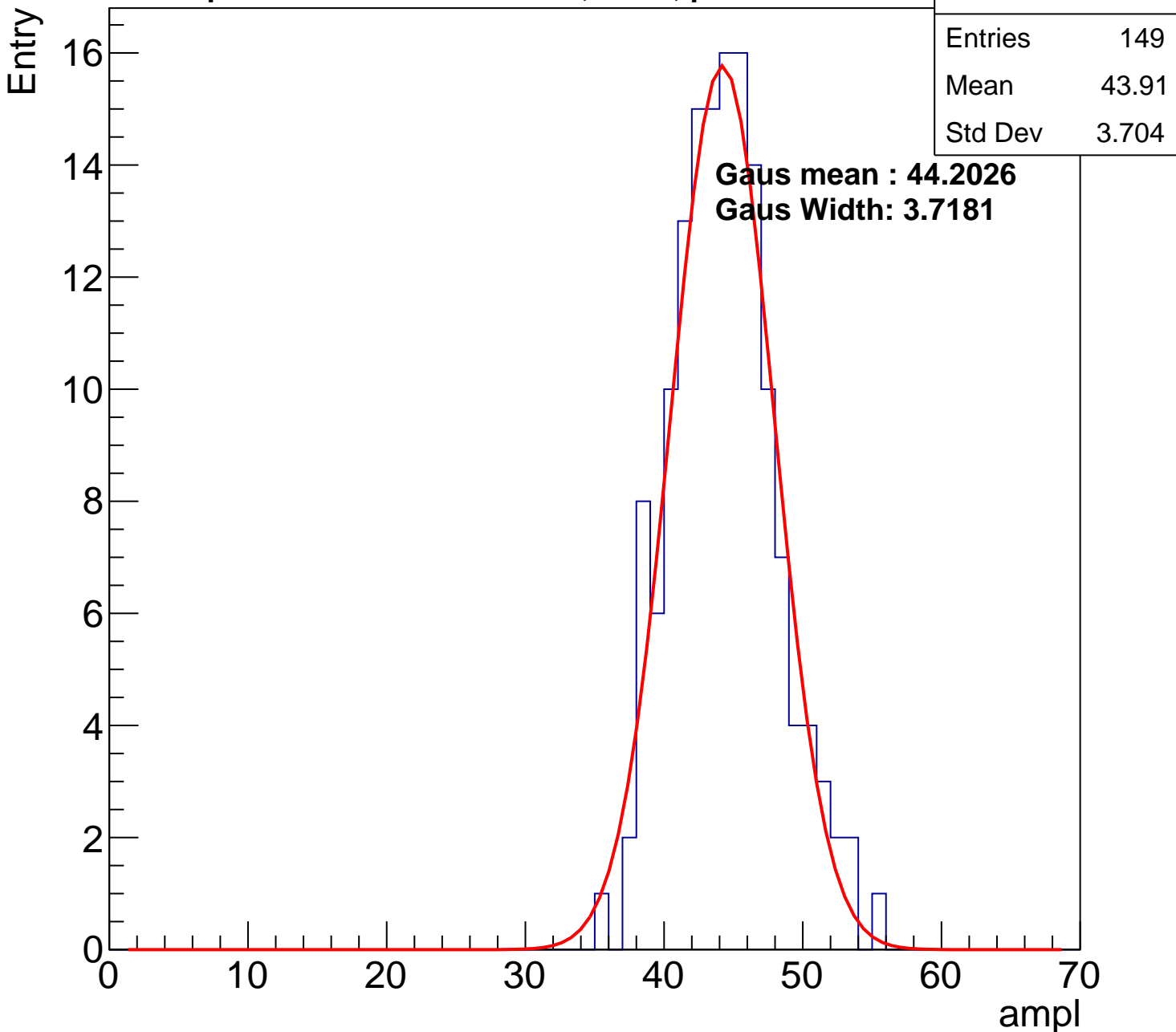
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	149
Mean	43.91
Std Dev	3.704

**Gaus mean : 44.2026**  
**Gaus Width: 3.7181**

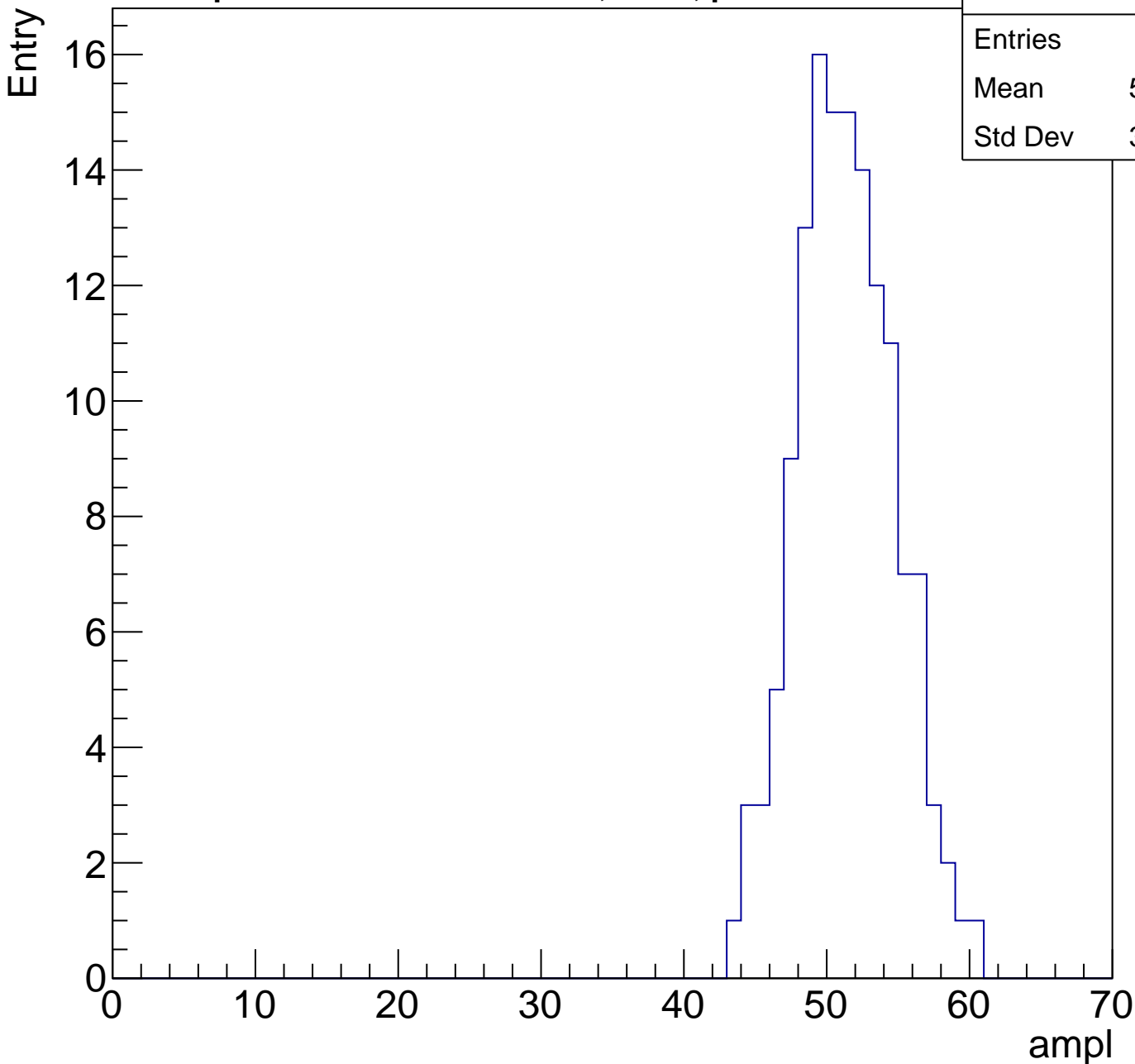
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U26-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	50.92
Std Dev	3.386



# B0L101S, U26-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

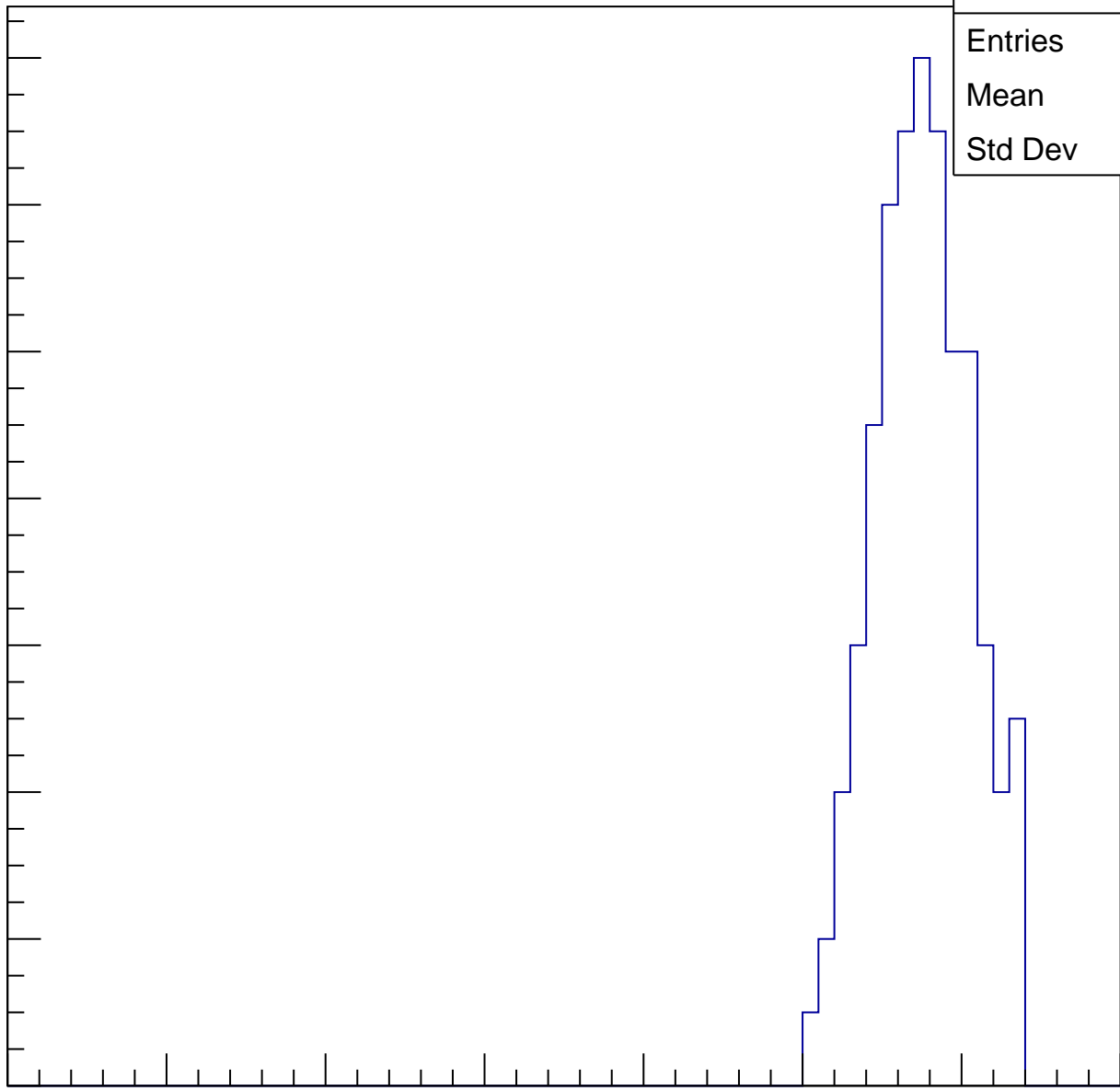
Entries	109
Mean	57.09
Std Dev	2.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch15, adc5

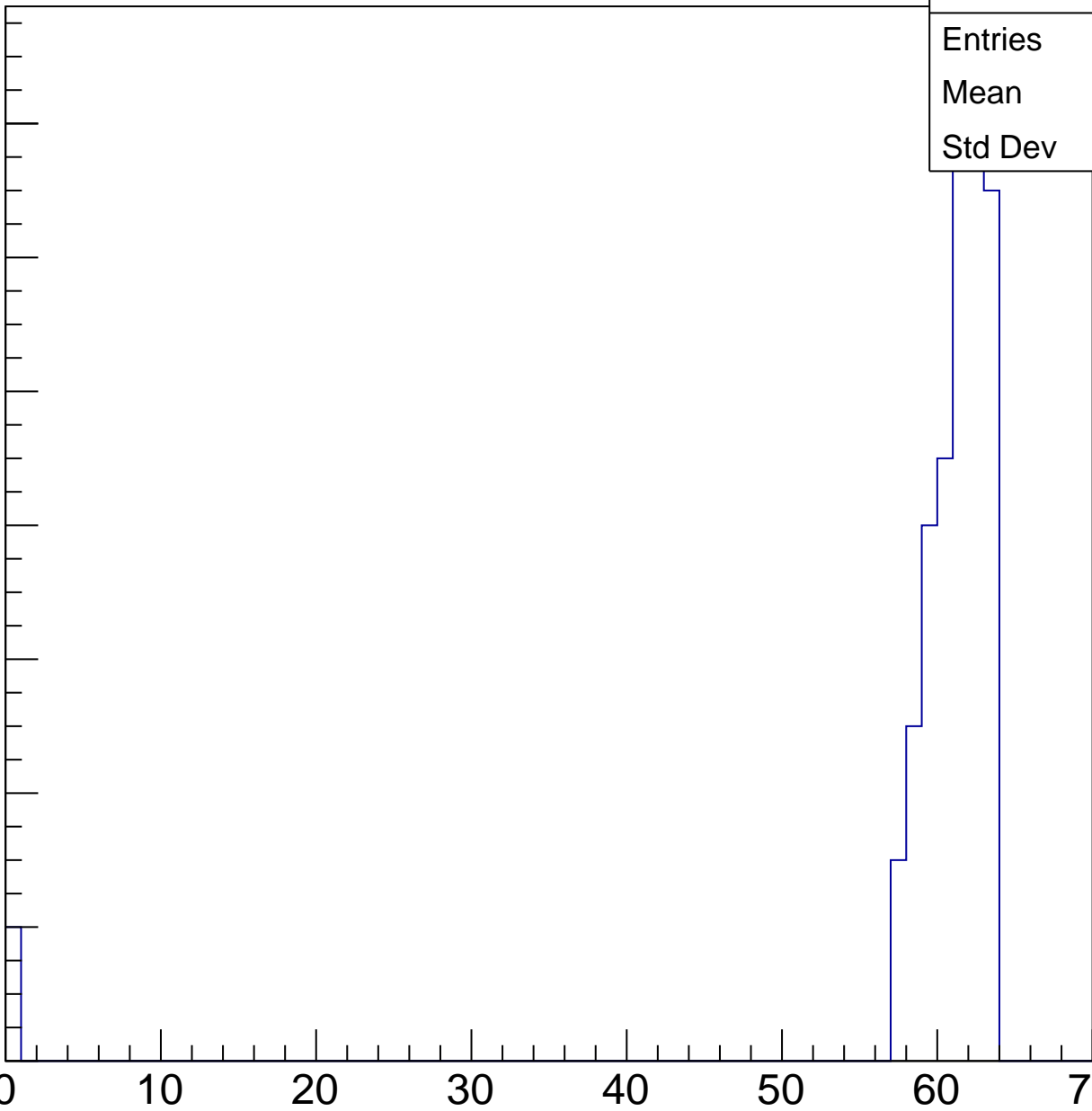
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	59.07
Std Dev	10.35

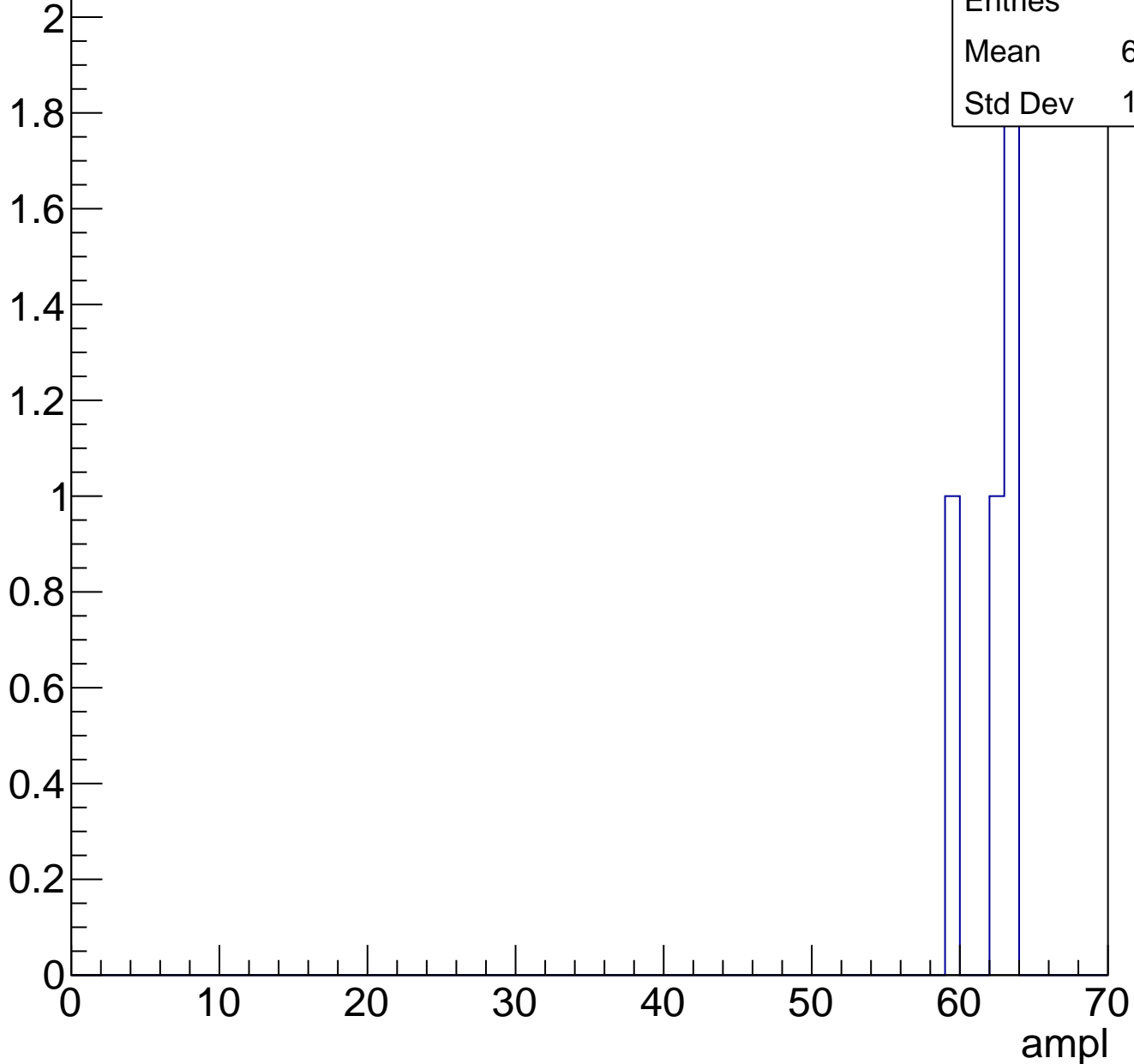
ampl



# B0L101S, U26-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L101S, U26-ch16, adc0

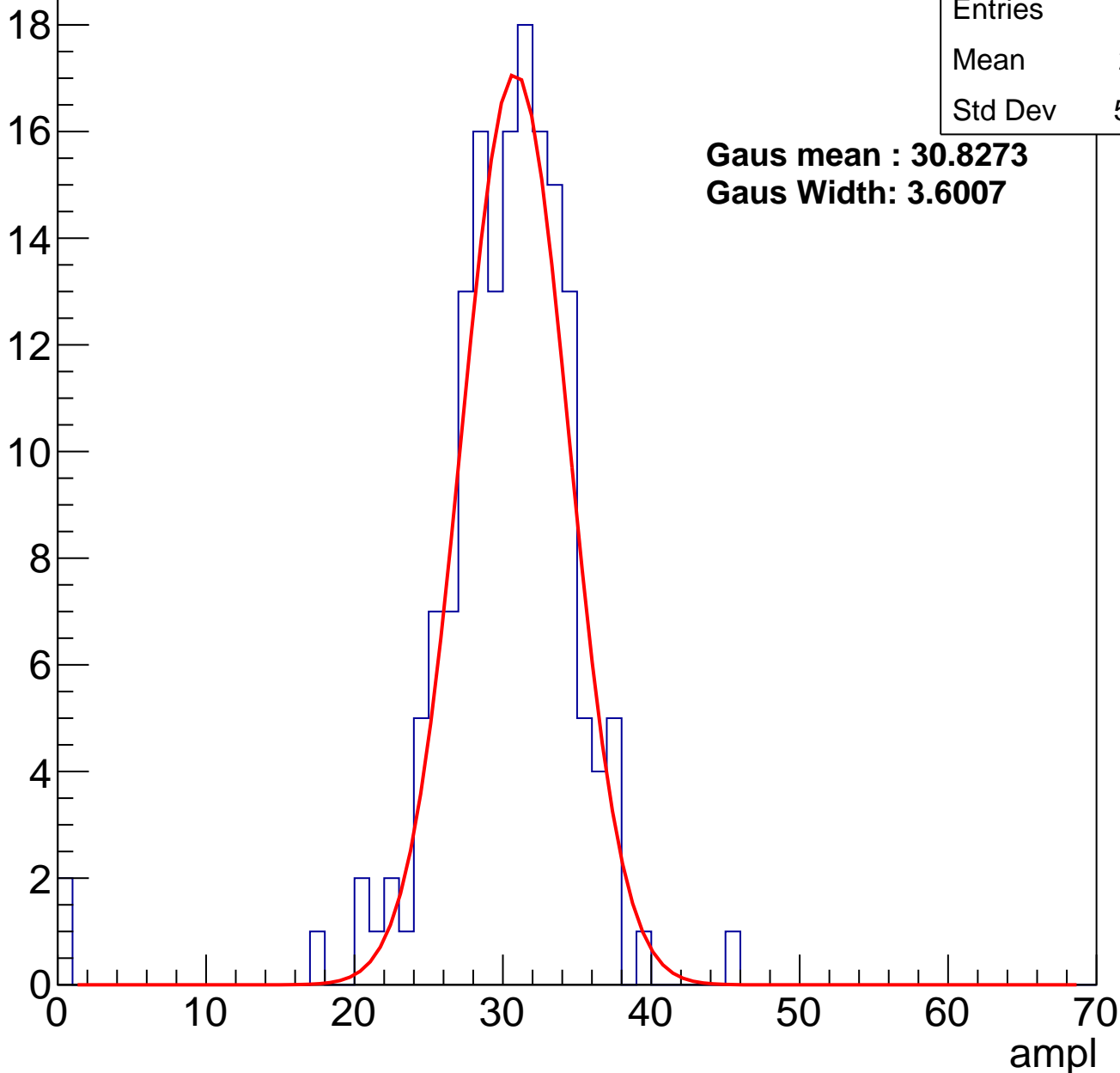
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	29.71
Std Dev	5.132

**Gaus mean : 30.8273**

**Gaus Width: 3.6007**

Entry



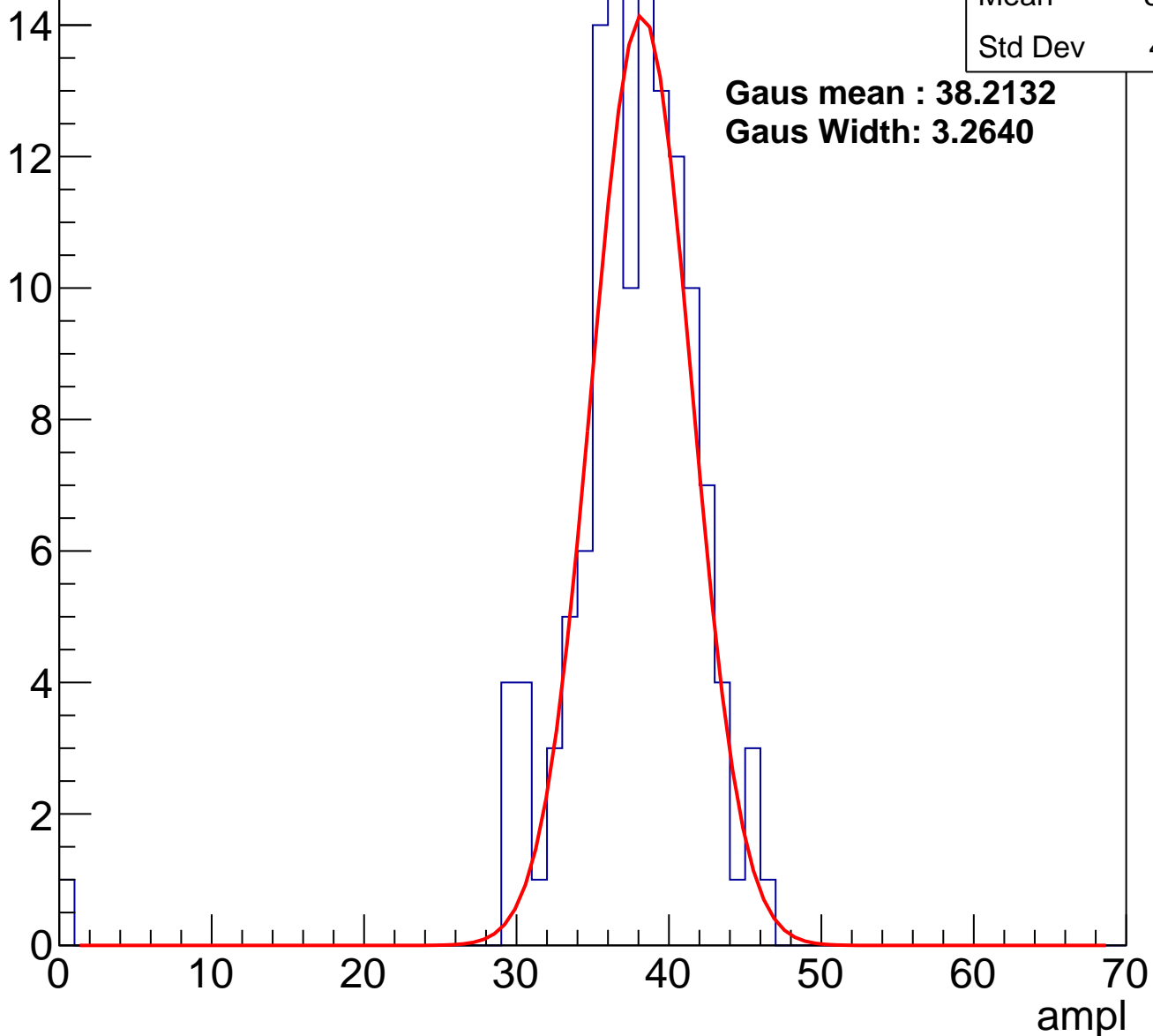
# B0L101S, U26-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	37.13
Std Dev	4.911

**Gaus mean : 38.2132**  
**Gaus Width: 3.2640**

Entry



# B0L101S, U26-ch16, adc2

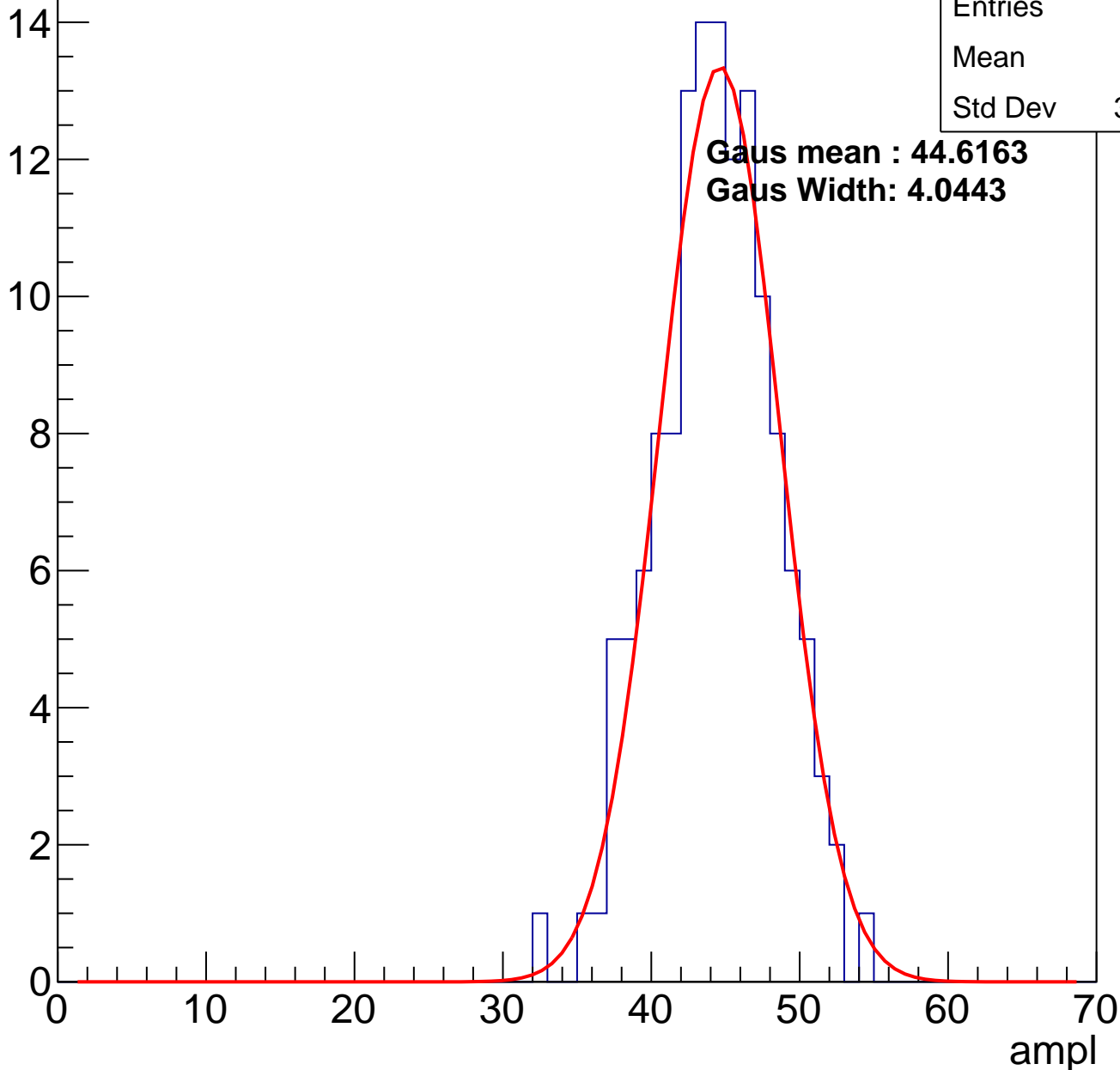
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	43.9
Std Dev	3.944

**Gaus mean : 44.6163**

**Gaus Width: 4.0443**

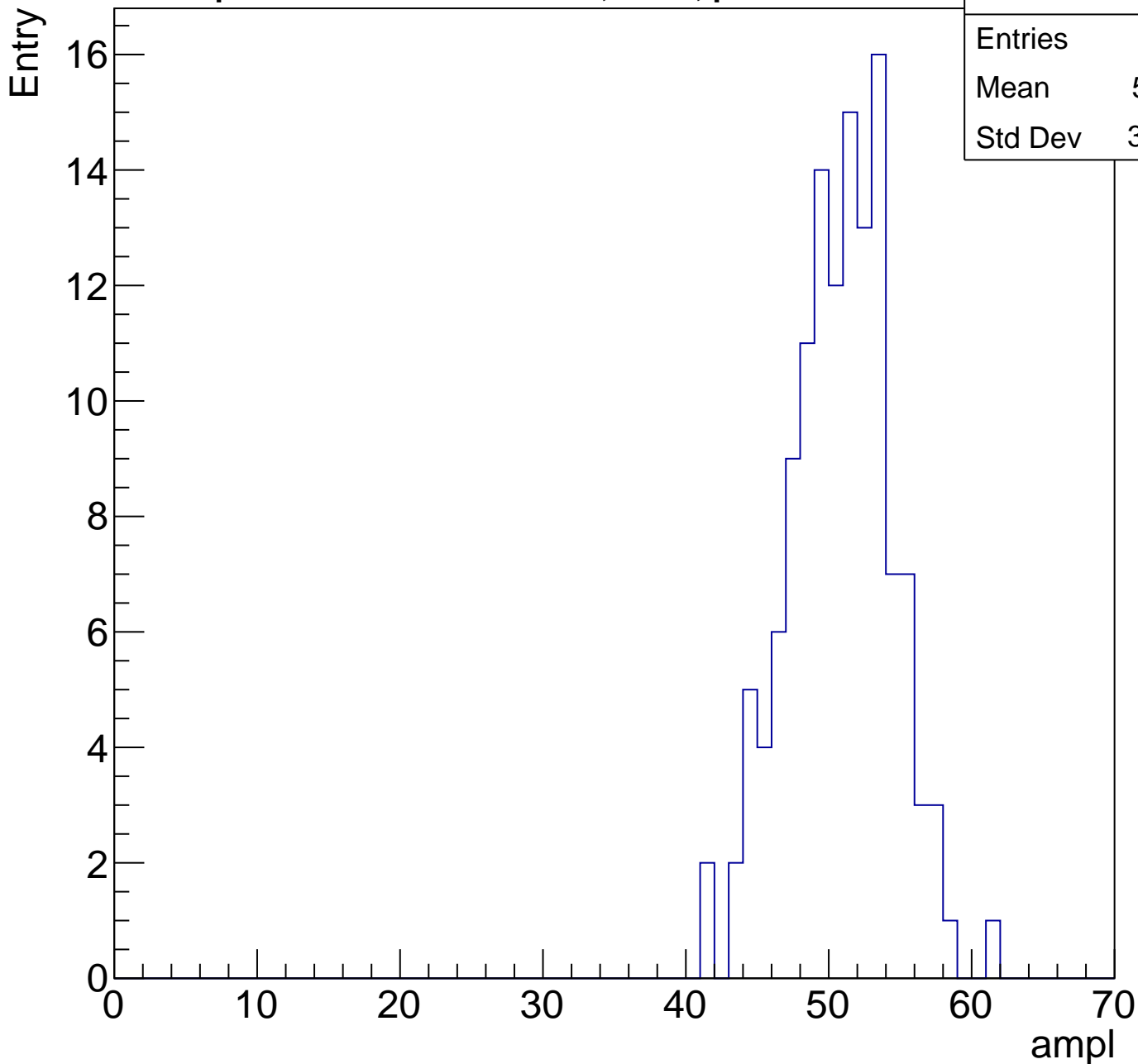
Entry



# B0L101S, U26-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	50.31
Std Dev	3.603



# B0L101S, U26-ch16, adc4

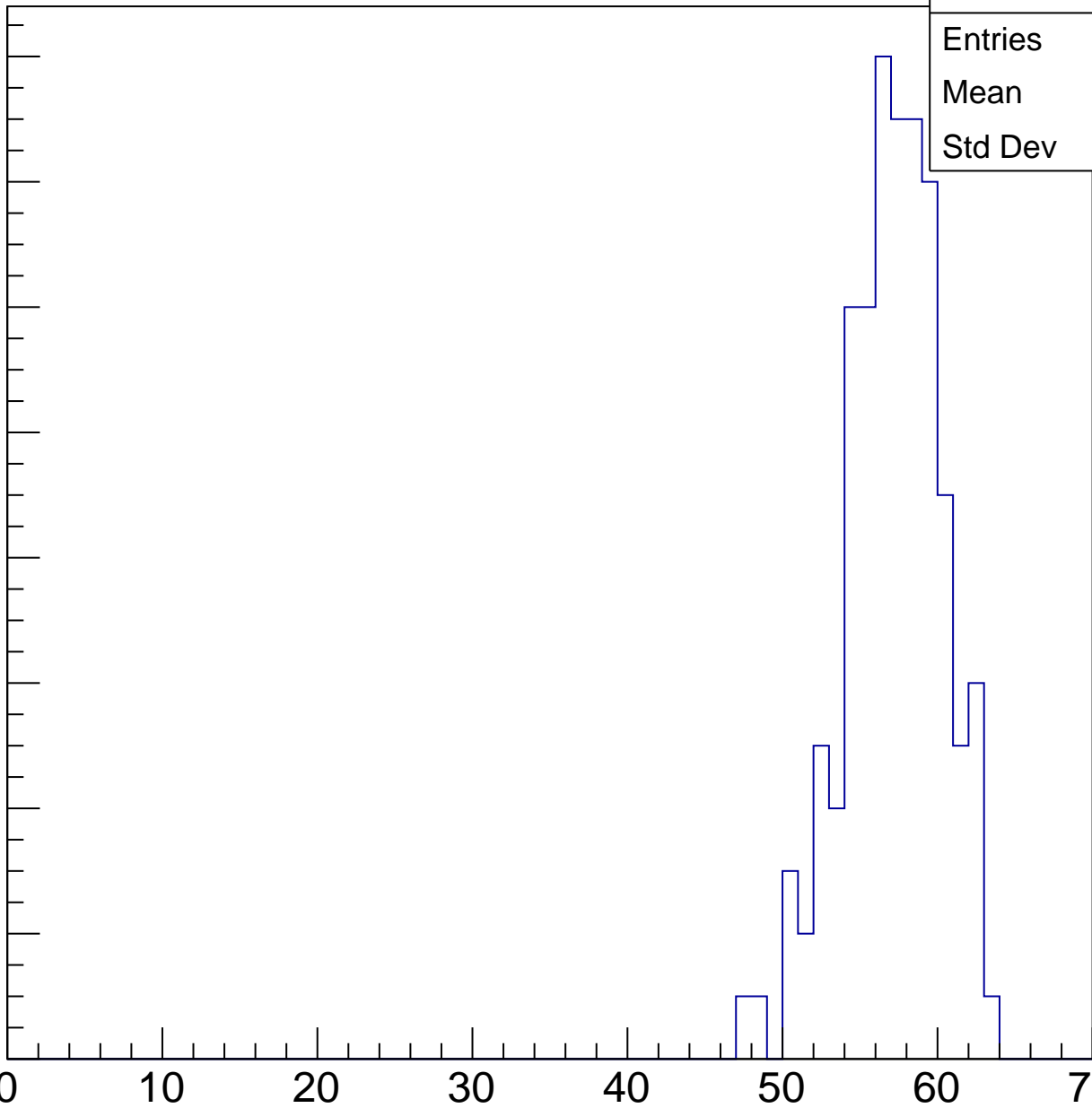
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	121
Mean	56.64
Std Dev	3.101

ampl



# B0L101S, U26-ch16, adc5

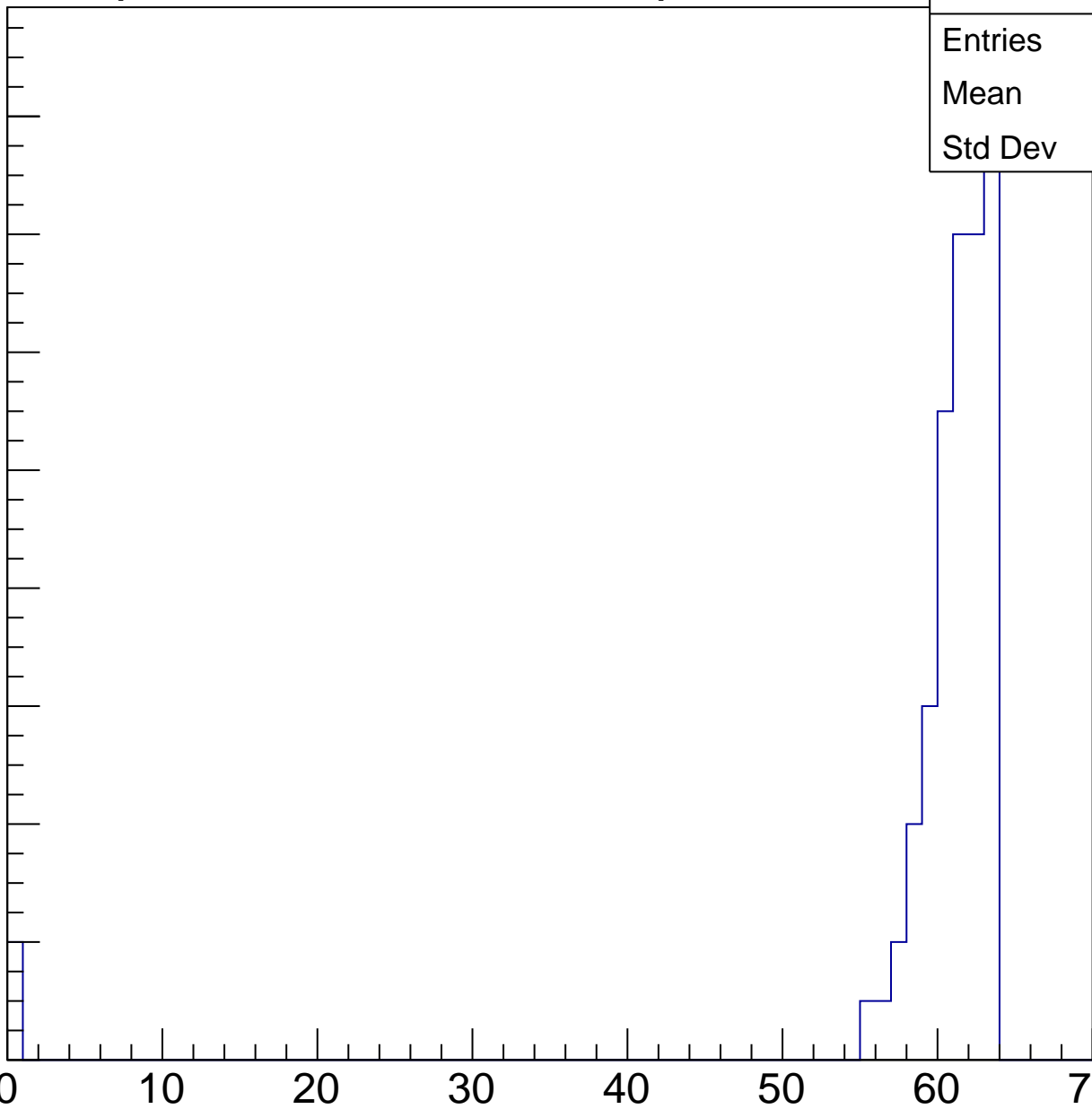
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	59.22
Std Dev	10.18

ampl



# B0L101S, U26-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch17, adc0

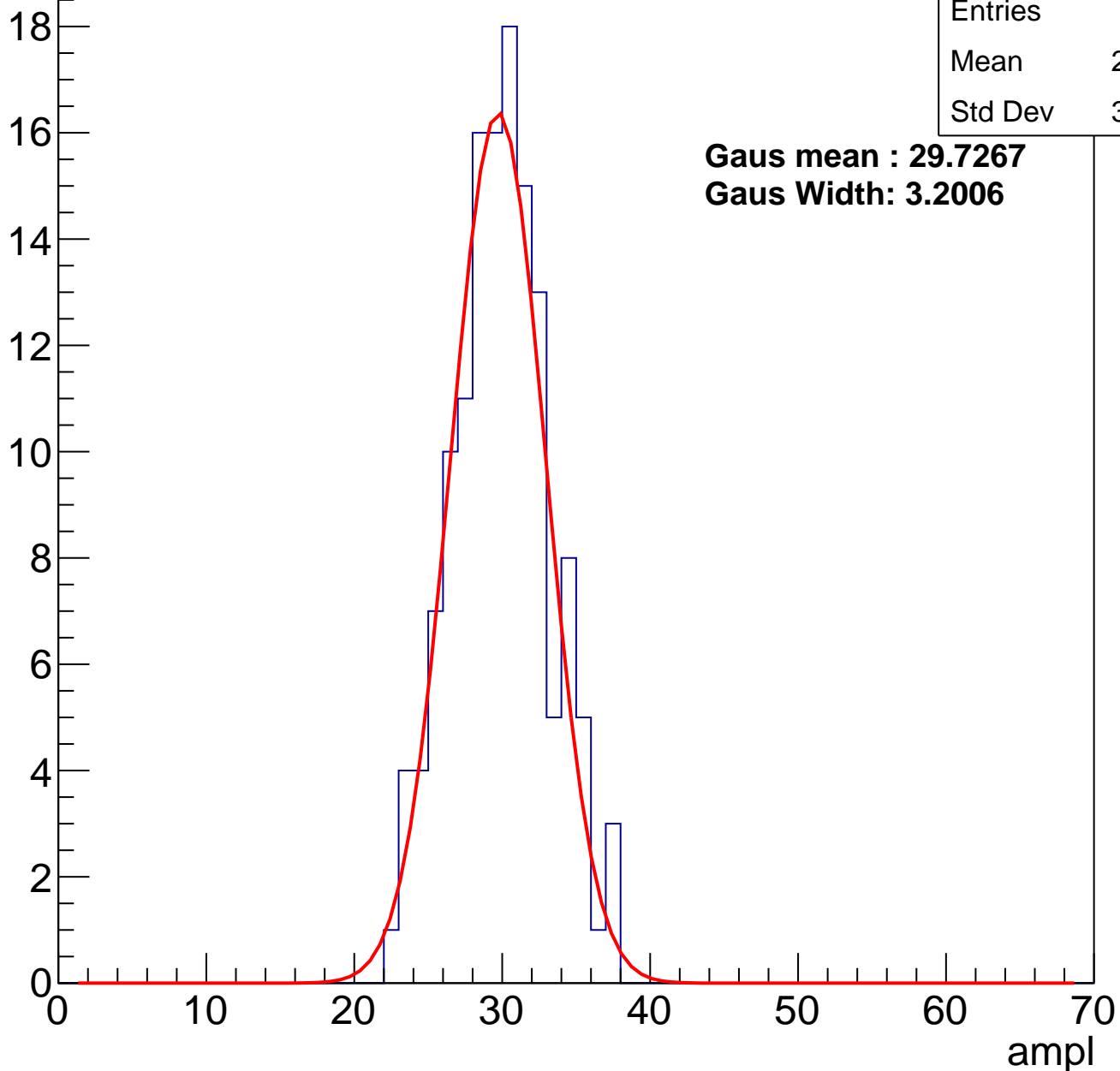
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	29.45
Std Dev	3.224

**Gaus mean : 29.7267**

**Gaus Width: 3.2006**

Entry



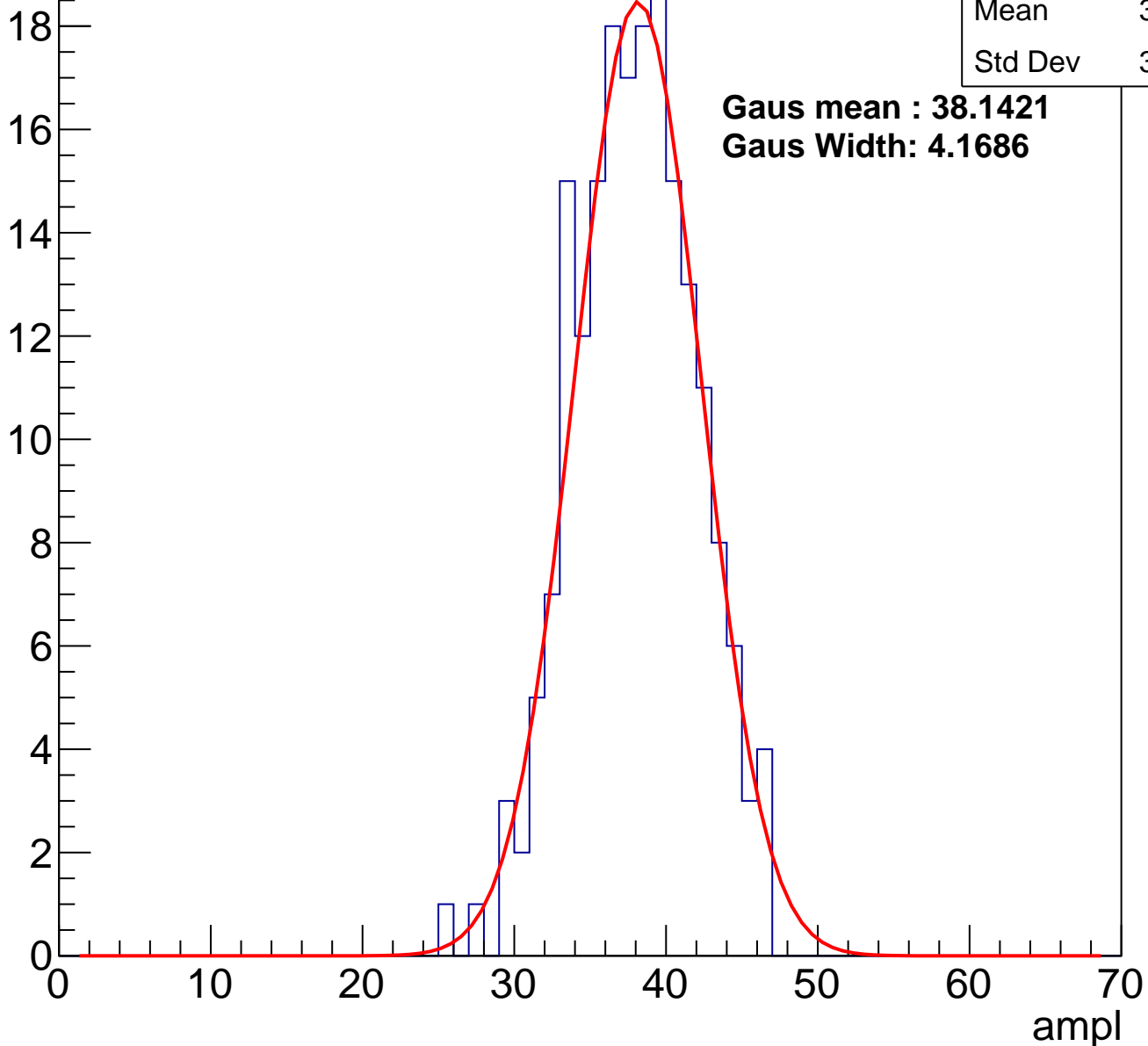
# B0L101S, U26-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	193
Mean	37.46
Std Dev	3.997

**Gaus mean : 38.1421**  
**Gaus Width: 4.1686**

Entry



# B0L101S, U26-ch17, adc2

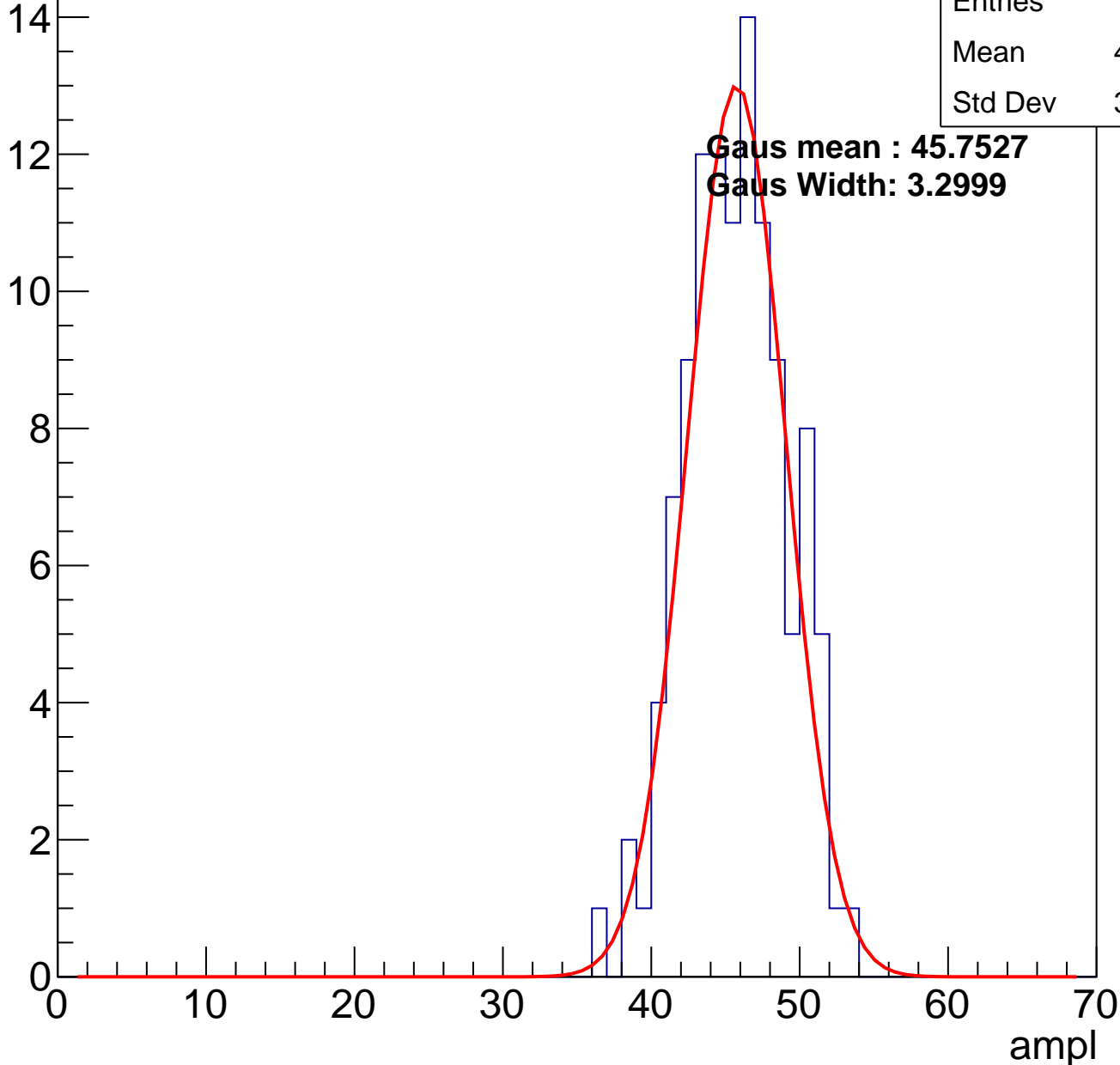
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	45.25
Std Dev	3.356

**Gaus mean : 45.7527**

**Gaus Width: 3.2999**

Entry



# B0L101S, U26-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

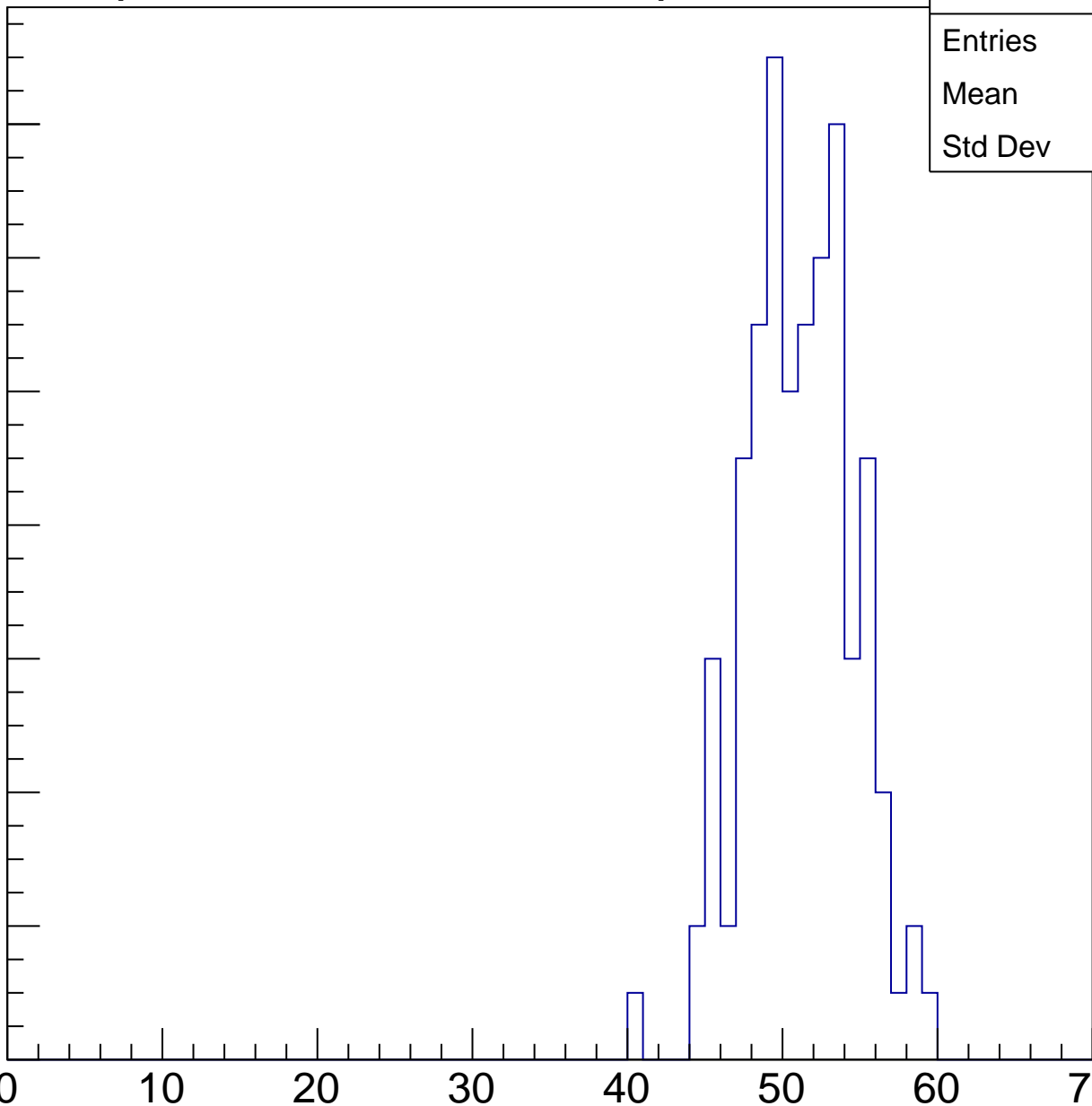
Entries	116
Mean	50.67
Std Dev	3.411

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

118

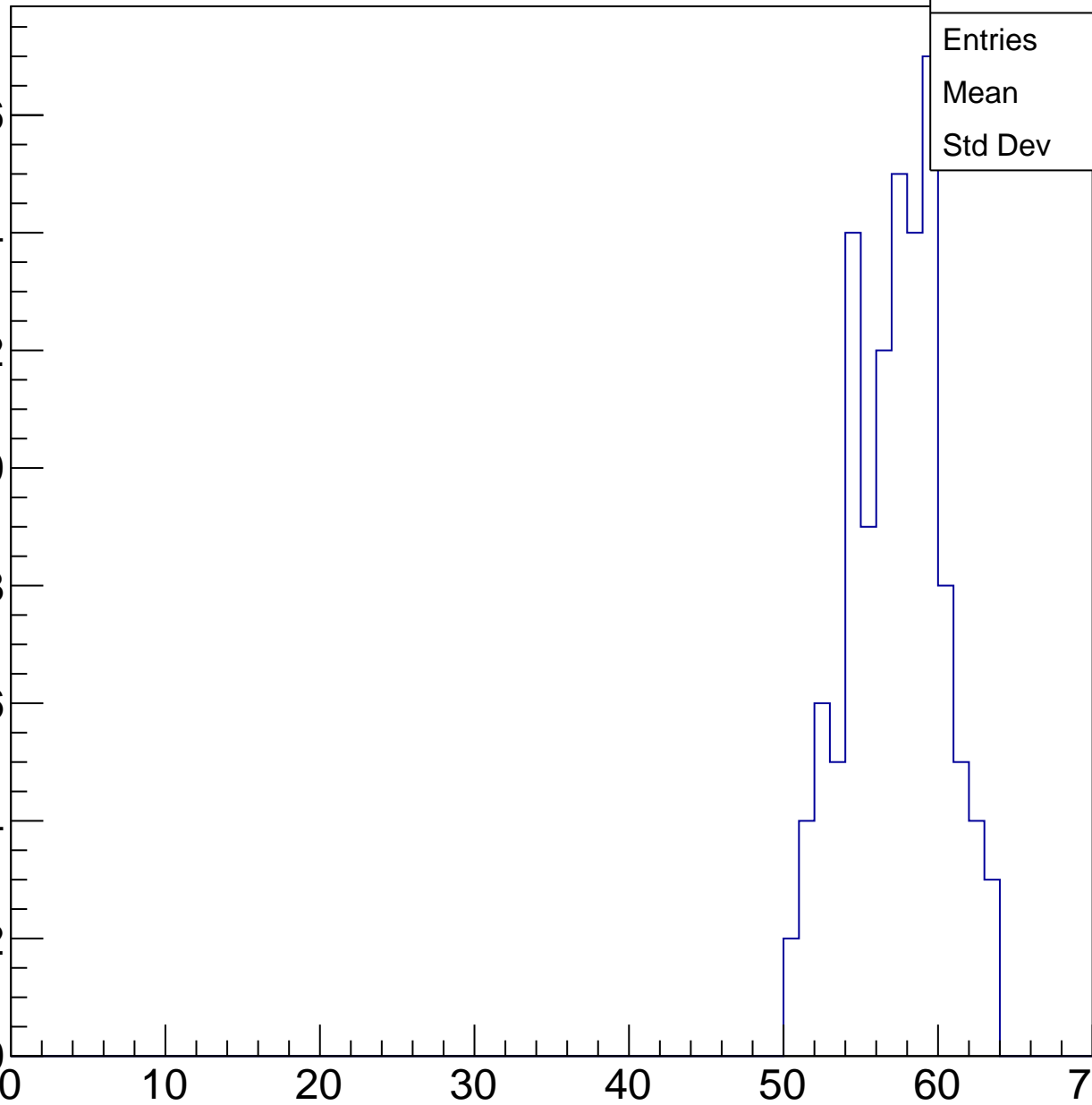
Mean

56.75

Std Dev

3.026

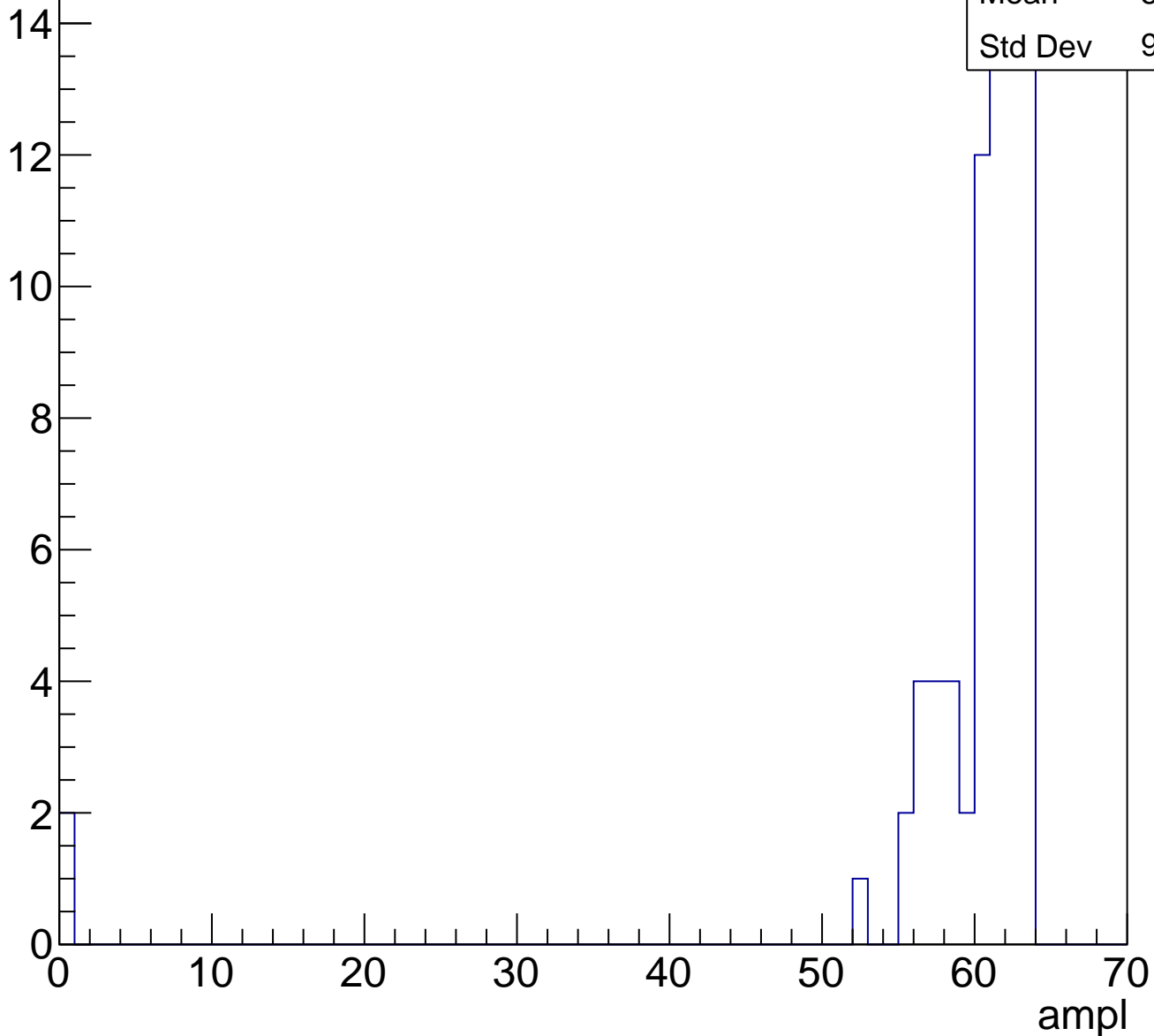
ampl



# B0L101S, U26-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L101S, U26-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	30.41
Std Dev	4.416

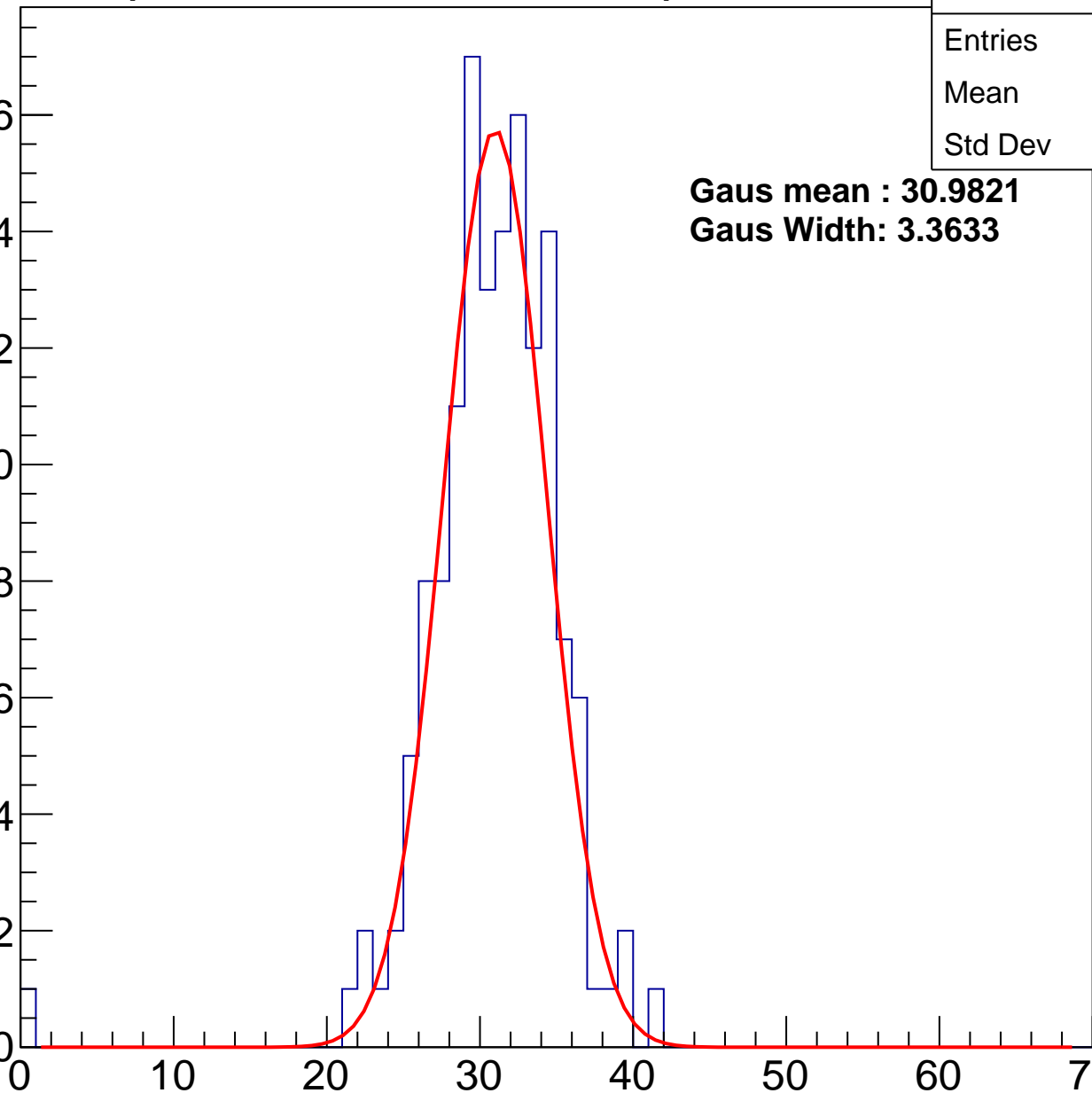
**Gaus mean : 30.9821**

**Gaus Width: 3.3633**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch18, adc1

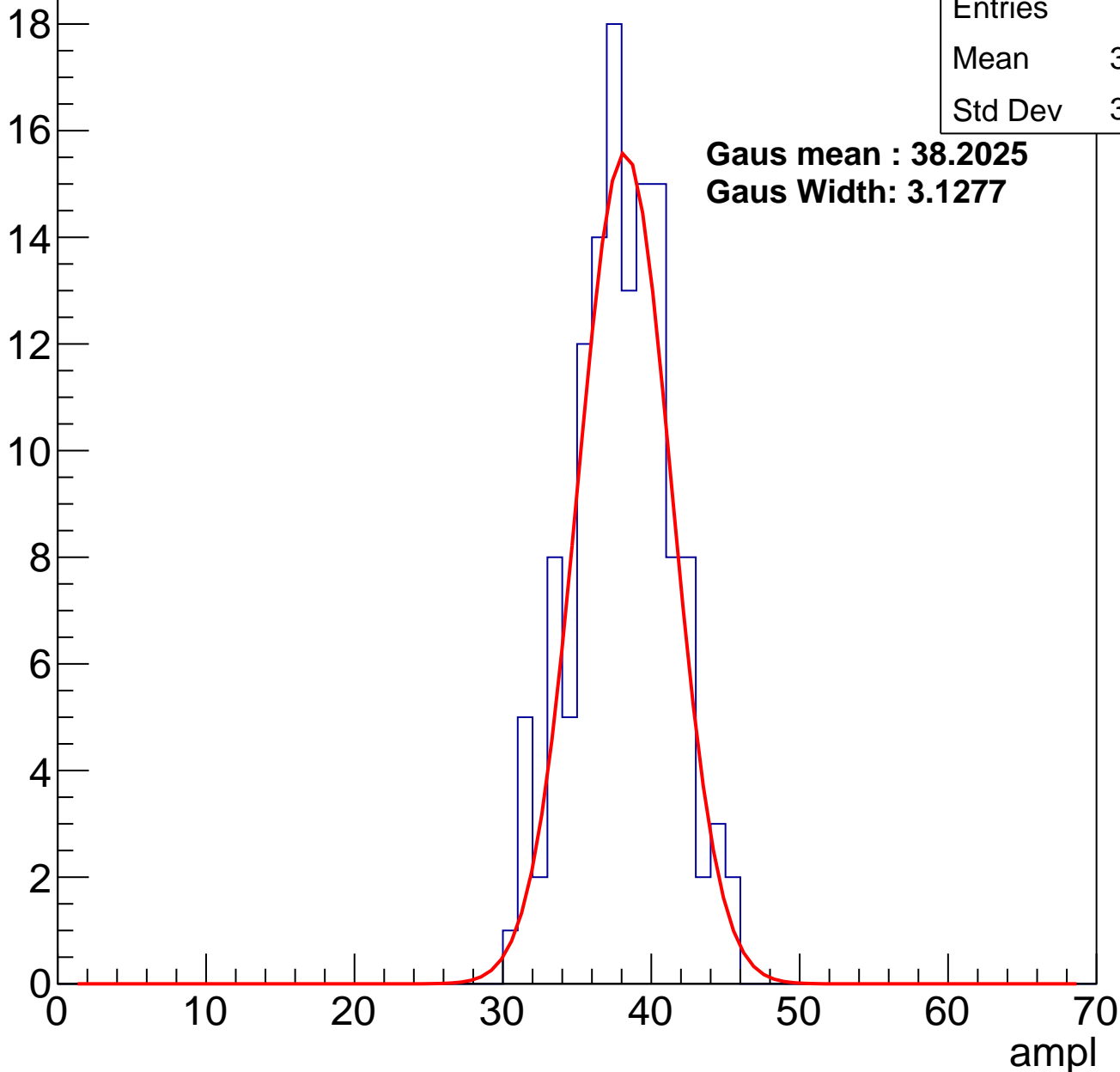
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	37.59
Std Dev	3.212

**Gaus mean : 38.2025**

**Gaus Width: 3.1277**

Entry



# B0L101S, U26-ch18, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

148

Mean

44.66

Std Dev

3.672

**Gaus mean : 45.4658**

**Gaus Width: 3.7511**

0

10

20

30

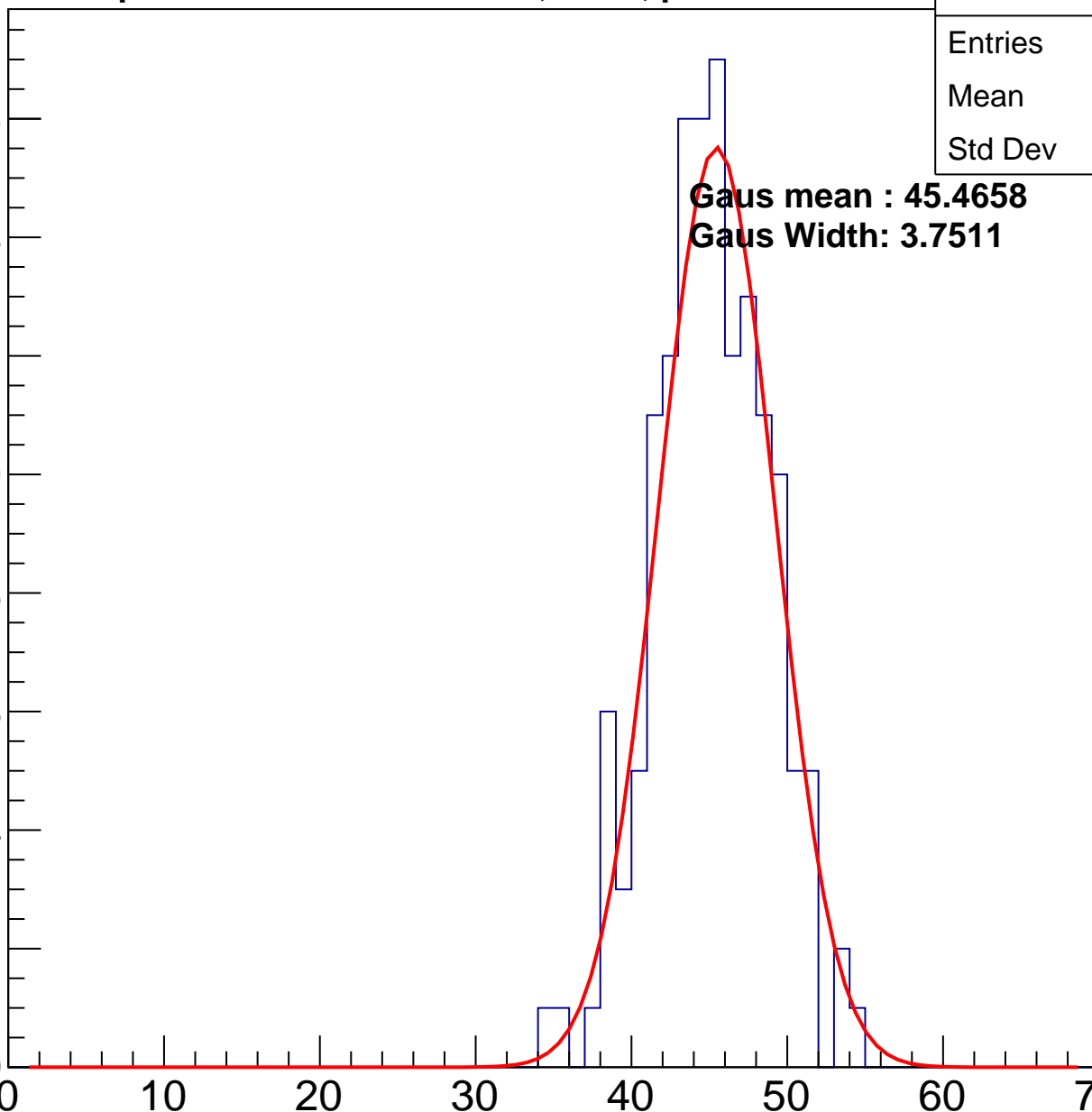
40

50

60

70

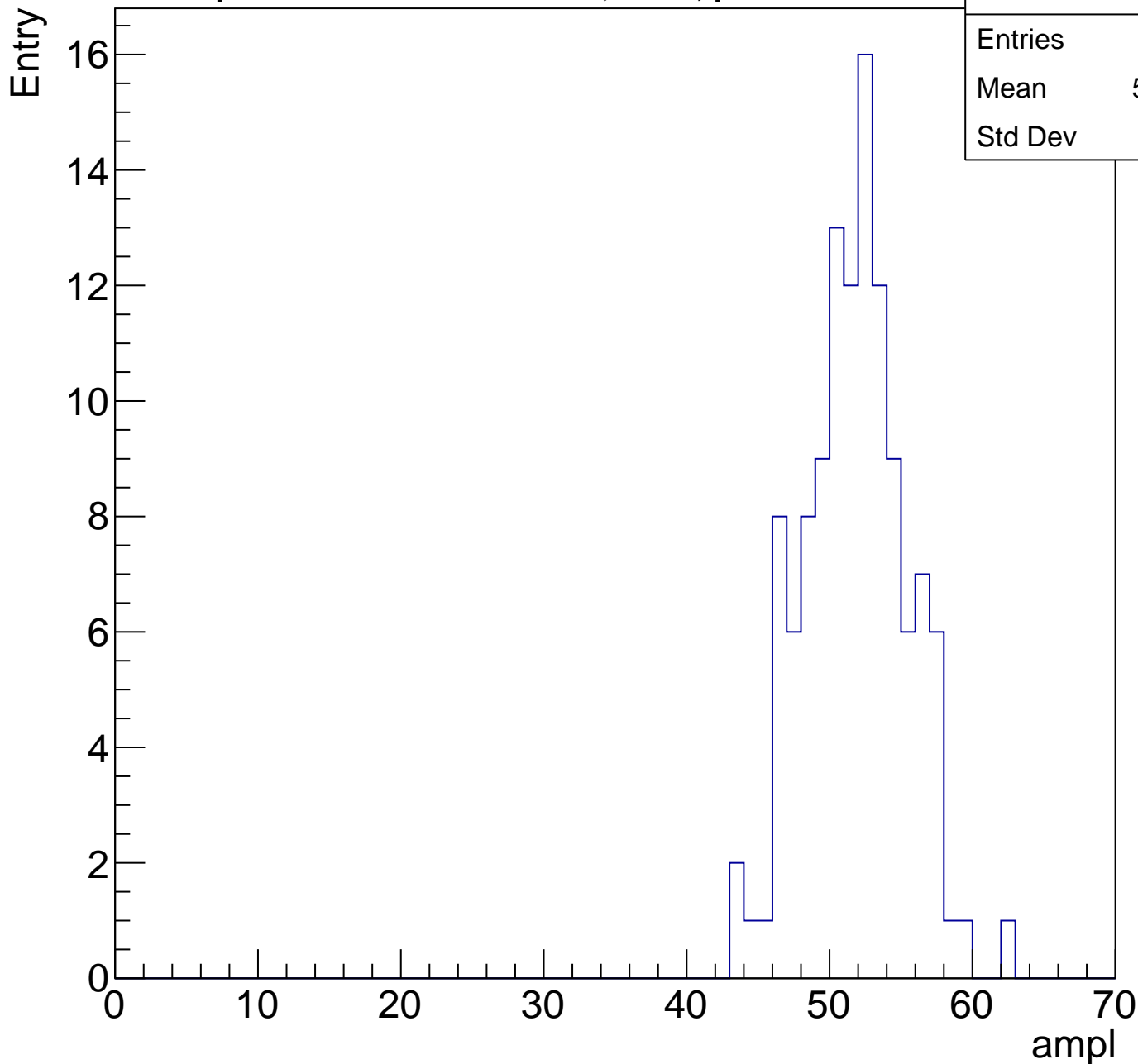
ampl



# B0L101S, U26-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	51.34
Std Dev	3.53



# B0L101S, U26-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	98
Mean	56.29
Std Dev	3.317

Entry

12

10

8

6

4

2

0

0

10

20

30

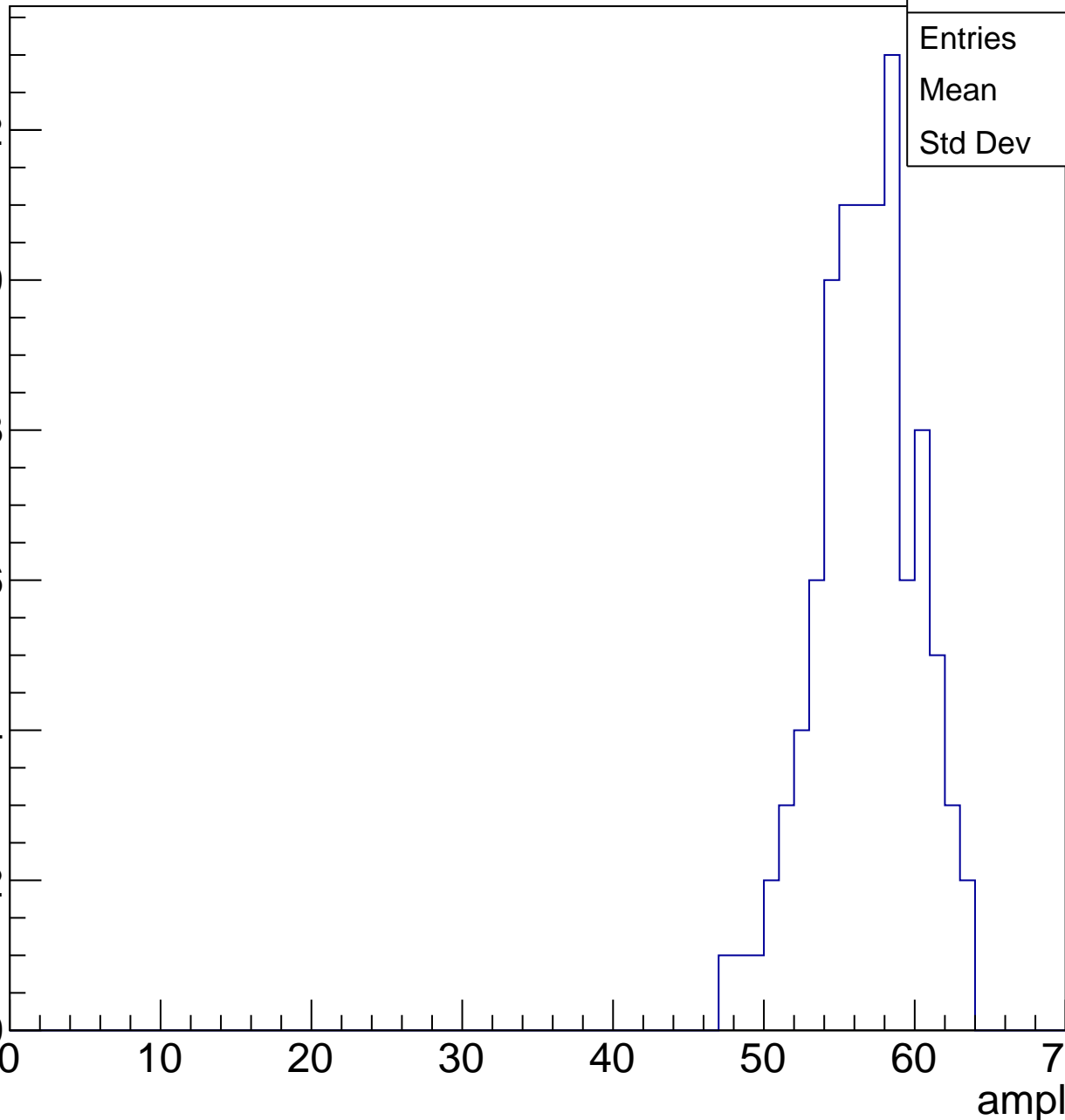
40

50

60

ampl

70



# B0L101S, U26-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	87
Mean	59.06
Std Dev	9.298

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

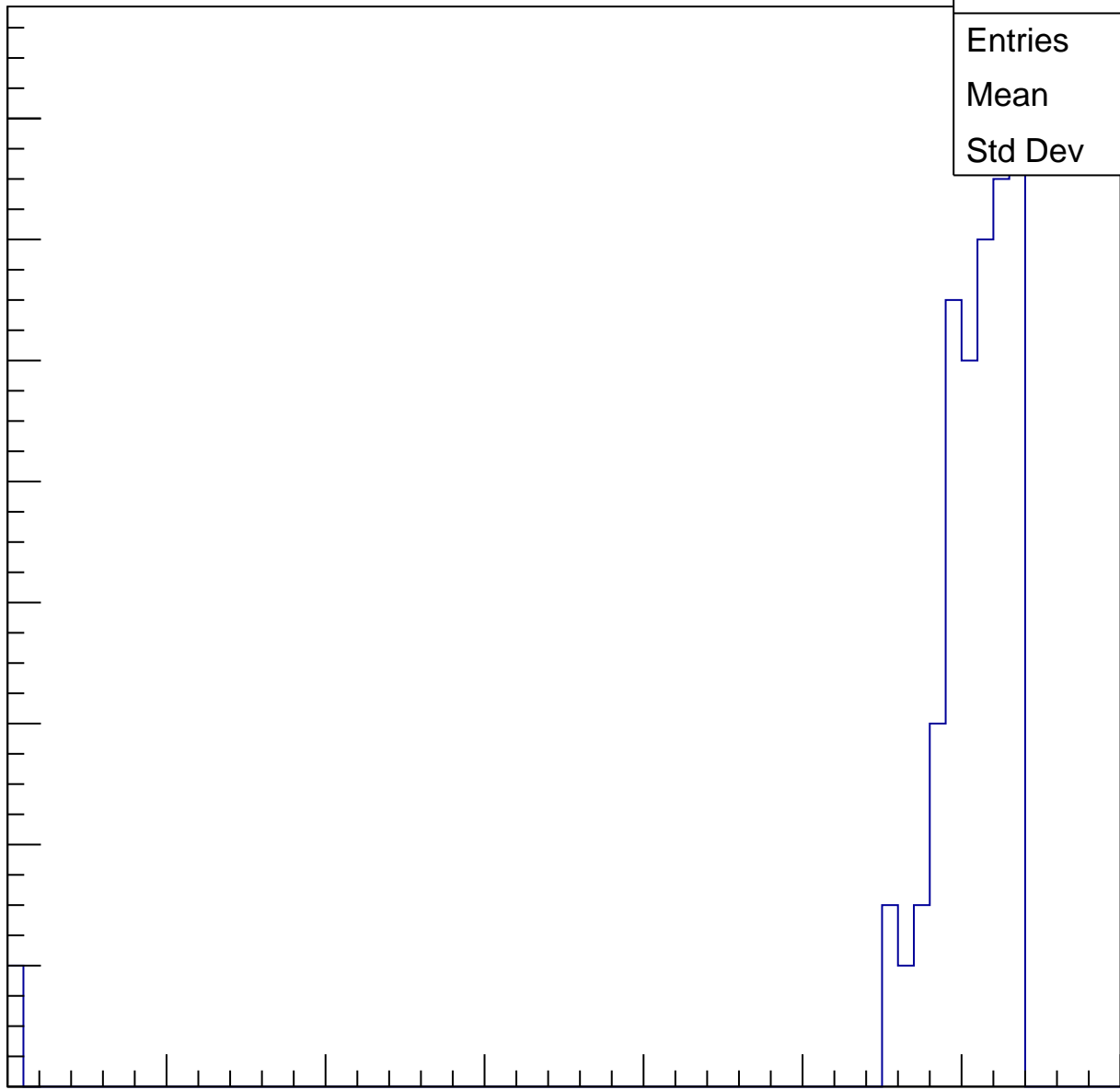
40

50

60

70

ampl



# B0L101S, U26-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	30.01
Std Dev	3.674

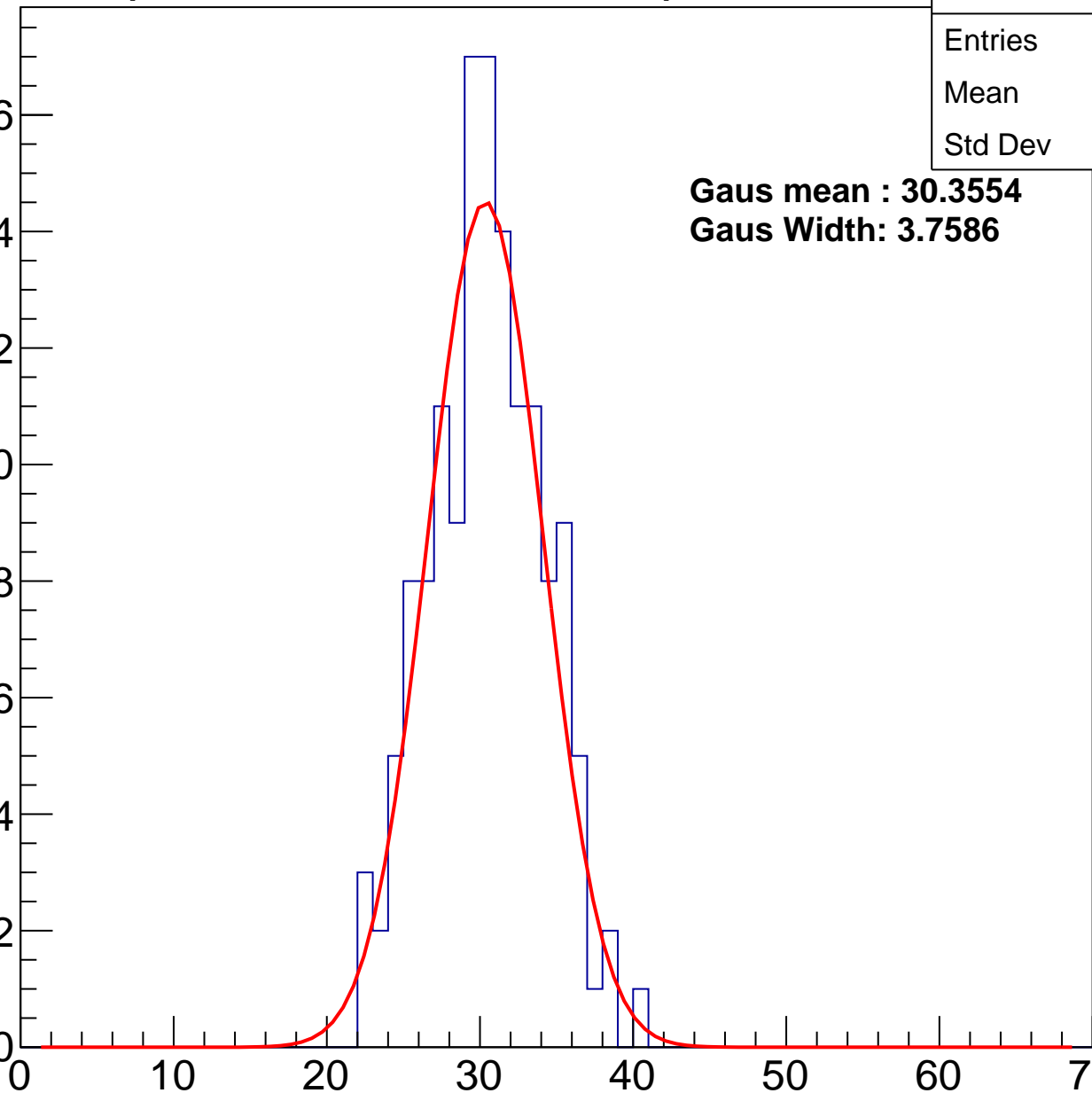
**Gaus mean : 30.3554**

**Gaus Width: 3.7586**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch19, adc1

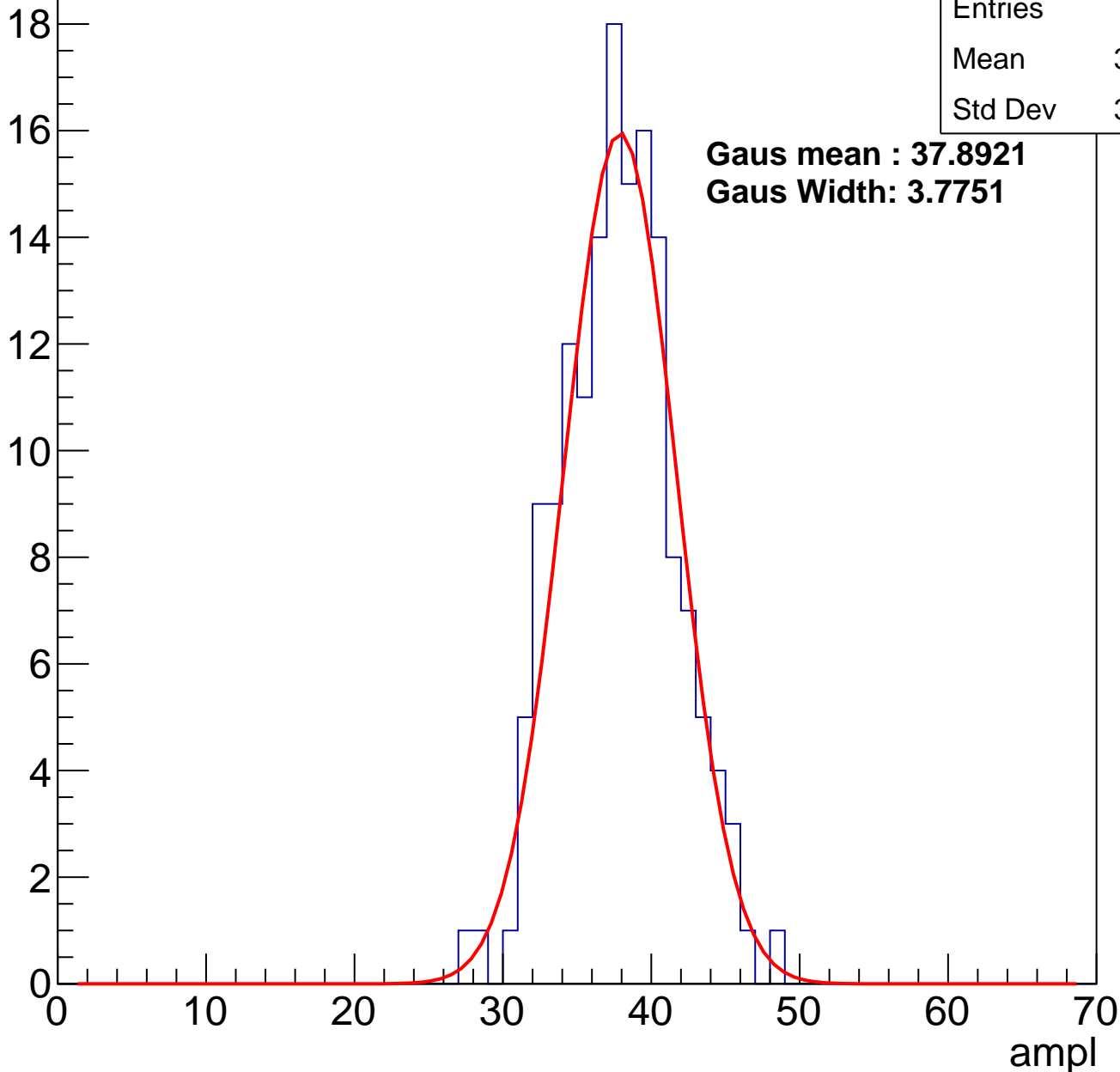
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	37.32
Std Dev	3.774

**Gaus mean : 37.8921**

**Gaus Width: 3.7751**

Entry



# B0L101S, U26-ch19, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

128

Mean

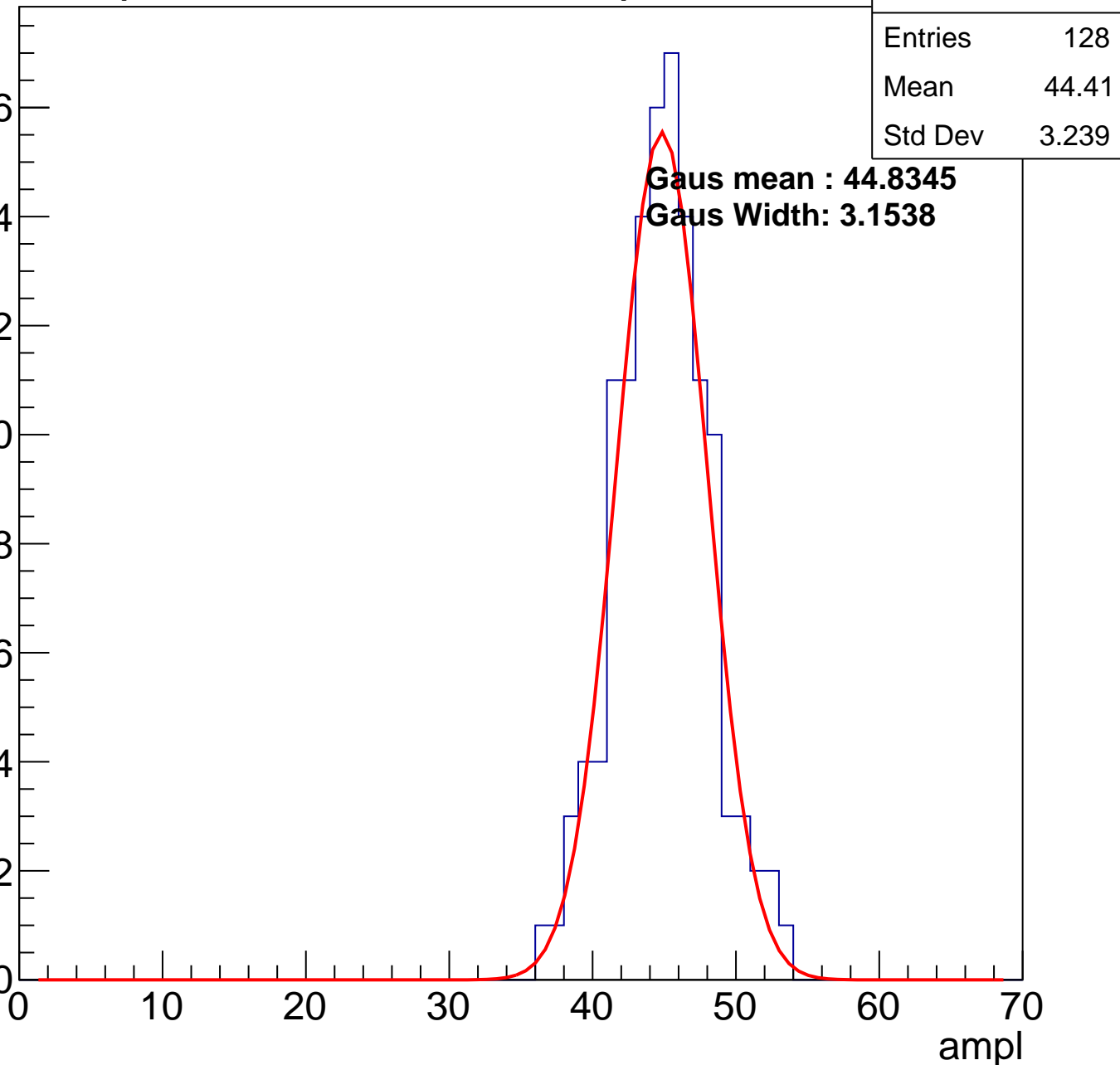
44.41

Std Dev

3.239

**Gaus mean : 44.8345**

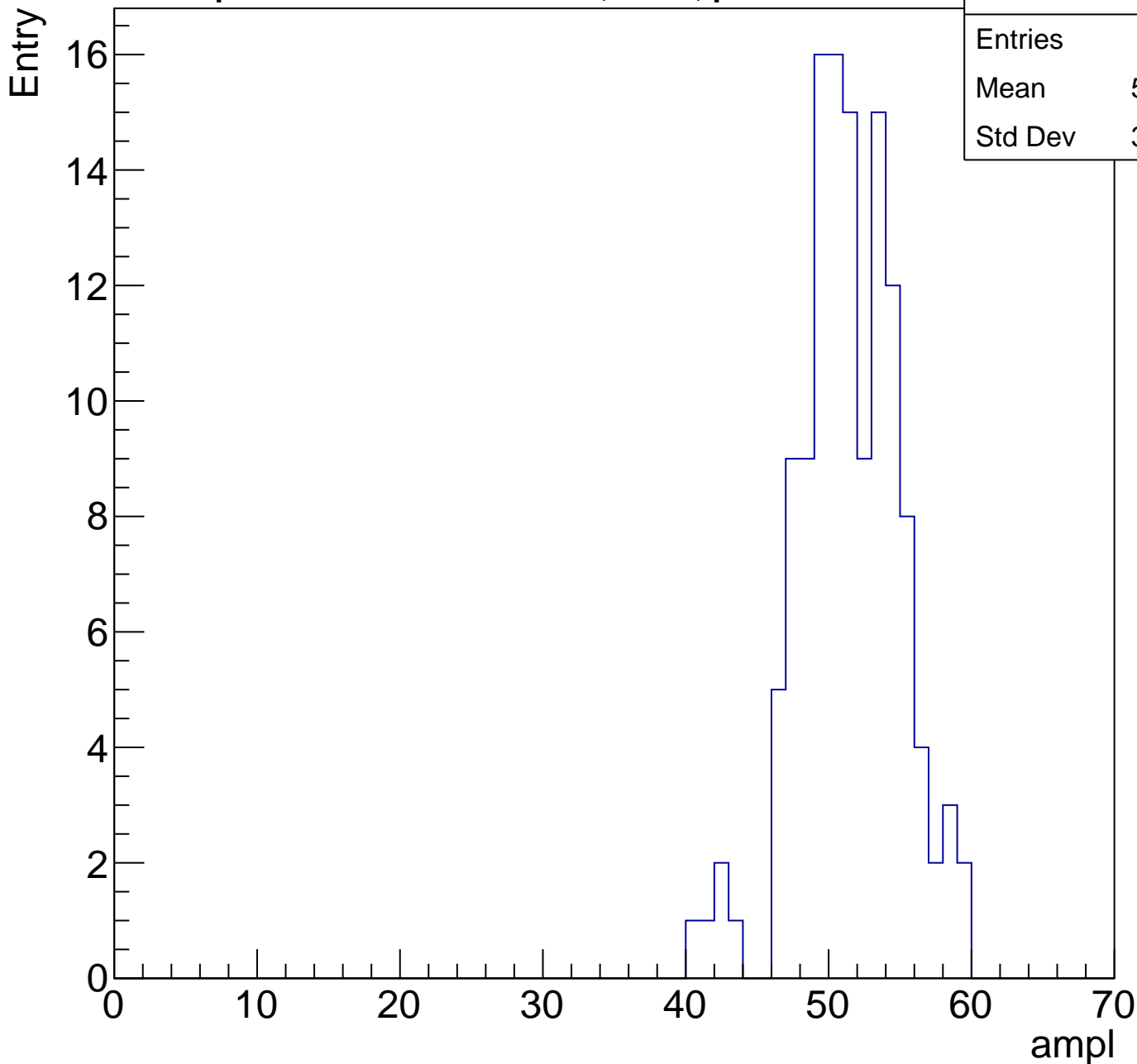
**Gaus Width: 3.1538**



# B0L101S, U26-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

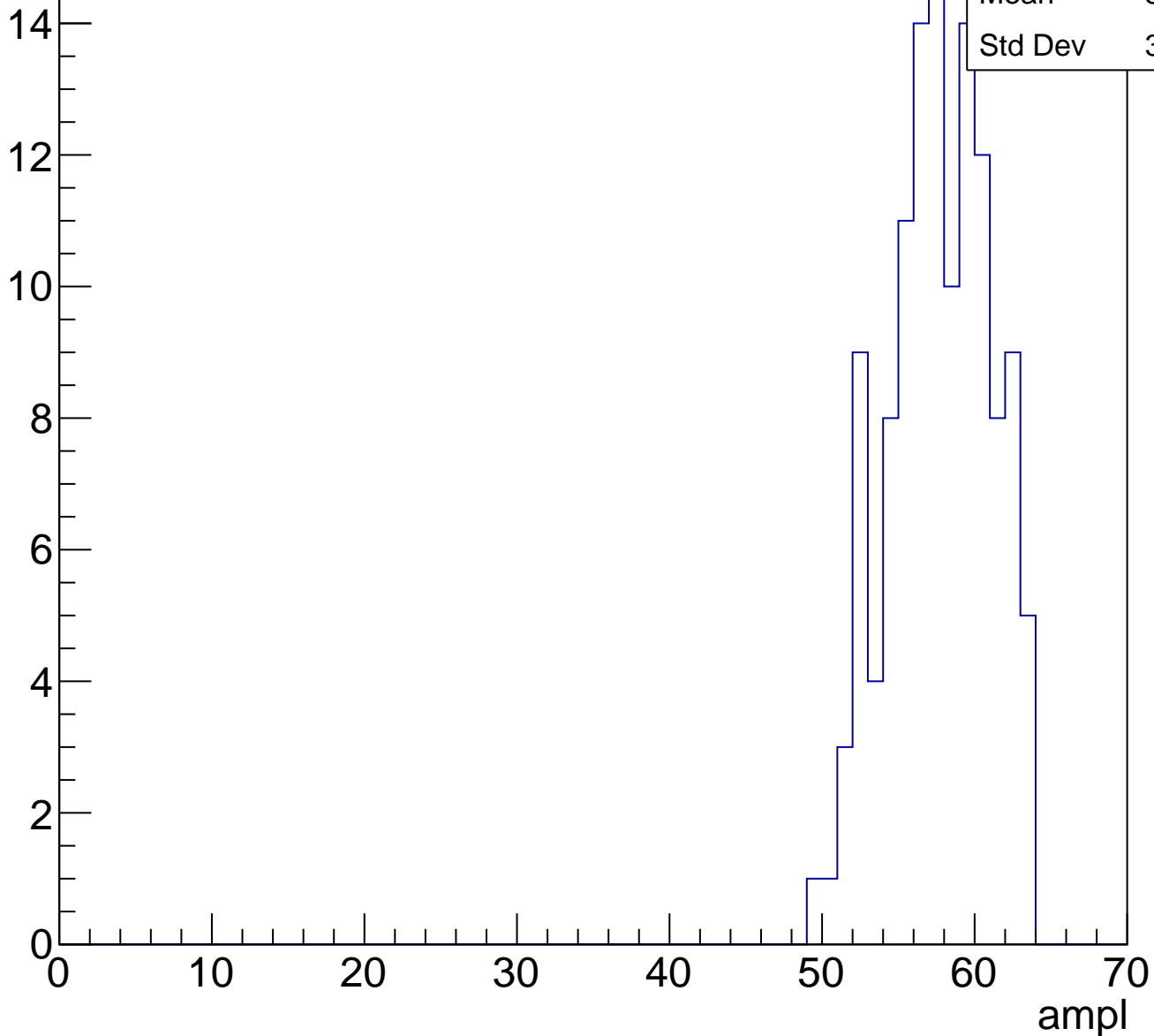
Entries	130
Mean	50.95
Std Dev	3.539



# B0L101S, U26-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	124
Mean	57.22
Std Dev	3.289

# B0L101S, U26-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	56
Mean	58.3
Std Dev	11.42

0

2

4

6

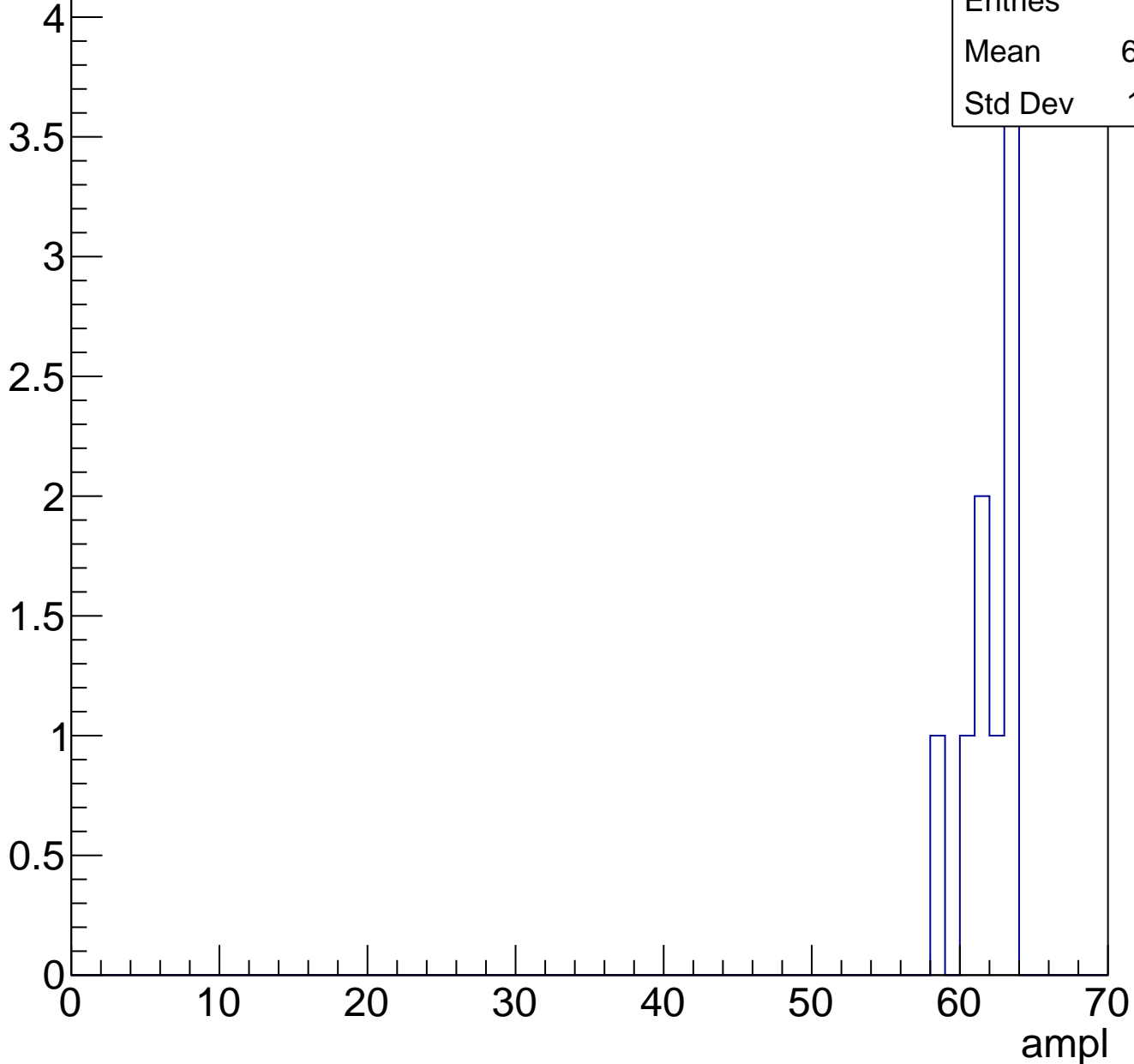
8

10

# B0L101S, U26-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	19
Std Dev	0

ampl

# B0L101S, U26-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	28.05
Std Dev	3.321

**Gaus mean : 28.5267**

**Gaus Width: 3.8231**

Entry

12

10

8

6

4

2

0

0

10

20

30

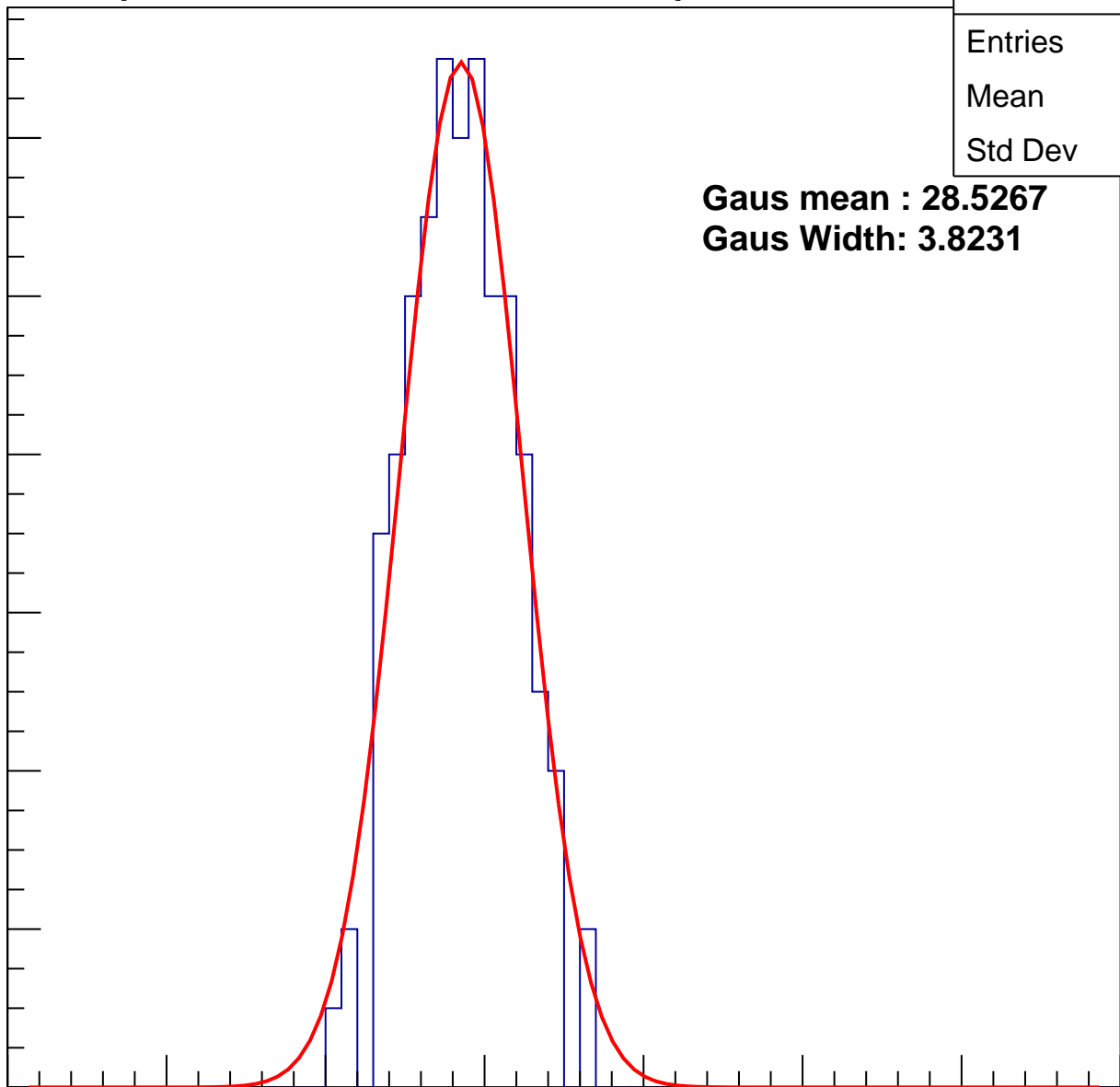
40

50

60

70

ampl



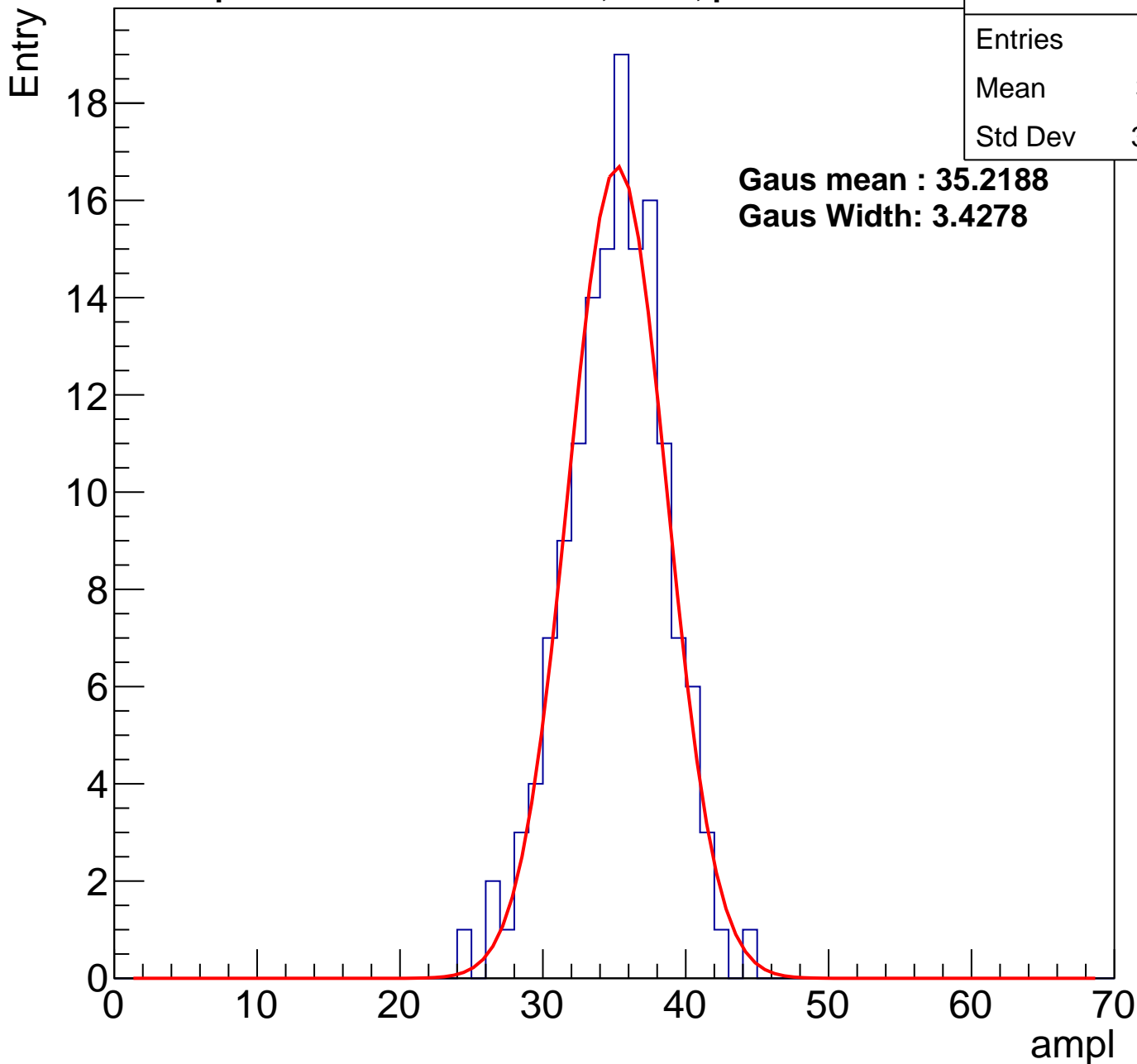
# B0L101S, U26-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	34.61
Std Dev	3.494

**Gaus mean : 35.2188**

**Gaus Width: 3.4278**



# B0L101S, U26-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	179
Mean	42.87
Std Dev	3.792

**Gaus mean : 43.5577**

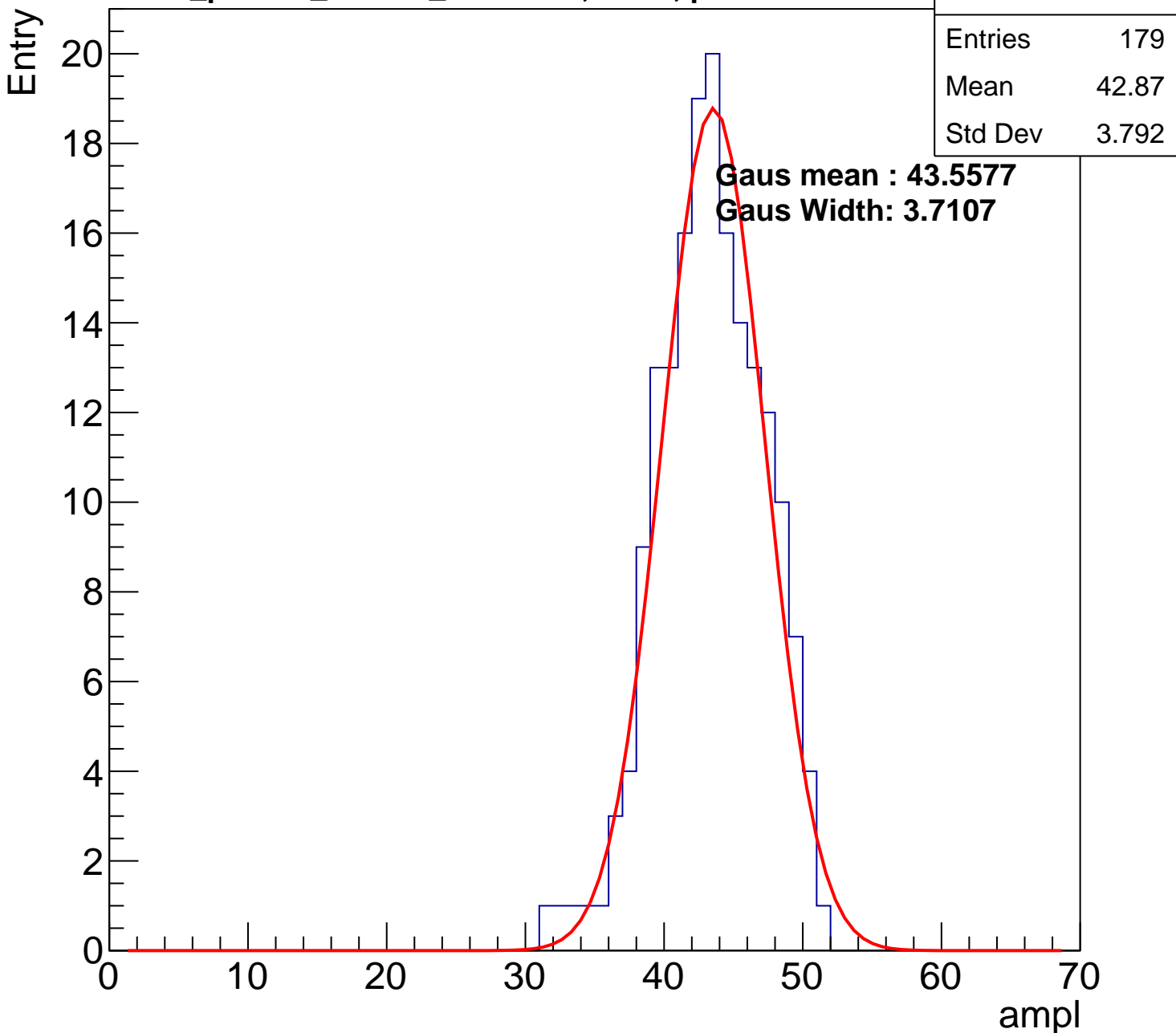
**Gaus Width: 3.7107**

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

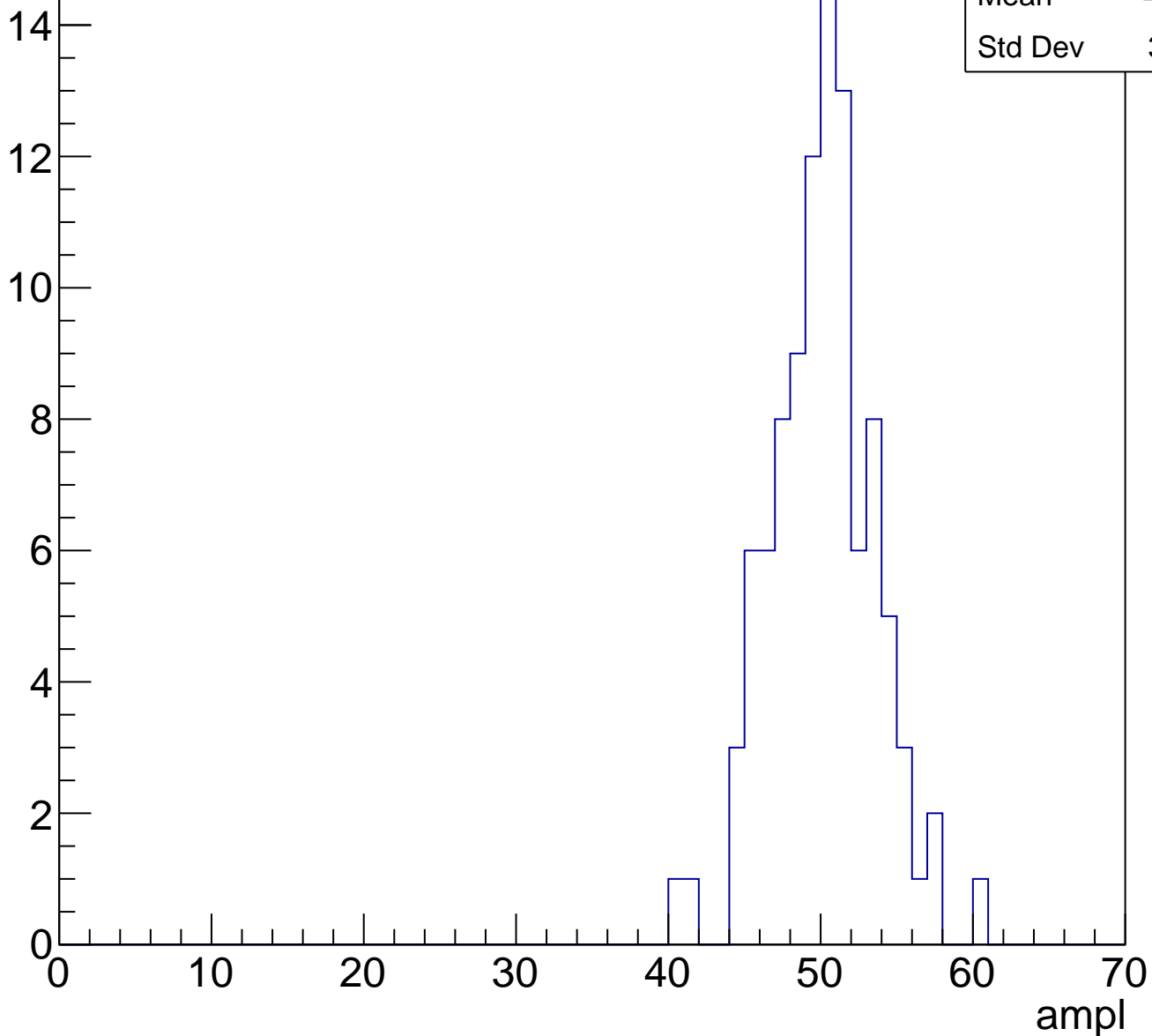


# B0L101S, U26-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	49.69
Std Dev	3.381

Entry



# B0L101S, U26-ch20, adc4

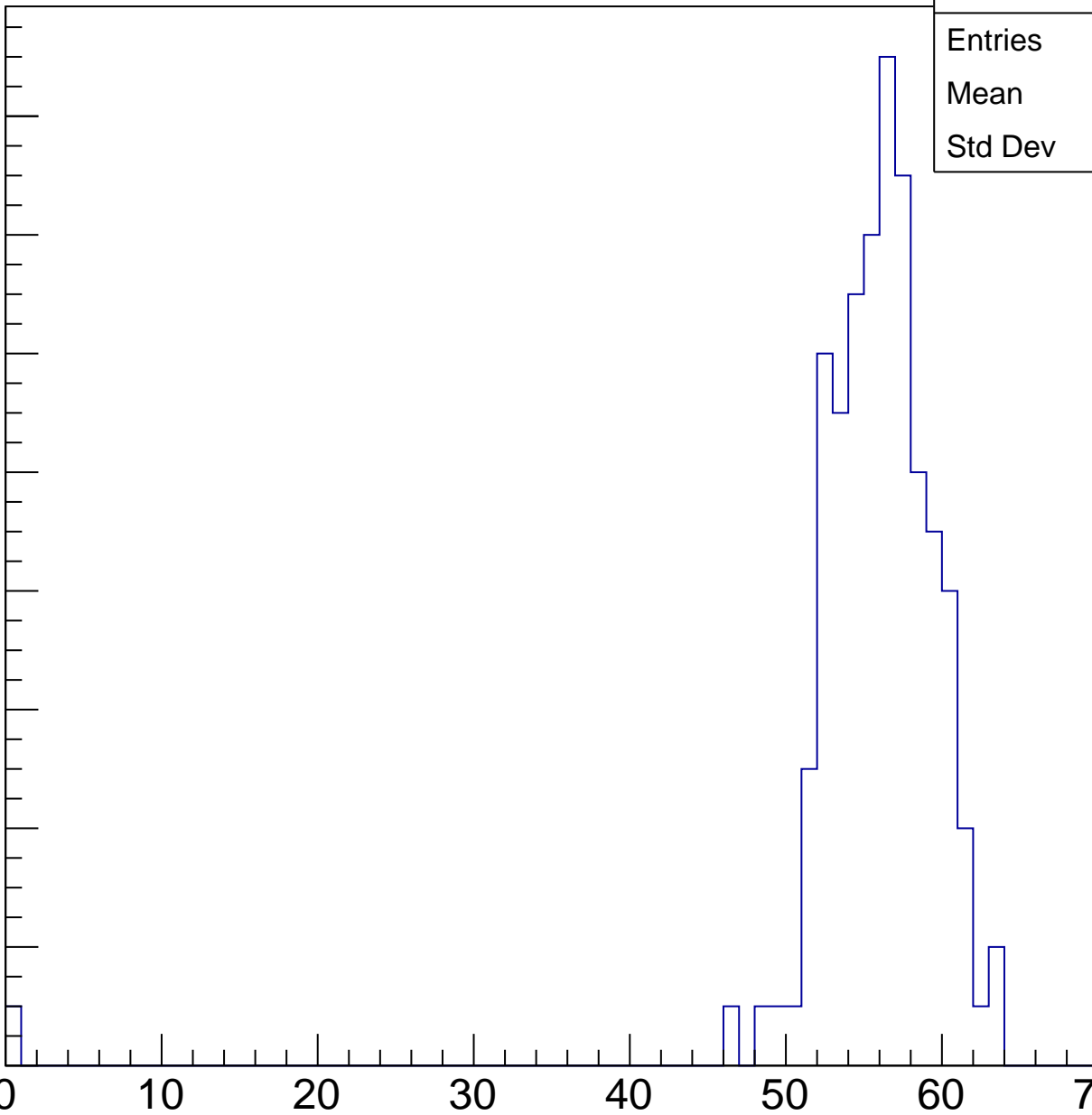
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	55.21
Std Dev	5.833

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch20, adc5

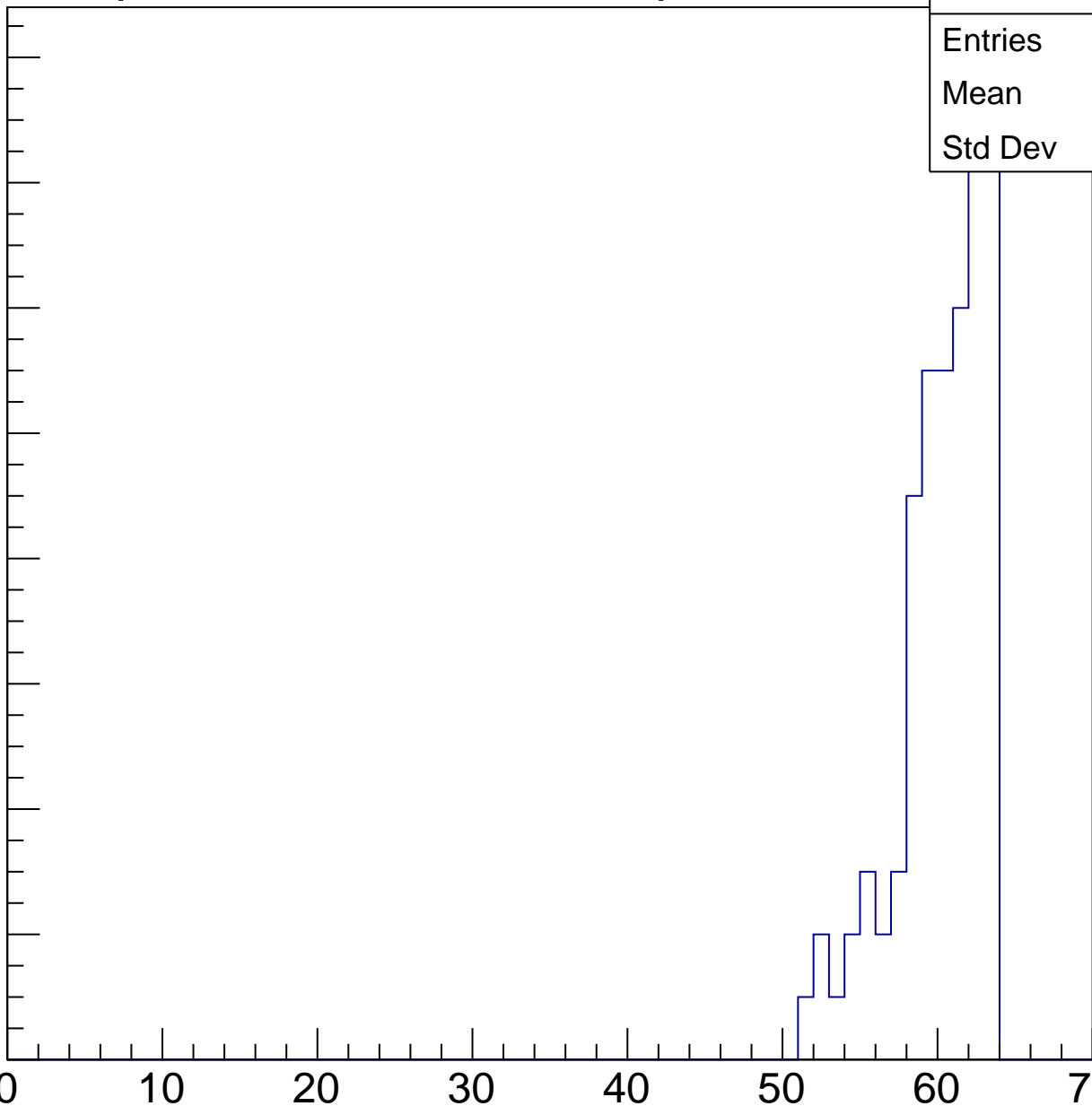
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	59.82
Std Dev	2.859

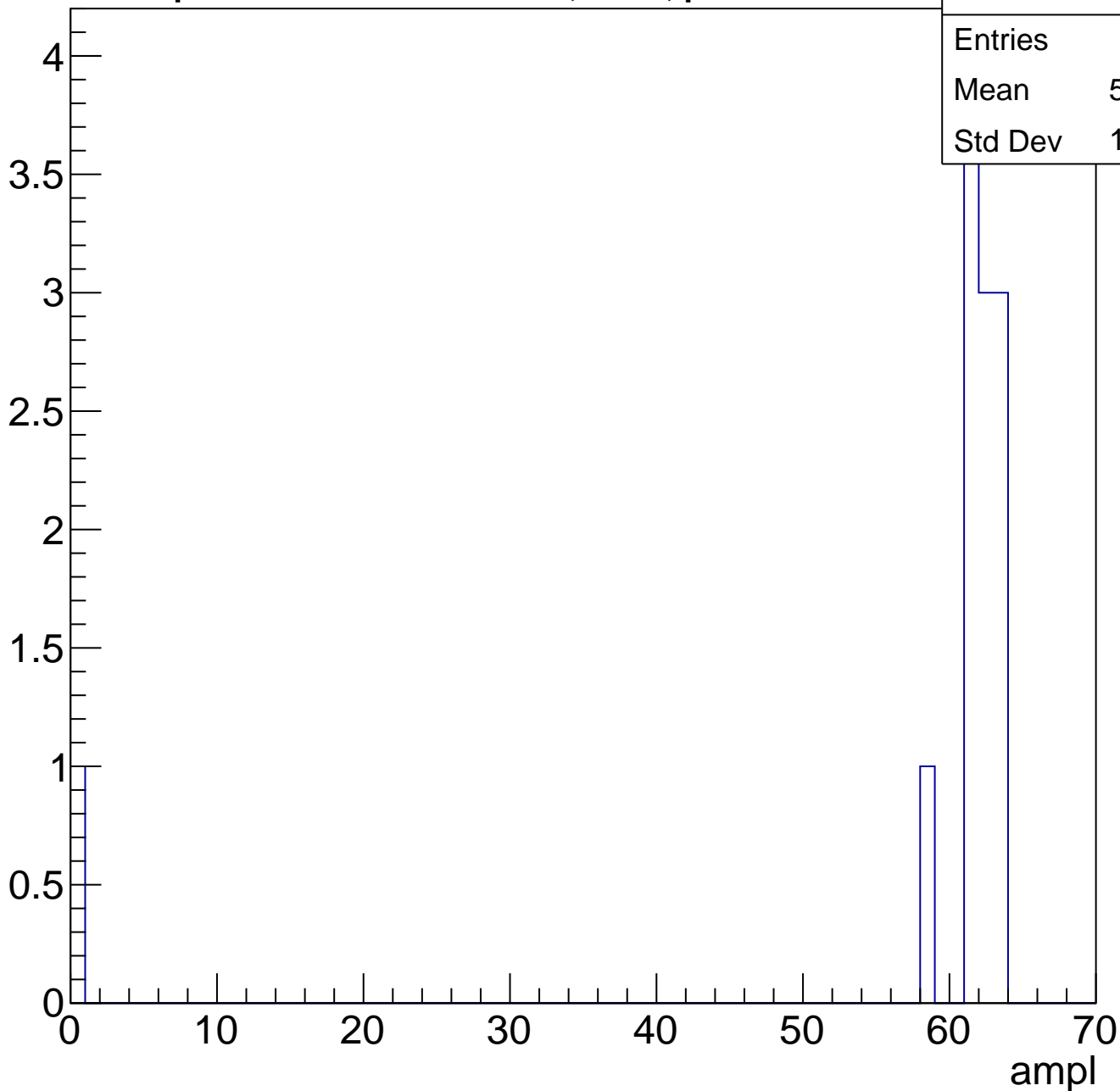
ampl



# B0L101S, U26-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

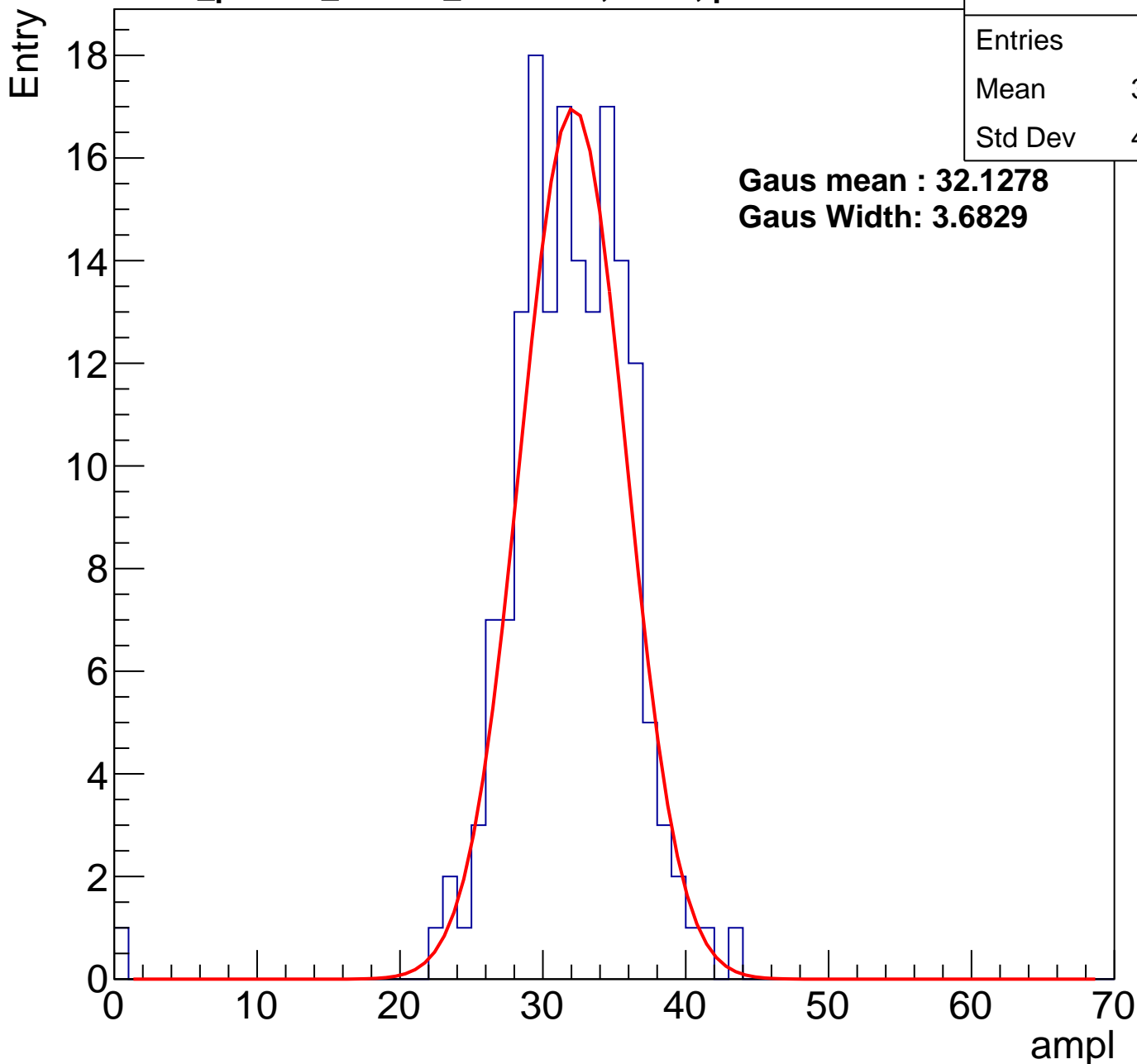
# B0L101S, U26-ch21, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	31.44
Std Dev	4.463

**Gaus mean : 32.1278**

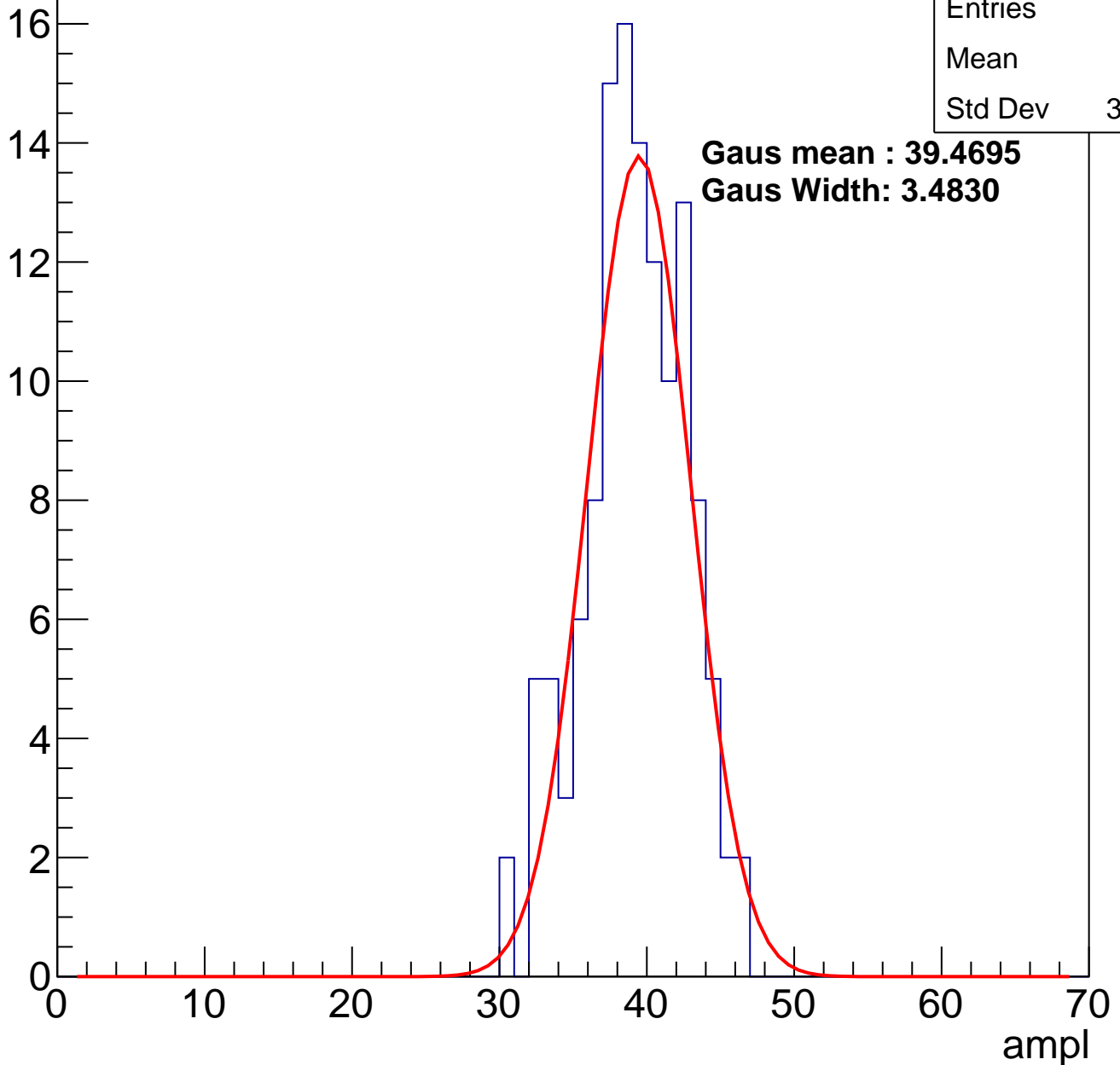
**Gaus Width: 3.6829**



# B0L101S, U26-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch21, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

45.47

Std Dev

3.517

**Gaus mean : 46.0782**

**Gaus Width: 3.8932**

0

10

20

30

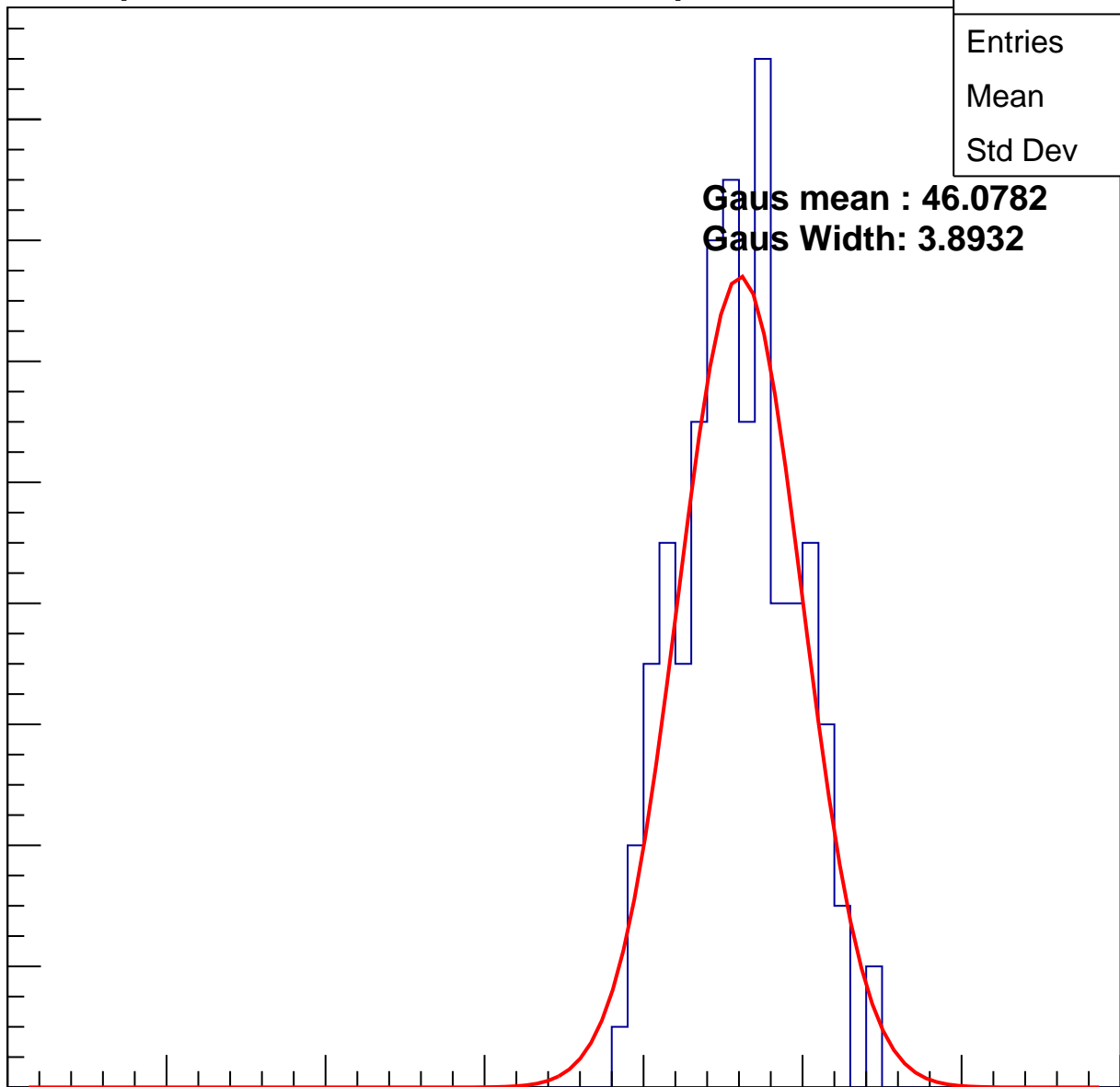
40

50

60

70

ampl



# B0L101S, U26-ch21, adc3

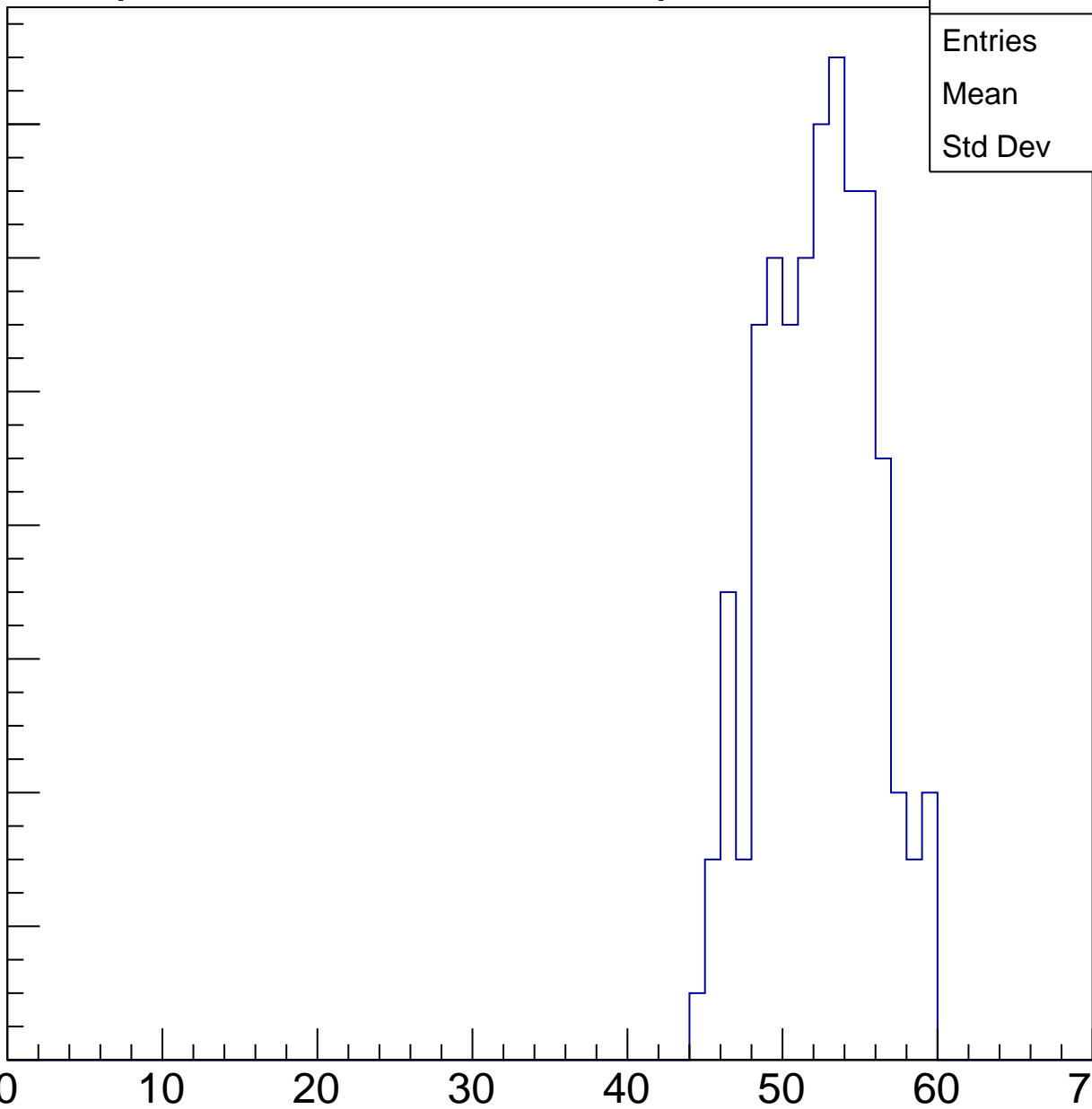
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	135
Mean	51.87
Std Dev	3.451

ampl



# B0L101S, U26-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

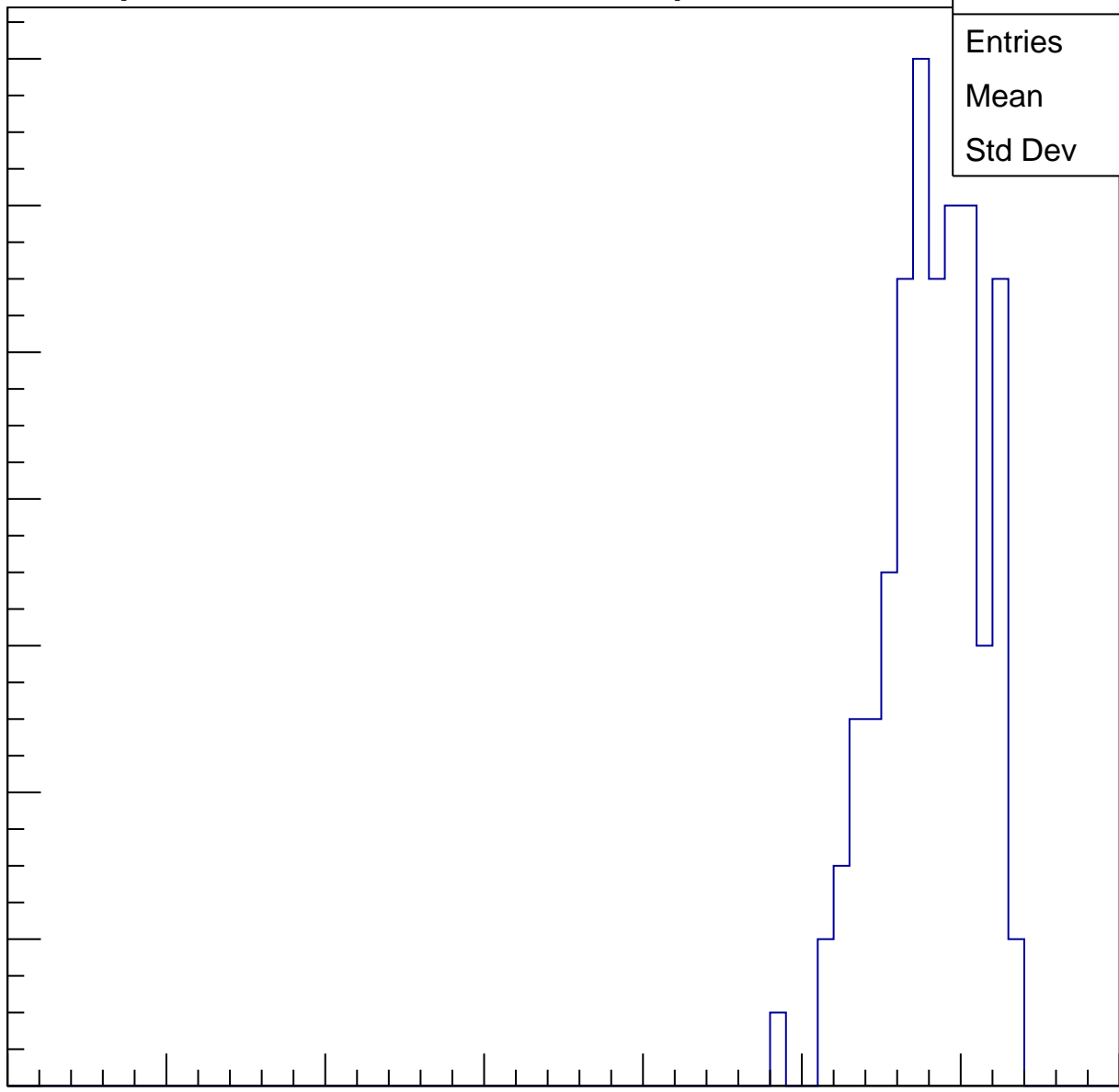
Entries	102
Mean	57.65
Std Dev	3.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch21, adc5

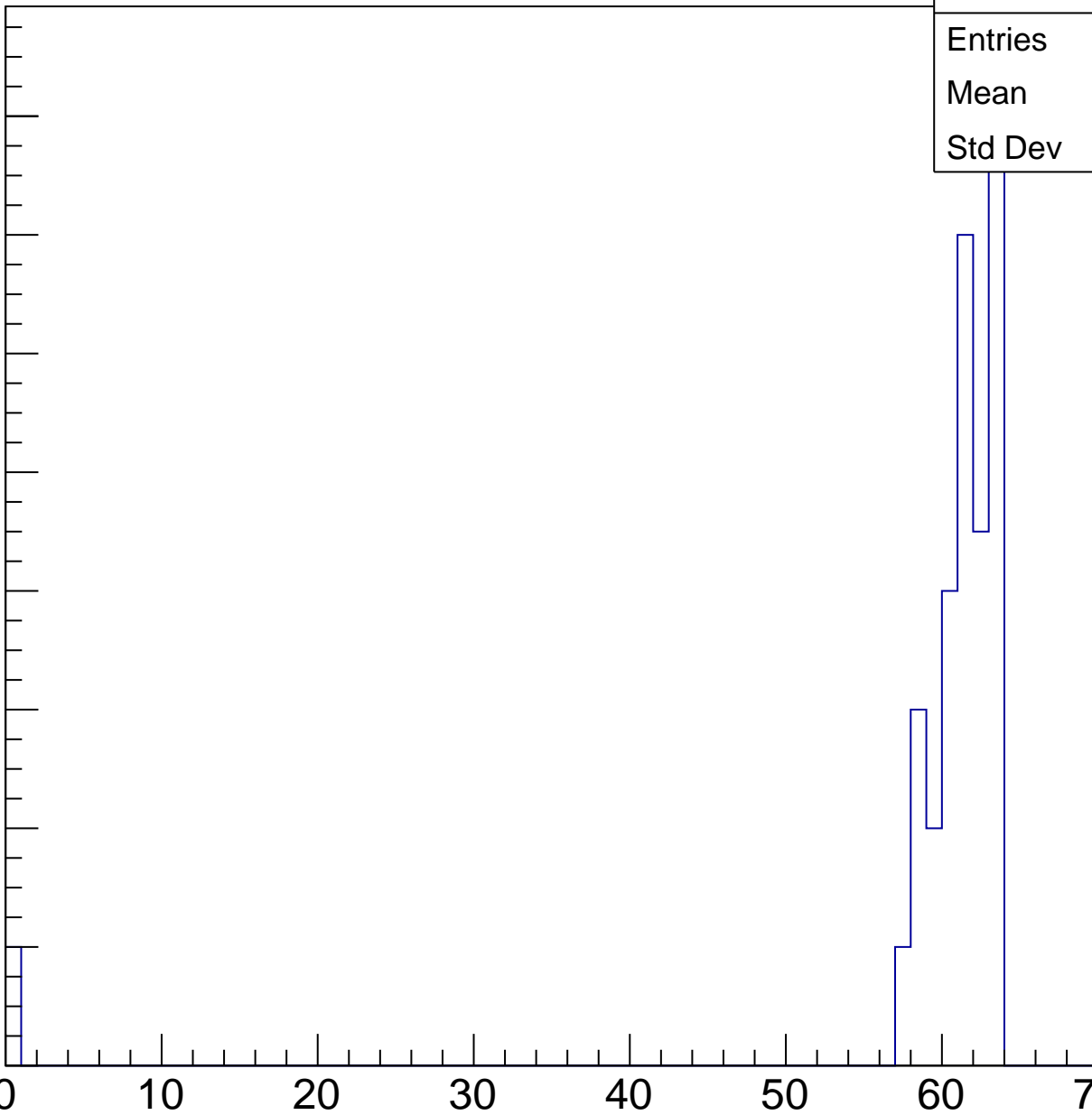
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	62
Mean	59.05
Std Dev	10.92

ampl



# B0L101S, U26-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



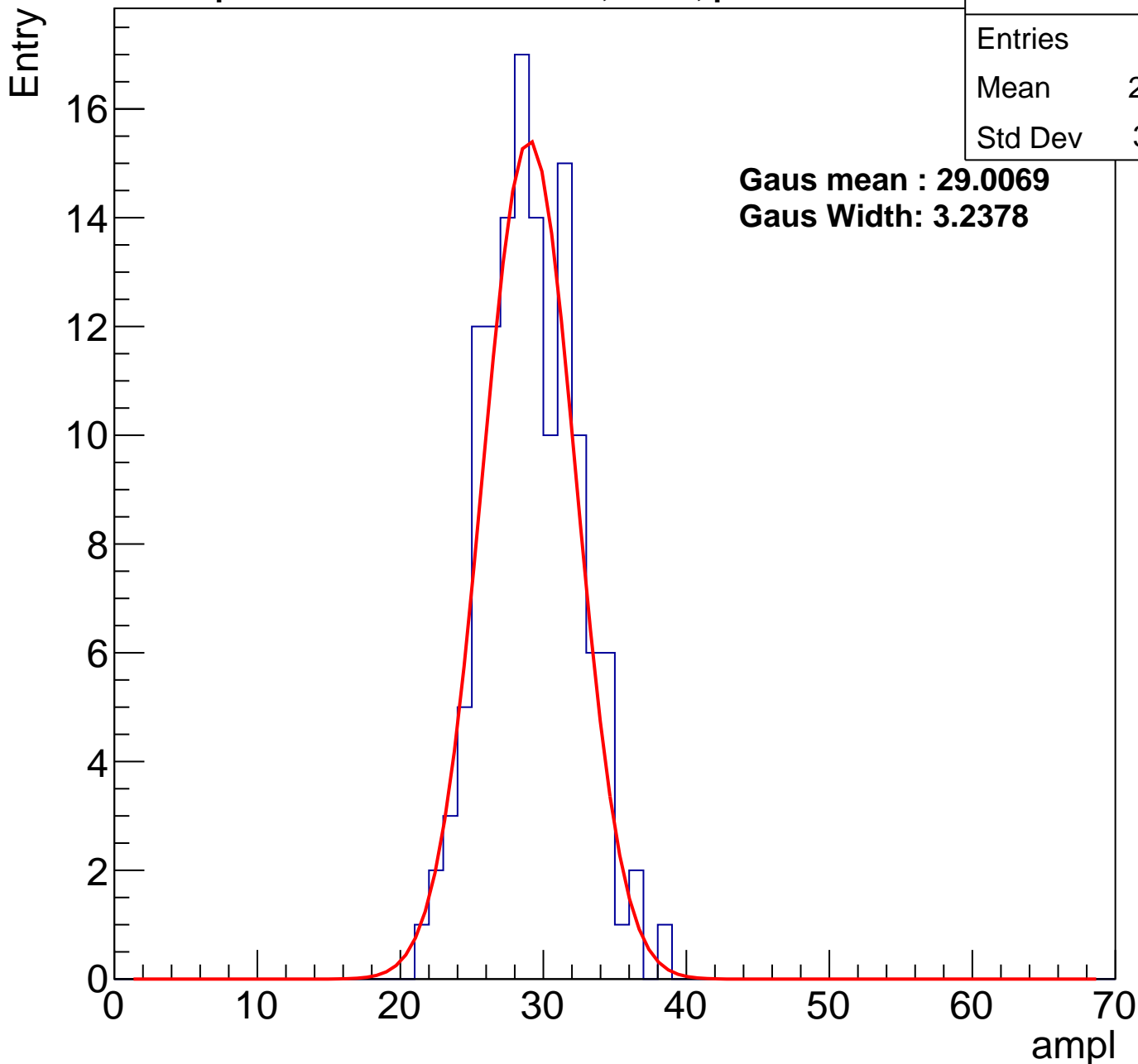
# B0L101S, U26-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	28.69
Std Dev	3.241

**Gaus mean : 29.0069**

**Gaus Width: 3.2378**



# B0L101S, U26-ch22, adc1

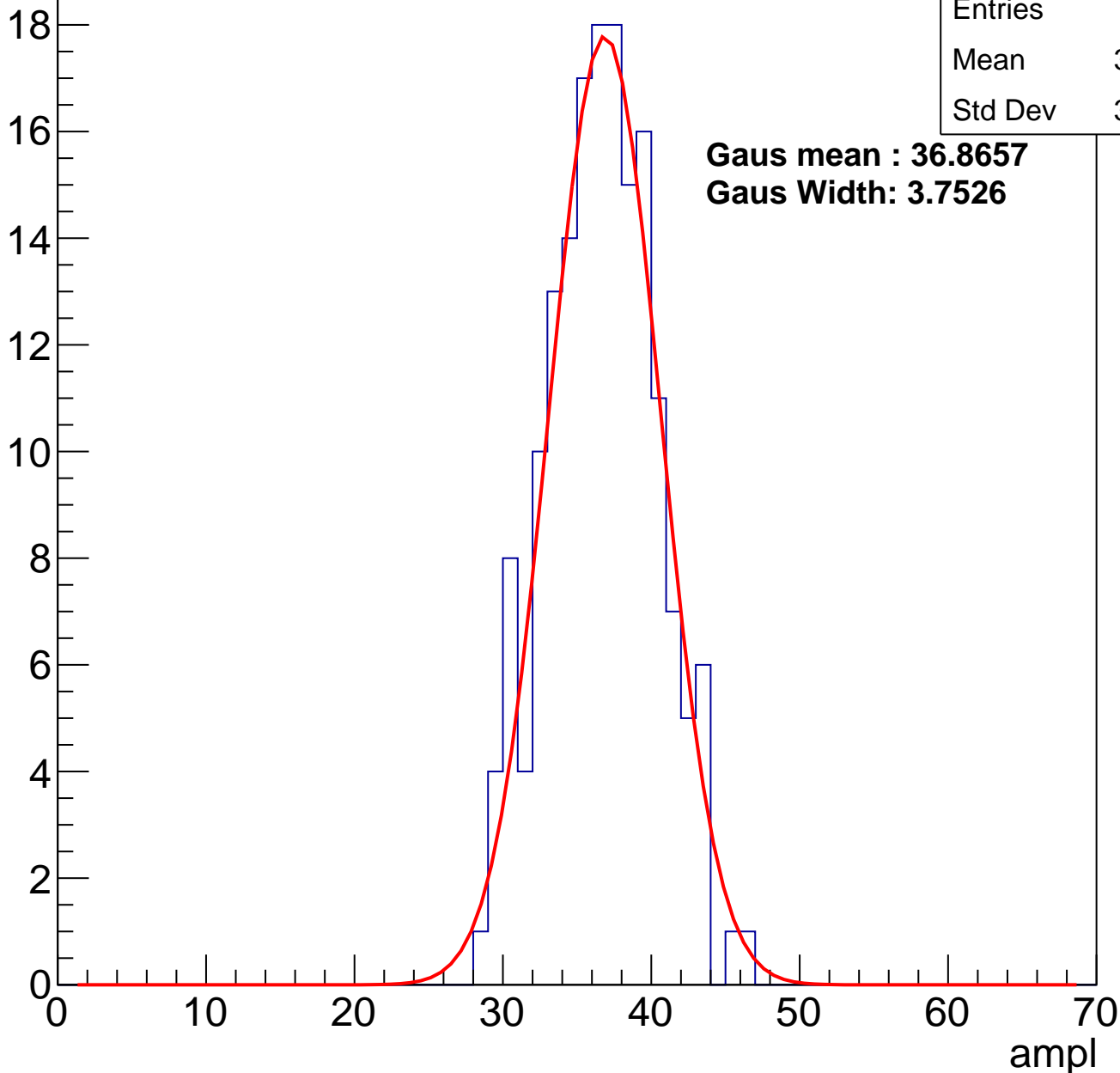
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	36.22
Std Dev	3.622

**Gaus mean : 36.8657**

**Gaus Width: 3.7526**

Entry



# B0L101S, U26-ch22, adc2

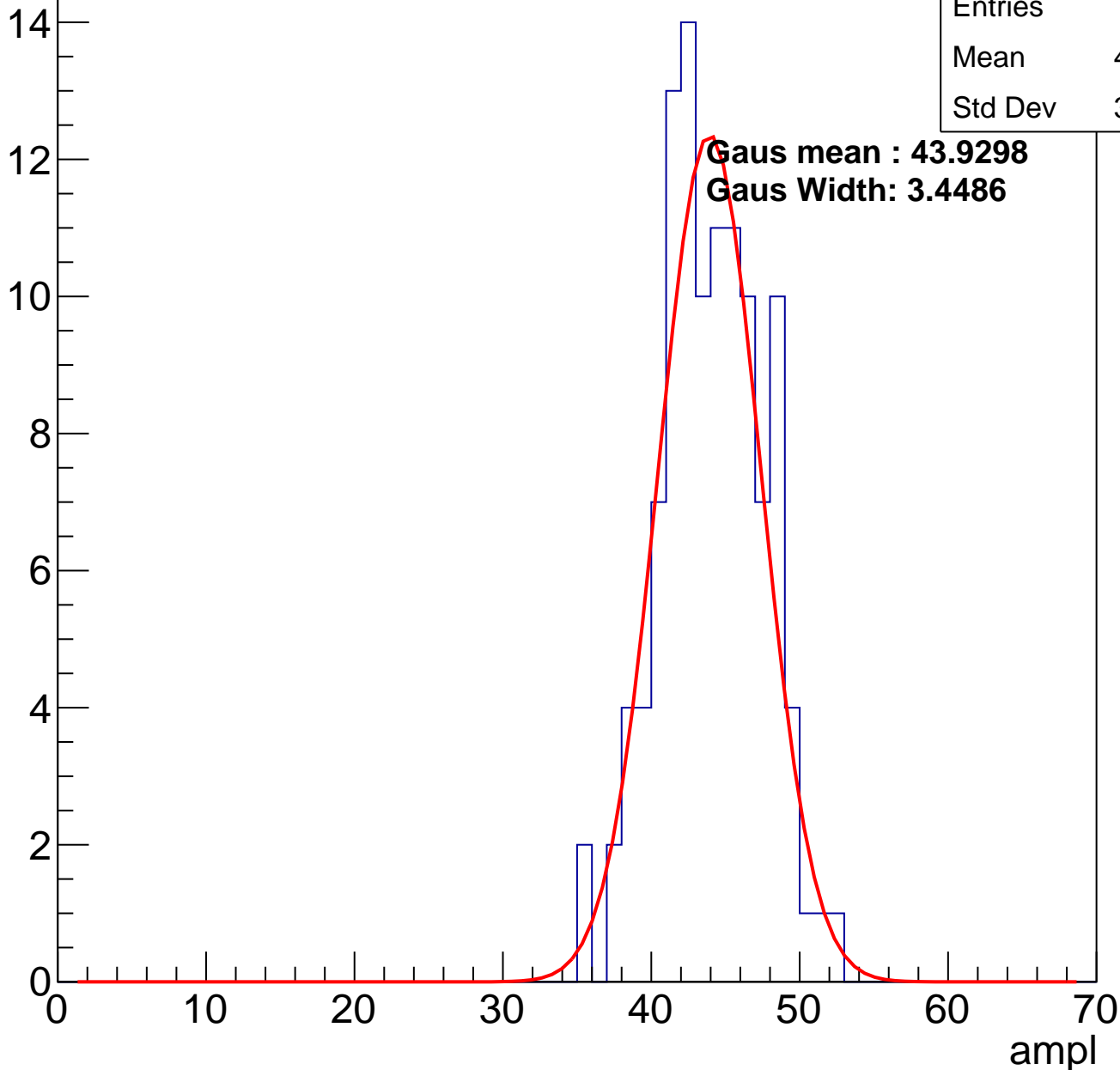
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	112
Mean	43.57
Std Dev	3.409

**Gaus mean : 43.9298**

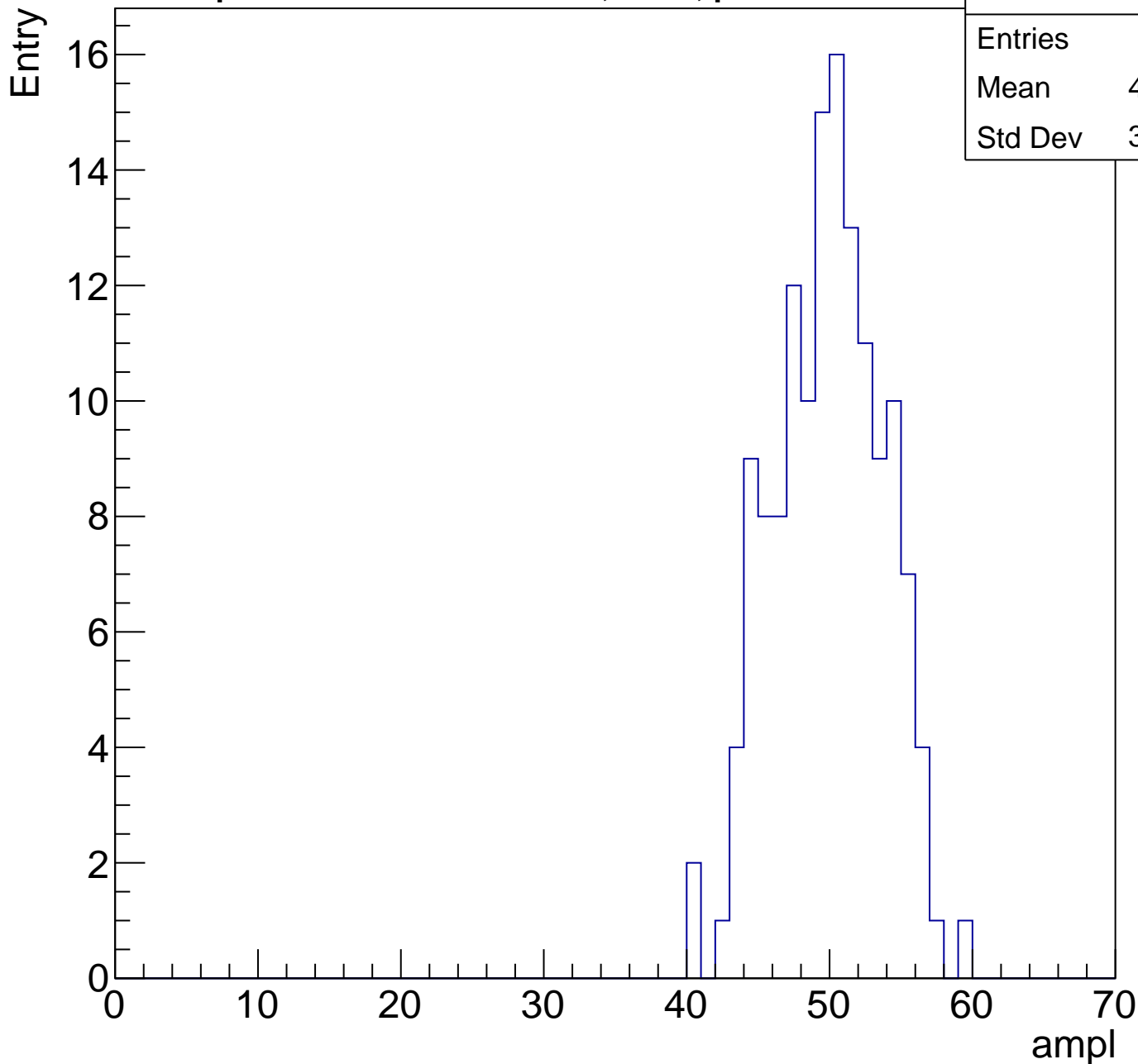
**Gaus Width: 3.4486**



# B0L101S, U26-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	49.46
Std Dev	3.748



# B0L101S, U26-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

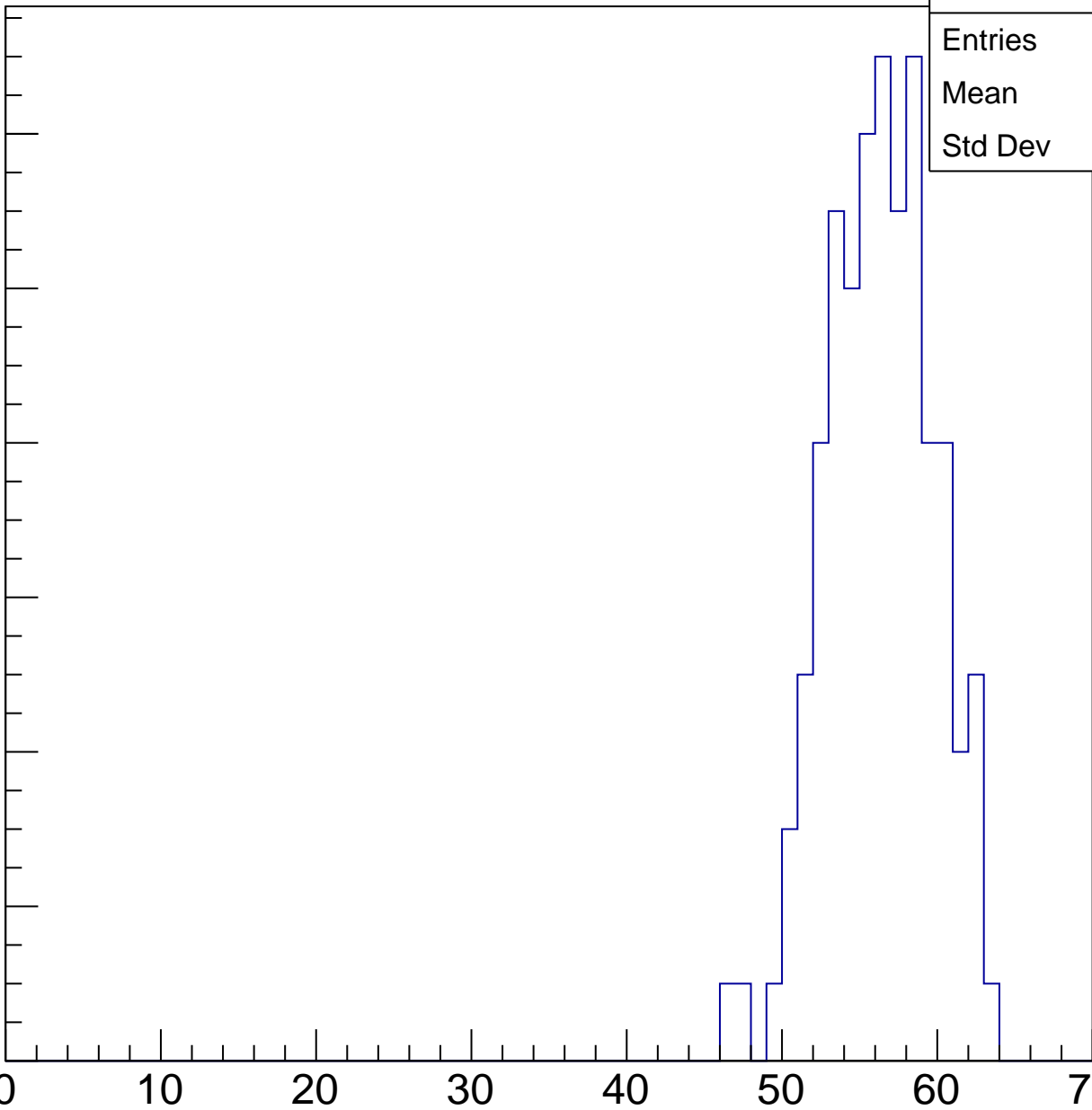
30

40

50

ampl

Entries	115
Mean	55.86
Std Dev	3.403



# B0L101S, U26-ch22, adc5

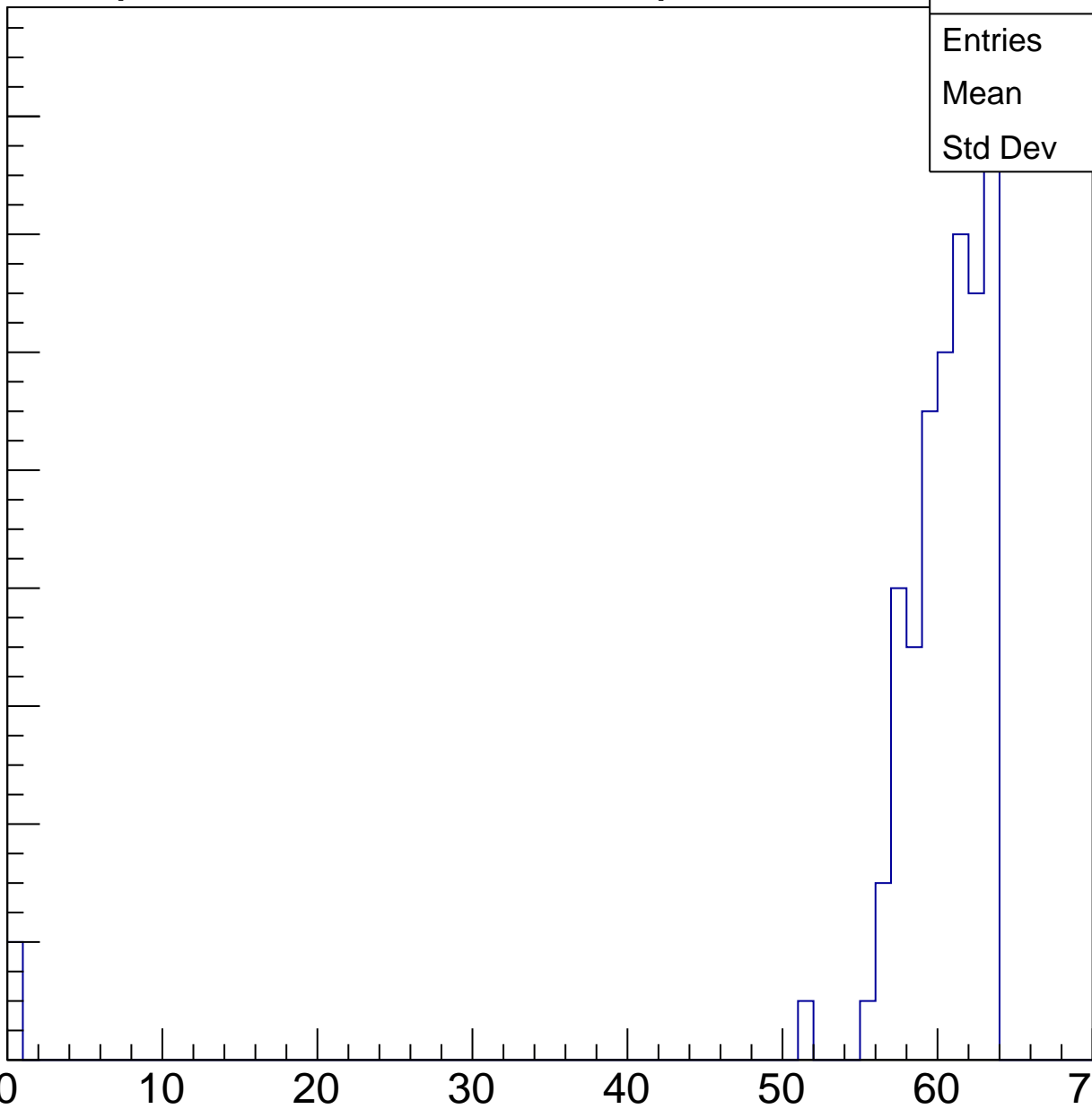
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	89
Mean	58.83
Std Dev	9.218

ampl



# B0L101S, U26-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	62
Std Dev	0.8165

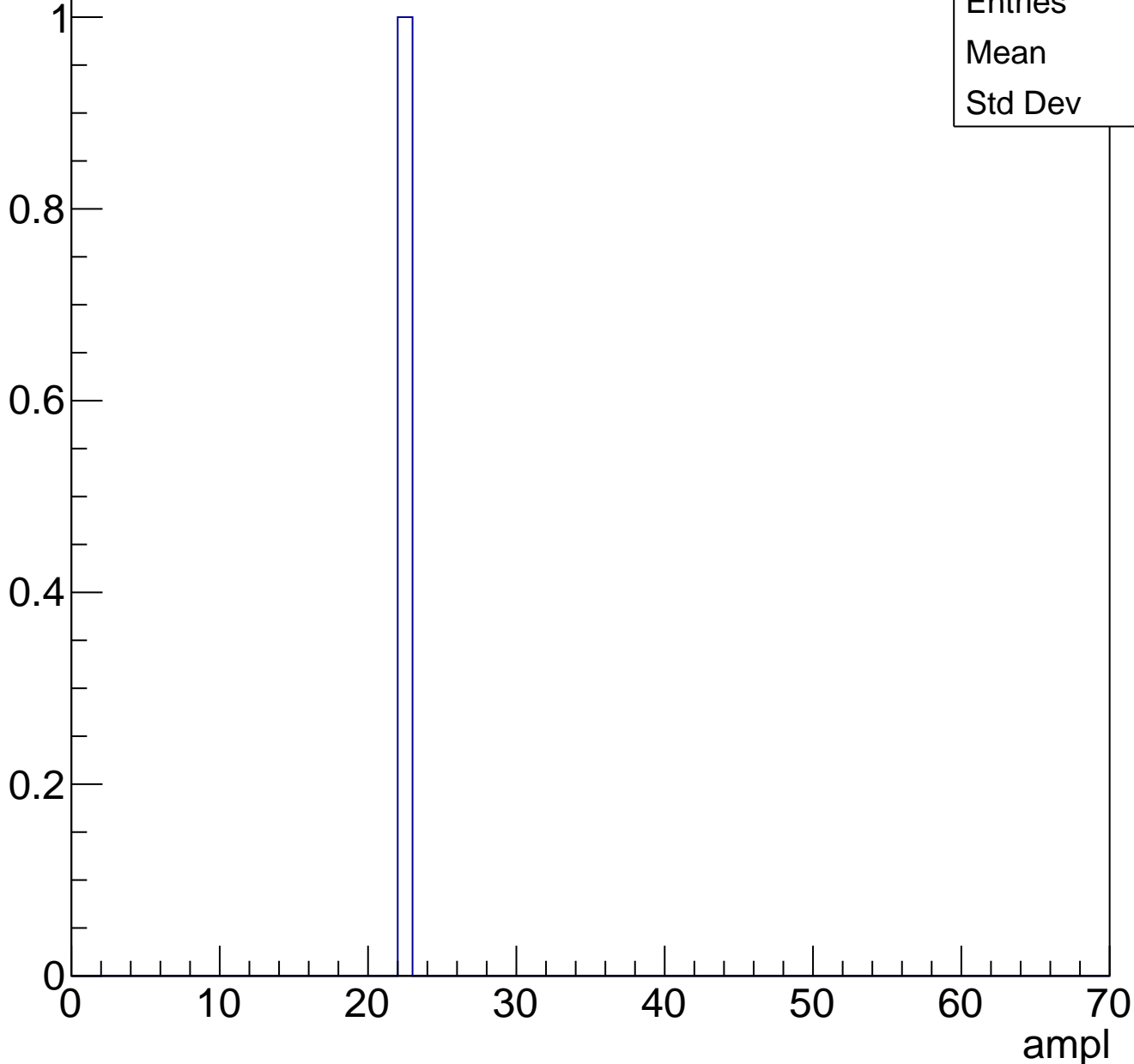
ampl



# B0L101S, U26-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



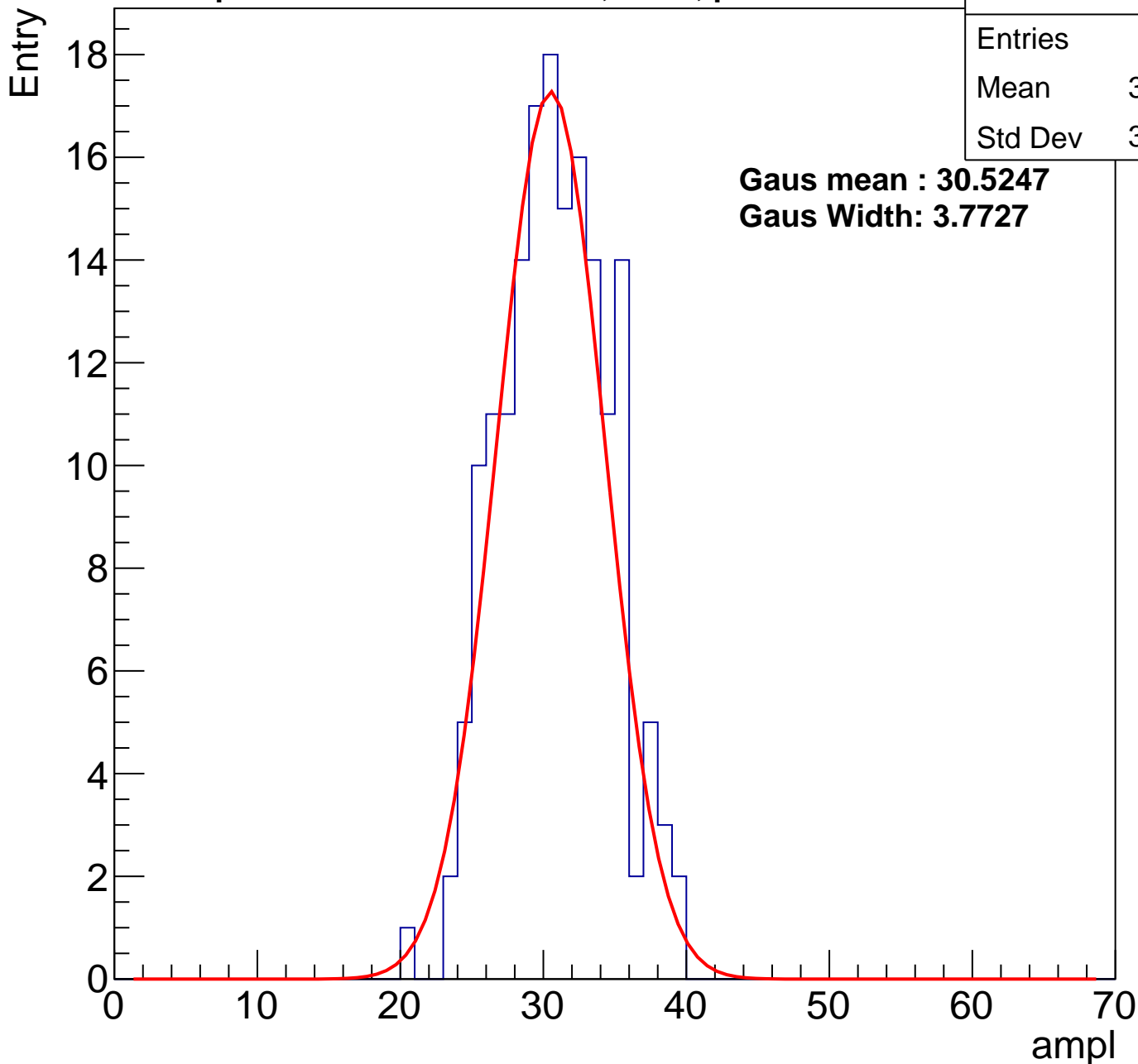
# B0L101S, U26-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	30.39
Std Dev	3.709

**Gaus mean : 30.5247**

**Gaus Width: 3.7727**



# B0L101S, U26-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	37.37
Std Dev	3.448

**Gaus mean : 37.5430**

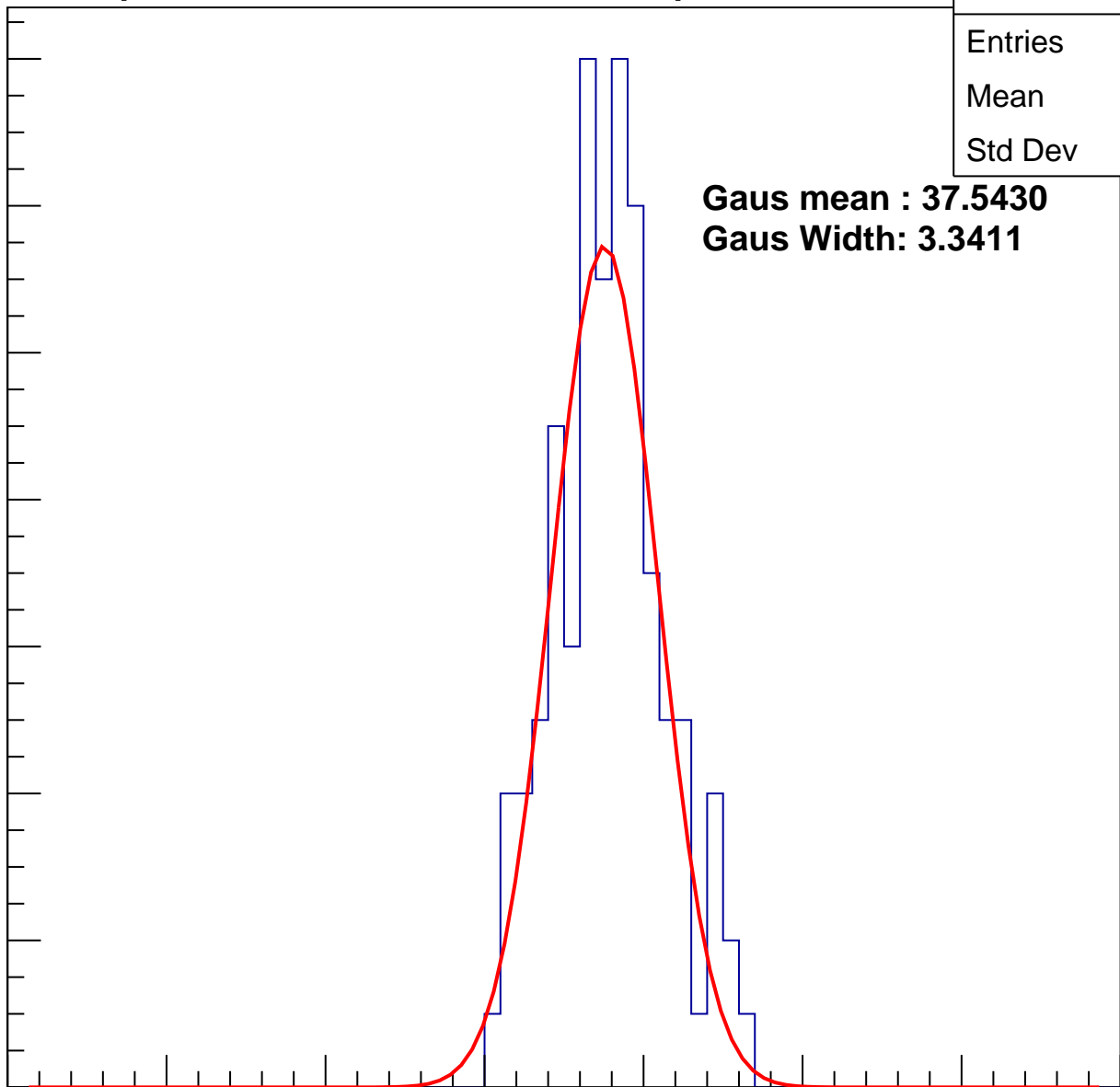
**Gaus Width: 3.3411**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	43.7
Std Dev	3.642

**Gaus mean : 44.2208**

**Gaus Width: 3.7774**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

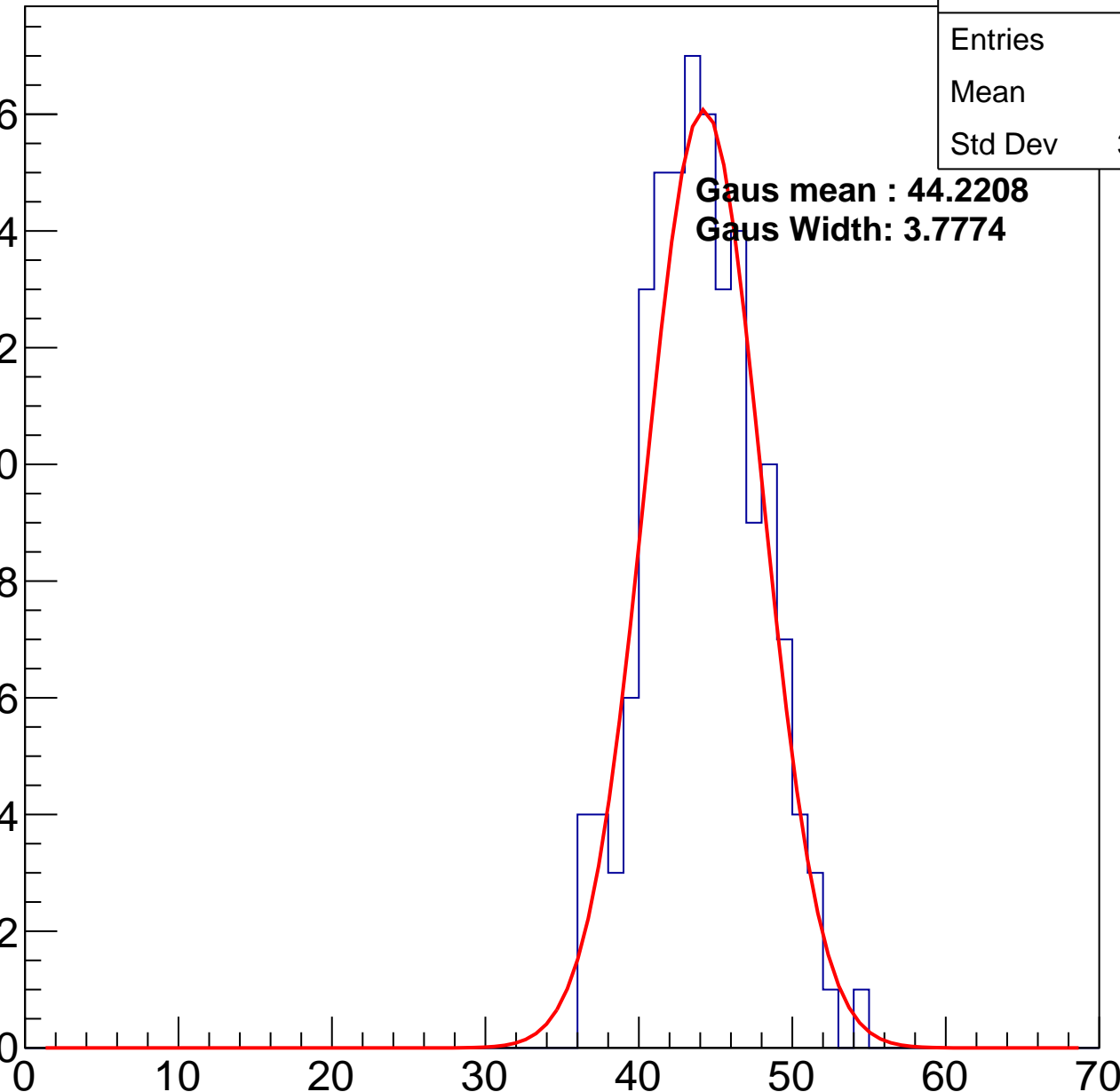
30

40

50

60

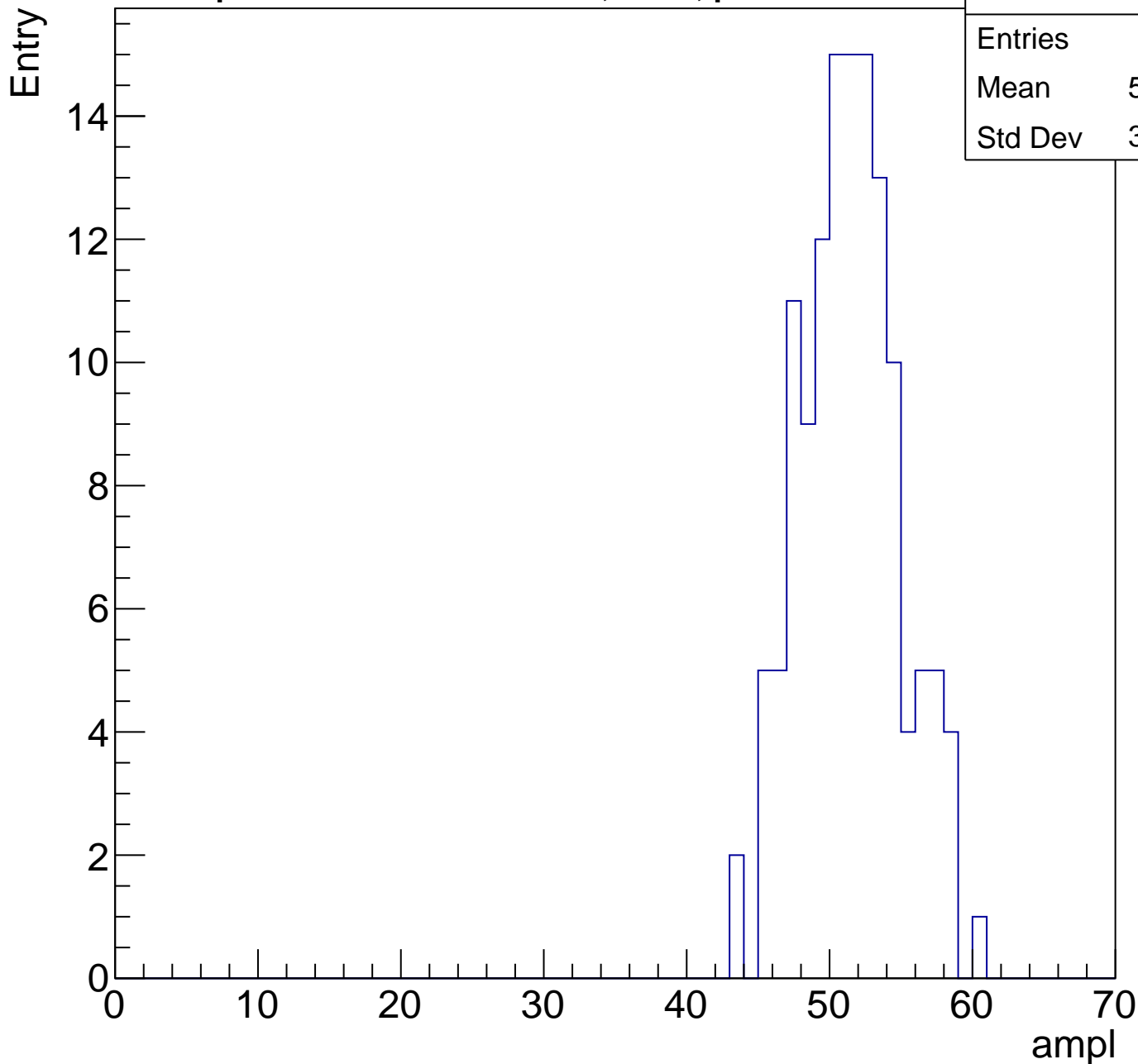
ampl



# B0L101S, U26-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	50.98
Std Dev	3.446

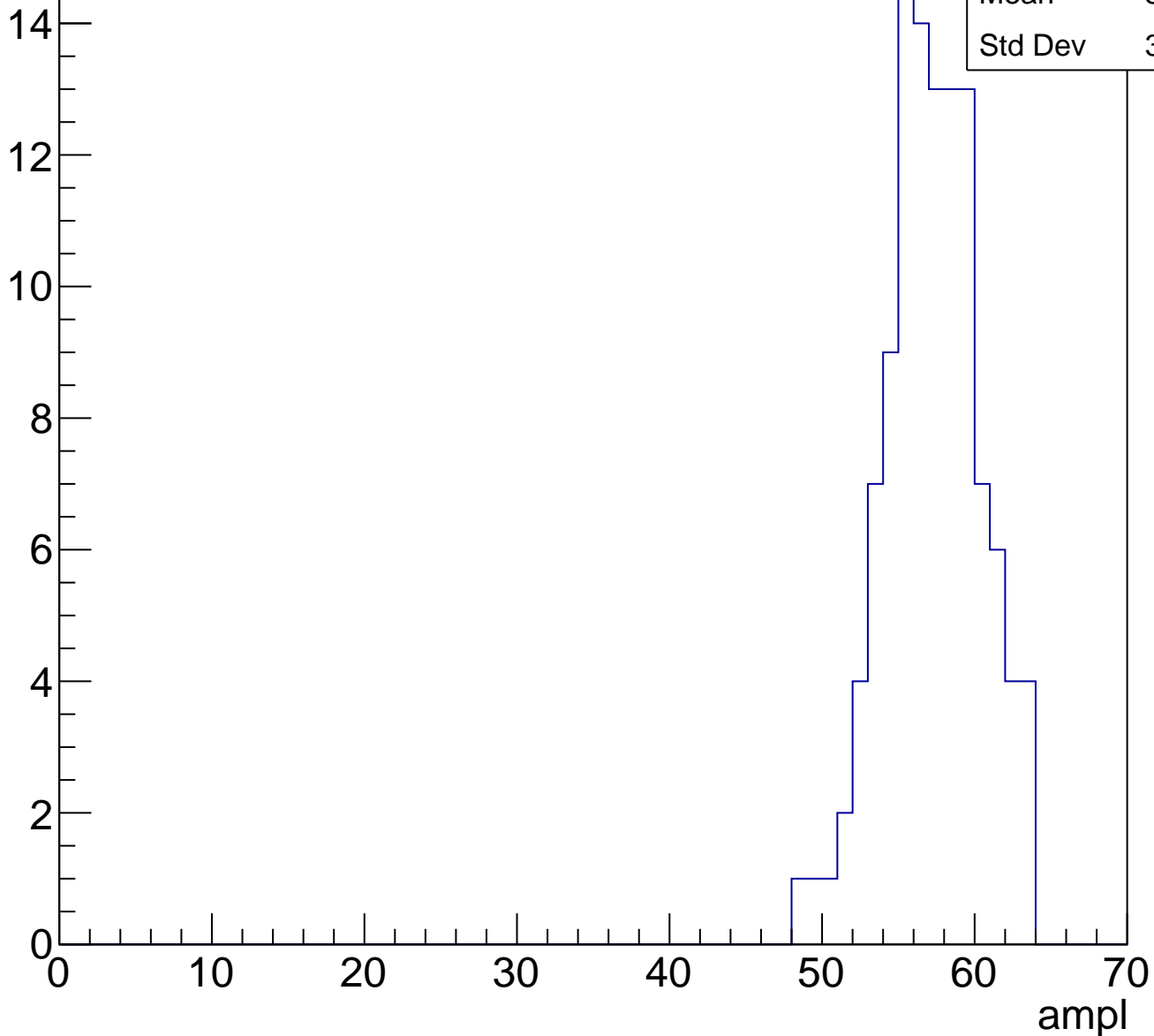


# B0L101S, U26-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	56.76
Std Dev	3.099

Entry



# B0L101S, U26-ch23, adc5

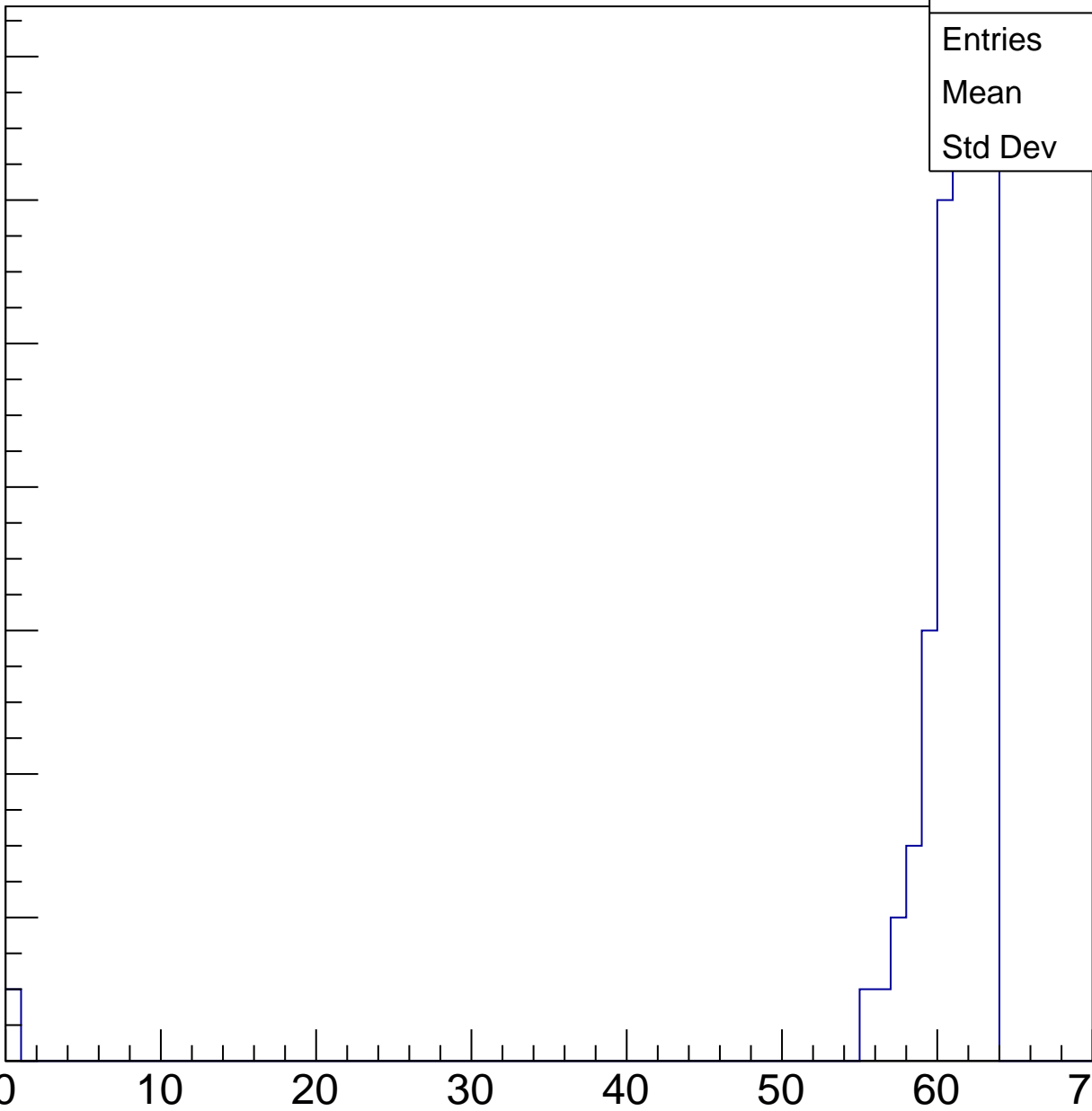
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	67
Mean	59.91
Std Dev	7.594

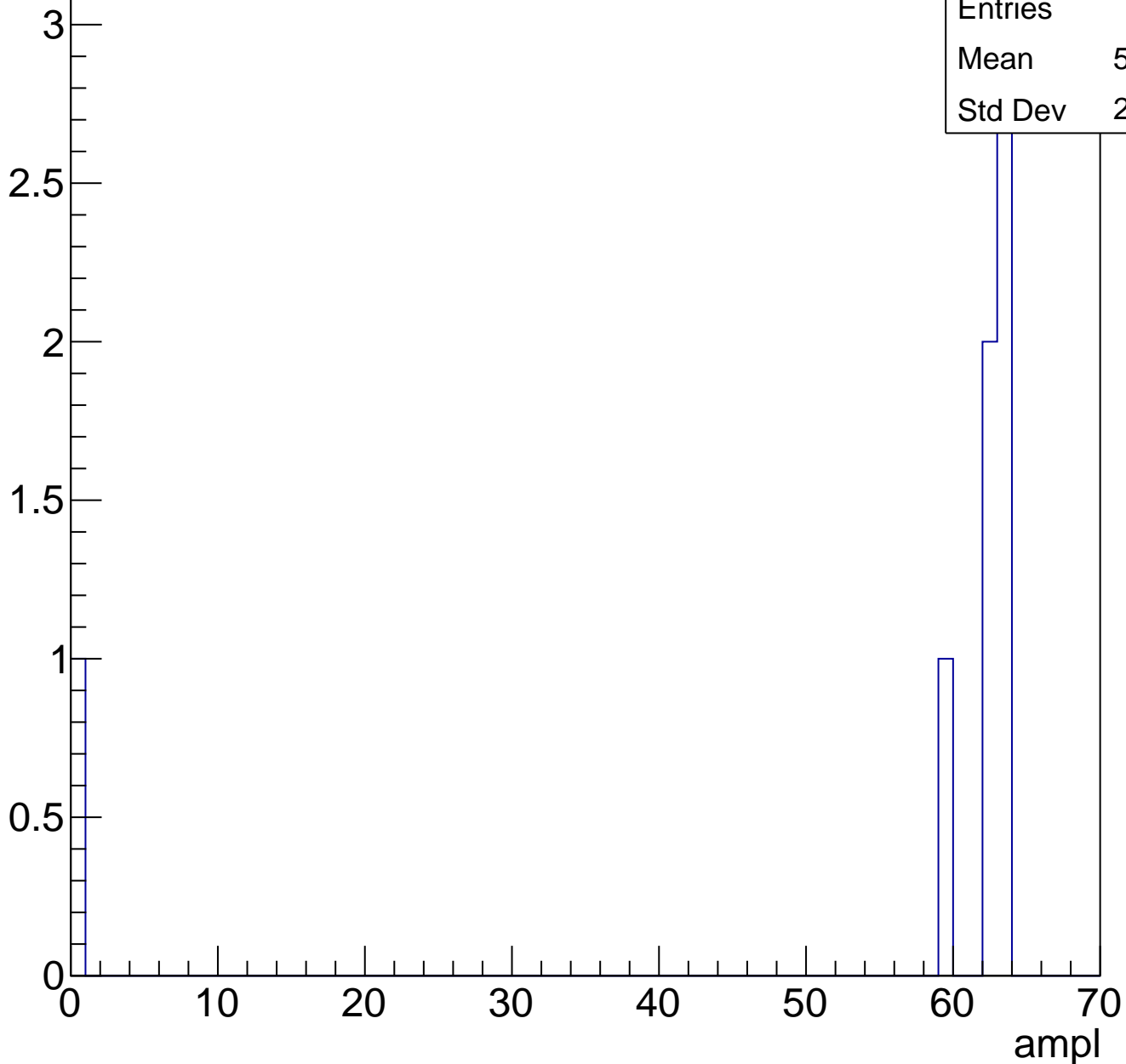
ampl



# B0L101S, U26-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

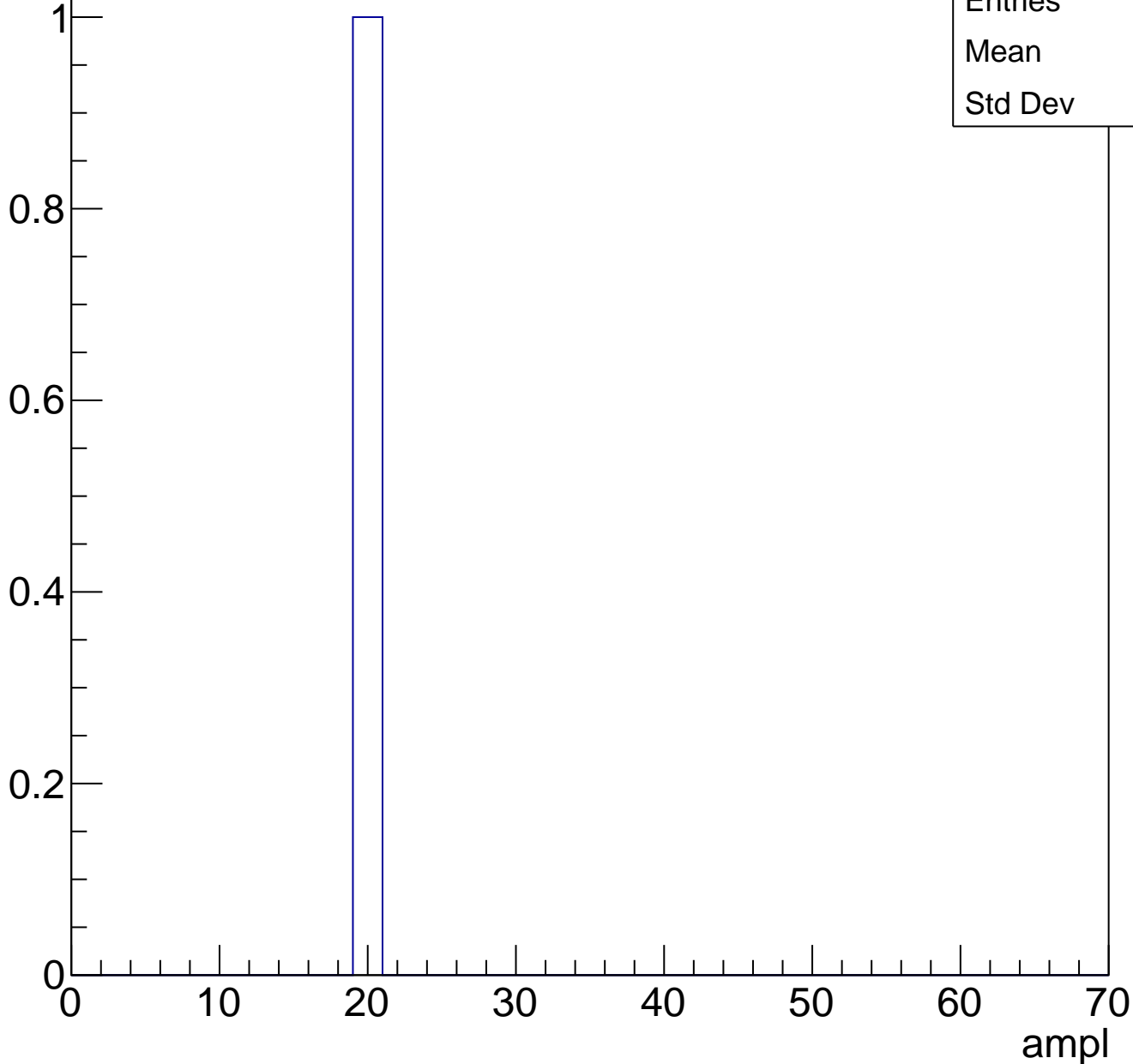




# B0L101S, U26-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



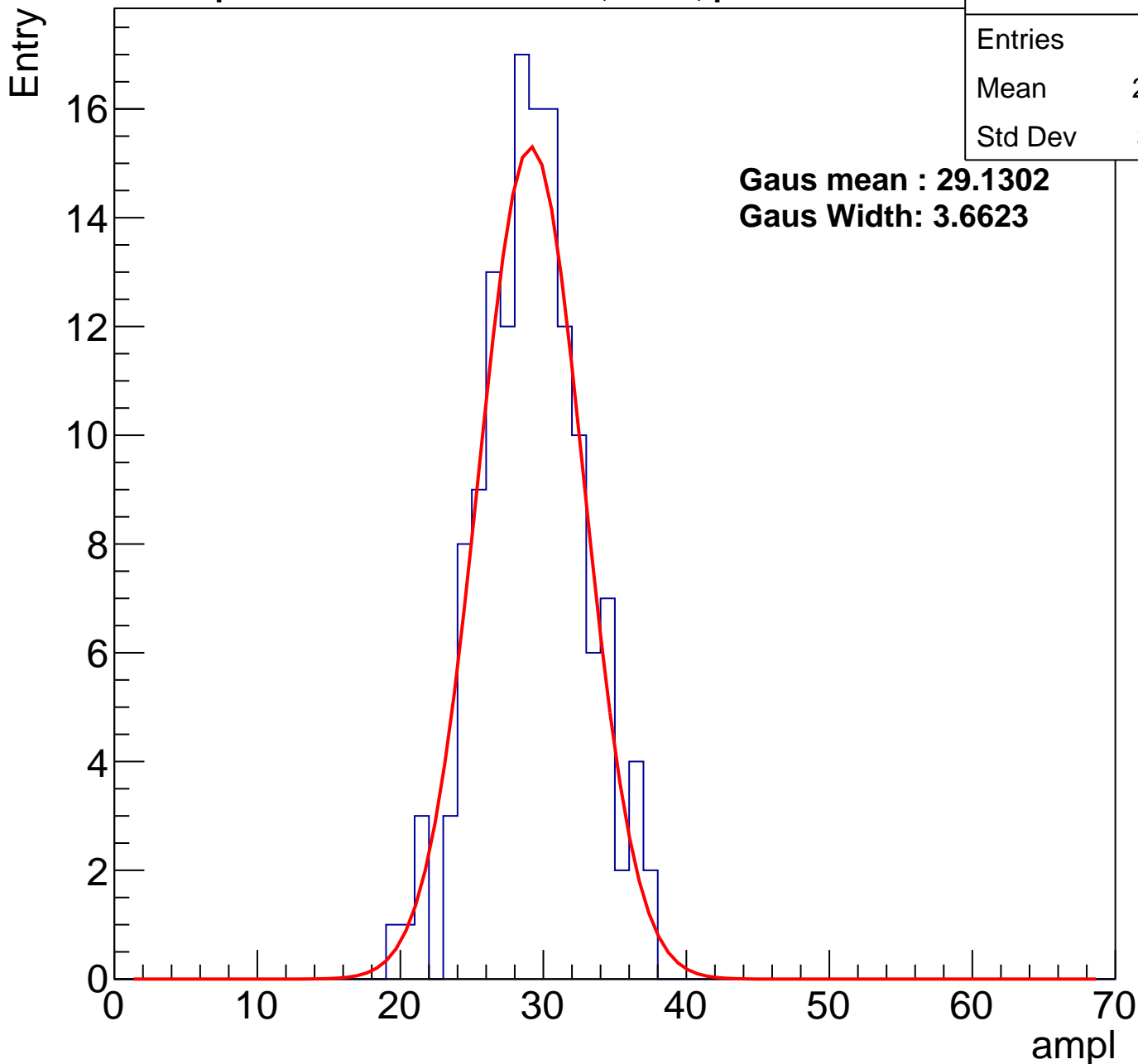
# B0L101S, U26-ch24, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	28.77
Std Dev	3.581

**Gaus mean : 29.1302**

**Gaus Width: 3.6623**



# B0L101S, U26-ch24, adc1

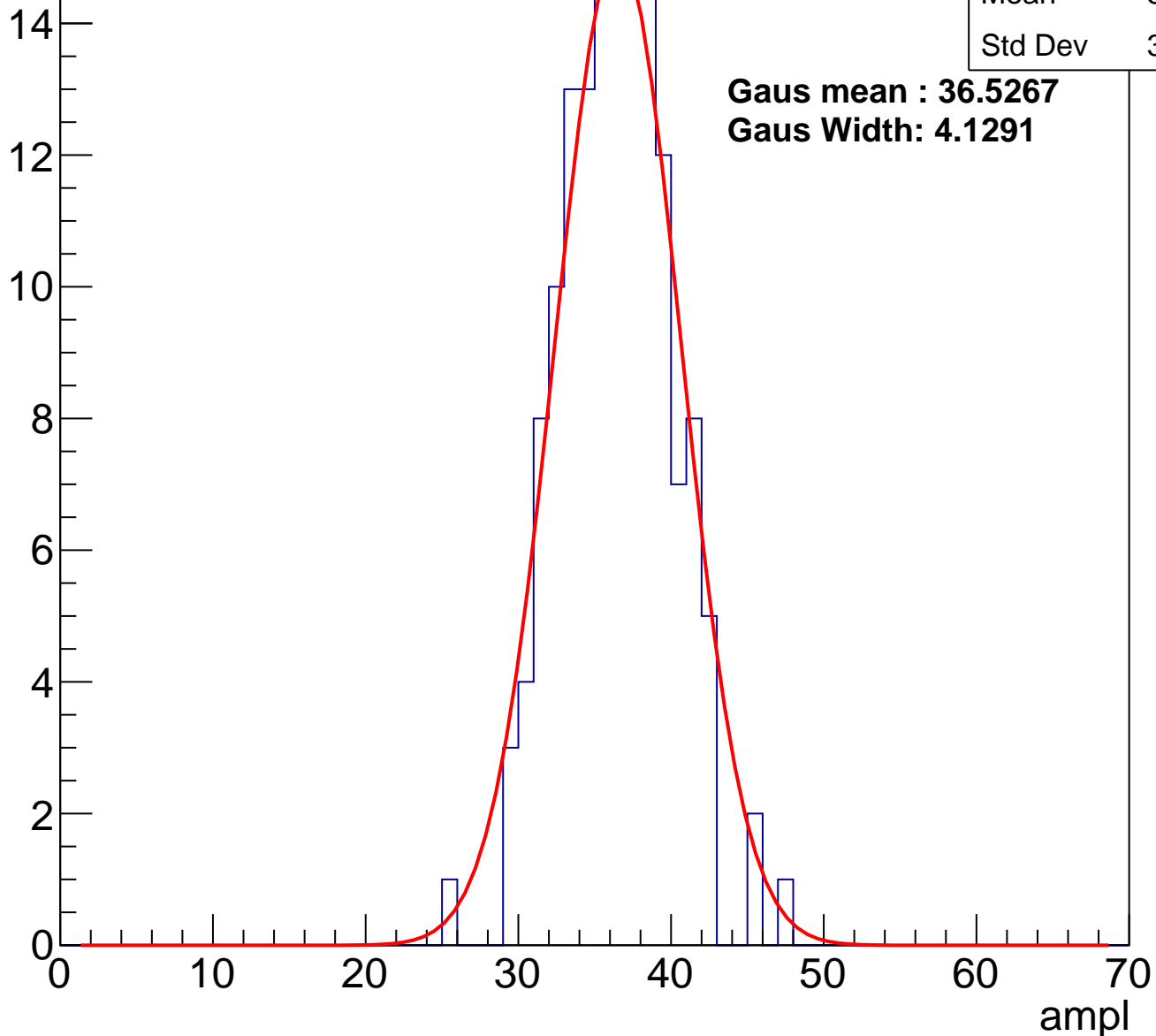
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	35.95
Std Dev	3.608

**Gaus mean : 36.5267**

**Gaus Width: 4.1291**

Entry



# B0L101S, U26-ch24, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

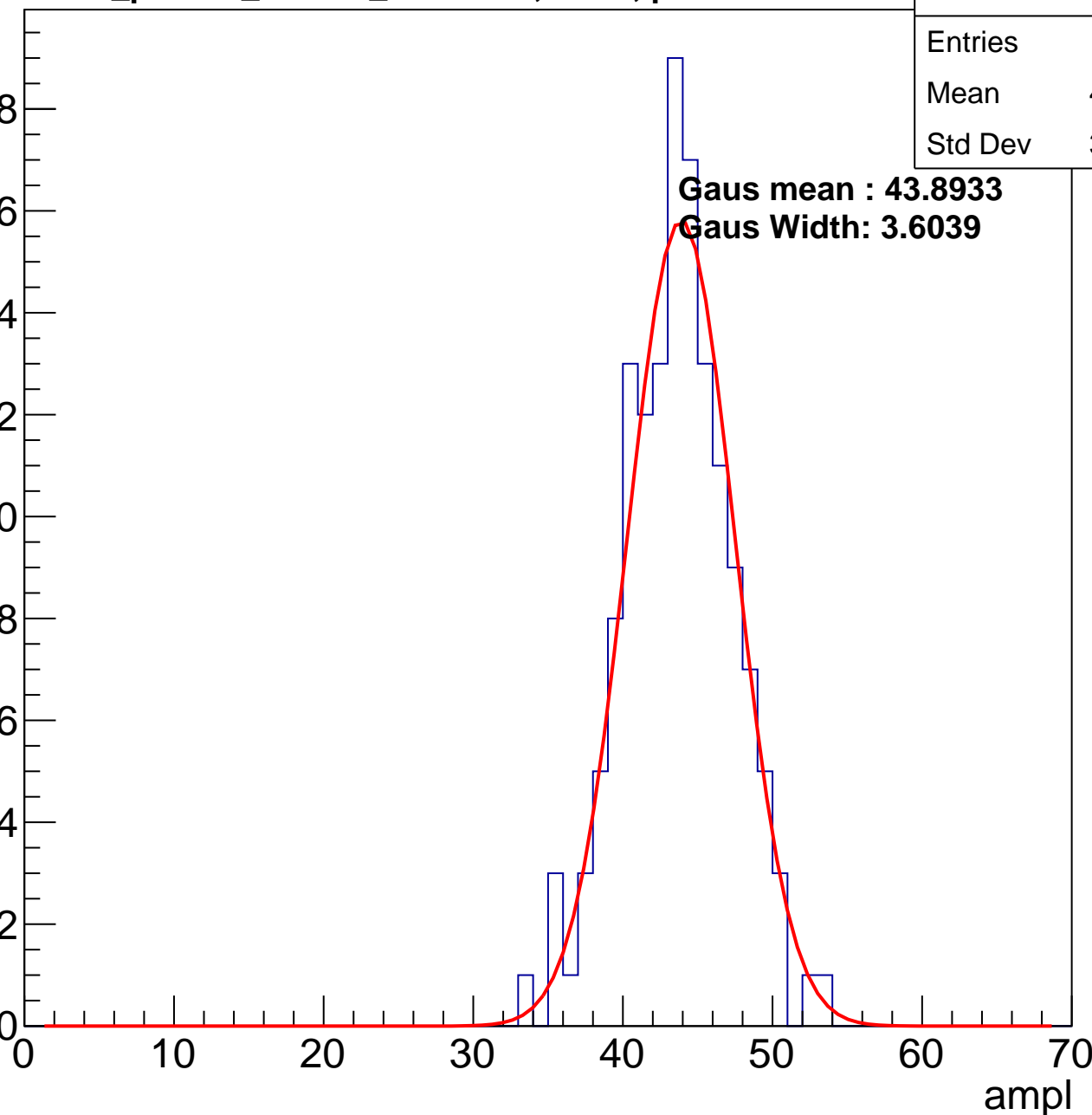
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	145
Mean	43.17
Std Dev	3.592

**Gaus mean : 43.8933**

**Gaus Width: 3.6039**

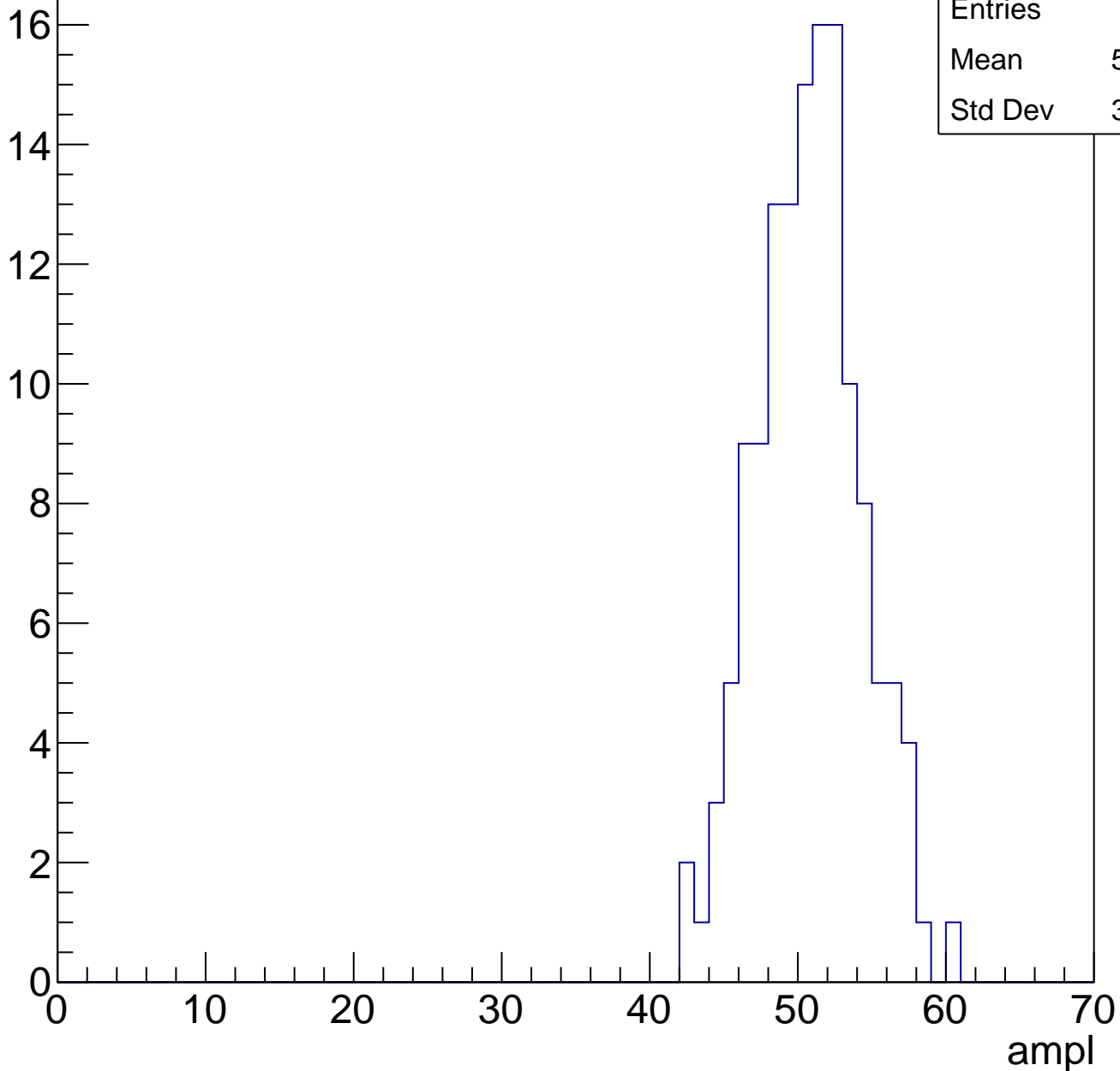


ampl

# B0L101S, U26-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	136
Mean	50.32
Std Dev	3.483

# B0L101S, U26-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	85
Mean	55.49
Std Dev	2.969

Entry

12

10

8

6

4

2

0

0

10

20

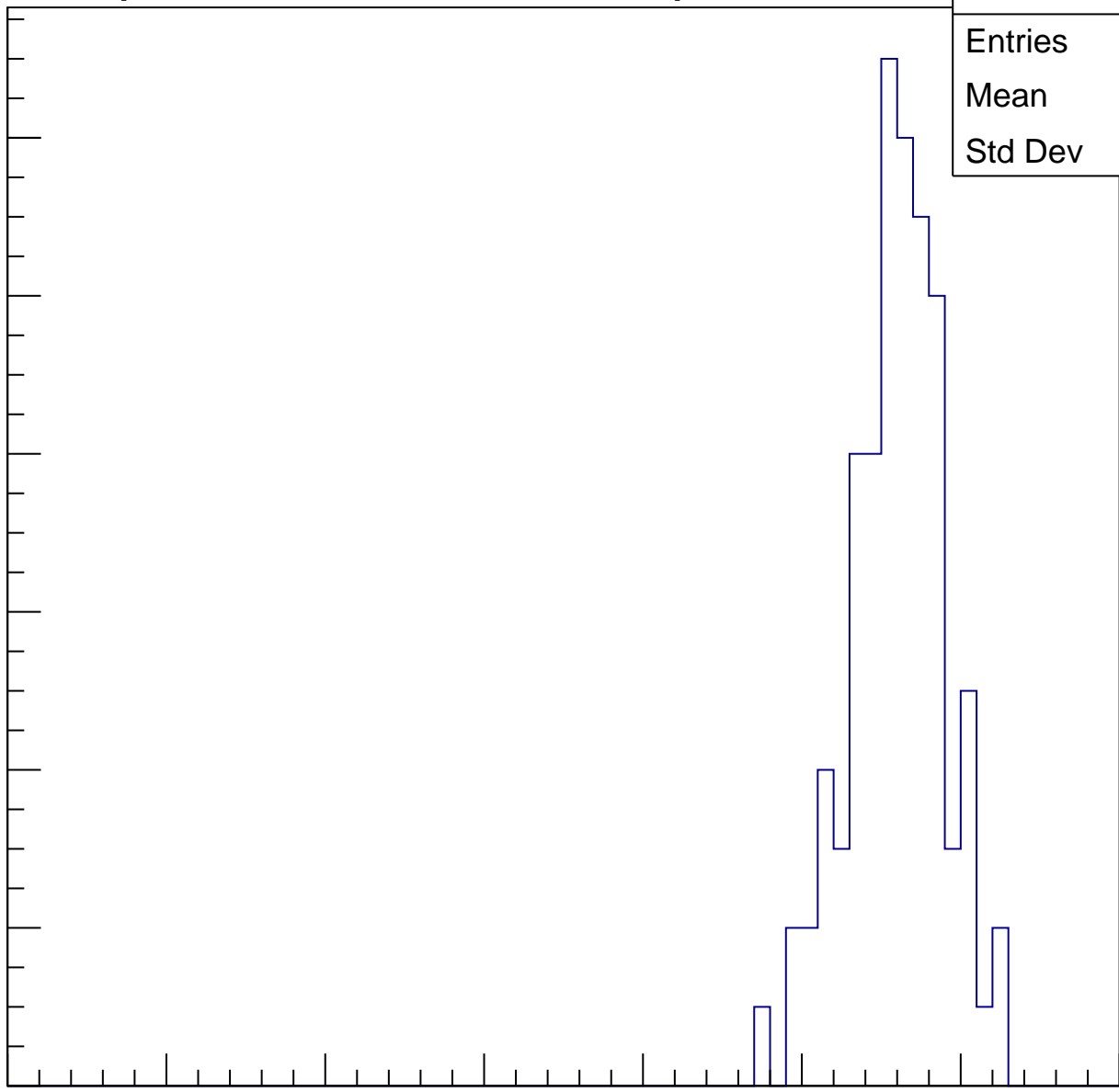
30

40

50

60

ampl



# B0L101S, U26-ch24, adc5

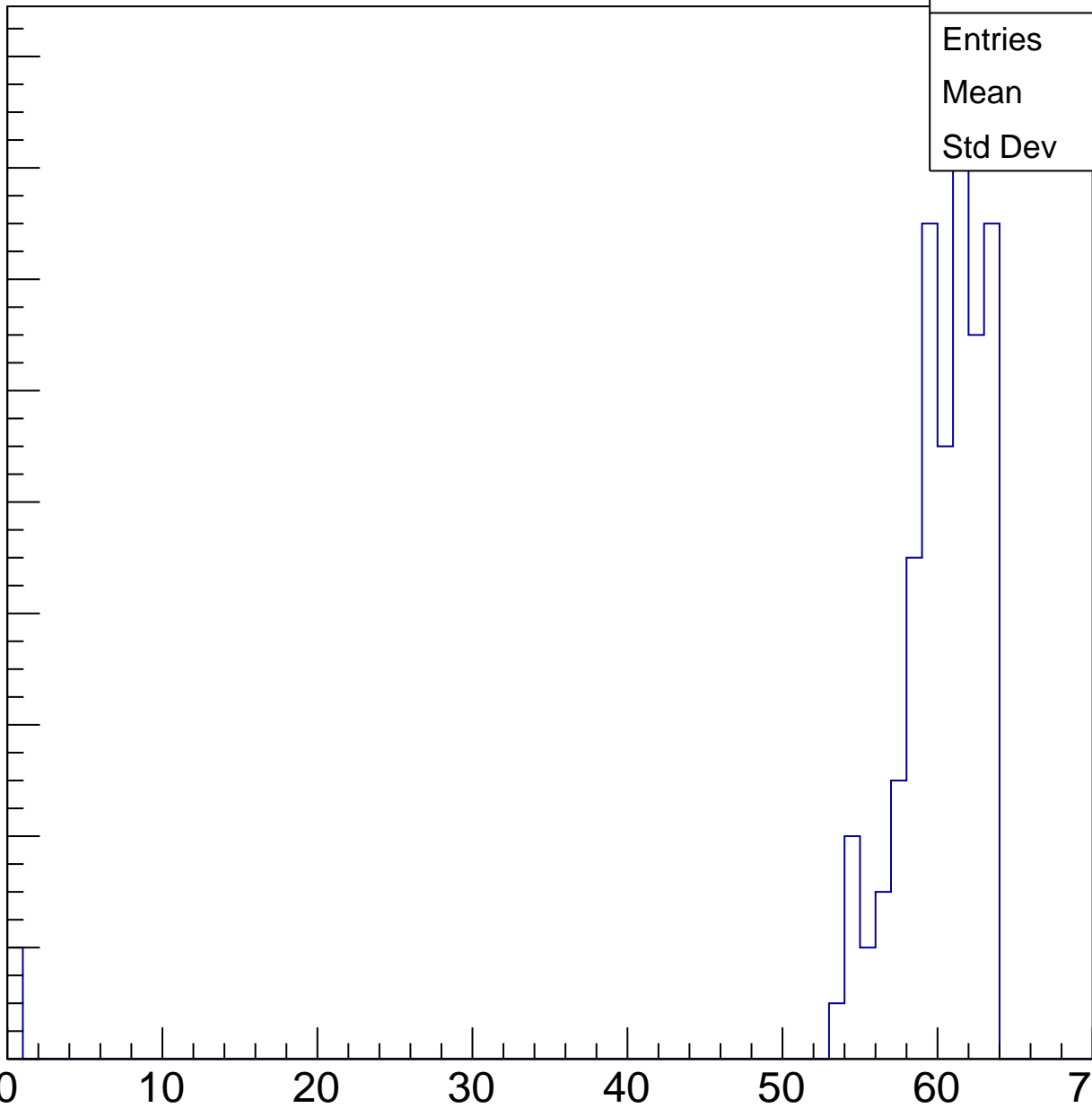
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	98
Mean	58.65
Std Dev	8.815

ampl

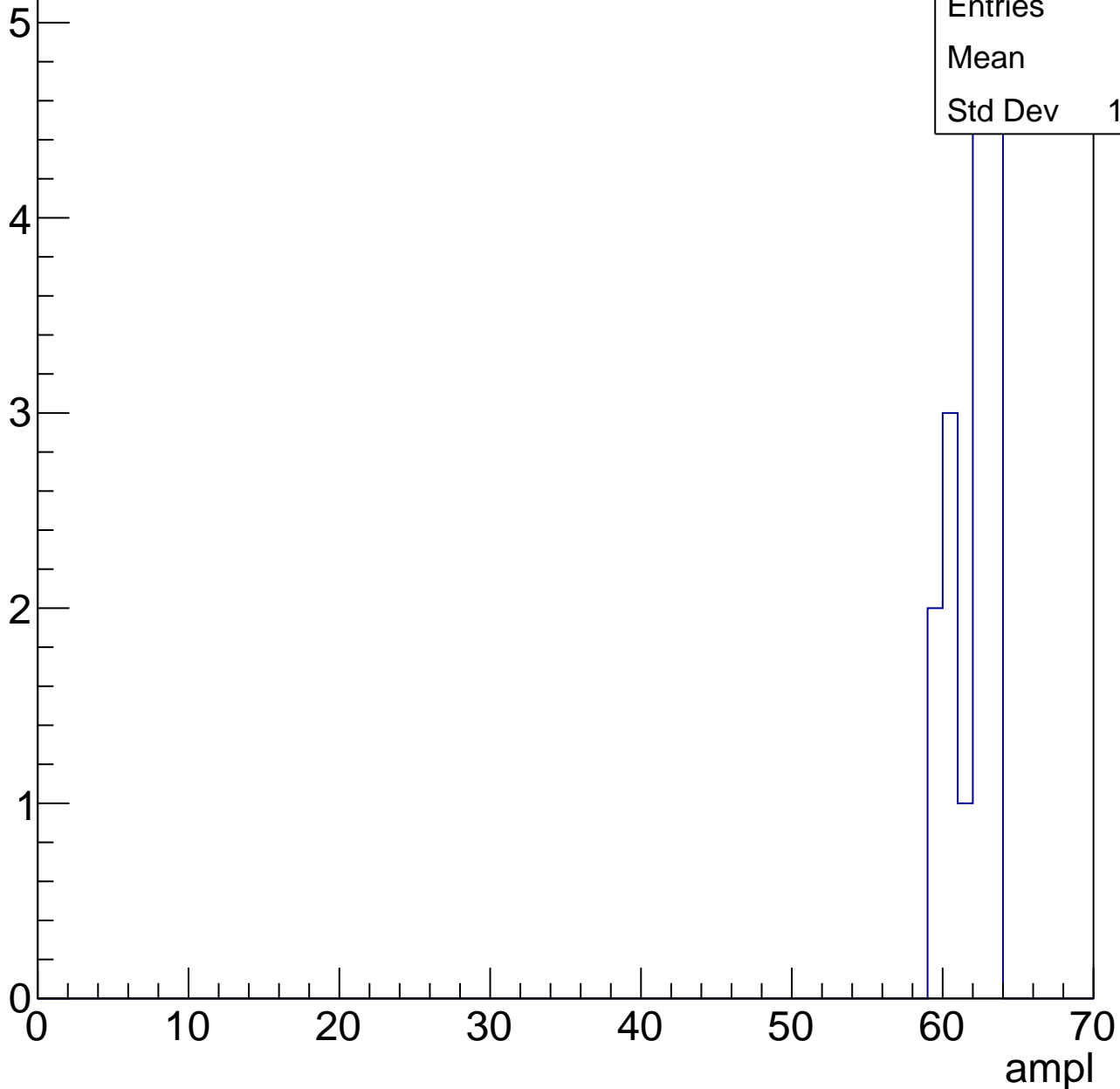


# B0L101S, U26-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.5
Std Dev	1.414





# B0L101S, U26-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

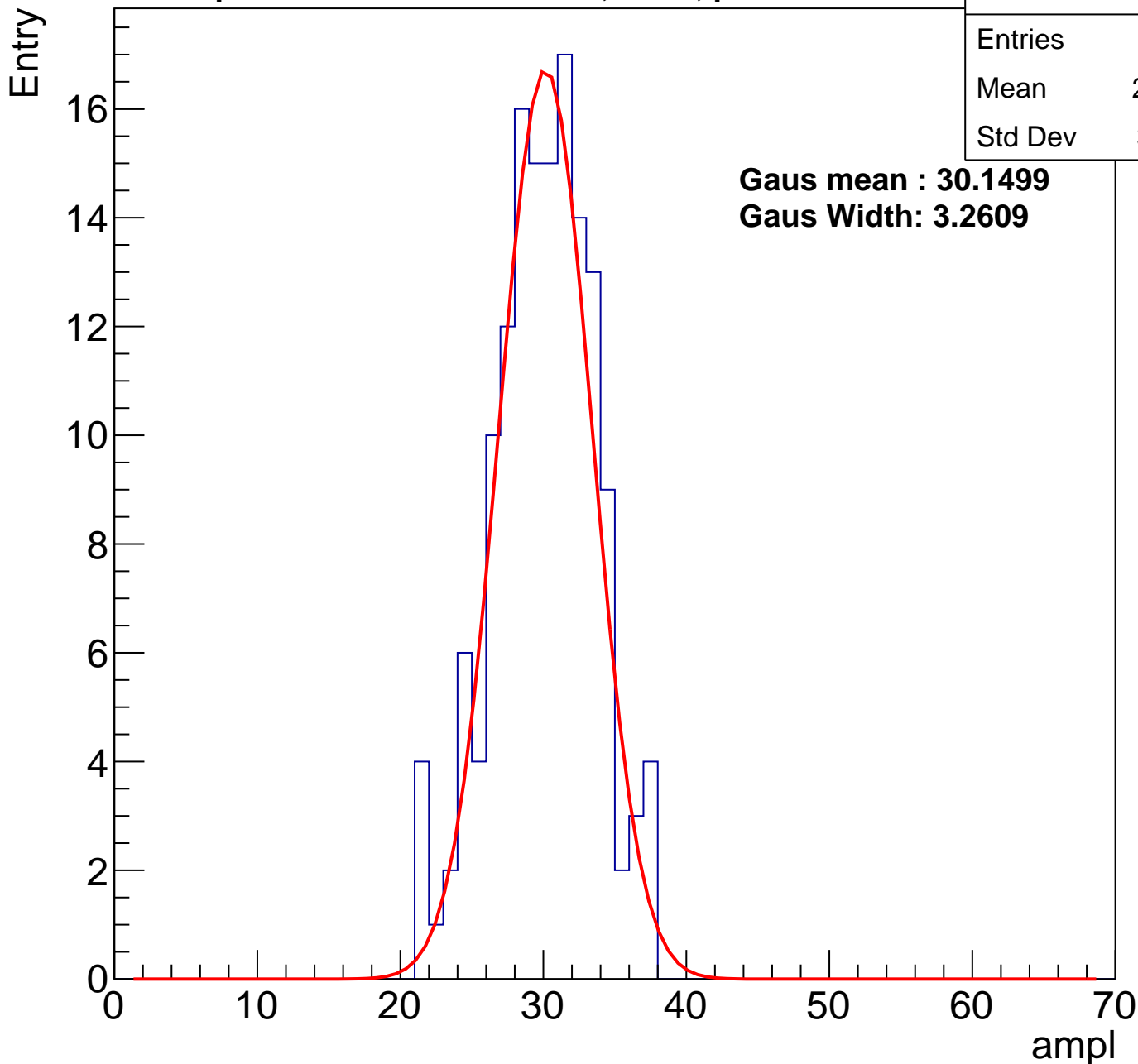
# B0L101S, U26-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	29.59
Std Dev	3.511

**Gaus mean : 30.1499**

**Gaus Width: 3.2609**



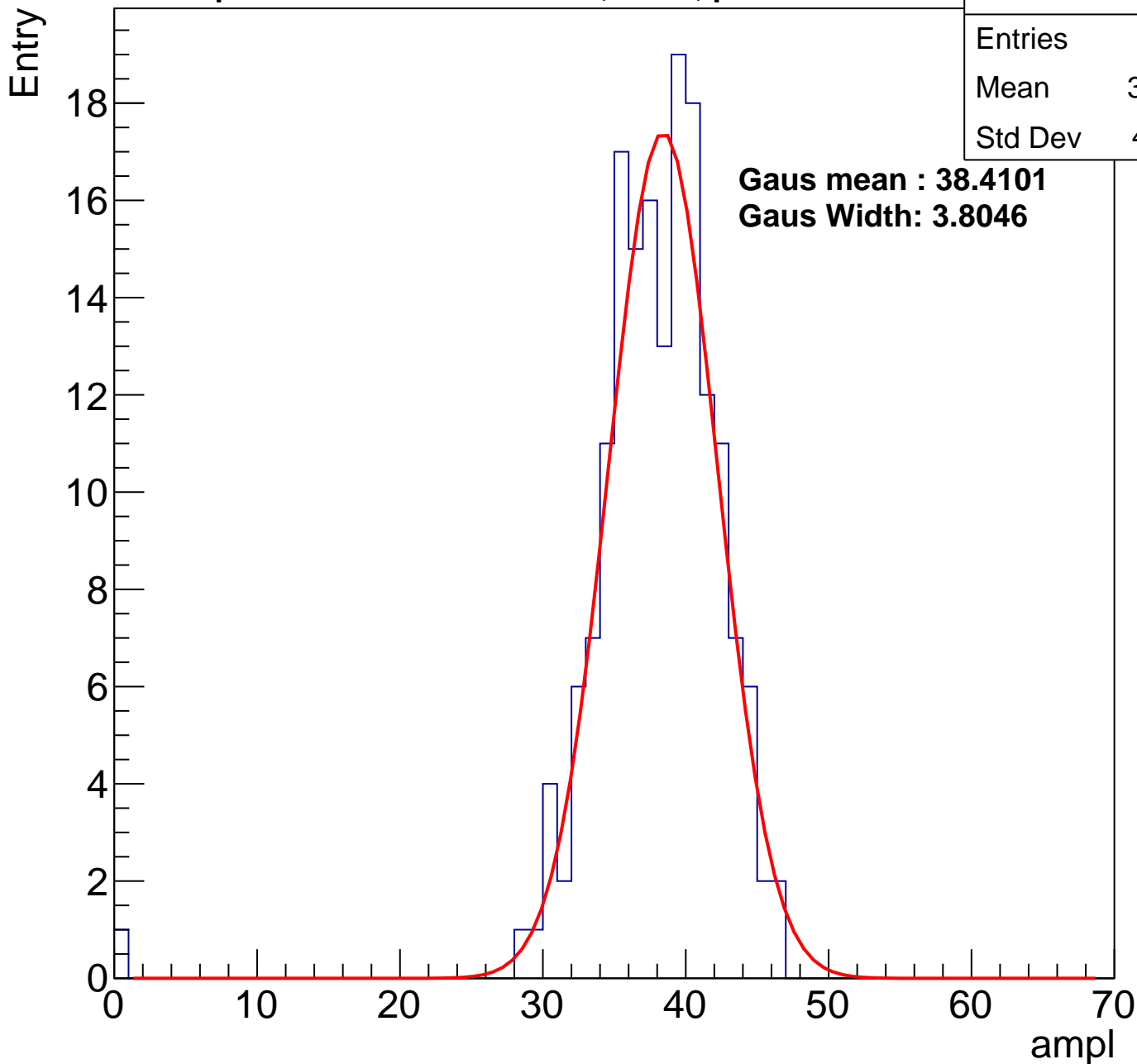
# B0L101S, U26-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	37.54
Std Dev	4.671

**Gaus mean : 38.4101**

**Gaus Width: 3.8046**



# B0L101S, U26-ch25, adc2

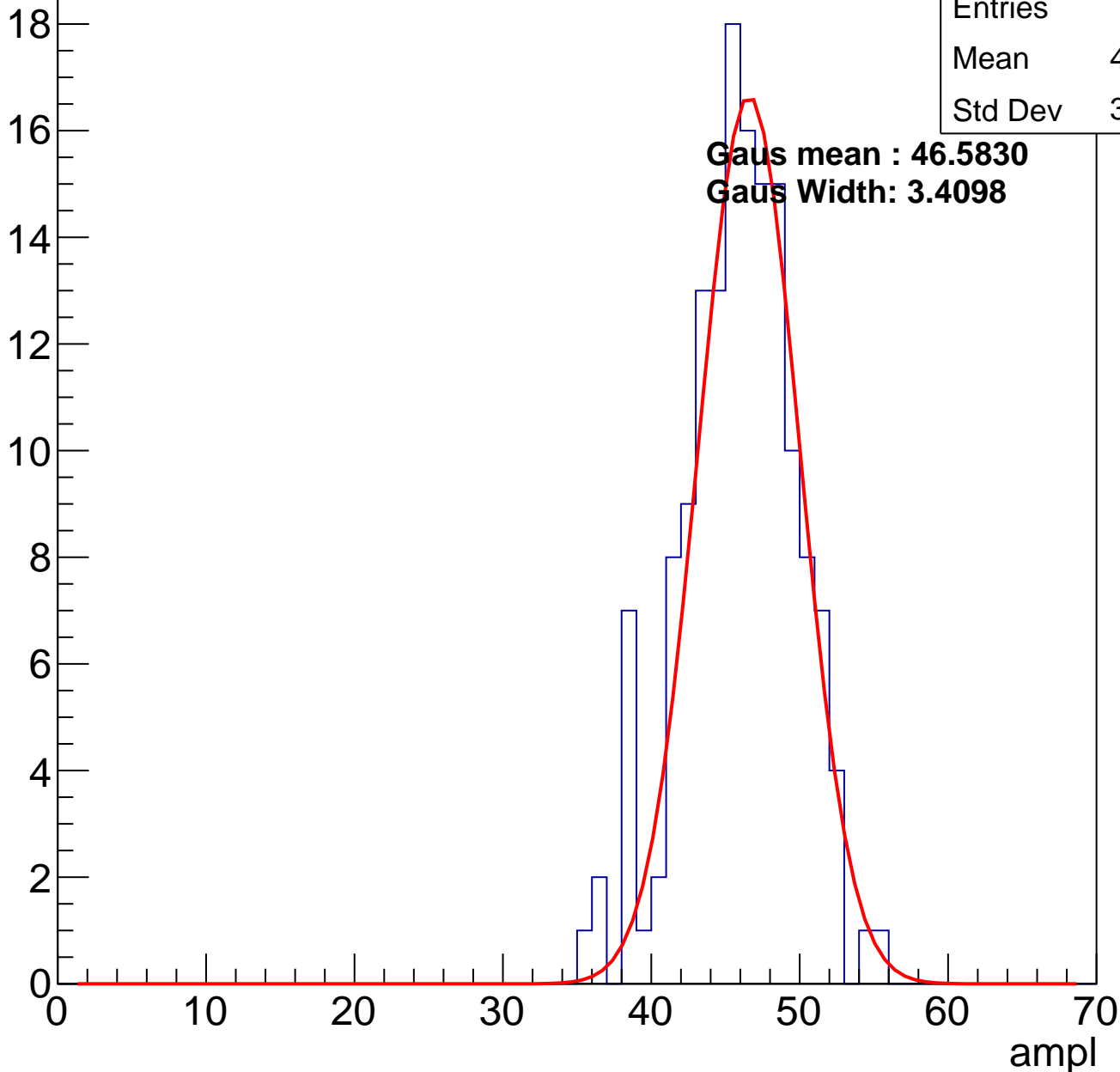
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	45.46
Std Dev	3.759

**Gaus mean : 46.5830**

**Gaus Width: 3.4098**

Entry



# B0L101S, U26-ch25, adc3

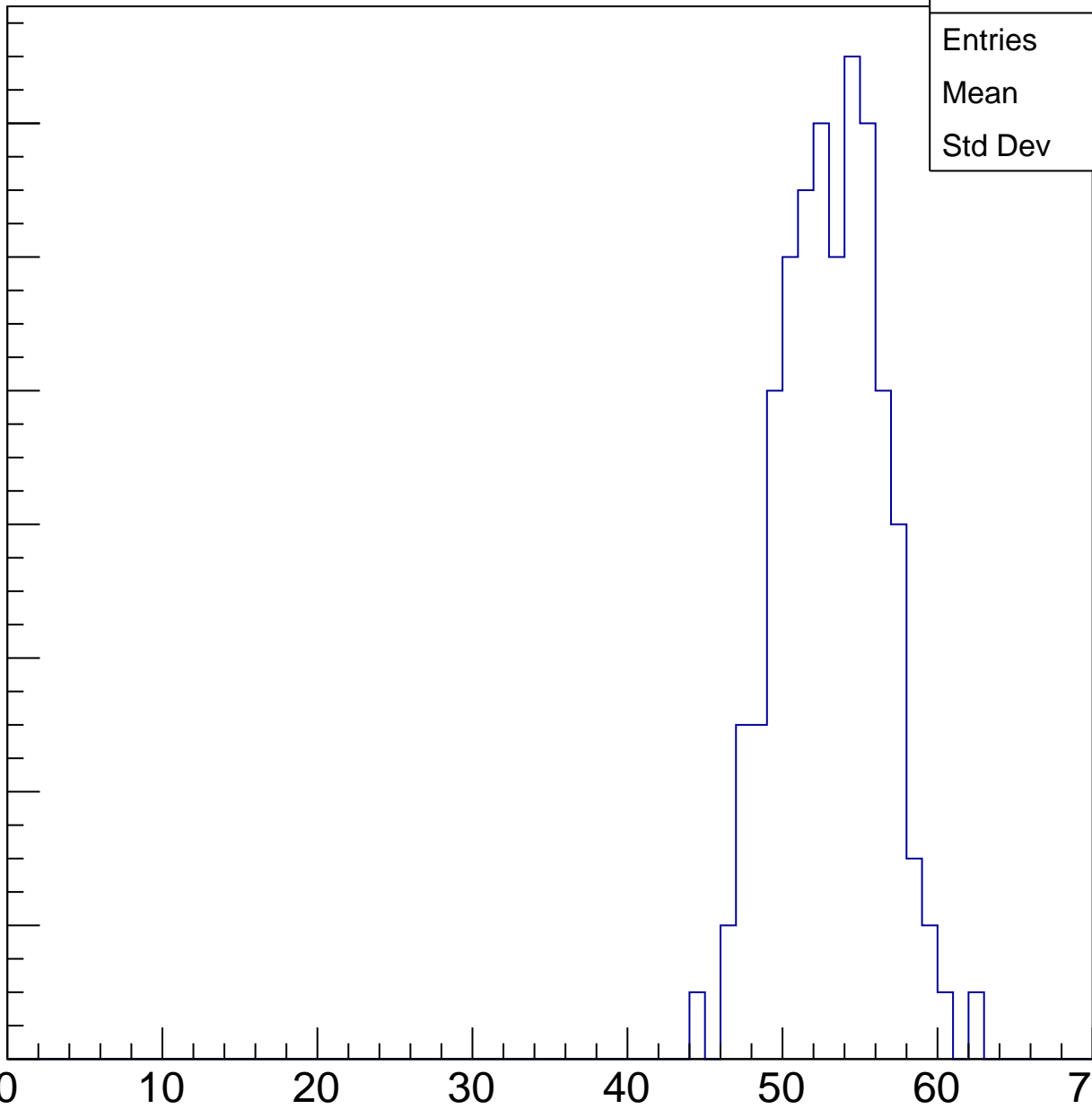
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	52.64
Std Dev	3.269

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch25, adc4

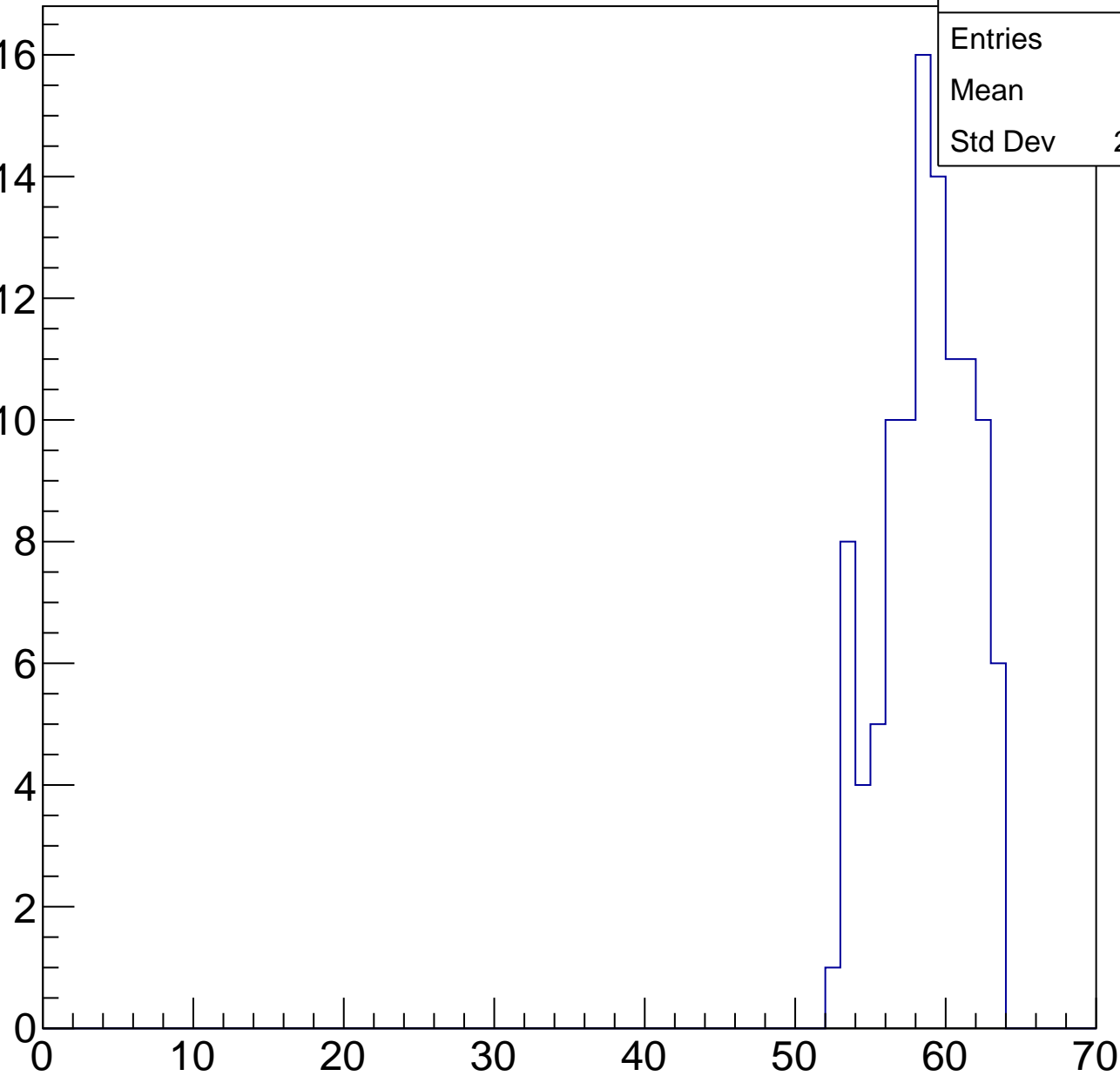
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	106
Mean	58.3
Std Dev	2.836

ampl



# B0L101S, U26-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

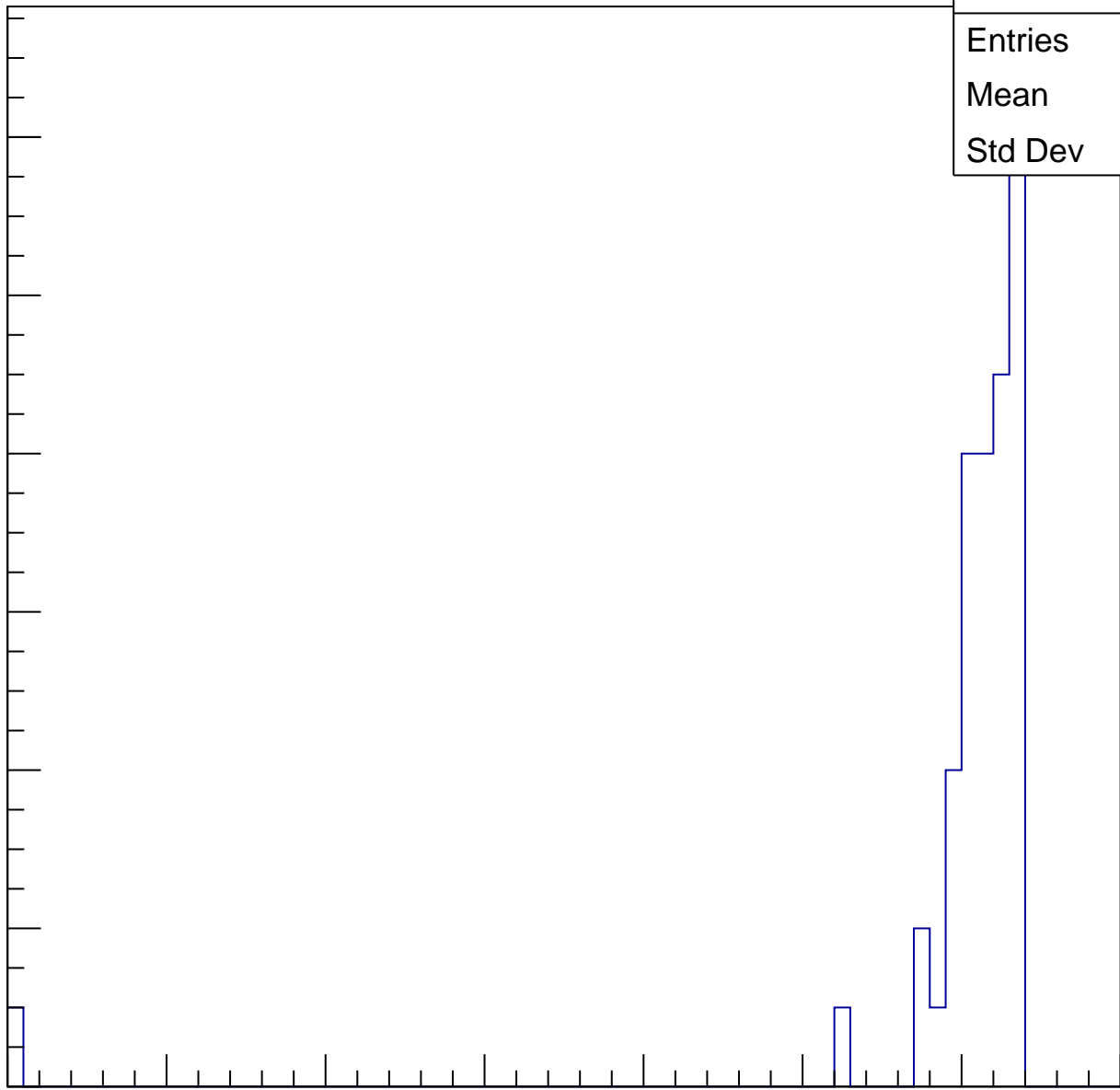
2

0

Entries	47
Mean	59.68
Std Dev	9.046

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch26, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	31.06
Std Dev	3.762

**Gaus mean : 31.4908**

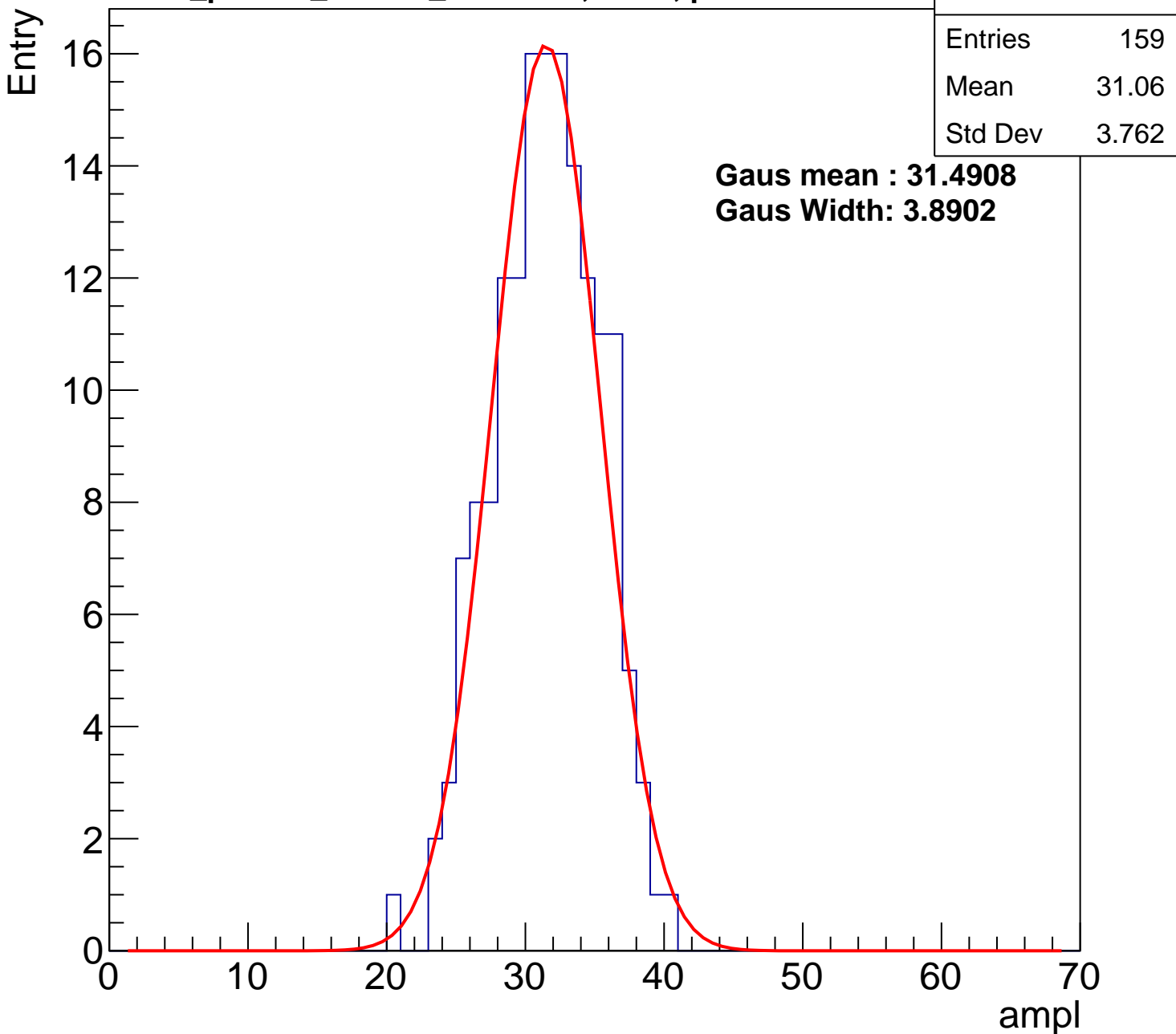
**Gaus Width: 3.8902**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	38.49
Std Dev	3.782

**Gaus mean : 39.1659**

**Gaus Width: 3.8069**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

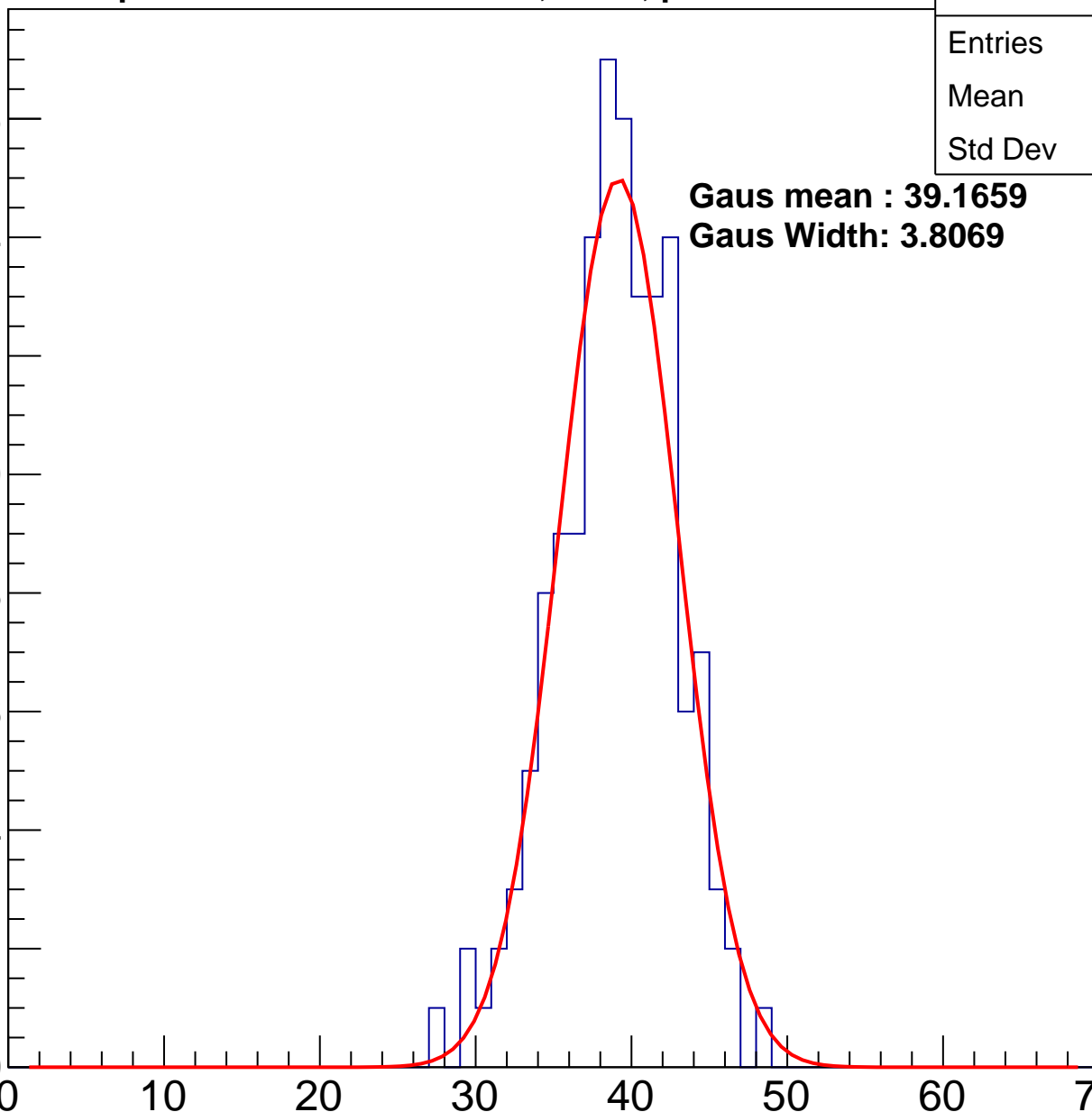
40

50

60

70

ampl



# B0L101S, U26-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

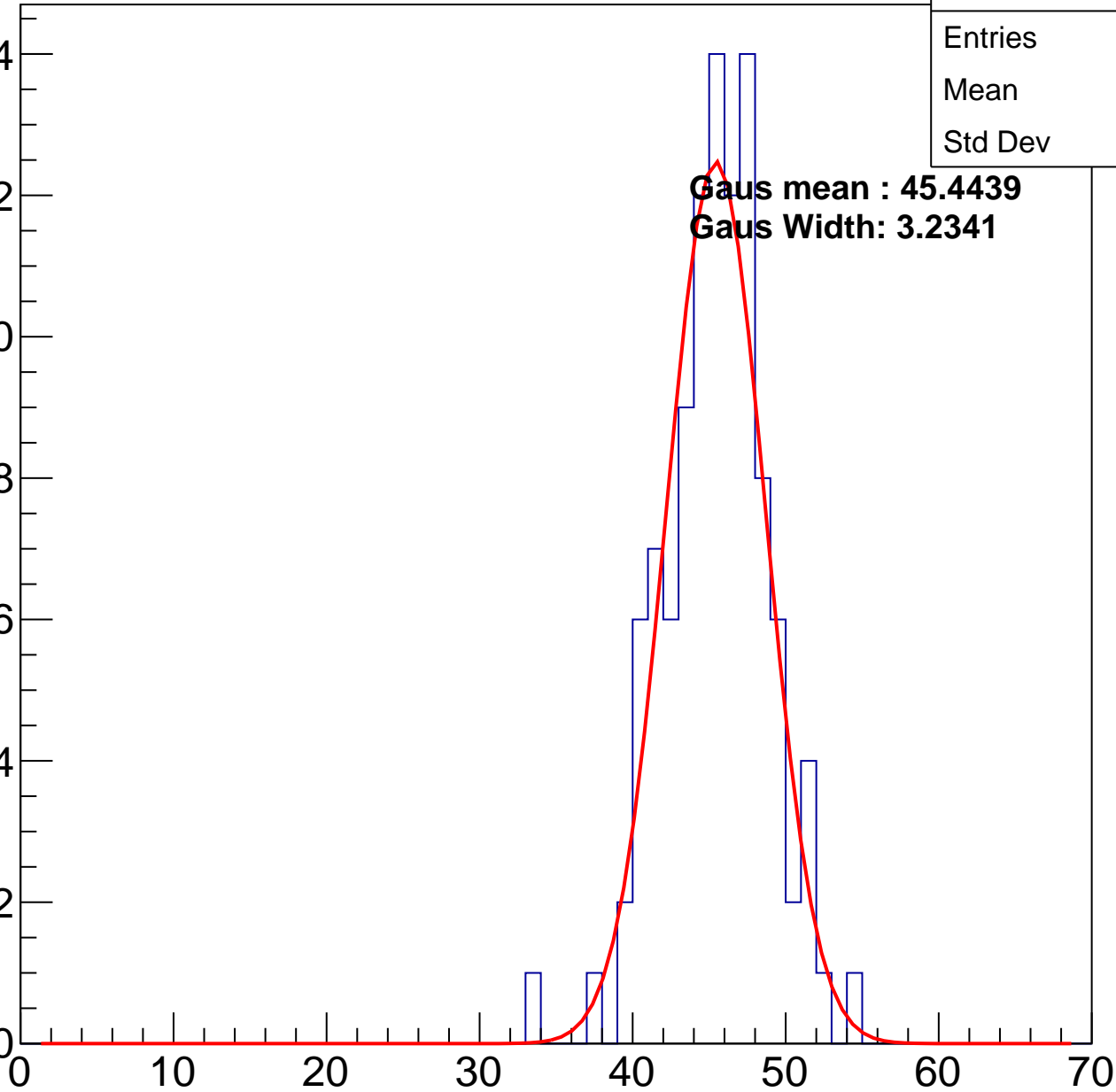
14  
12  
10  
8  
6  
4  
2  
0

Entries	106
Mean	45
Std Dev	3.37

**Gaus mean : 45.4439**

**Gaus Width: 3.2341**

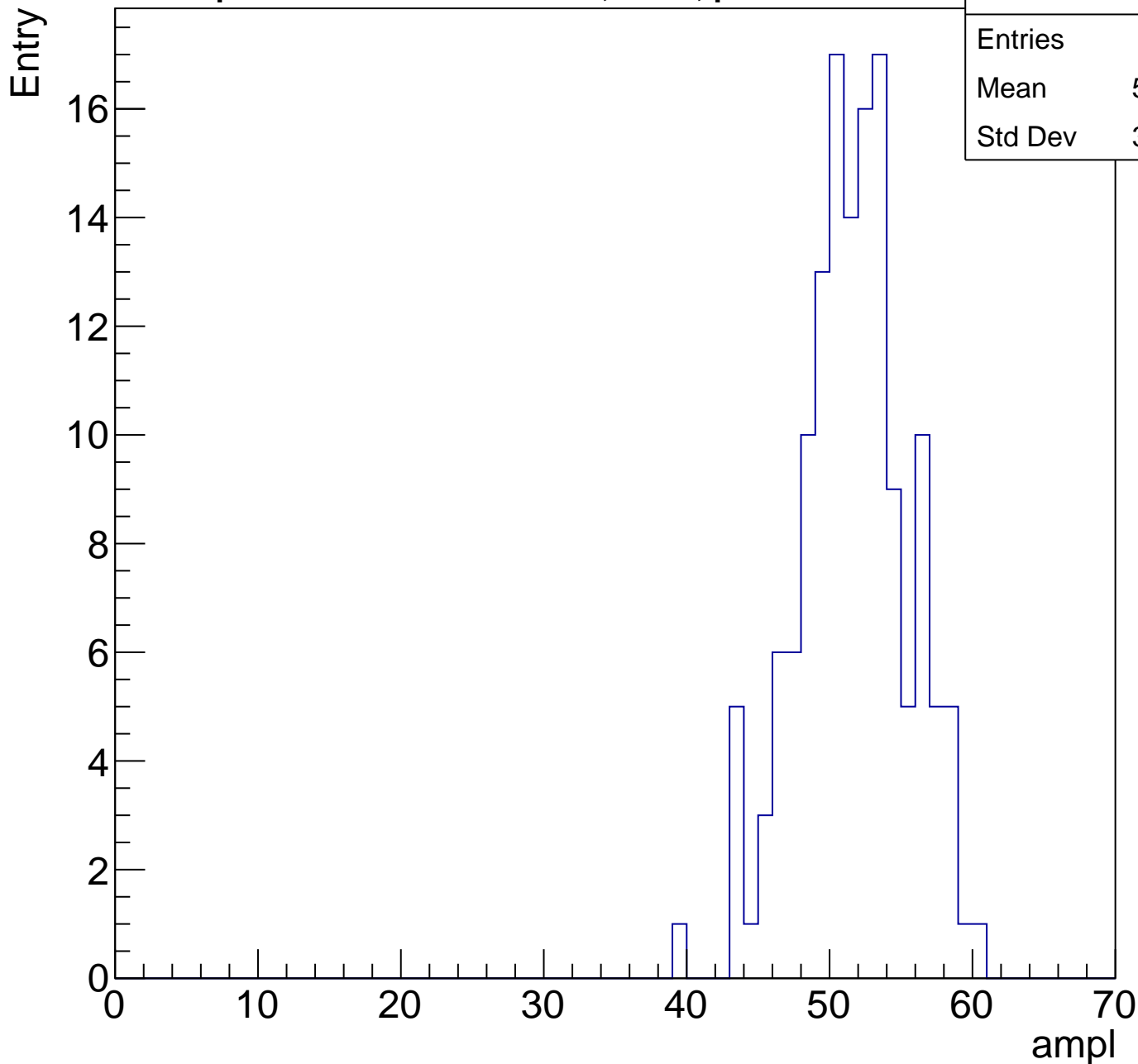
ampl



# B0L101S, U26-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	51.17
Std Dev	3.794



# B0L101S, U26-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	97
Mean	56.85
Std Dev	3.067

Entry

12

10

8

6

4

2

0

0

10

20

30

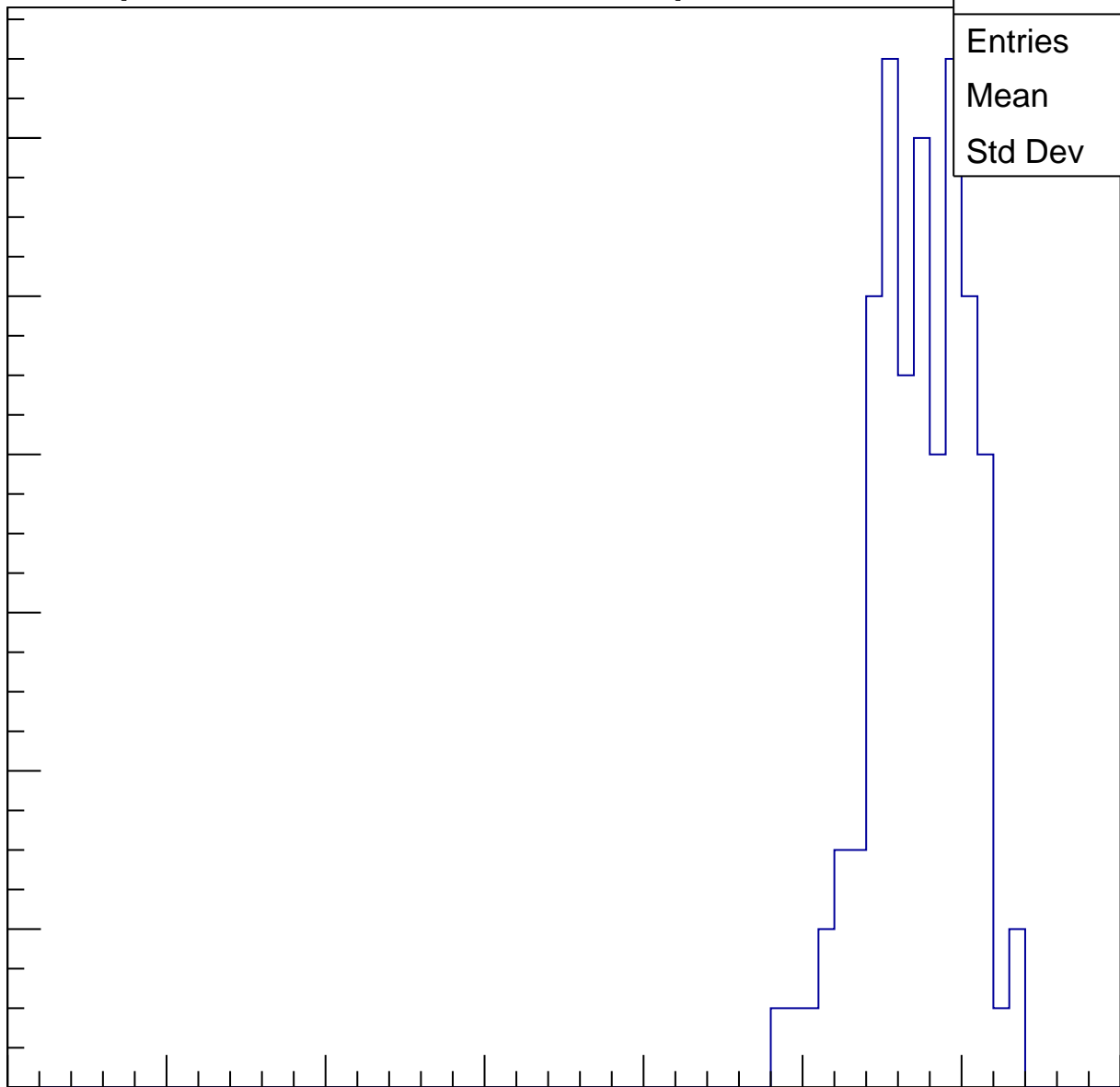
40

50

60

70

ampl



# B0L101S, U26-ch26, adc5

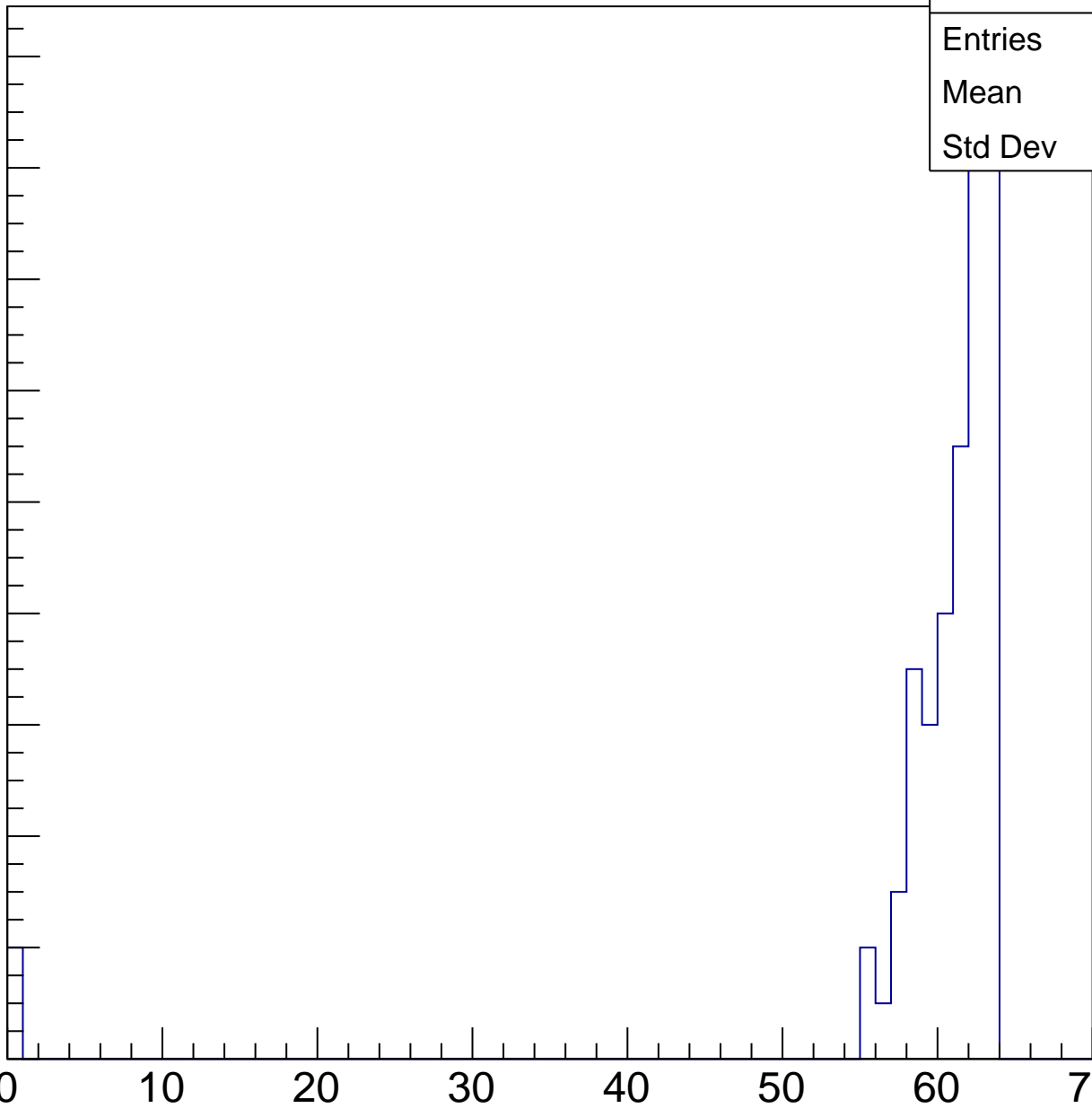
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	75
Mean	59.13
Std Dev	10

ampl



# B0L101S, U26-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

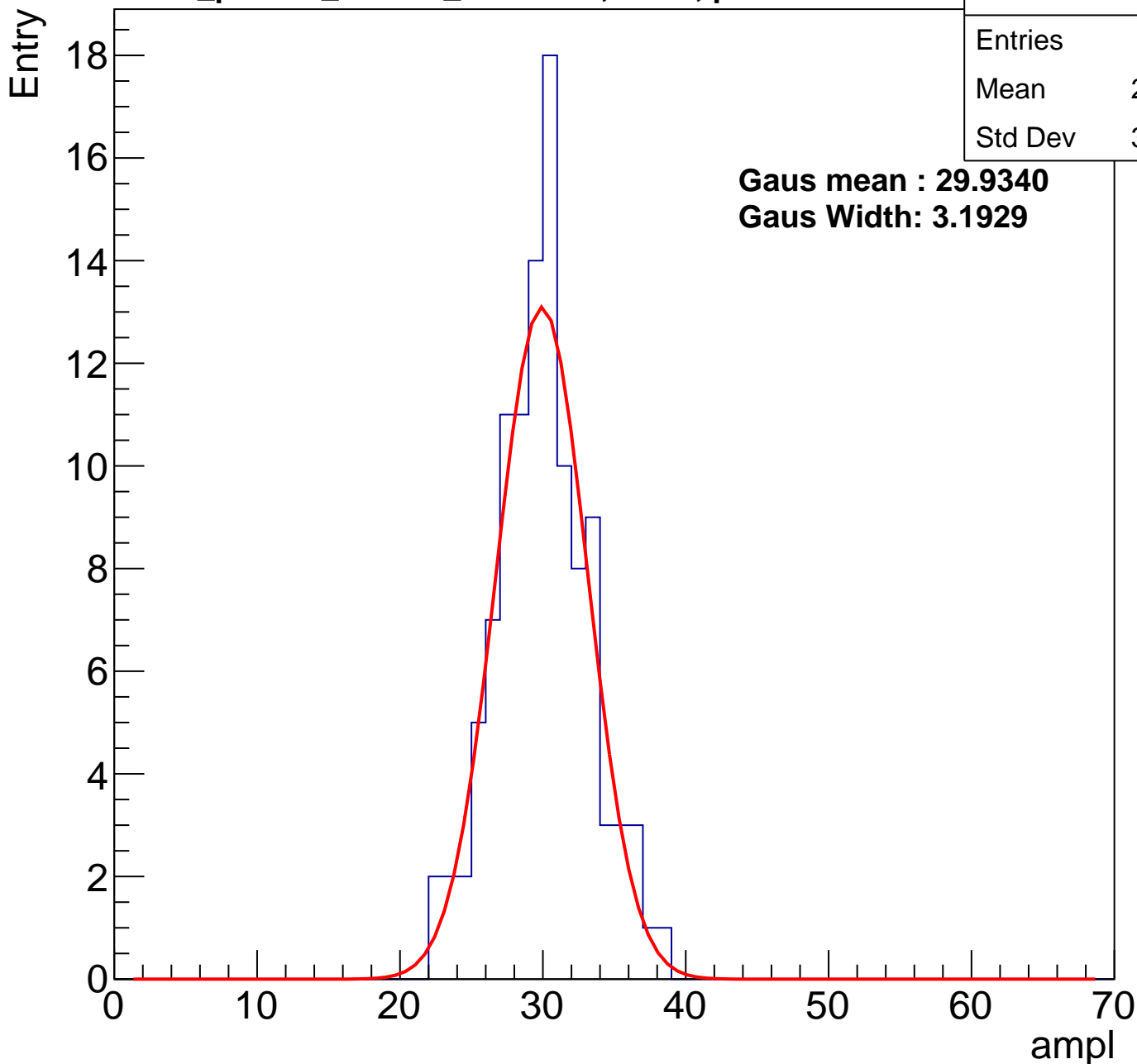
# B0L101S, U26-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	29.54
Std Dev	3.213

**Gaus mean : 29.9340**

**Gaus Width: 3.1929**



# B0L101S, U26-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	35.75
Std Dev	3.656

**Gaus mean : 36.5554**

**Gaus Width: 3.3680**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	42.55
Std Dev	3.763

**Gaus mean : 42.7742**

**Gaus Width: 3.5264**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

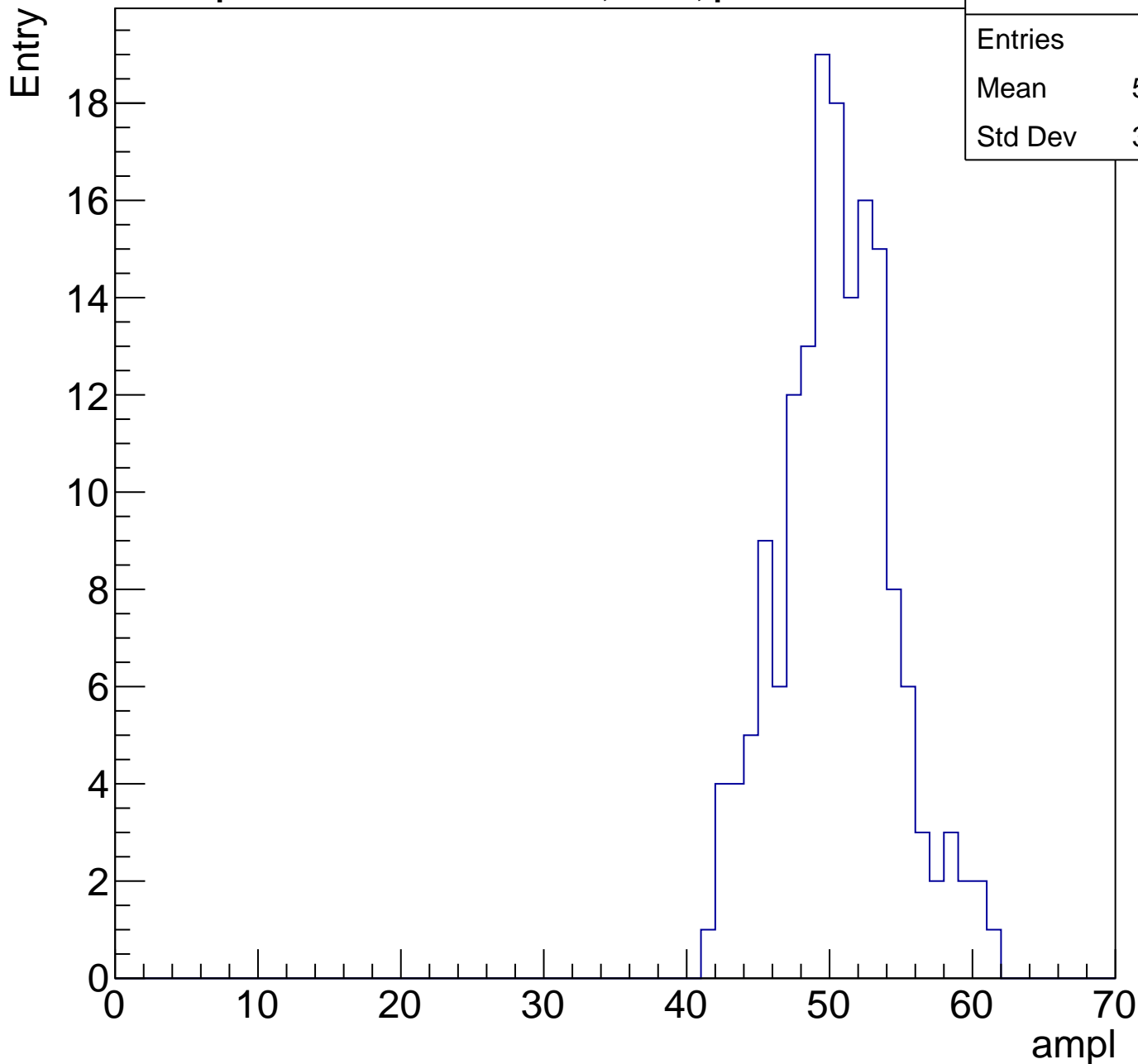
60

70

# B0L101S, U26-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	50.06
Std Dev	3.982

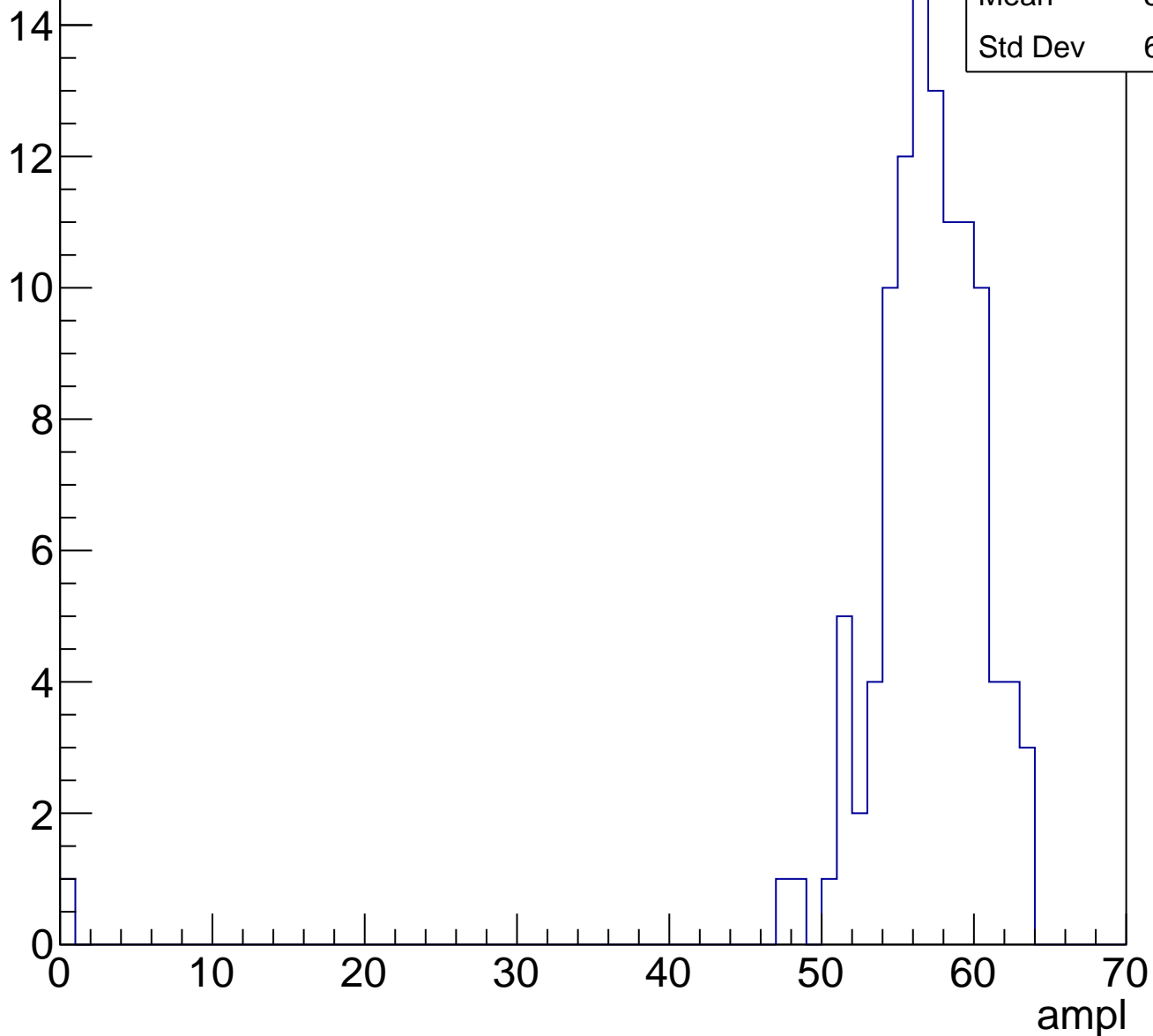


# B0L101S, U26-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	56.16
Std Dev	6.285

Entry



# B0L101S, U26-ch27, adc5

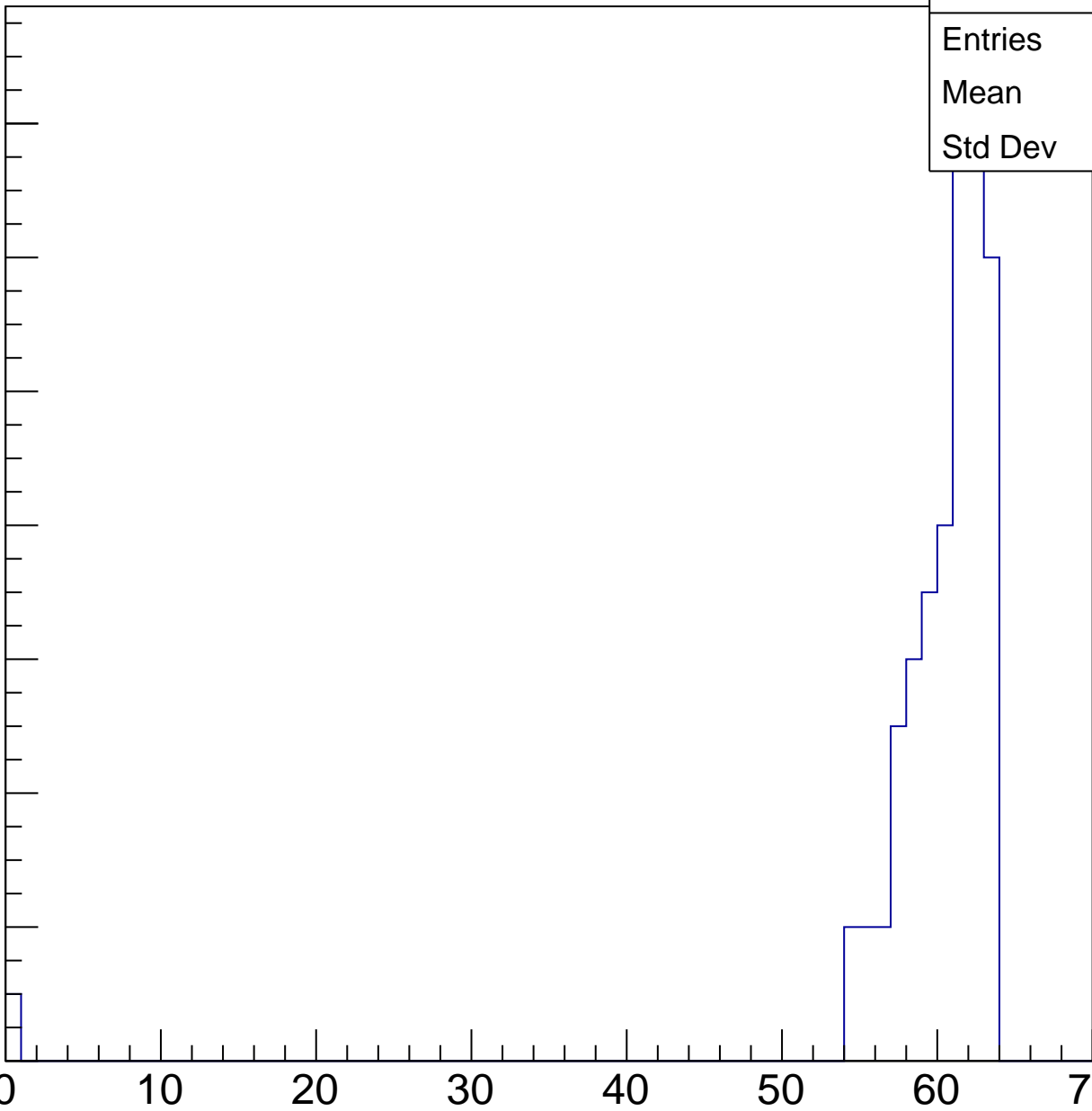
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	74
Mean	59.39
Std Dev	7.335

ampl

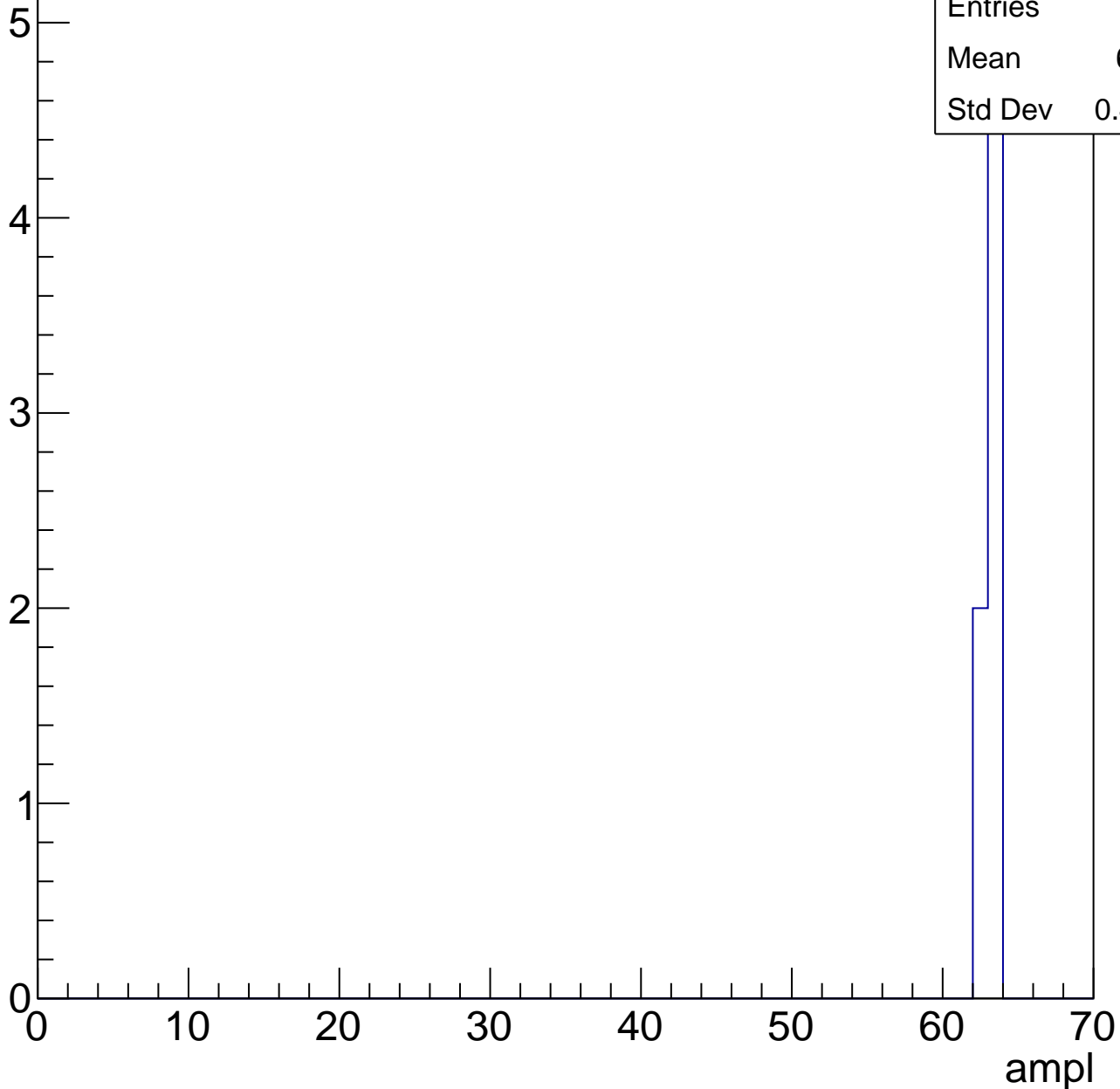


# B0L101S, U26-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.71
Std Dev	0.4518

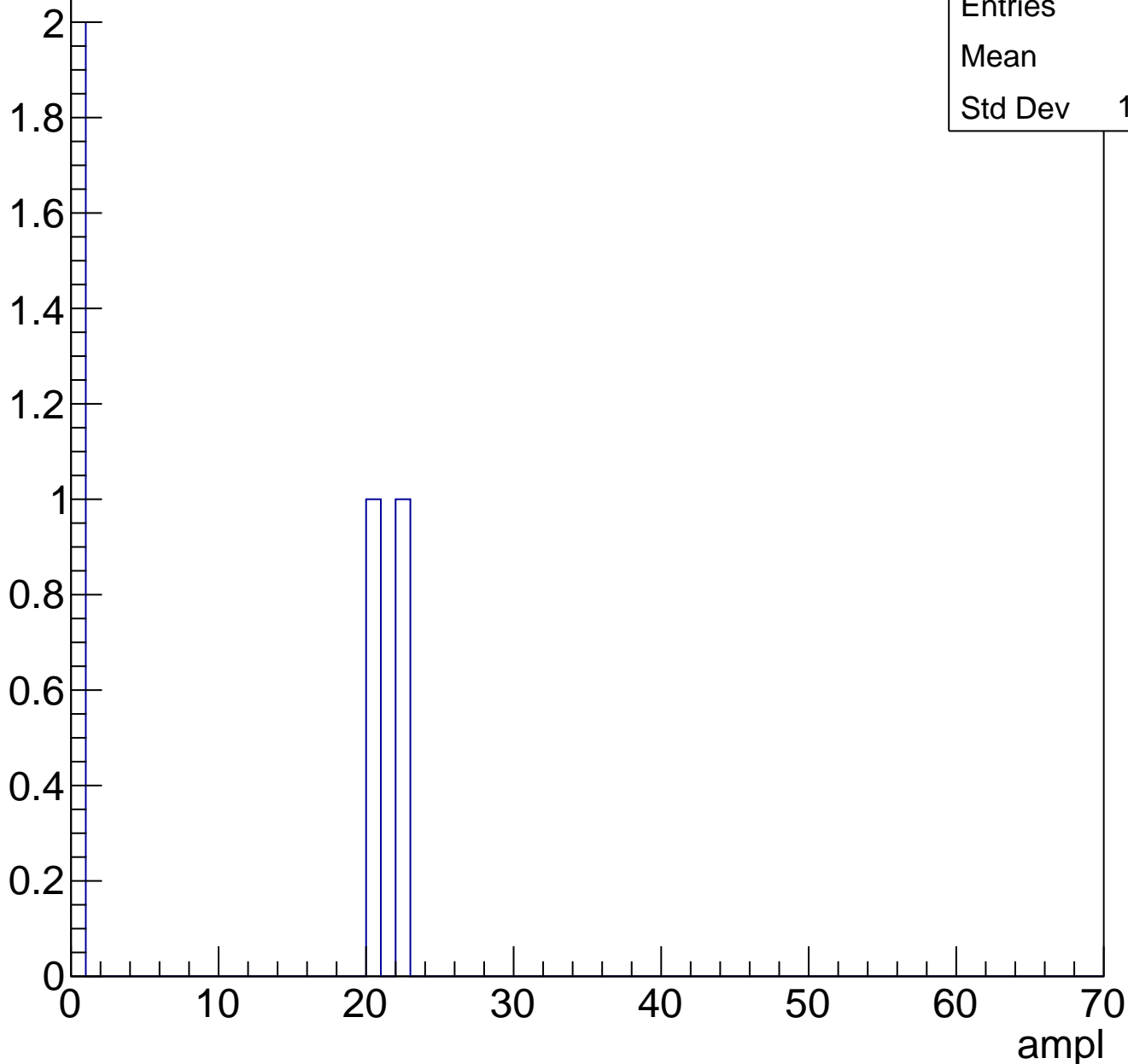




# B0L101S, U26-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



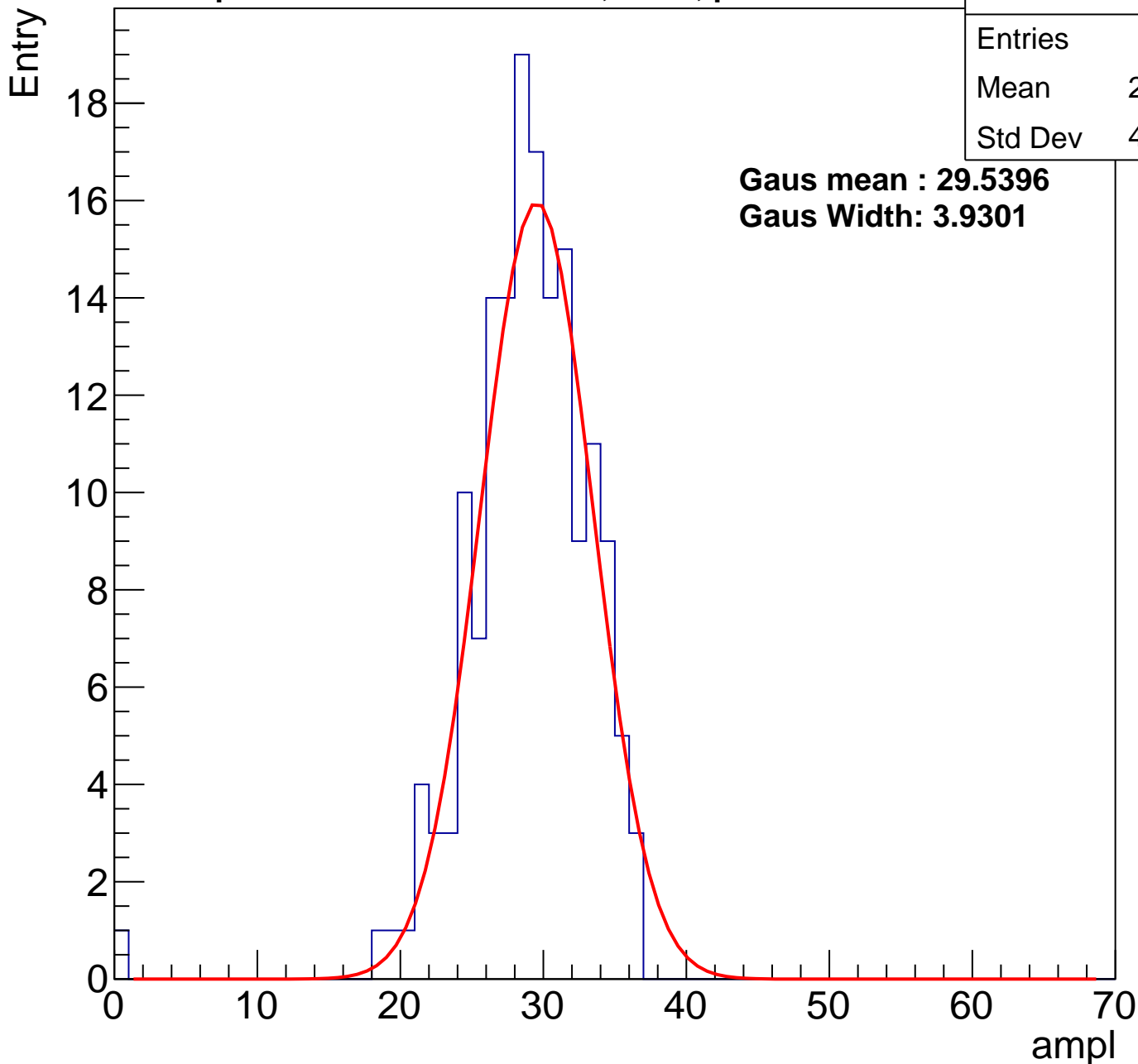
# B0L101S, U26-ch28, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	28.47
Std Dev	4.355

**Gaus mean : 29.5396**

**Gaus Width: 3.9301**



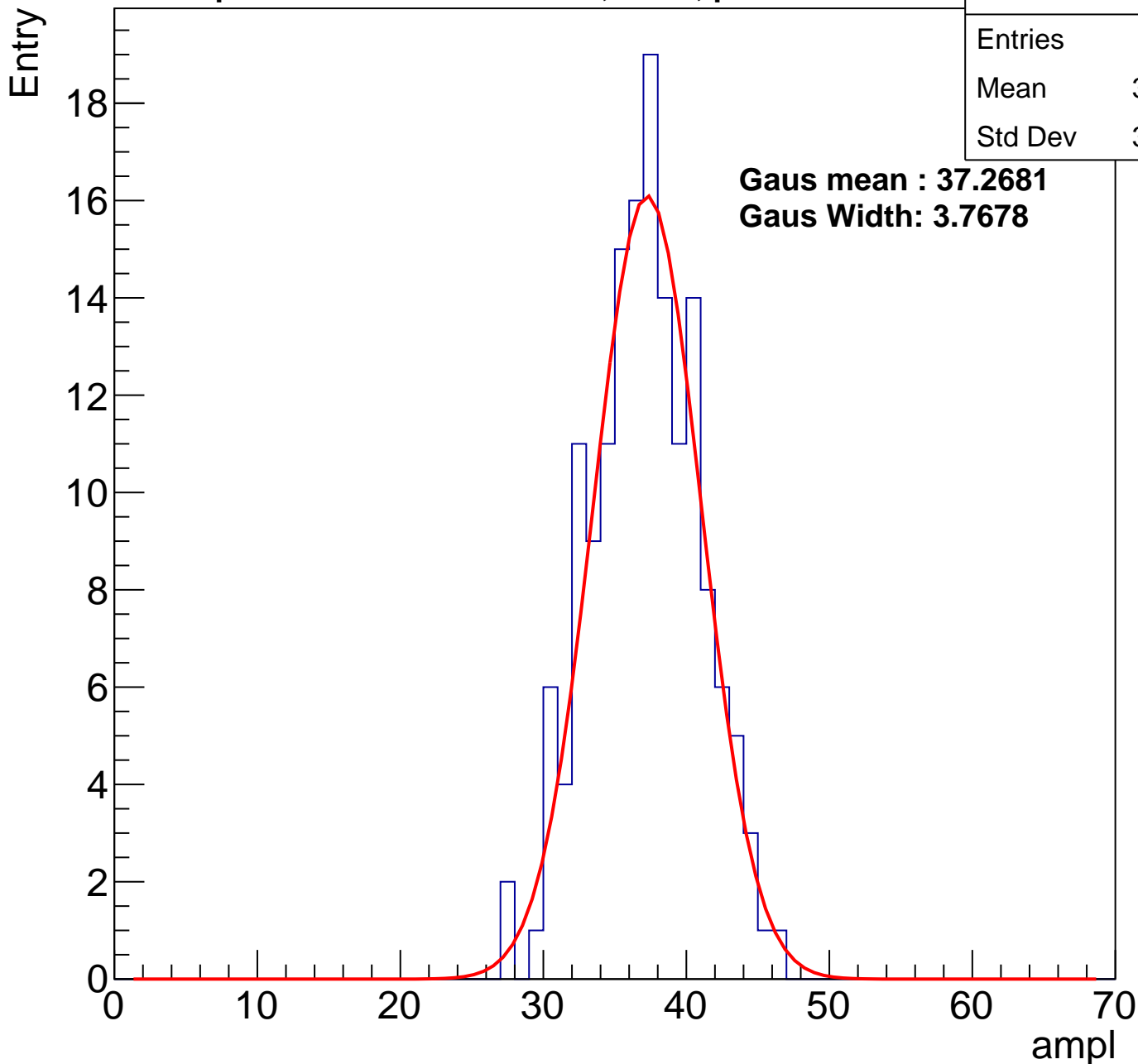
# B0L101S, U26-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	157
Mean	36.64
Std Dev	3.752

**Gaus mean : 37.2681**

**Gaus Width: 3.7678**



# B0L101S, U26-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

43.46

Std Dev

3.327

**Gaus mean : 44.0206**

**Gaus Width: 3.3515**

0

10

20

30

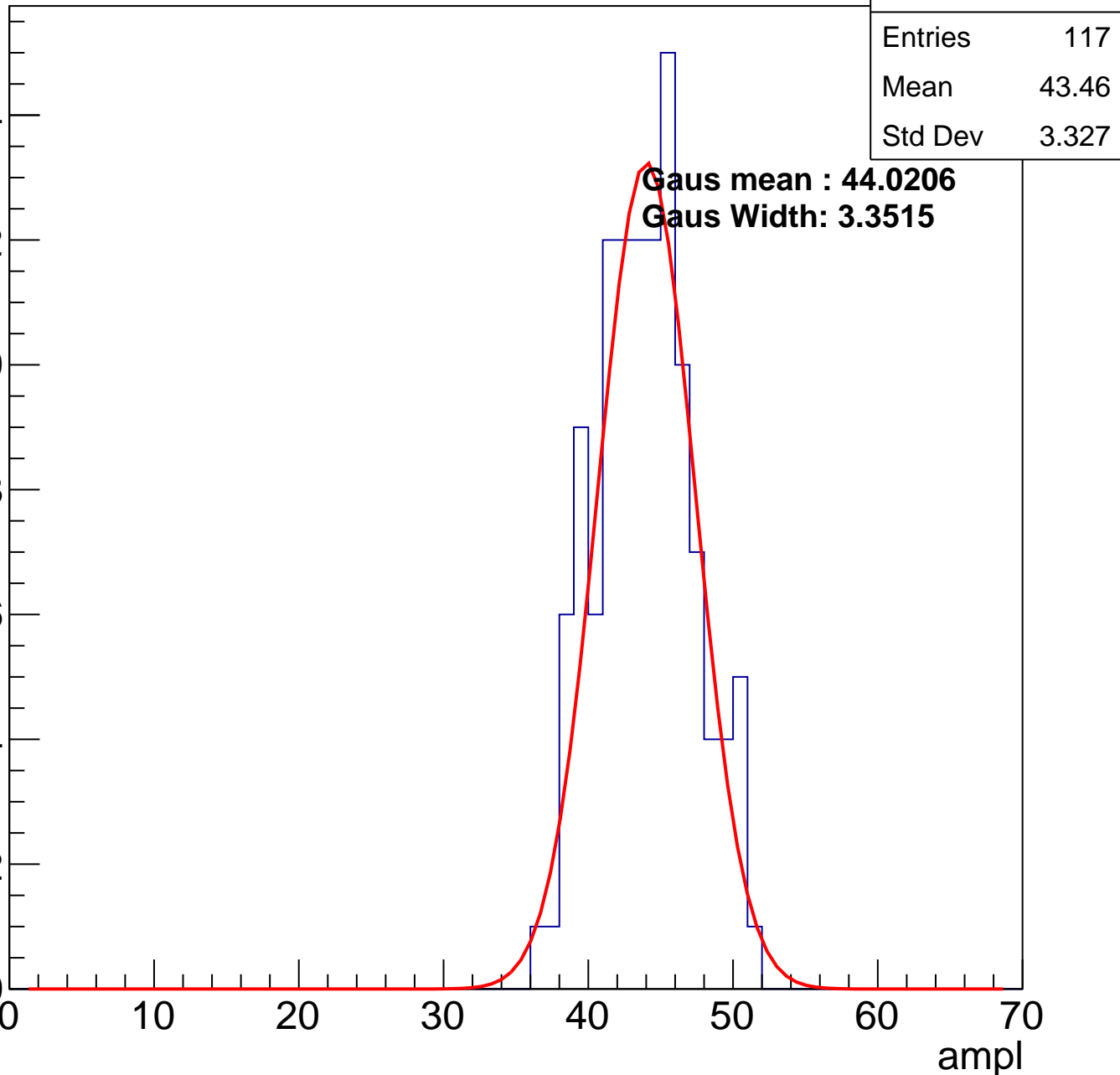
40

50

60

70

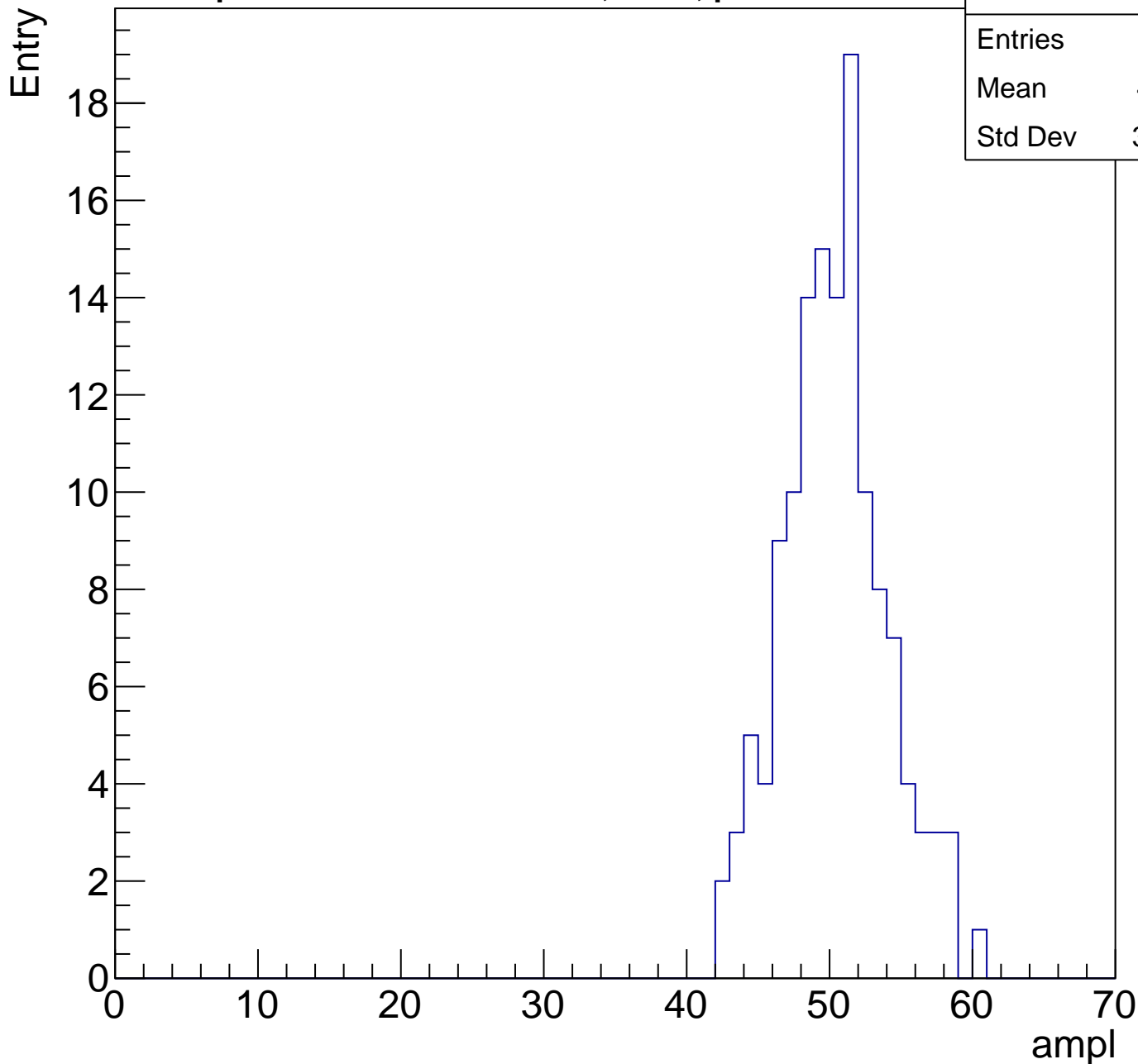
ampl



# B0L101S, U26-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	49.91
Std Dev	3.613



# B0L101S, U26-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	55.74
Std Dev	3.544

Entry

12

10

8

6

4

2

0

0

10

20

30

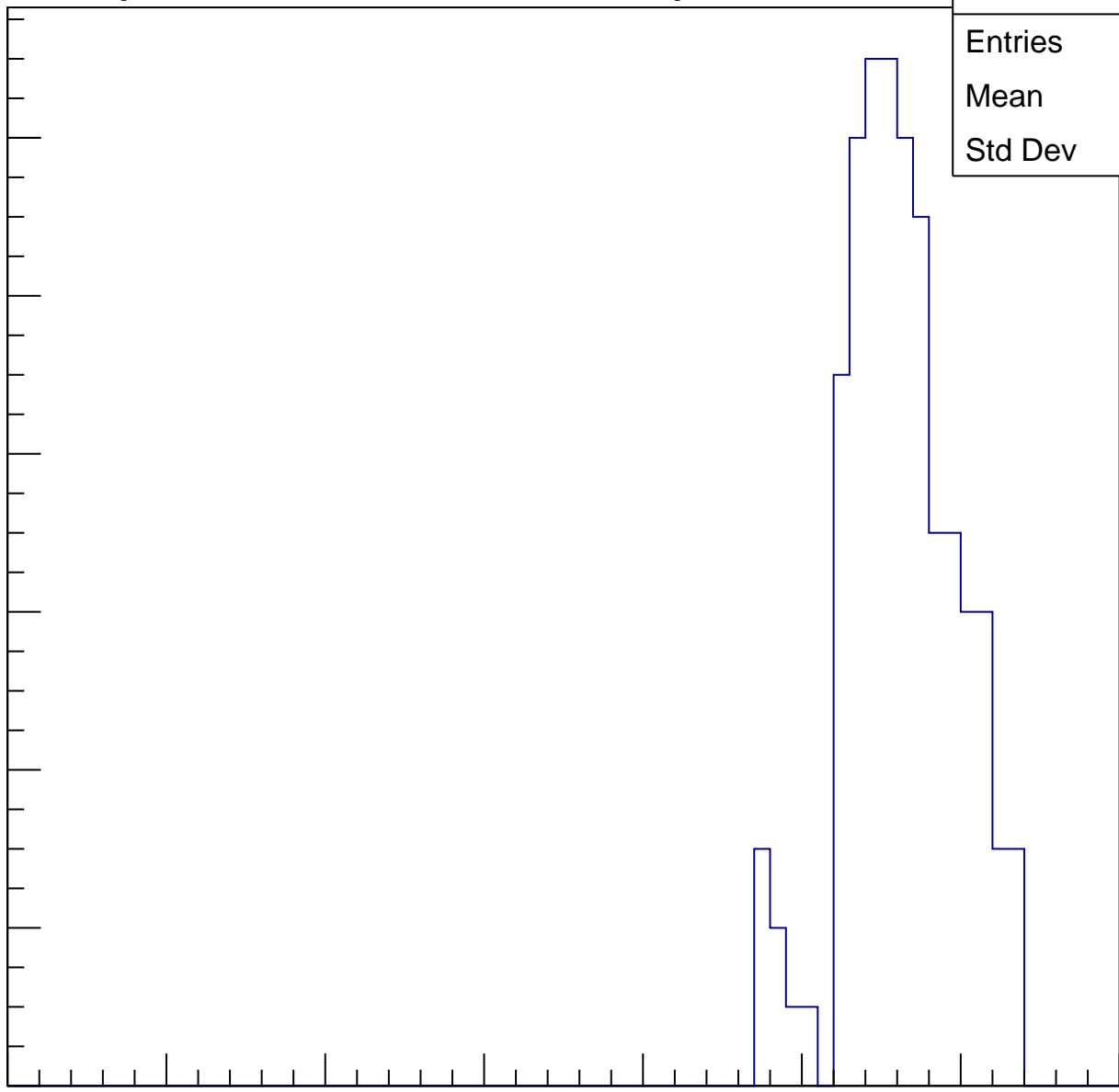
40

50

60

70

ampl



# B0L101S, U26-ch28, adc5

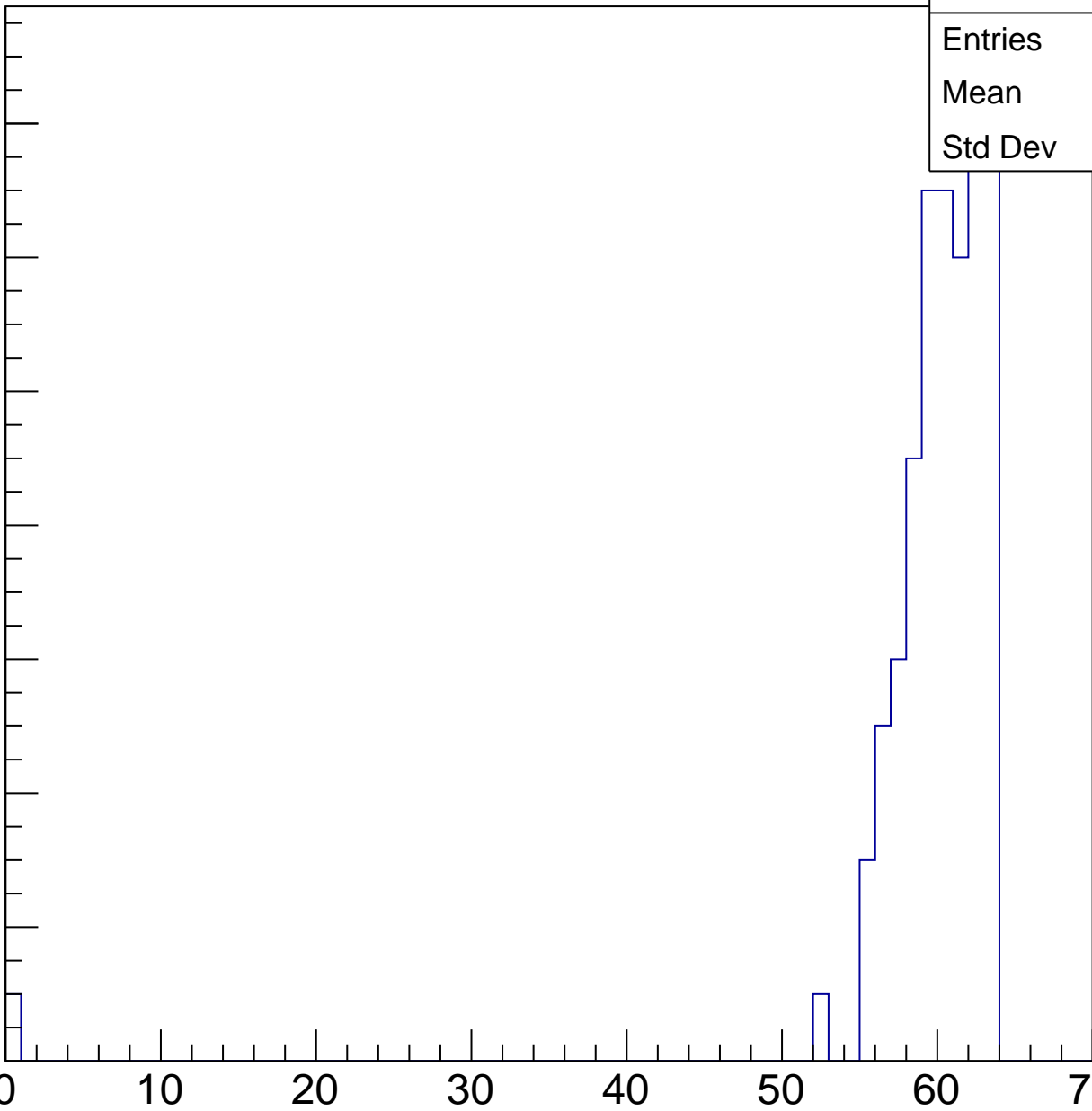
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	93
Mean	59.3
Std Dev	6.626

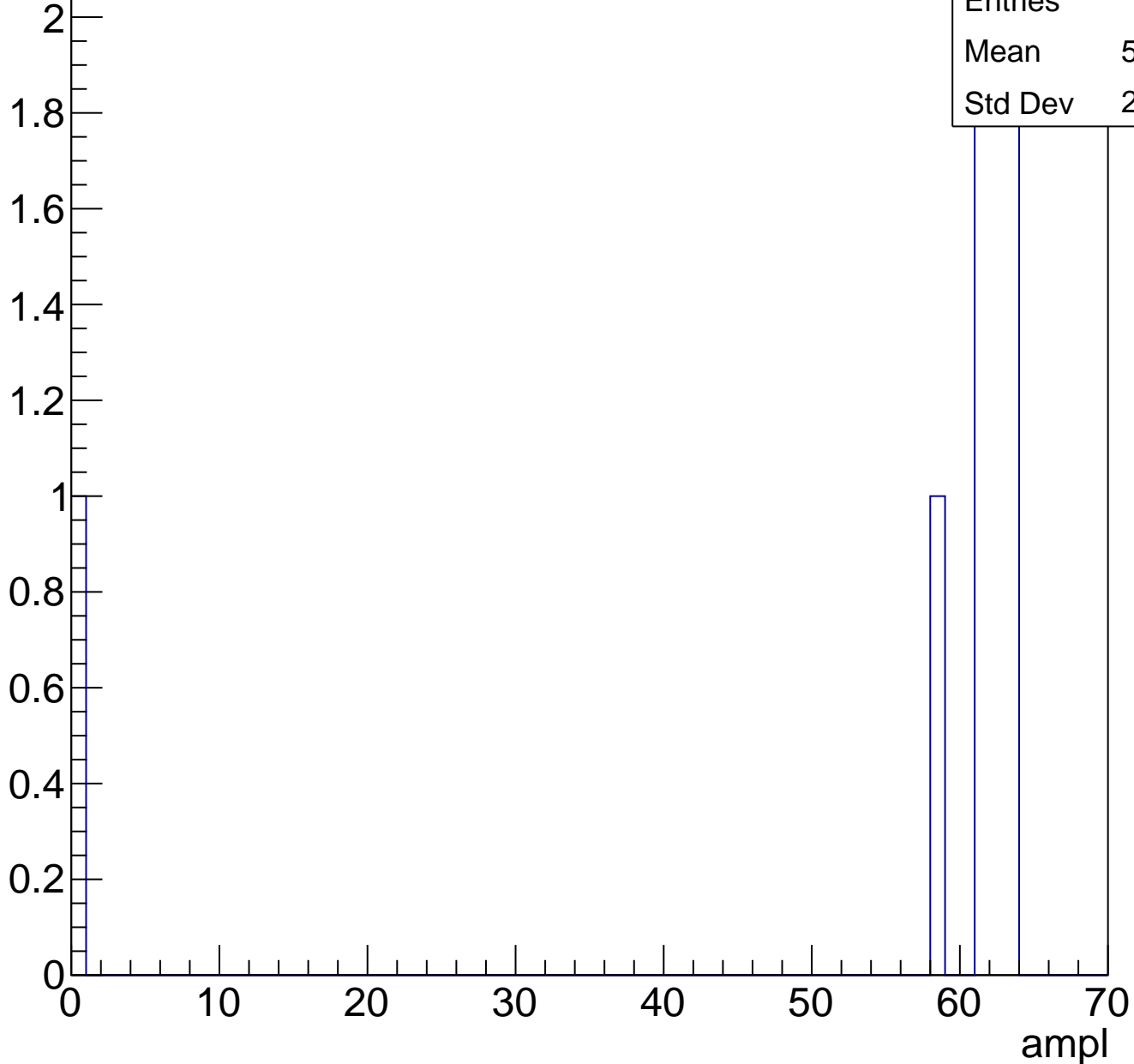
ampl



# B0L101S, U26-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch29, adc0

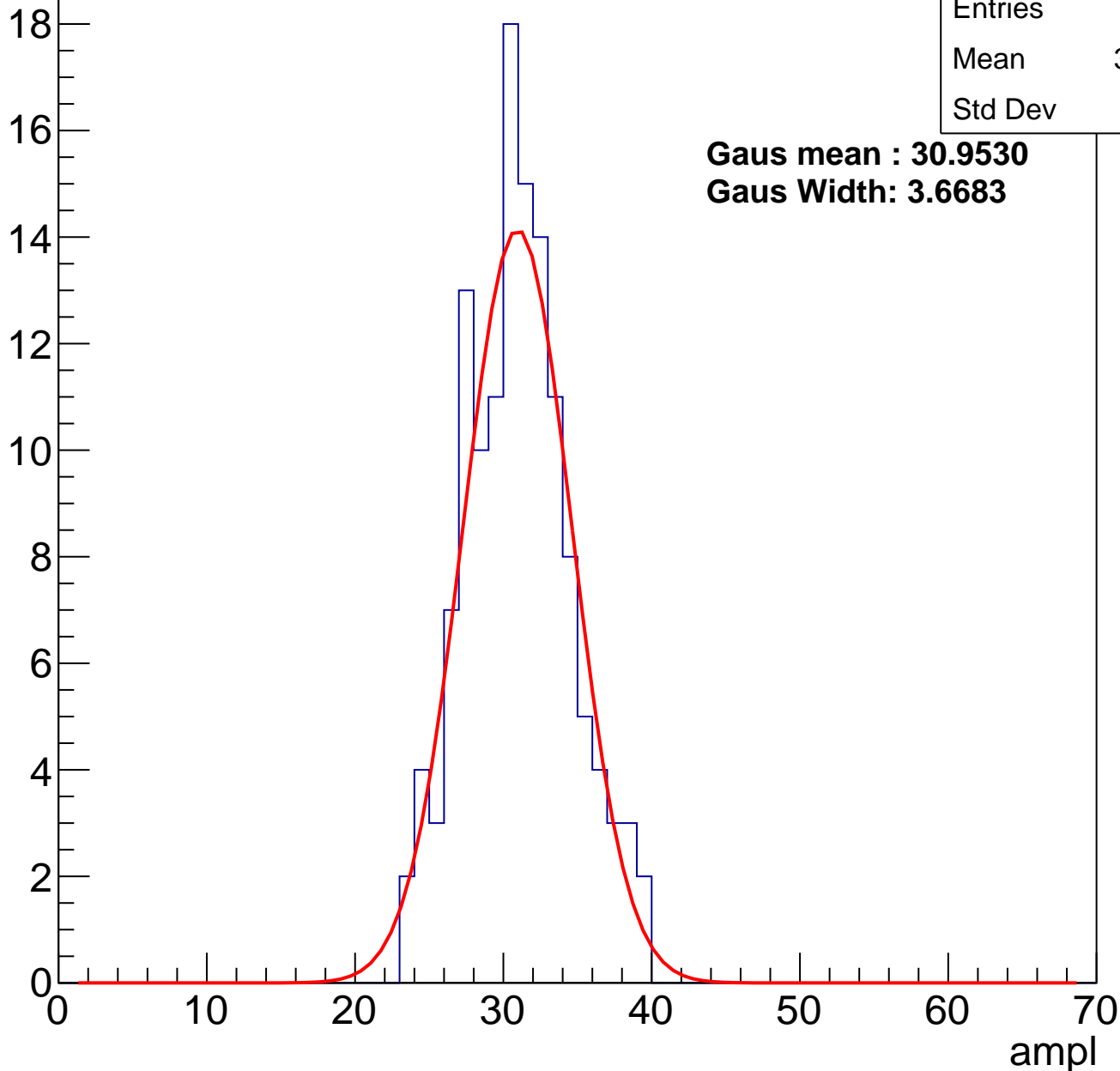
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	30.52
Std Dev	3.5

**Gaus mean : 30.9530**

**Gaus Width: 3.6683**

Entry



# B0L101S, U26-ch29, adc1

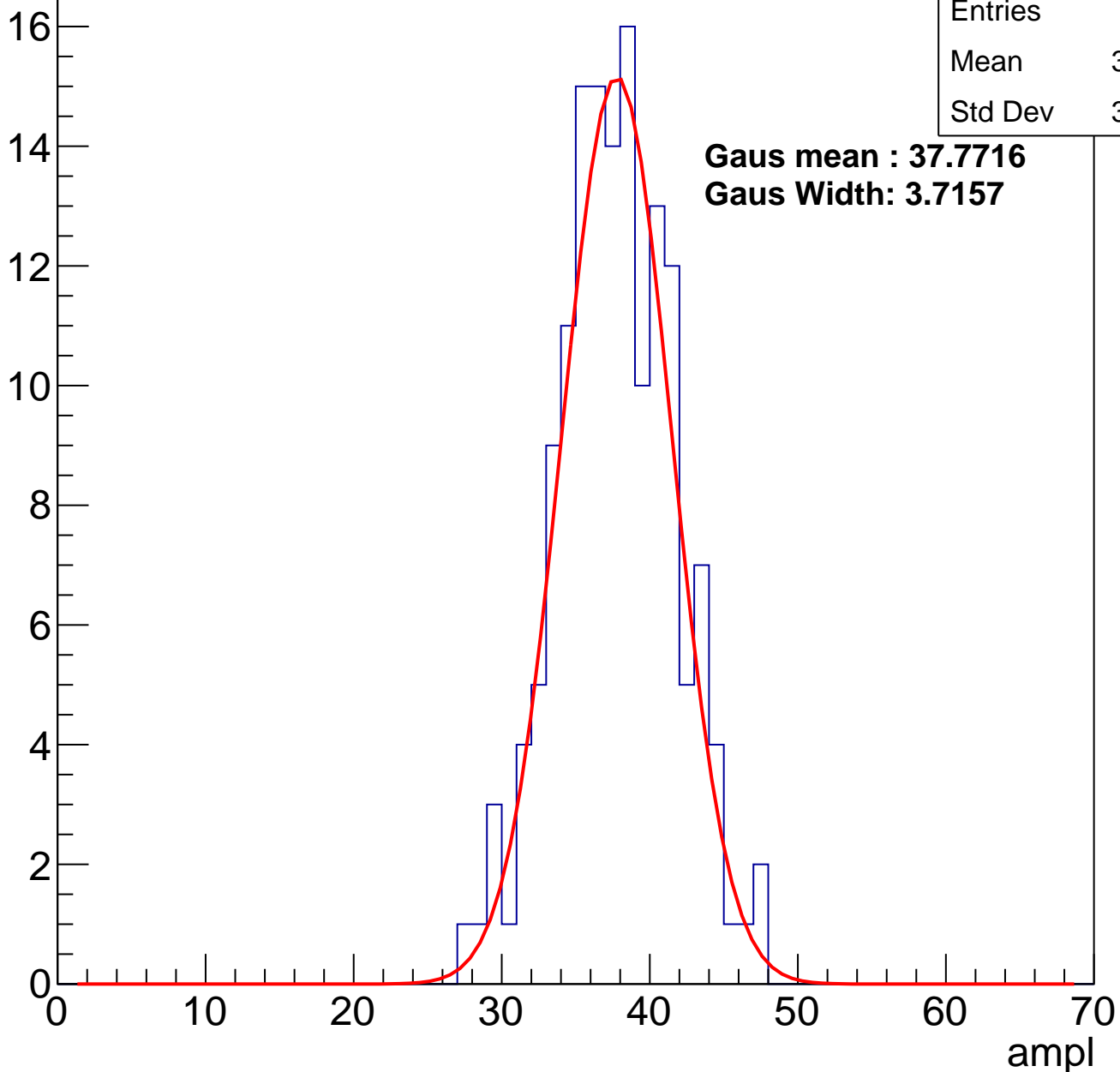
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	37.28
Std Dev	3.899

**Gaus mean : 37.7716**

**Gaus Width: 3.7157**

Entry



# B0L101S, U26-ch29, adc2

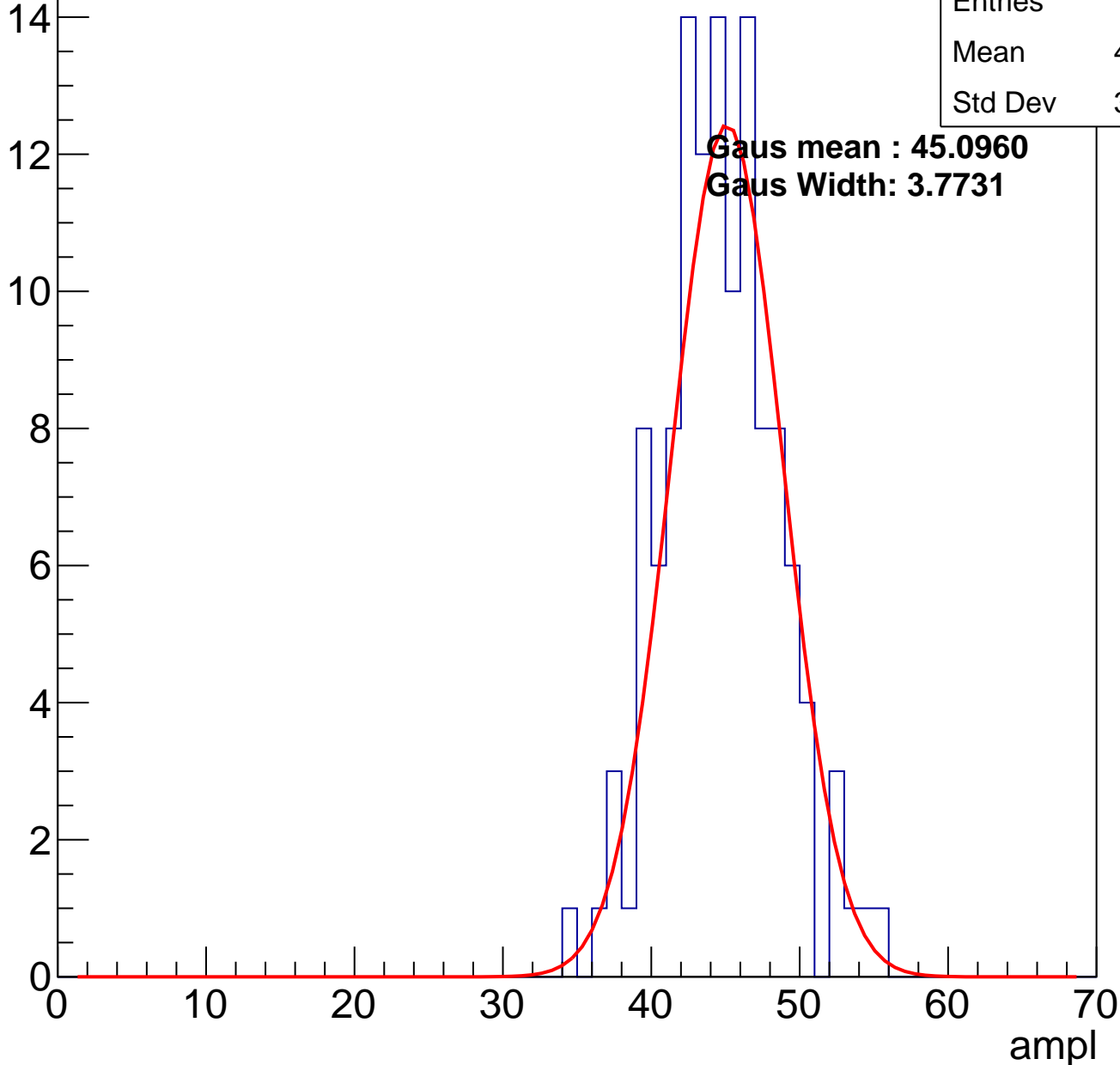
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	44.23
Std Dev	3.842

**Gaus mean : 45.0960**

**Gaus Width: 3.7731**

Entry

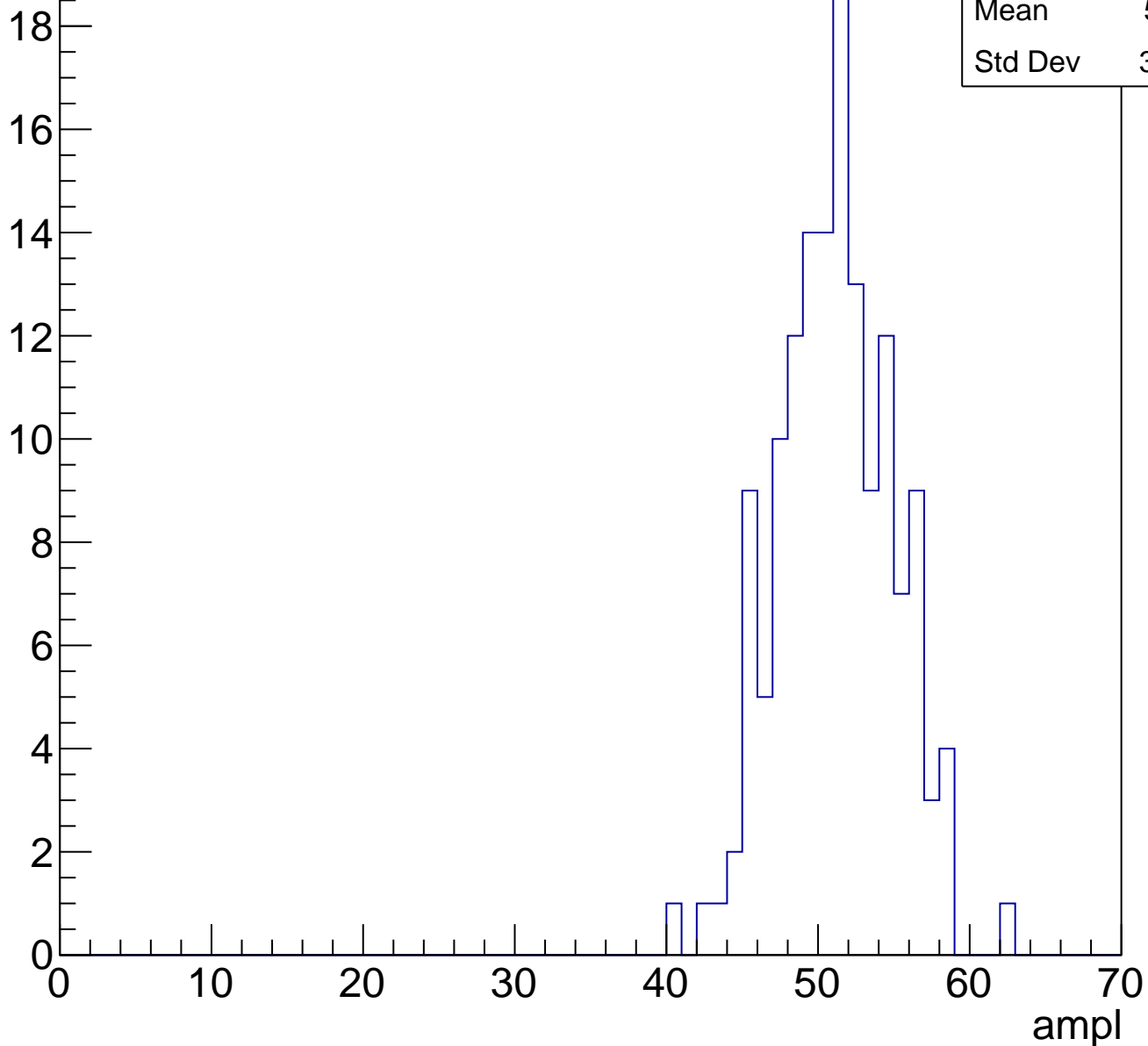


# B0L101S, U26-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	50.71
Std Dev	3.754

Entry



# B0L101S, U26-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	102
Mean	56.61
Std Dev	6.438

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

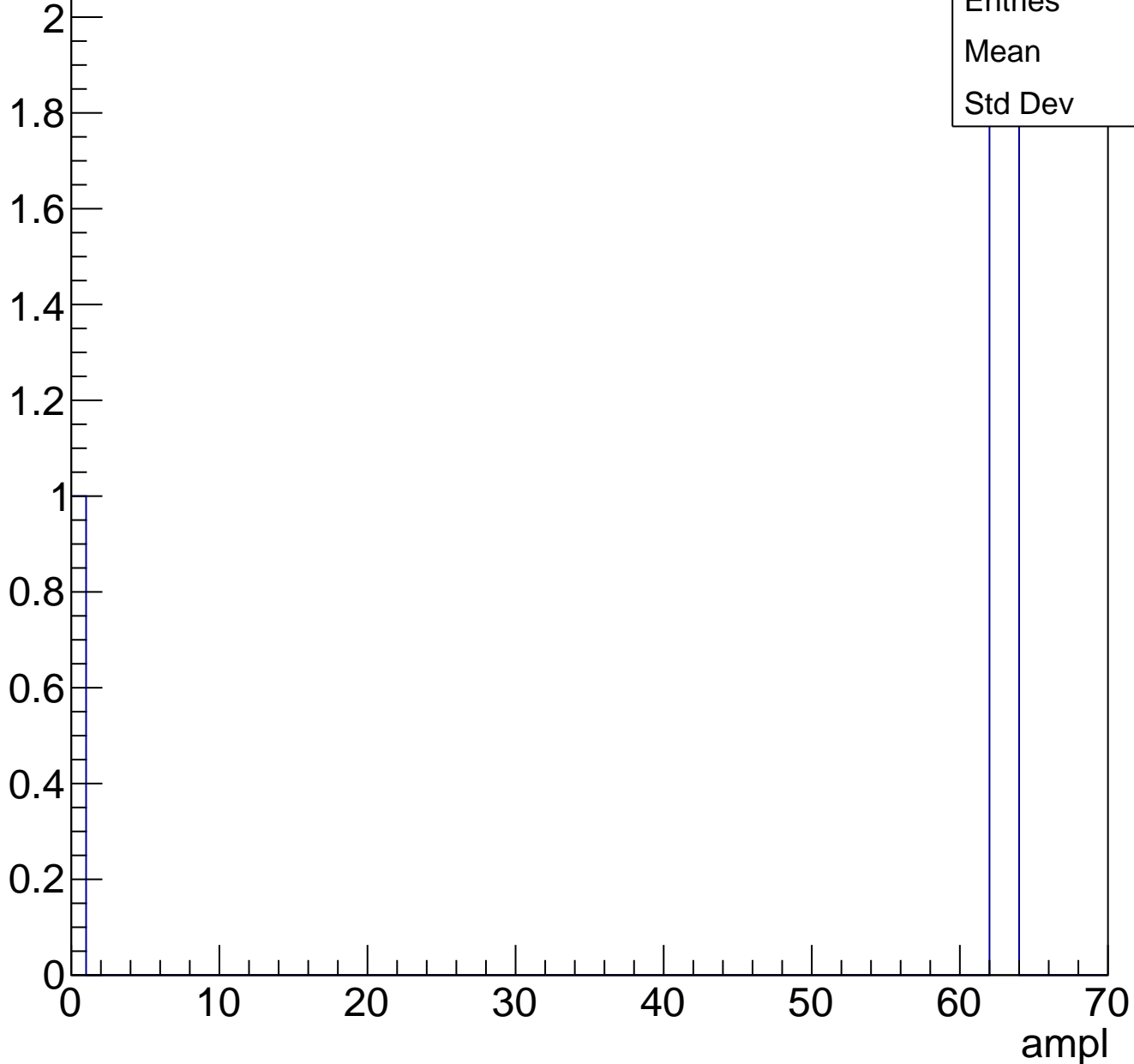
ampl

Entries	74
Mean	60.19
Std Dev	2.431

# B0L101S, U26-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



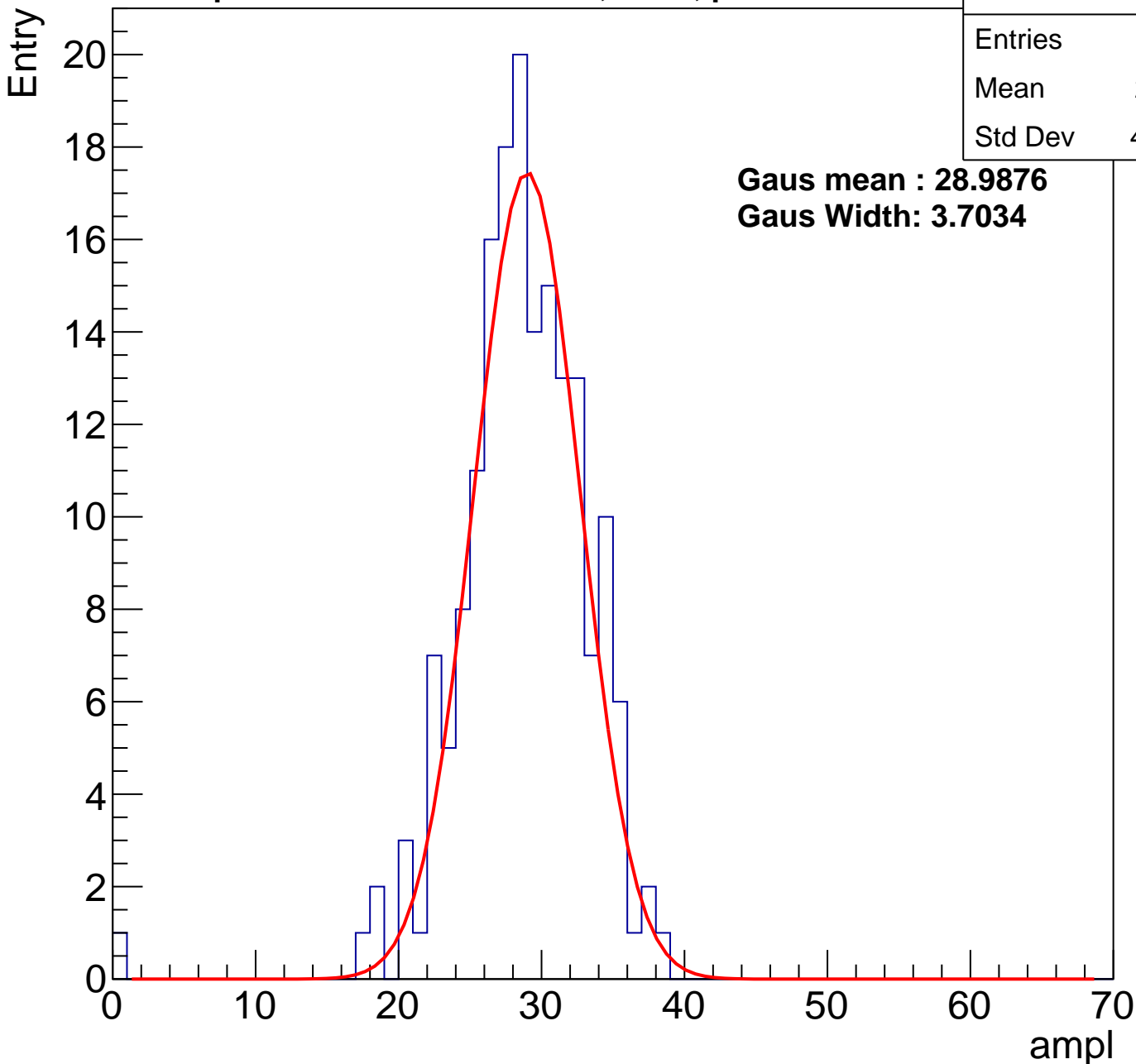
# B0L101S, U26-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	28.21
Std Dev	4.522

**Gaus mean : 28.9876**

**Gaus Width: 3.7034**



# B0L101S, U26-ch30, adc1

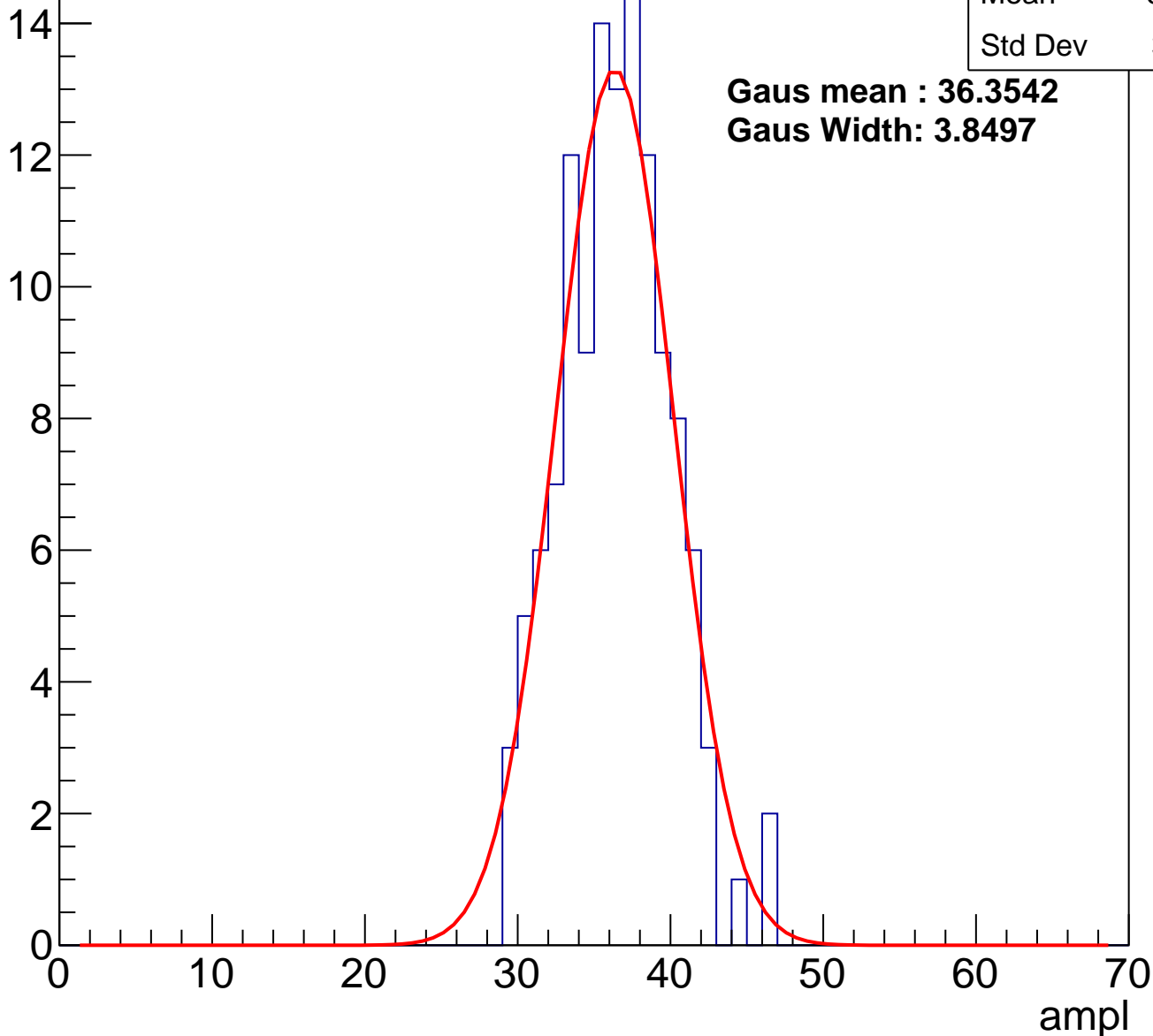
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	35.98
Std Dev	3.511

**Gaus mean : 36.3542**

**Gaus Width: 3.8497**

Entry



# B0L101S, U26-ch30, adc2

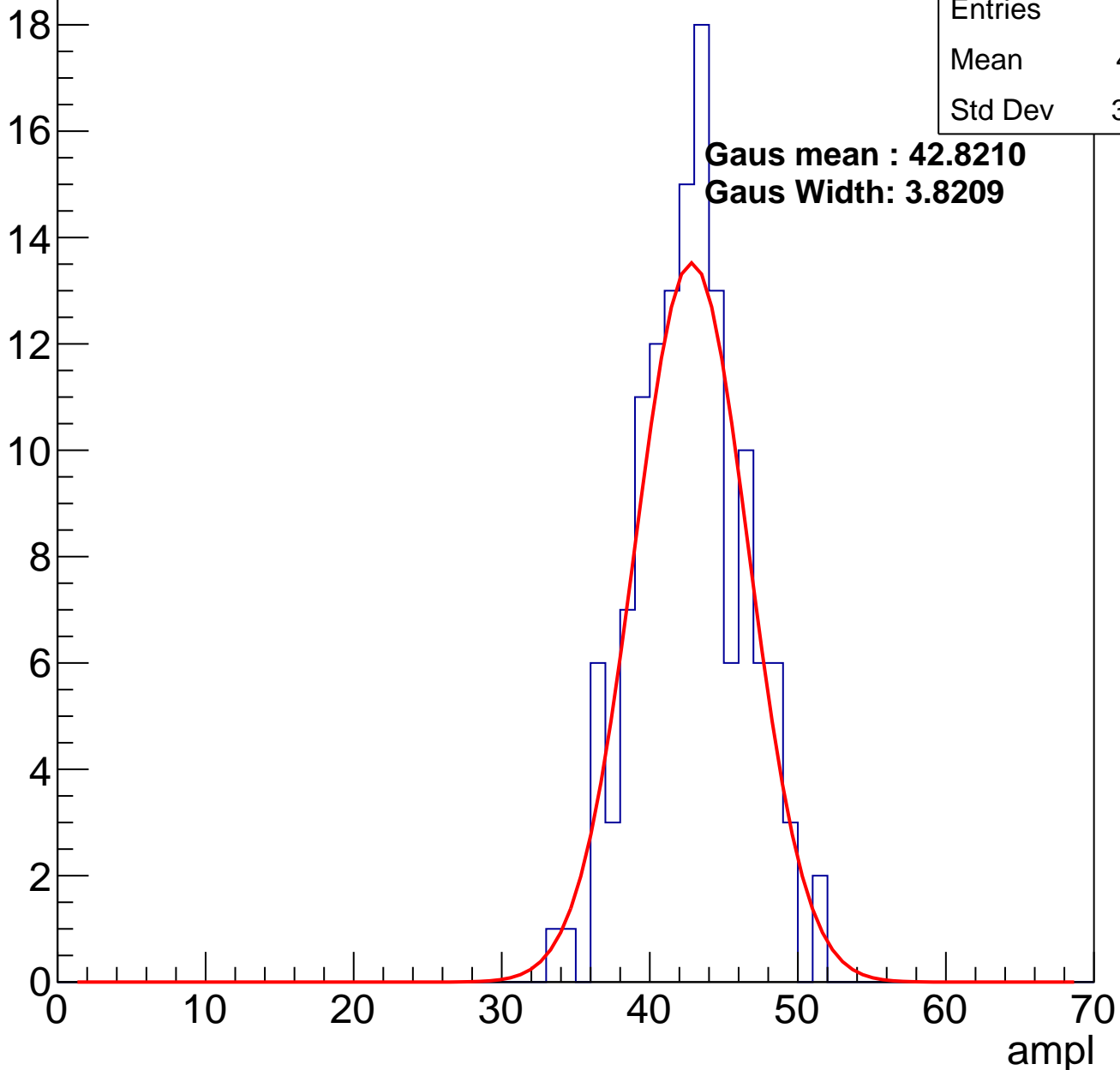
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	42.31
Std Dev	3.527

**Gaus mean : 42.8210**

**Gaus Width: 3.8209**

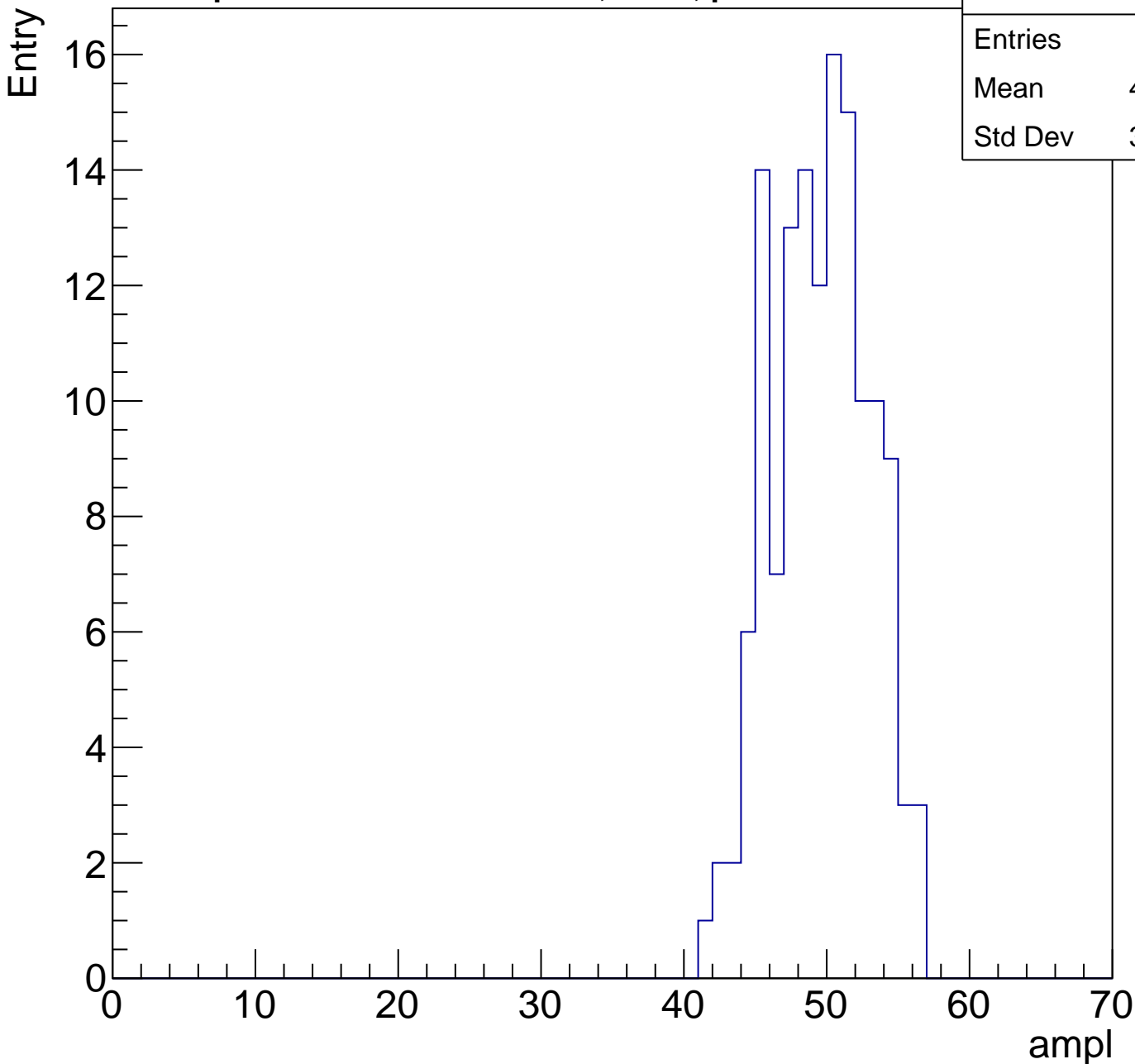
Entry



# B0L101S, U26-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	49.14
Std Dev	3.353



# B0L101S, U26-ch30, adc4

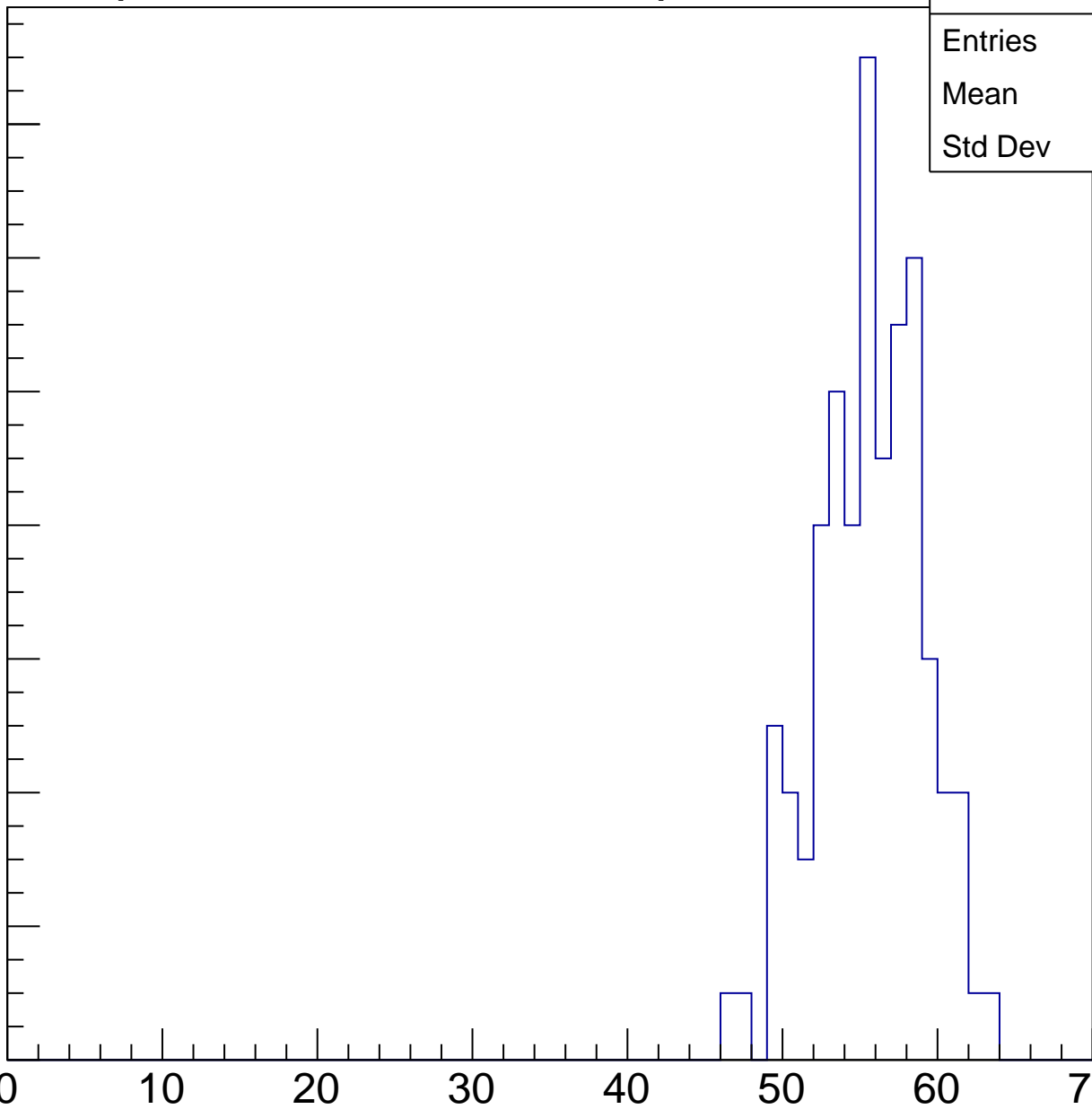
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	103
Mean	55.18
Std Dev	3.418

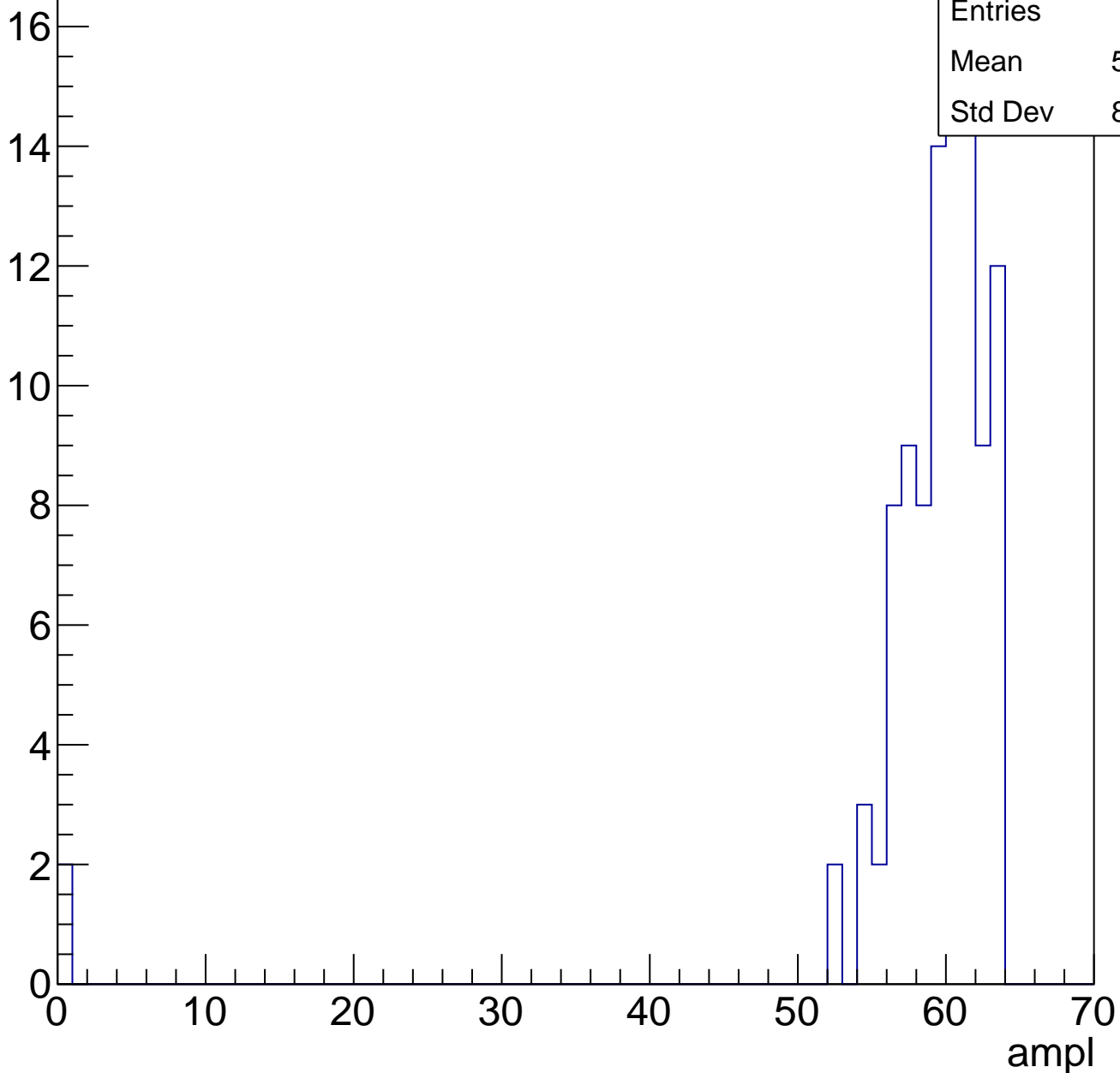
ampl



# B0L101S, U26-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

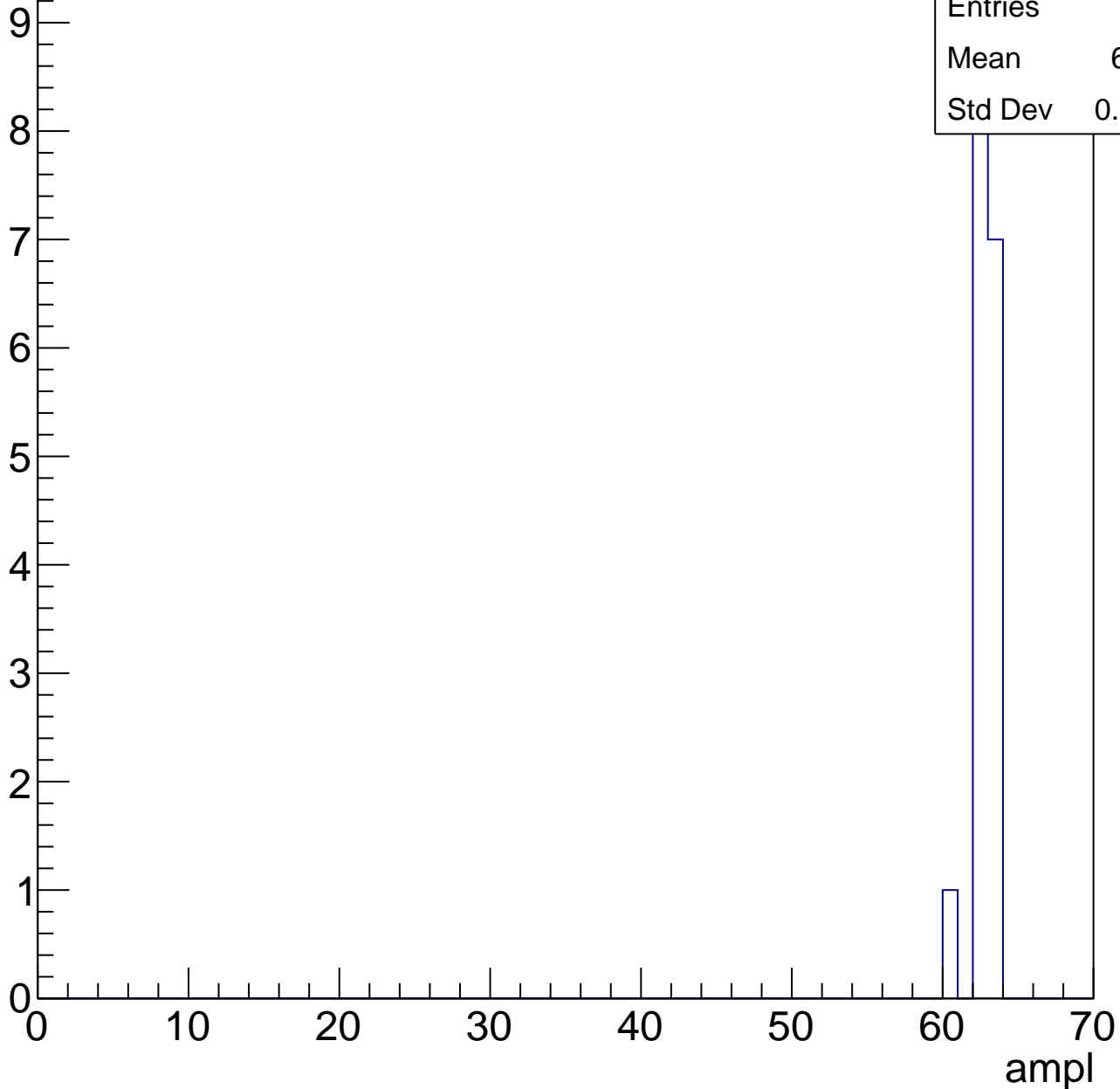


# B0L101S, U26-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	62.29
Std Dev	0.7487





# B0L101S, U26-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch31, adc0

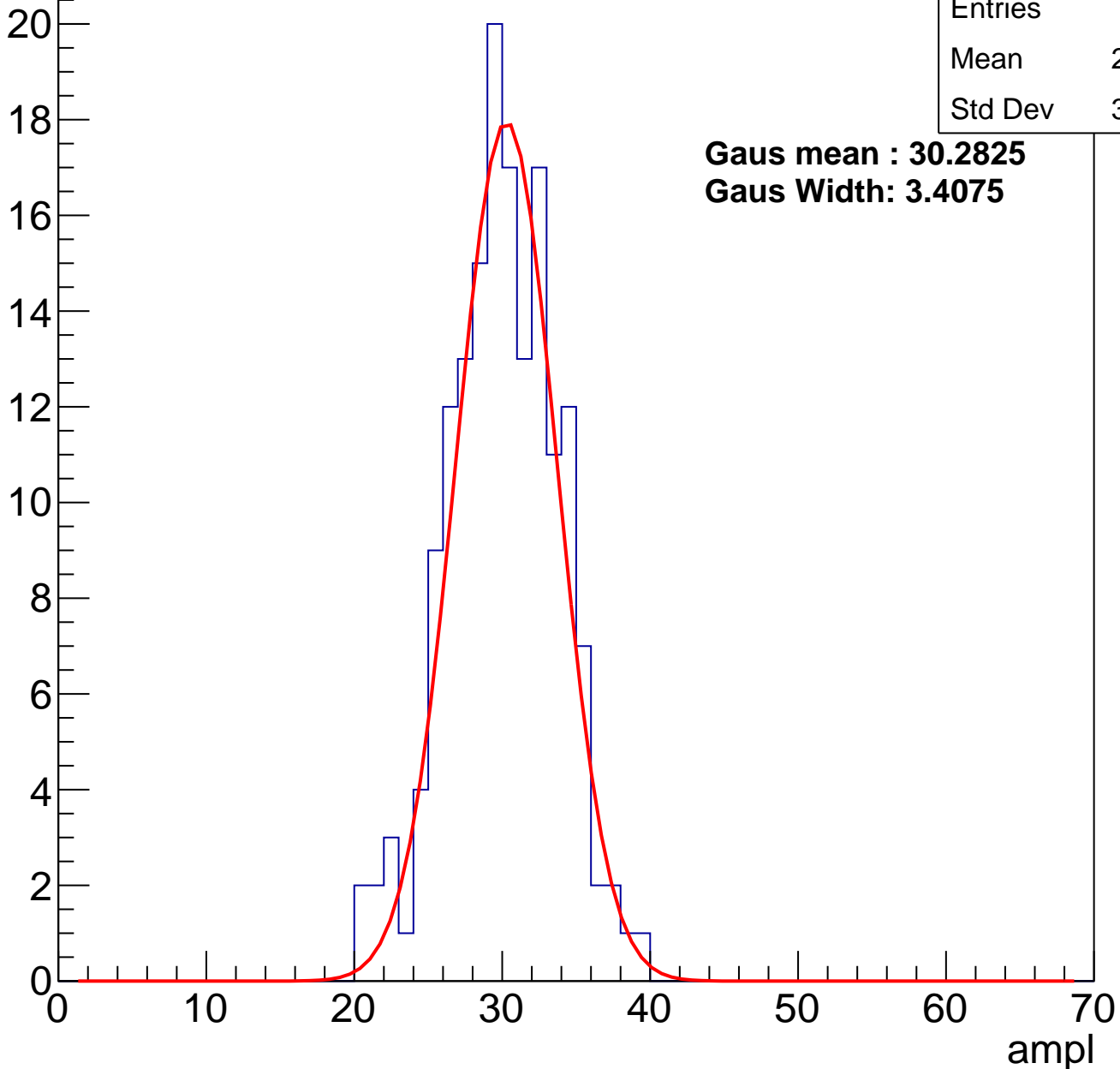
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	29.58
Std Dev	3.658

**Gaus mean : 30.2825**

**Gaus Width: 3.4075**

Entry



# B0L101S, U26-ch31, adc1

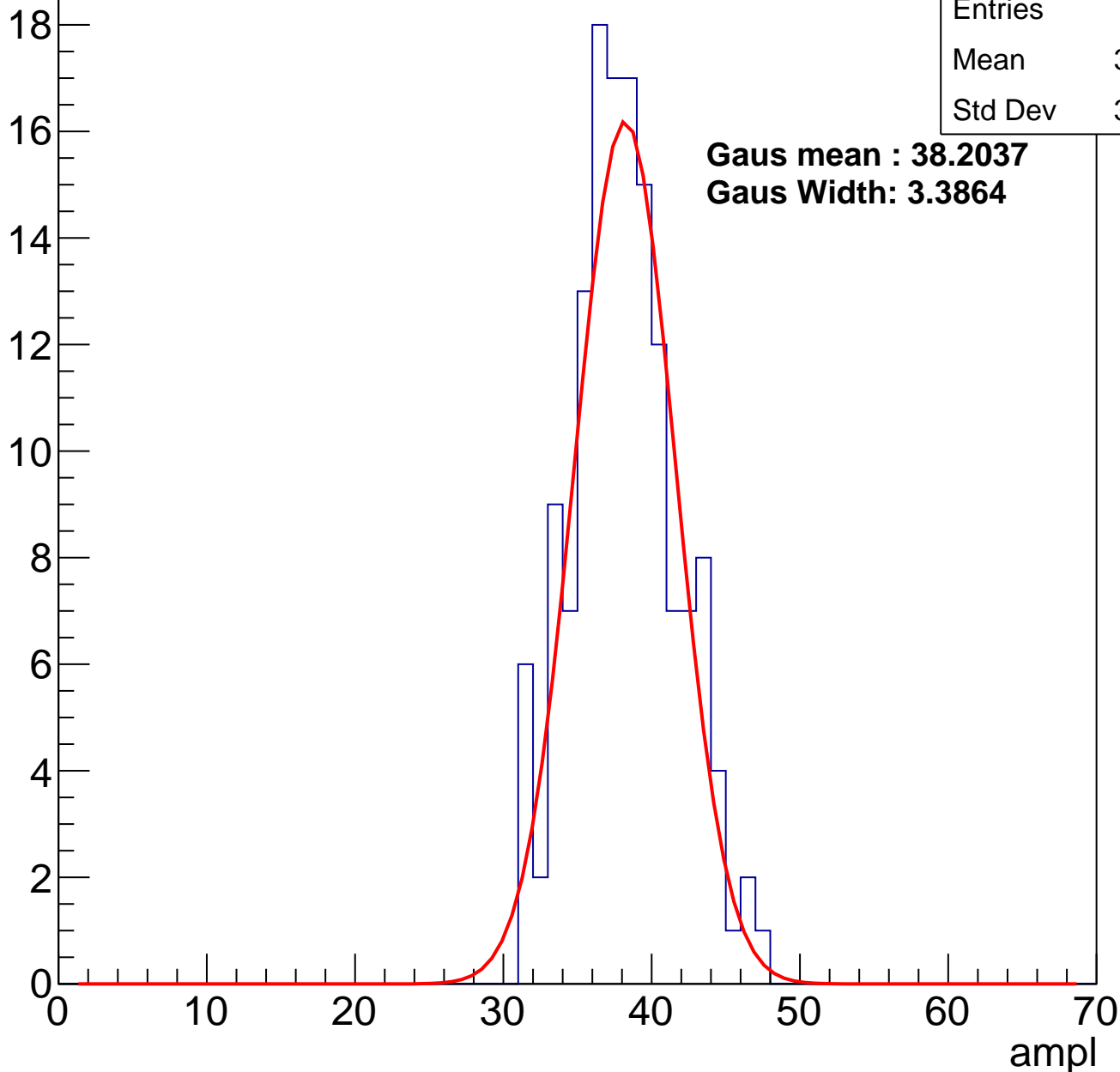
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	37.76
Std Dev	3.453

**Gaus mean : 38.2037**

**Gaus Width: 3.3864**

Entry



# B0L101S, U26-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

45.21

Std Dev

3.731

**Gaus mean : 45.5798**

**Gaus Width: 3.7749**

0

10

20

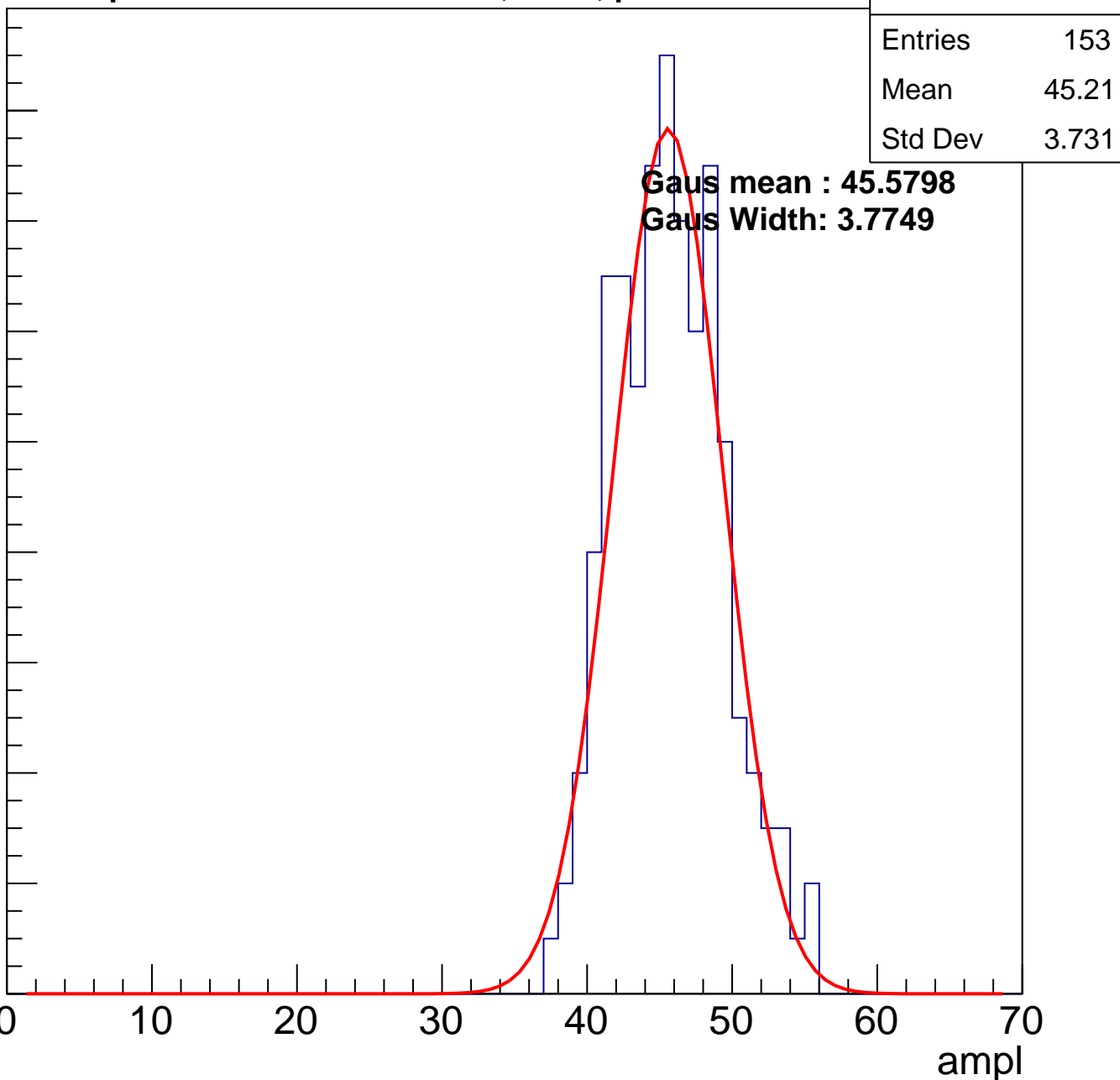
30

40

50

60

ampl

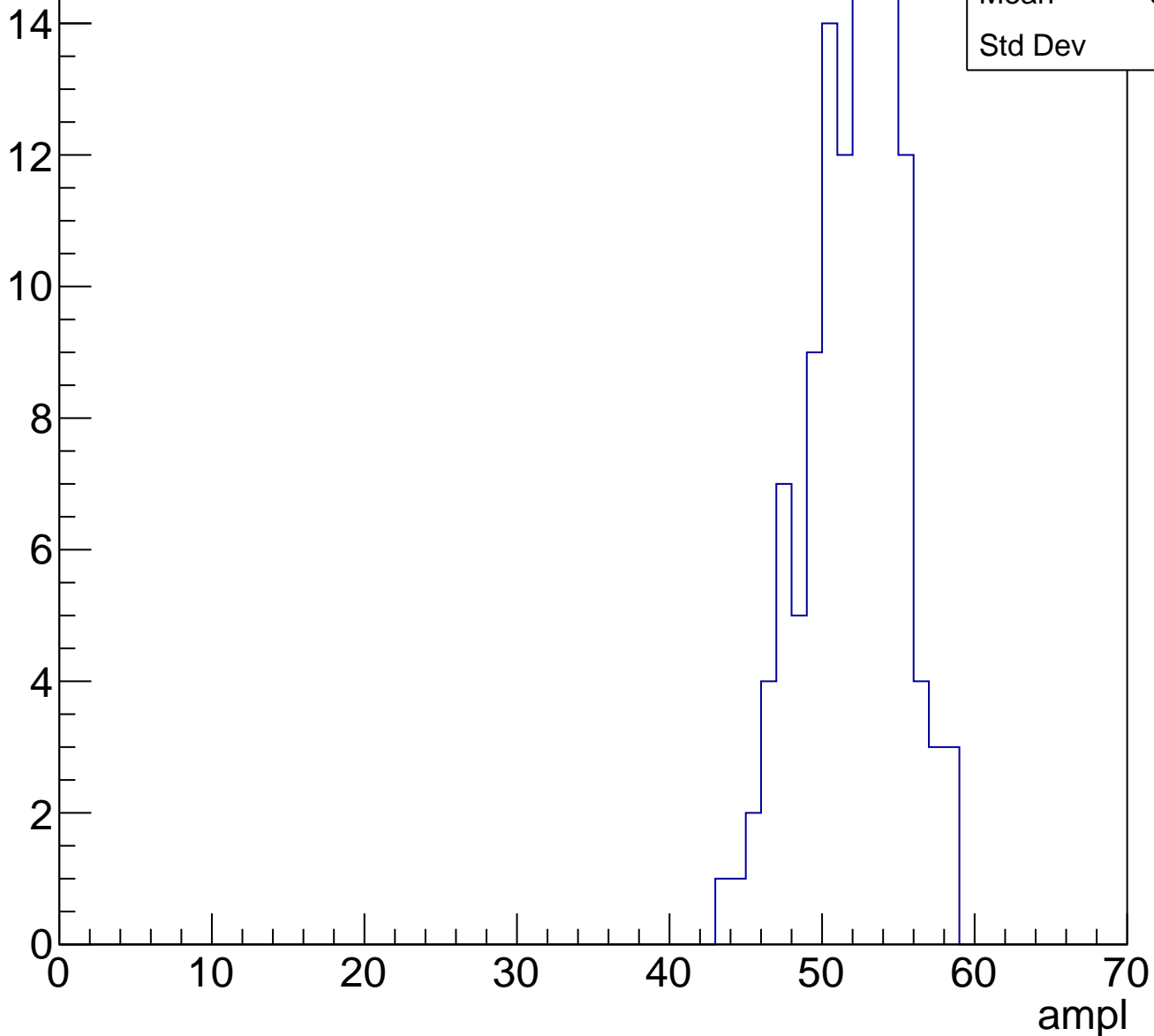


# B0L101S, U26-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	51.61
Std Dev	3.14

Entry



# B0L101S, U26-ch31, adc4

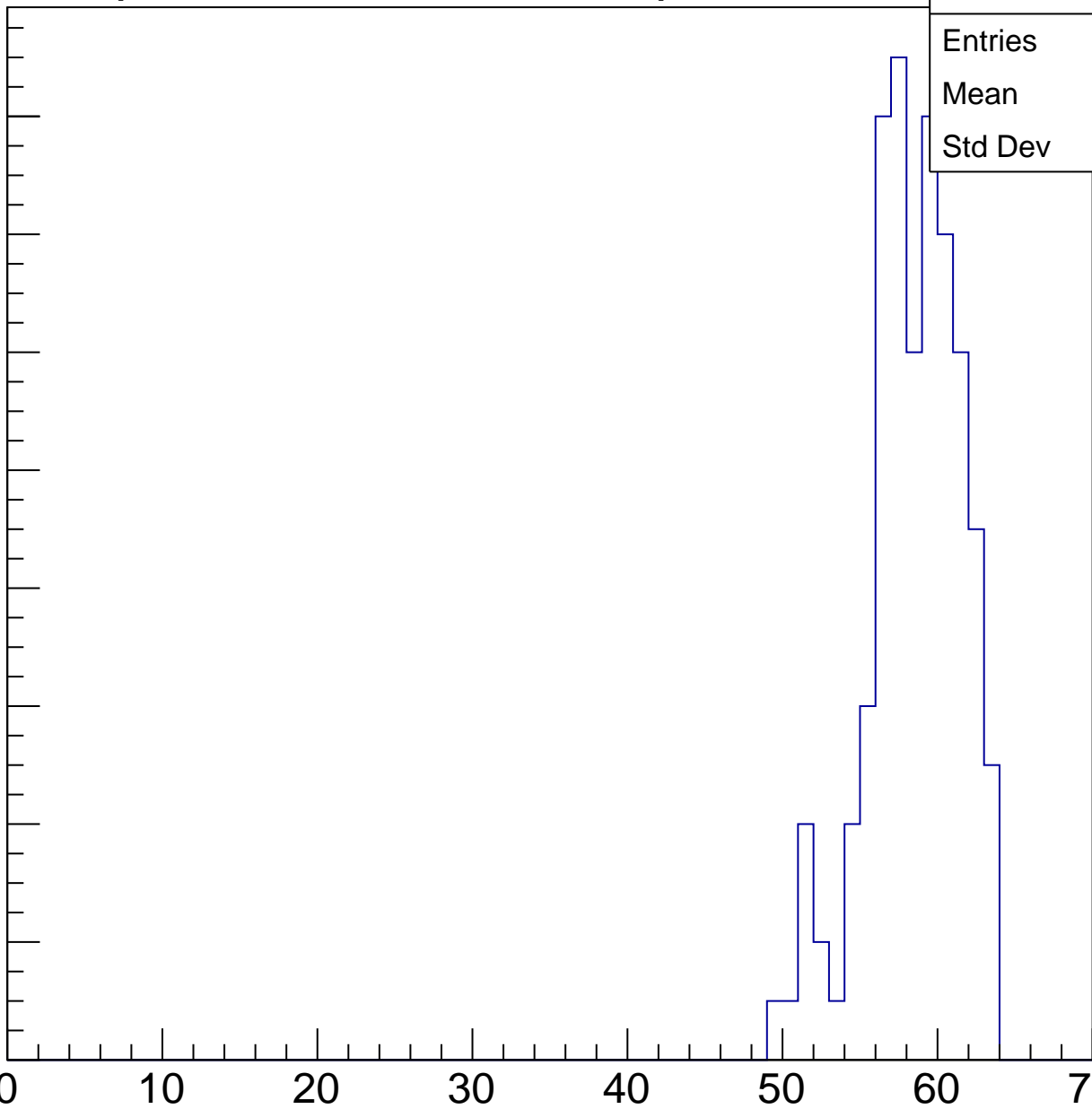
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	120
Mean	57.97
Std Dev	3.008

ampl



# B0L101S, U26-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	45
Mean	61
Std Dev	2.066

1

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679

681

683

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707

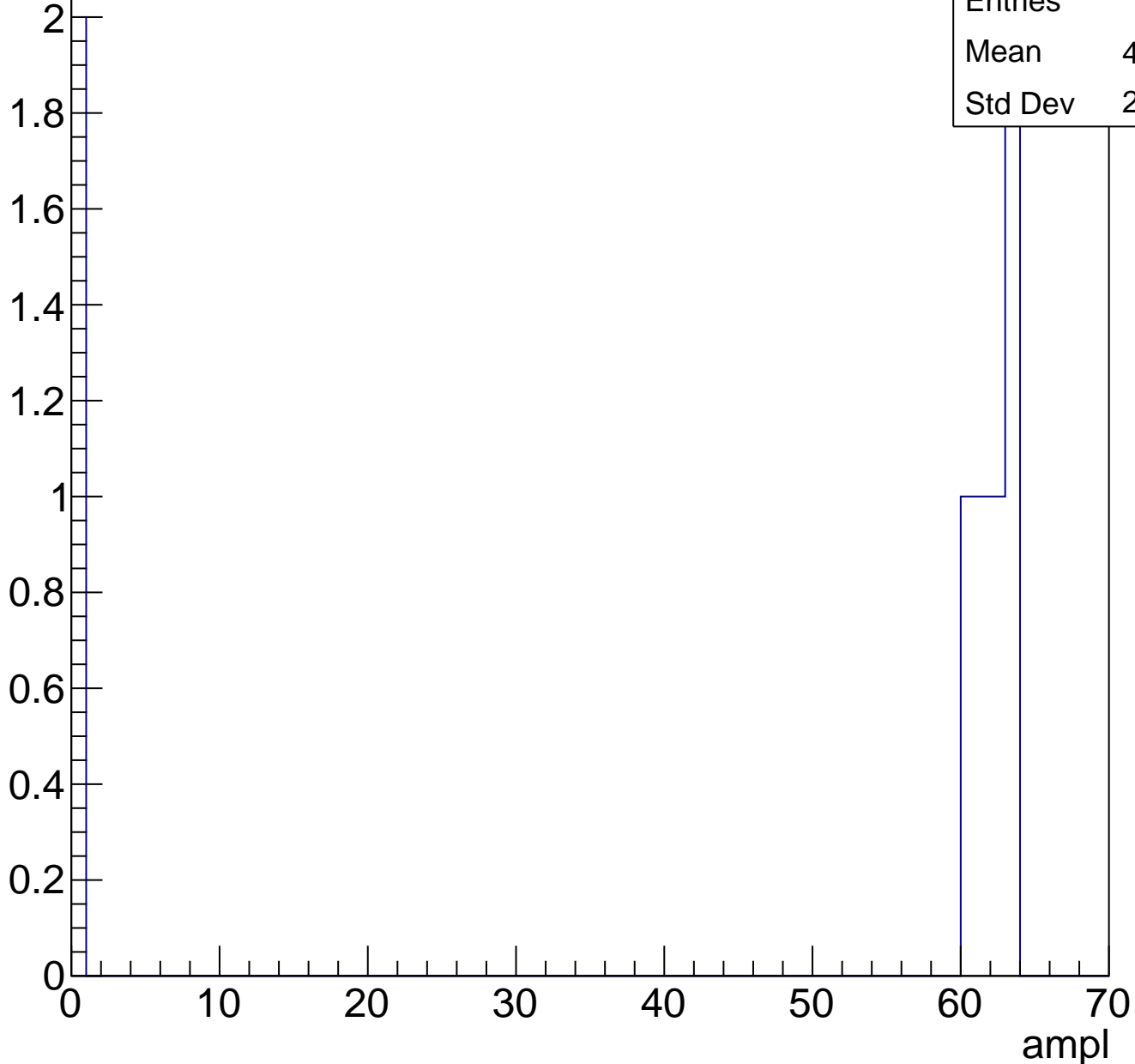
709

711

# B0L101S, U26-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	7
Mean	44.14
Std Dev	27.94



# B0L101S, U26-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	28.27
Std Dev	3.898

**Gaus mean : 28.8142**

**Gaus Width: 4.2418**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

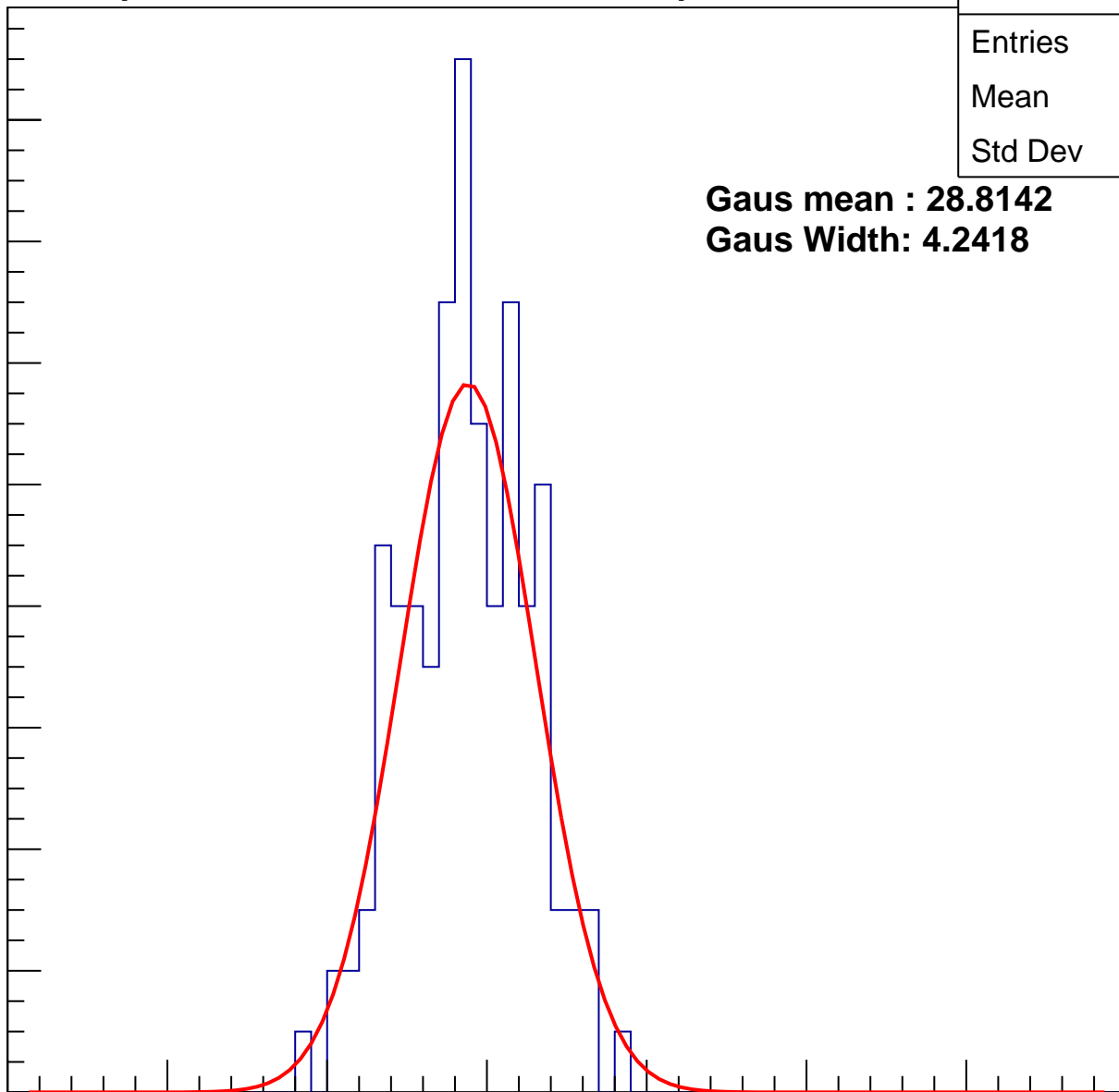
40

50

60

70

ampl



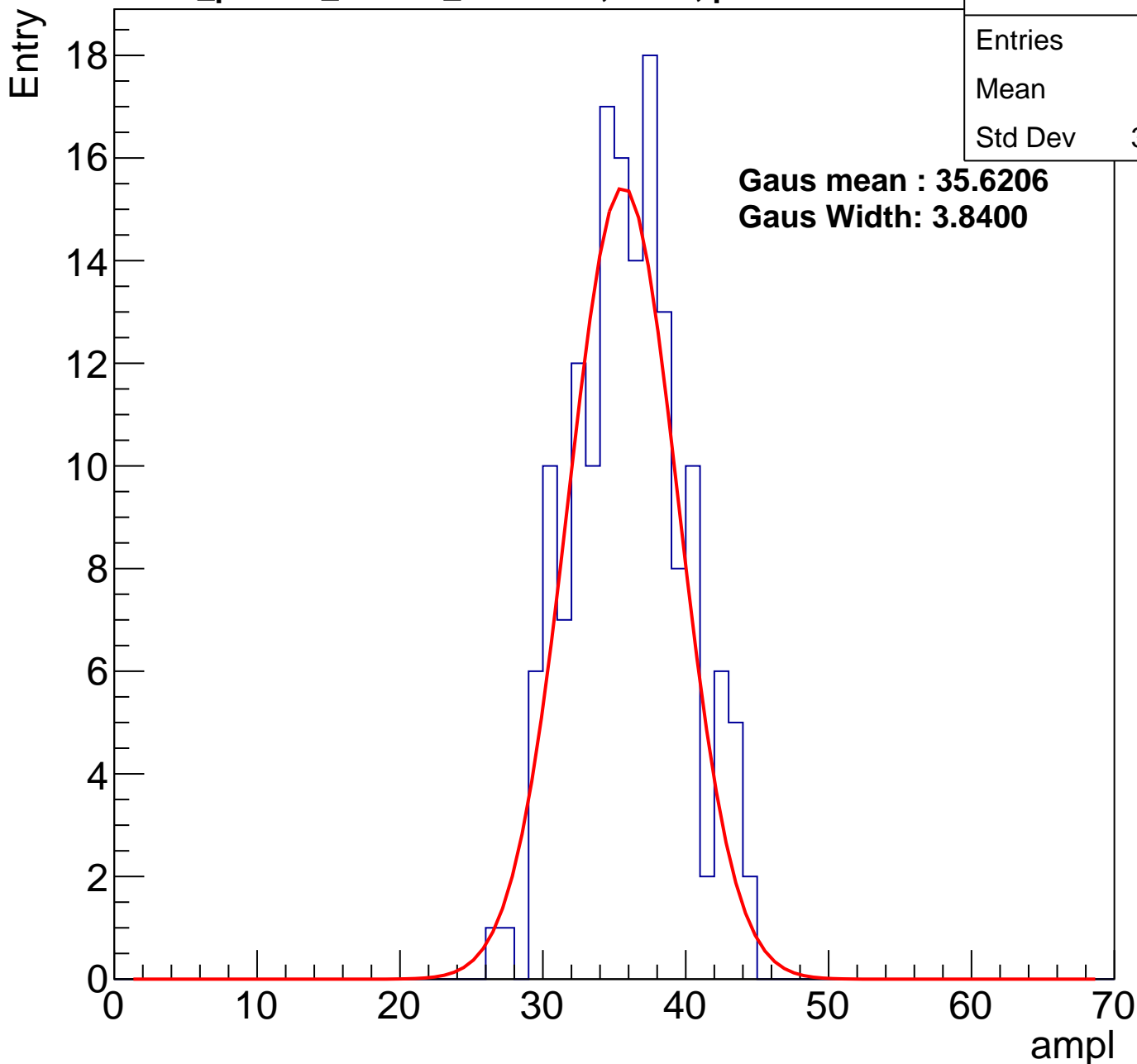
# B0L101S, U26-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	35.5
Std Dev	3.797

**Gaus mean : 35.6206**

**Gaus Width: 3.8400**



# B0L101S, U26-ch32, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

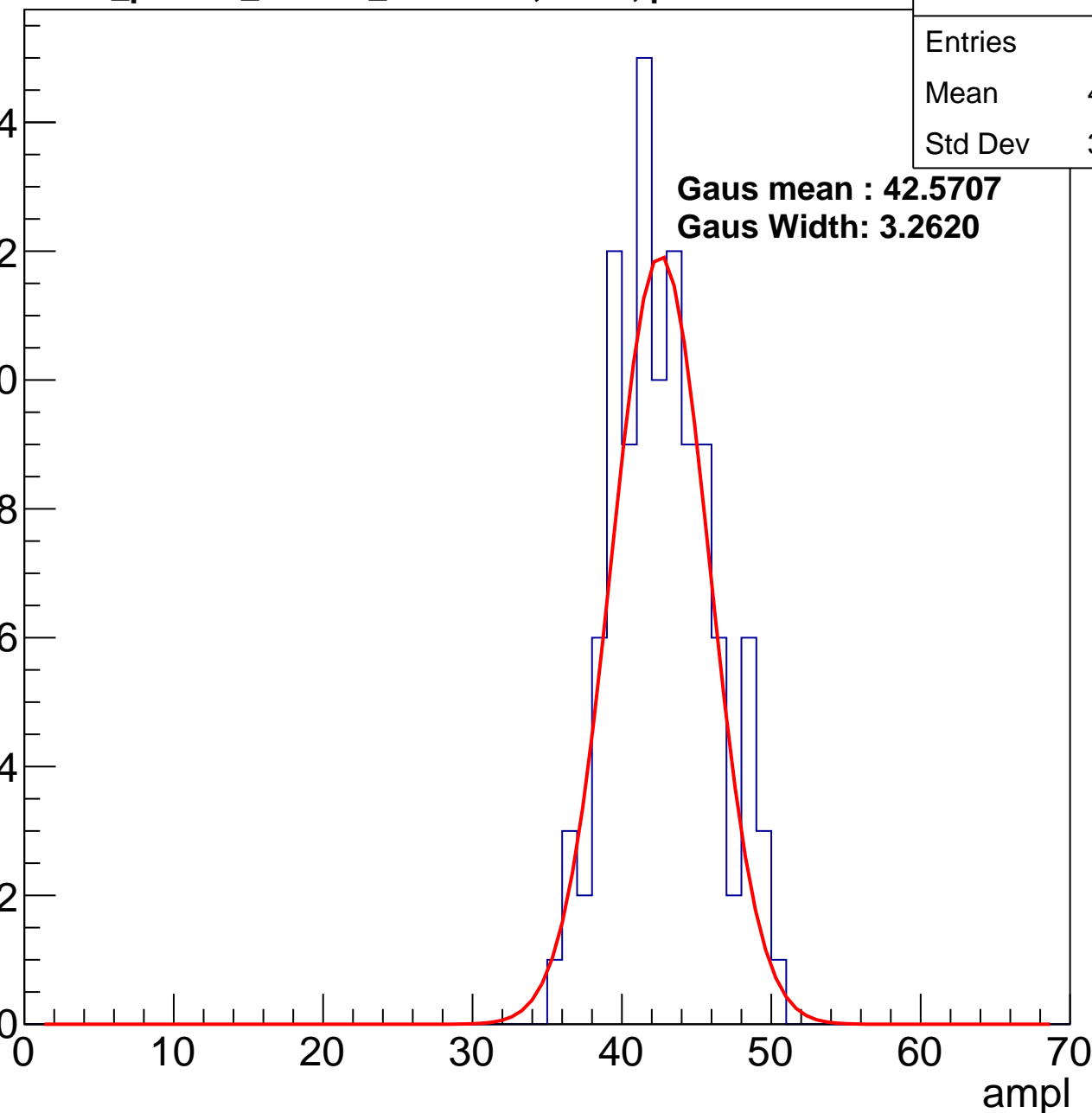
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	106
Mean	42.26
Std Dev	3.303

**Gaus mean : 42.5707**

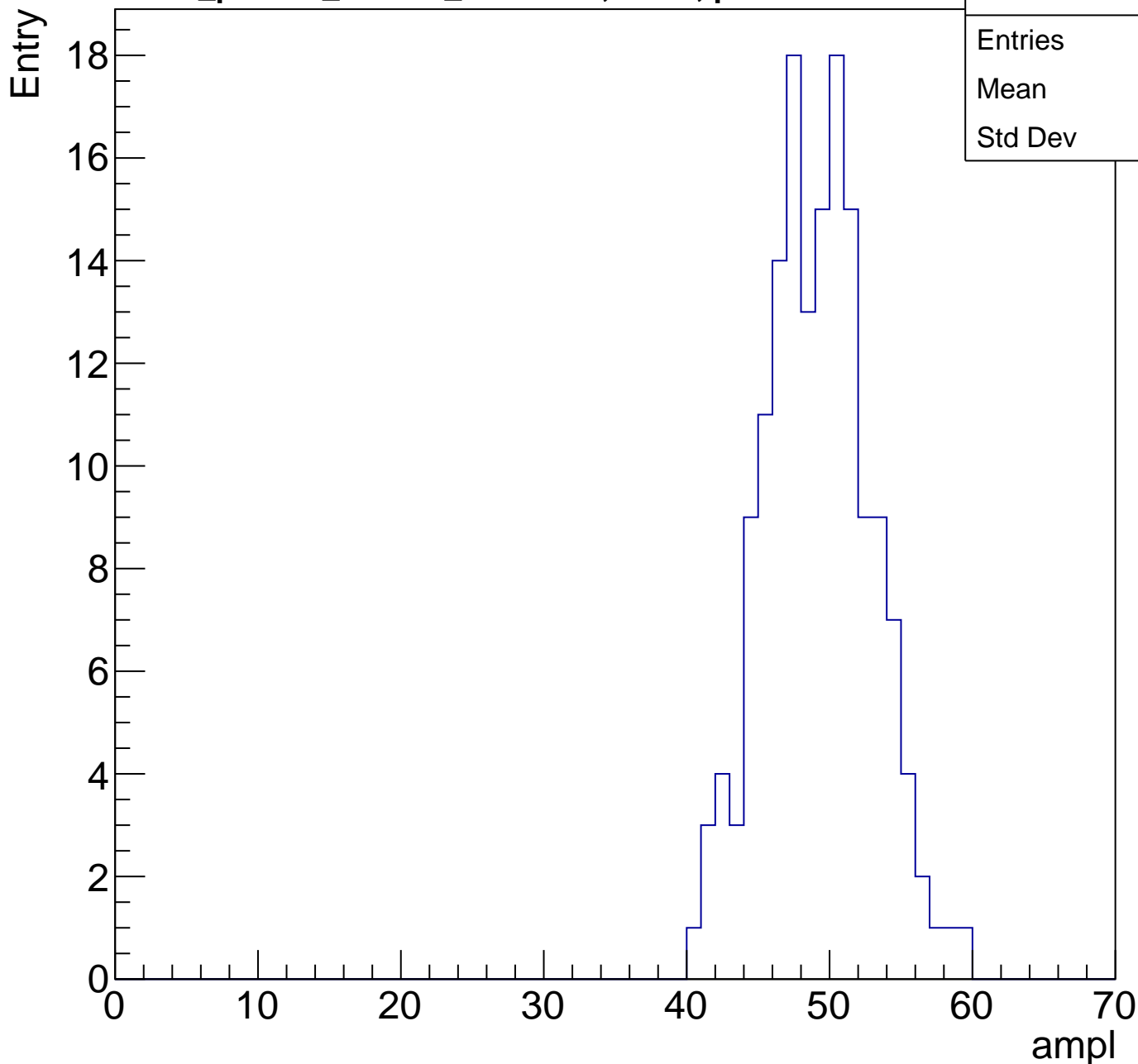
**Gaus Width: 3.2620**



# B0L101S, U26-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	48.7
Std Dev	3.68



# B0L101S, U26-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

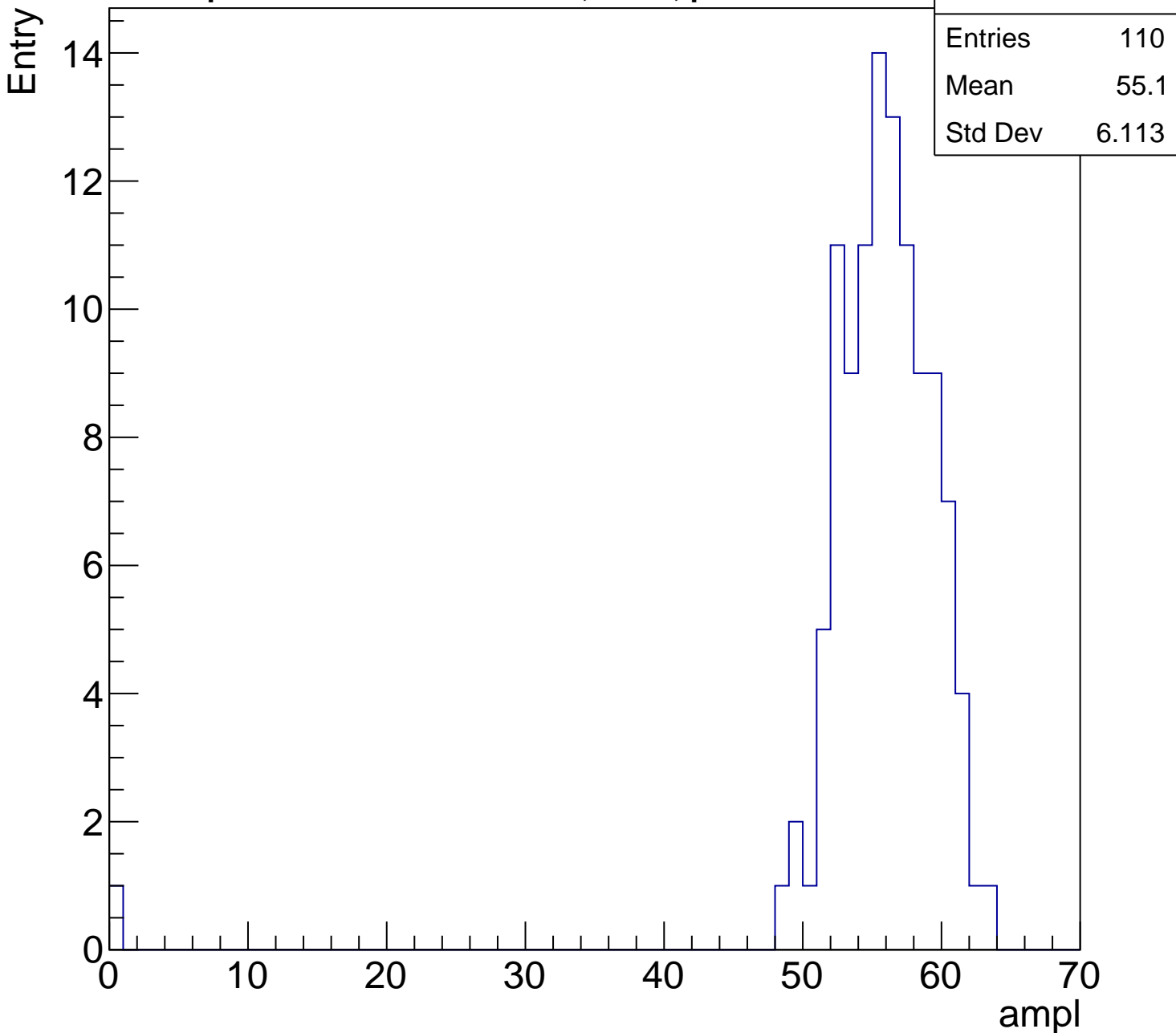
Entries	110
Mean	55.1
Std Dev	6.113

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch32, adc5

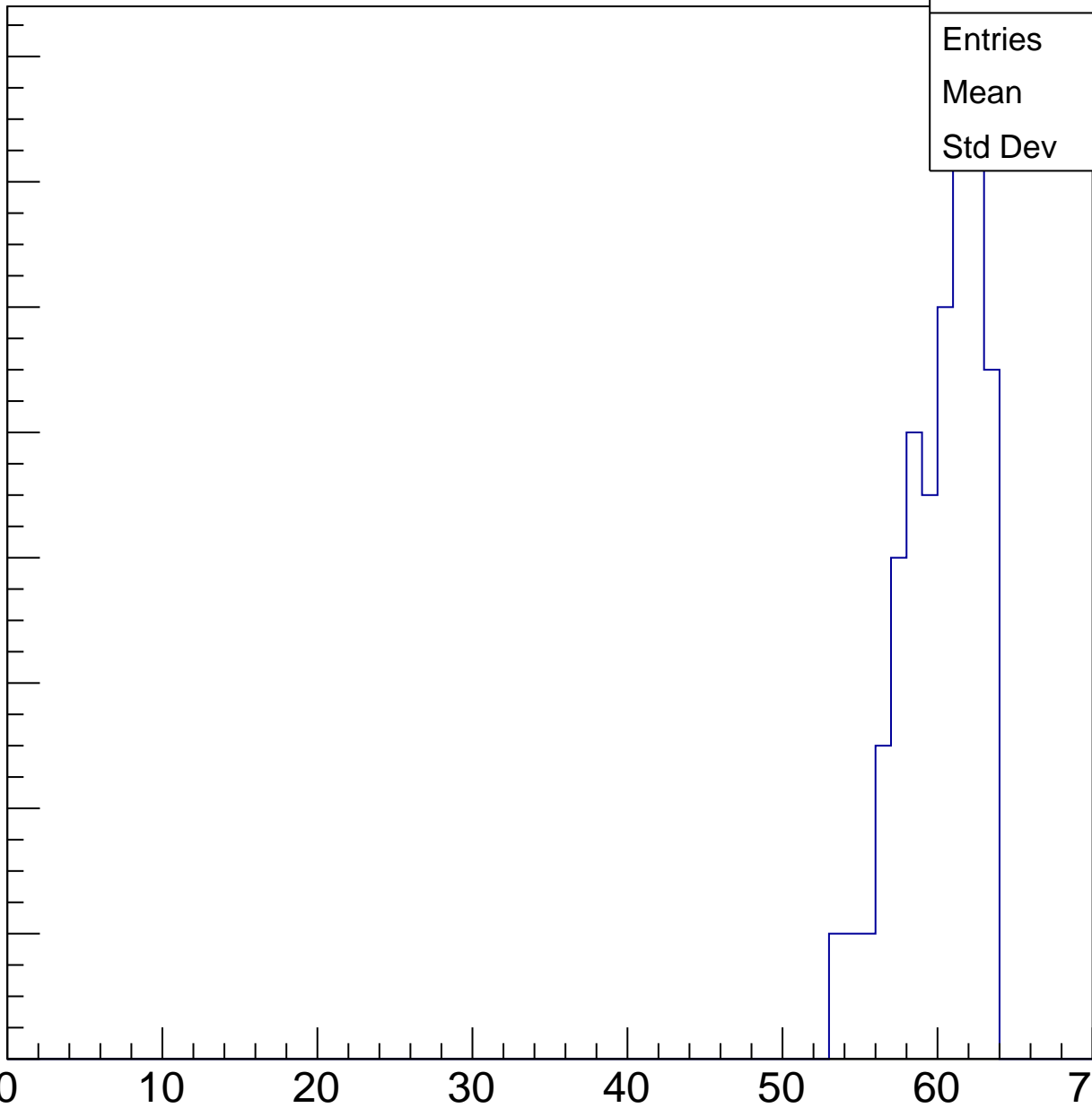
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	93
Mean	59.7
Std Dev	2.522

ampl

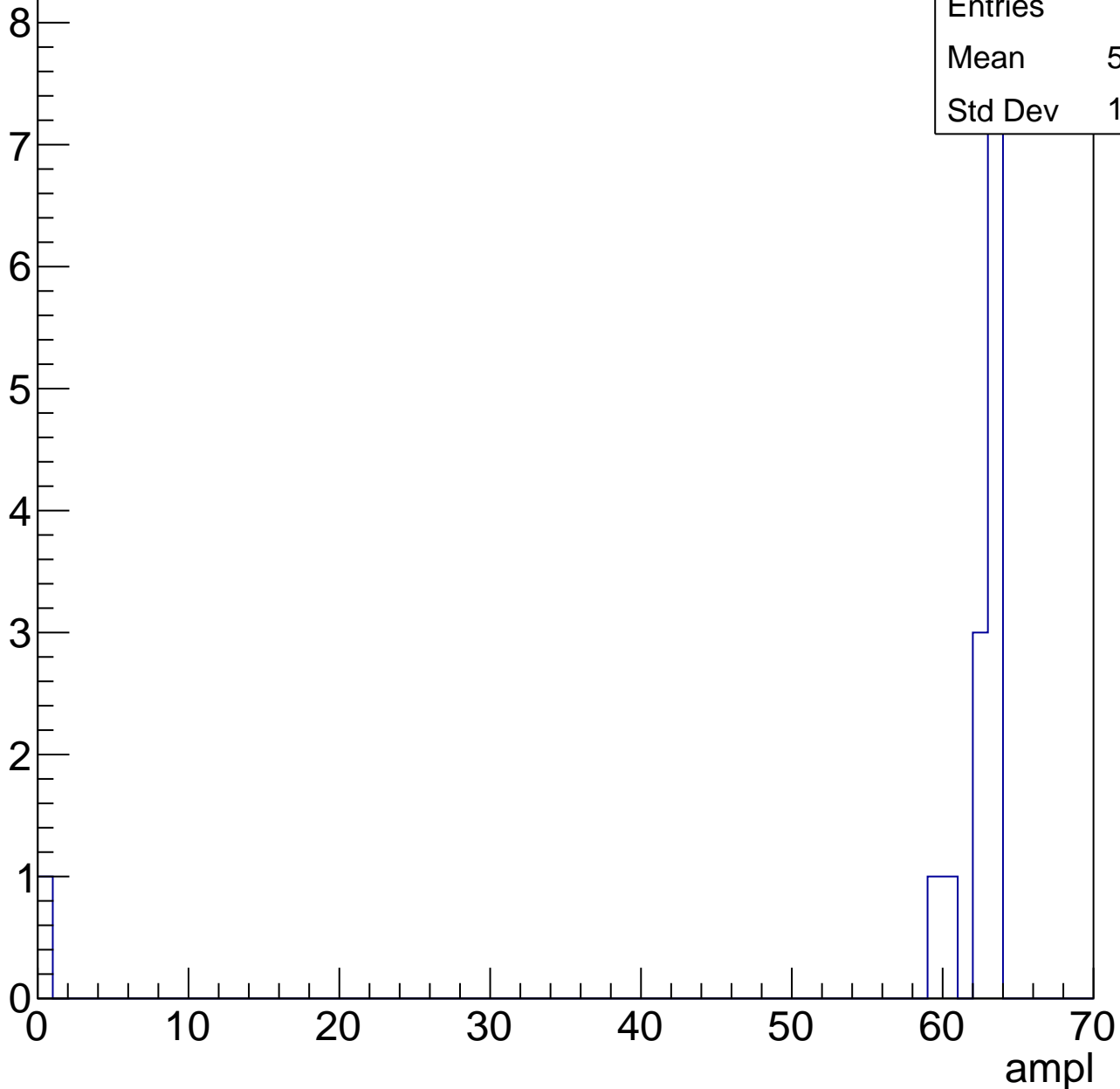


# B0L101S, U26-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.79
Std Dev	16.07





# B0L101S, U26-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

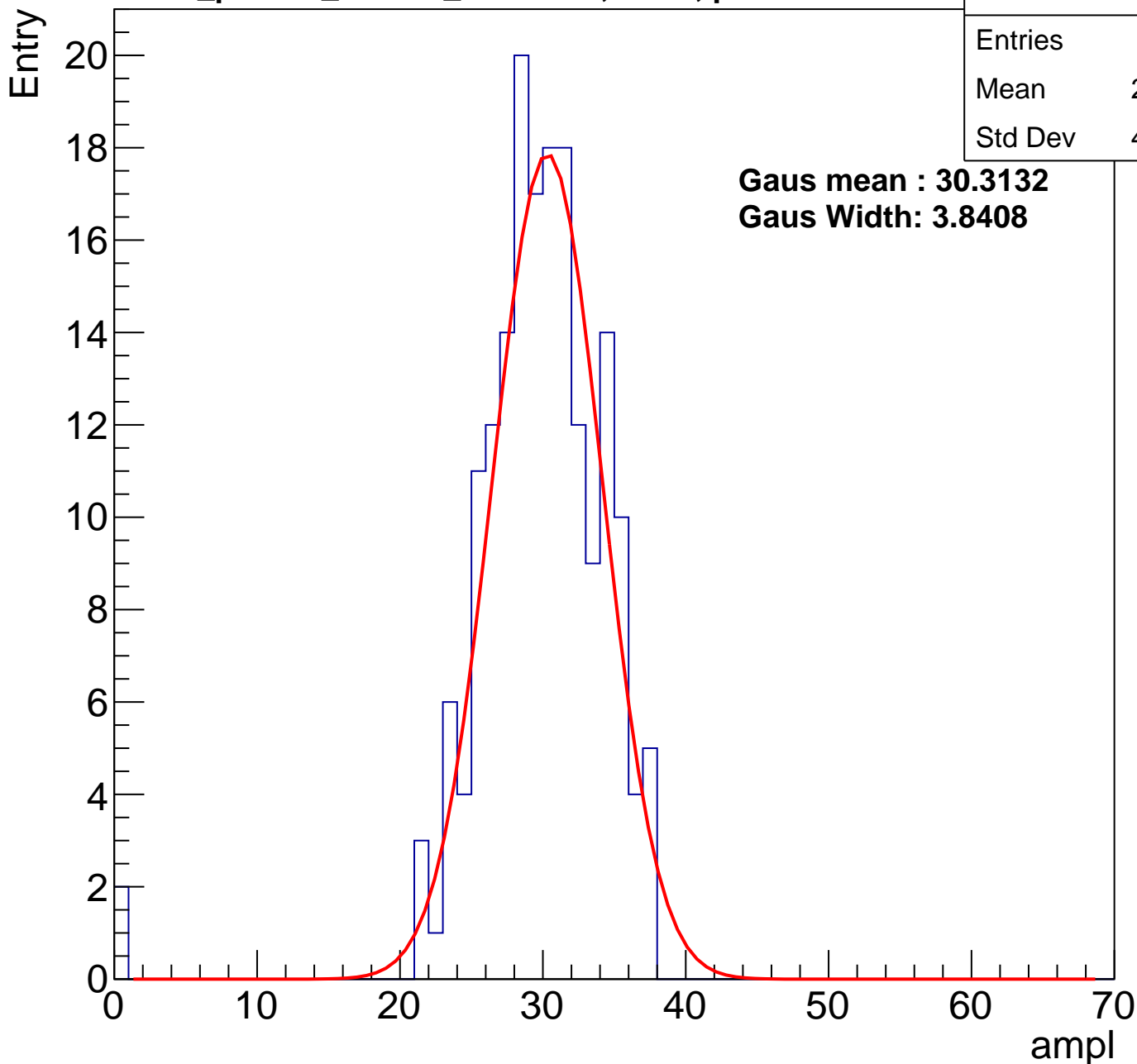
# B0L101S, U26-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	180
Mean	29.28
Std Dev	4.803

**Gaus mean : 30.3132**

**Gaus Width: 3.8408**



# B0L101S, U26-ch33, adc1

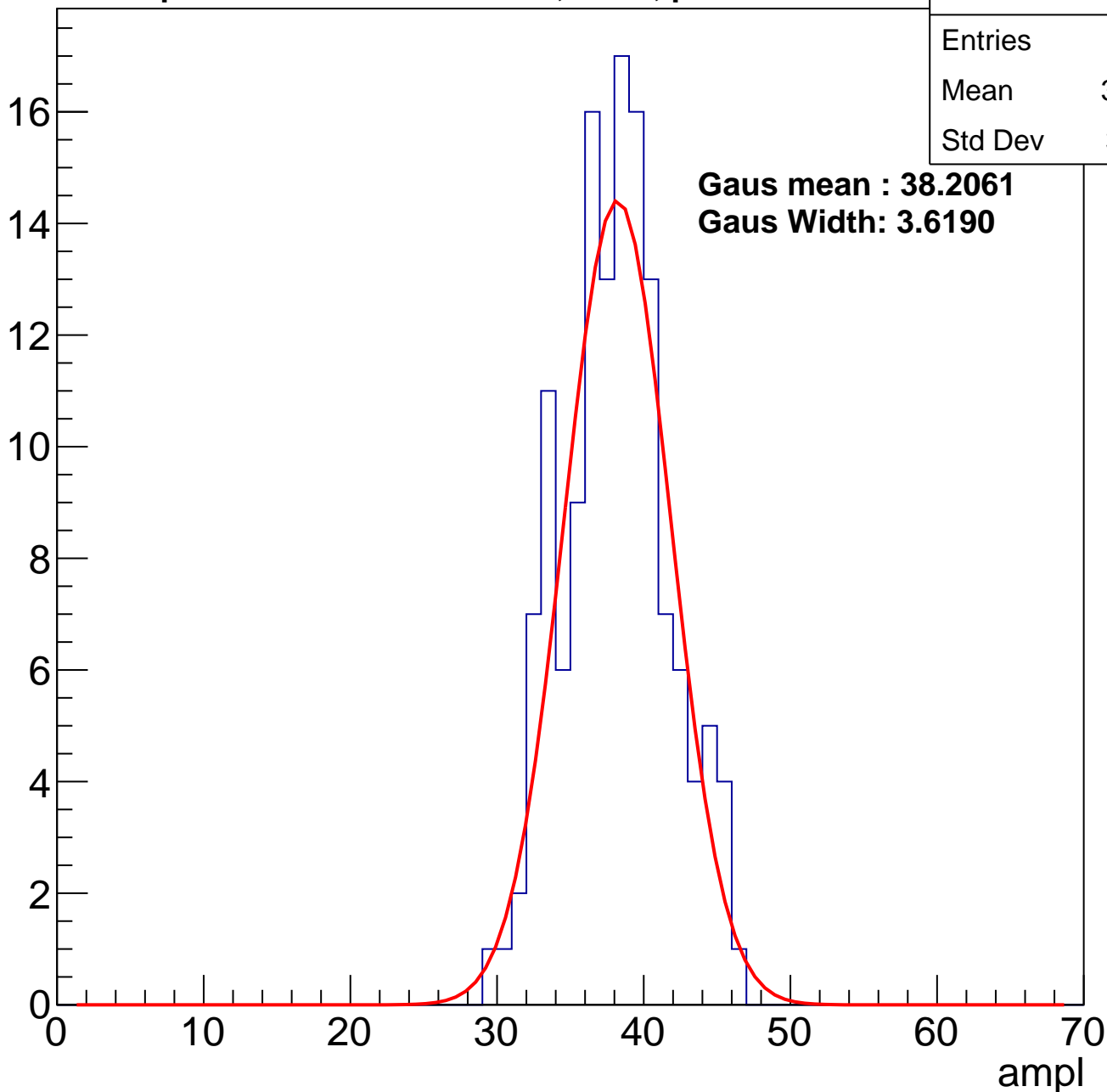
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	37.63
Std Dev	3.571

**Gaus mean : 38.2061**

**Gaus Width: 3.6190**

Entry



# B0L101S, U26-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	44.07
Std Dev	3.456

**Gaus mean : 44.2206**

**Gaus Width: 3.3586**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

# B0L101S, U26-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

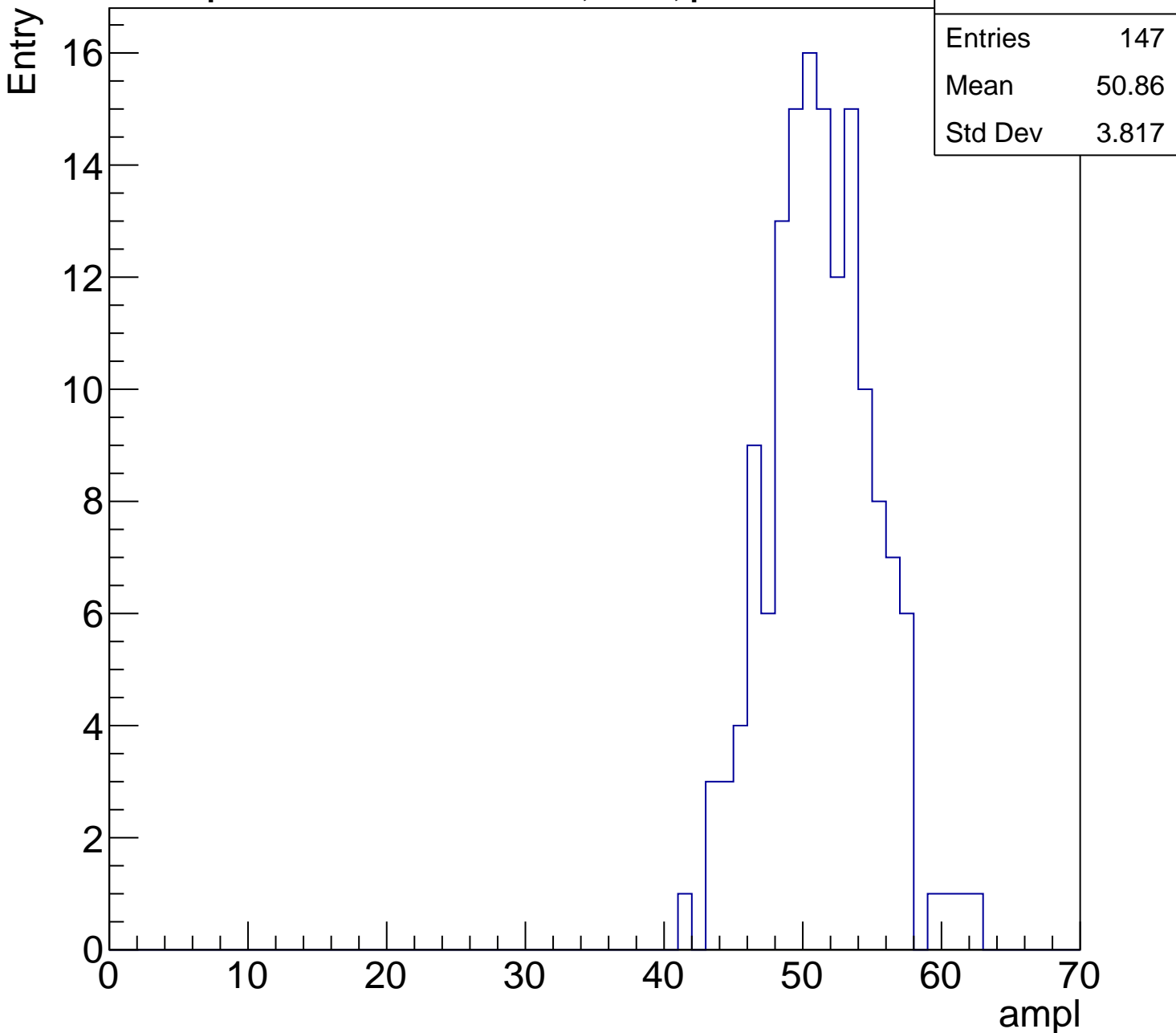
Entries	147
Mean	50.86
Std Dev	3.817

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

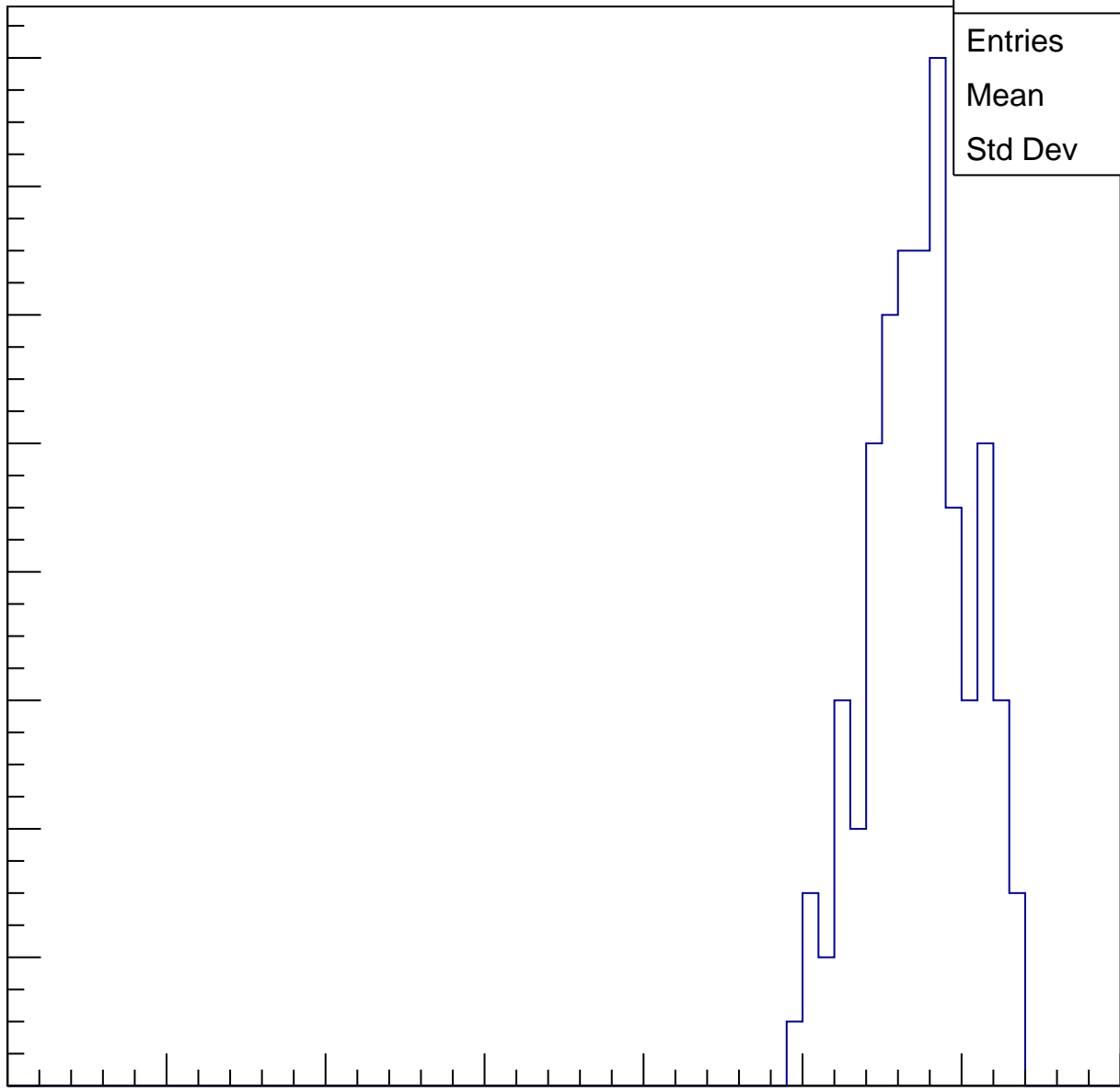
Entries	114
Mean	56.88
Std Dev	3.204

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch33, adc5

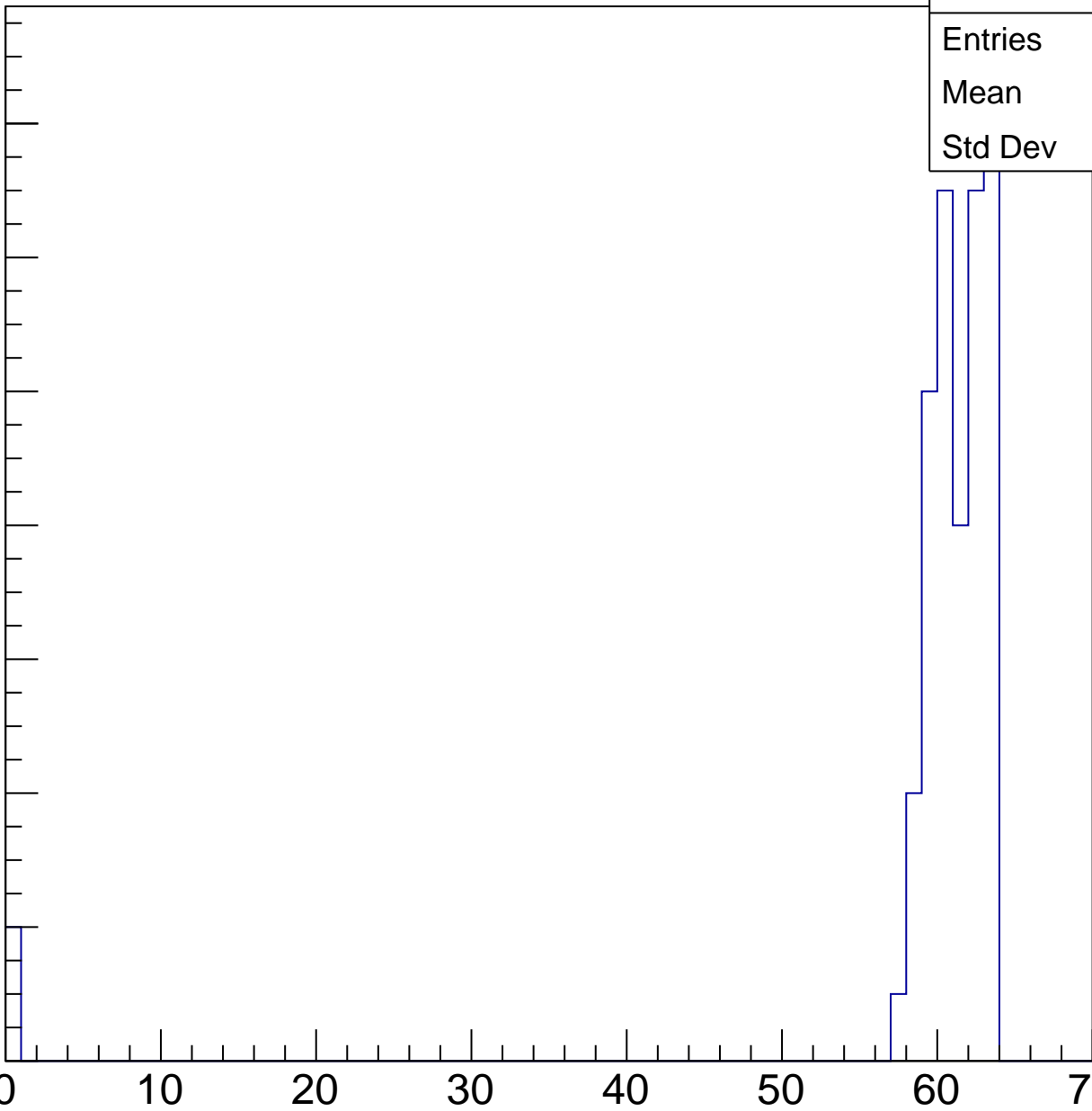
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	66
Mean	59.06
Std Dev	10.57

ampl



# B0L101S, U26-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl

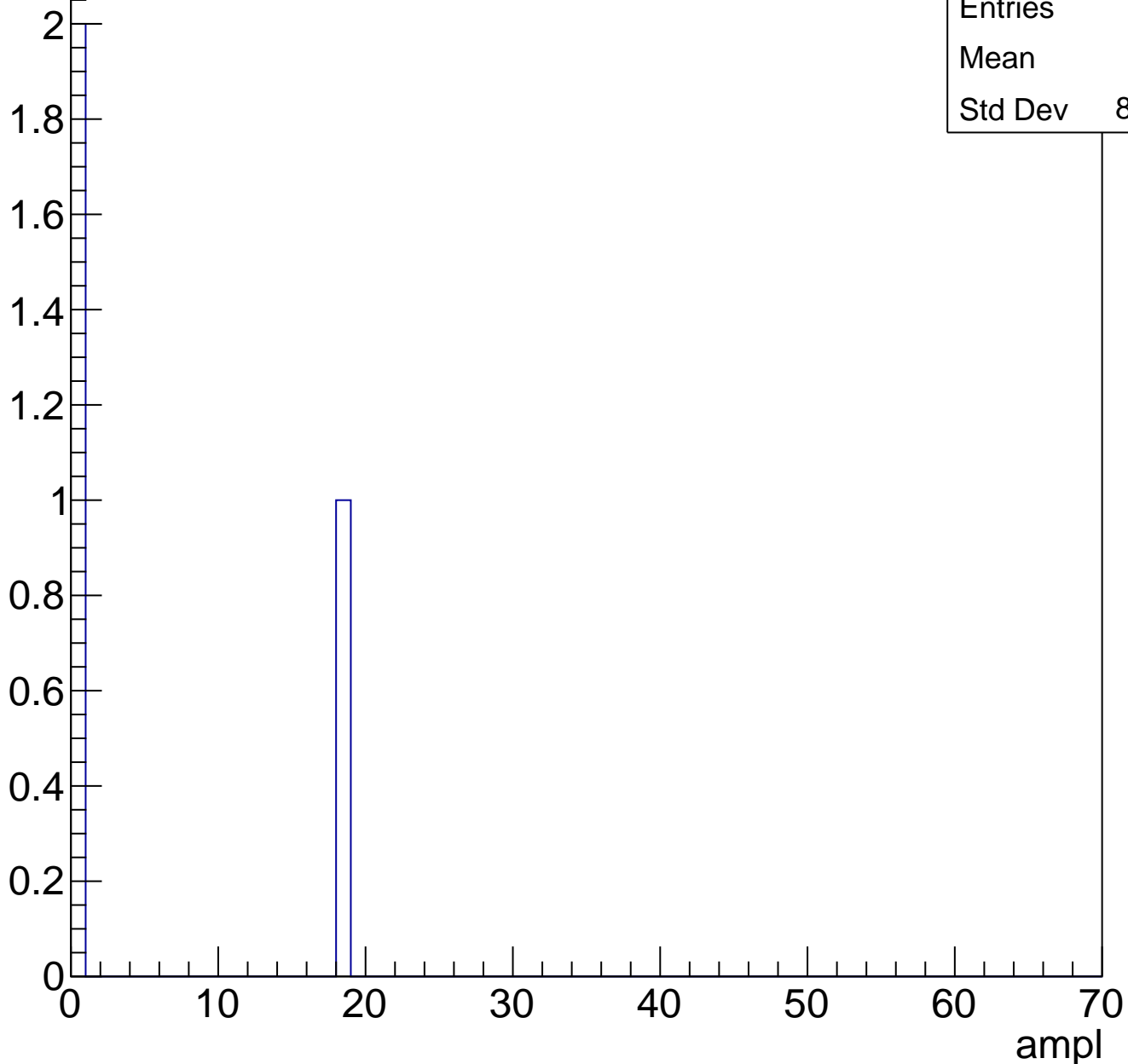




# B0L101S, U26-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6
Std Dev	8.485

# B0L101S, U26-ch34, adc0

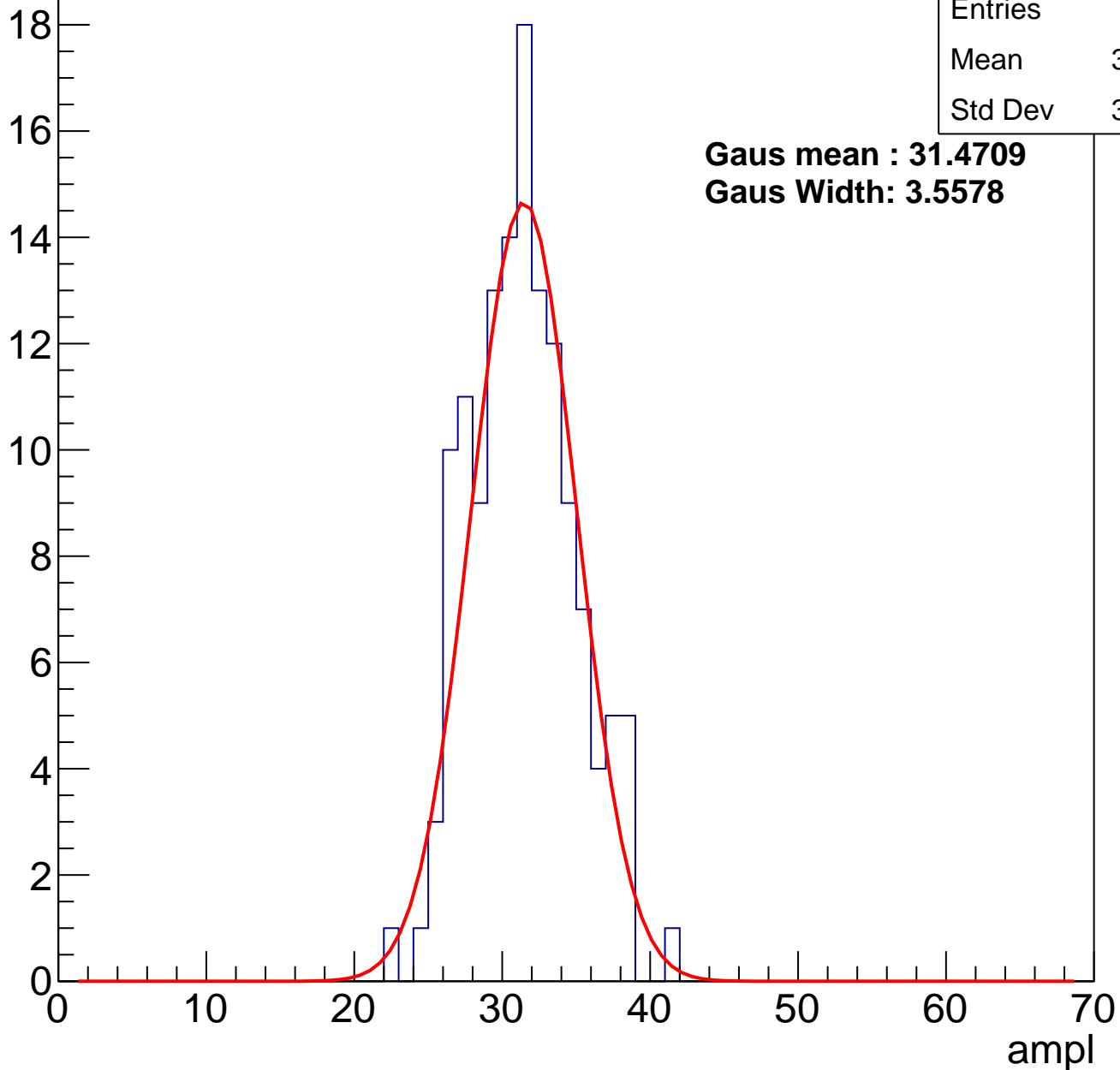
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	30.94
Std Dev	3.525

**Gaus mean : 31.4709**

**Gaus Width: 3.5578**

Entry



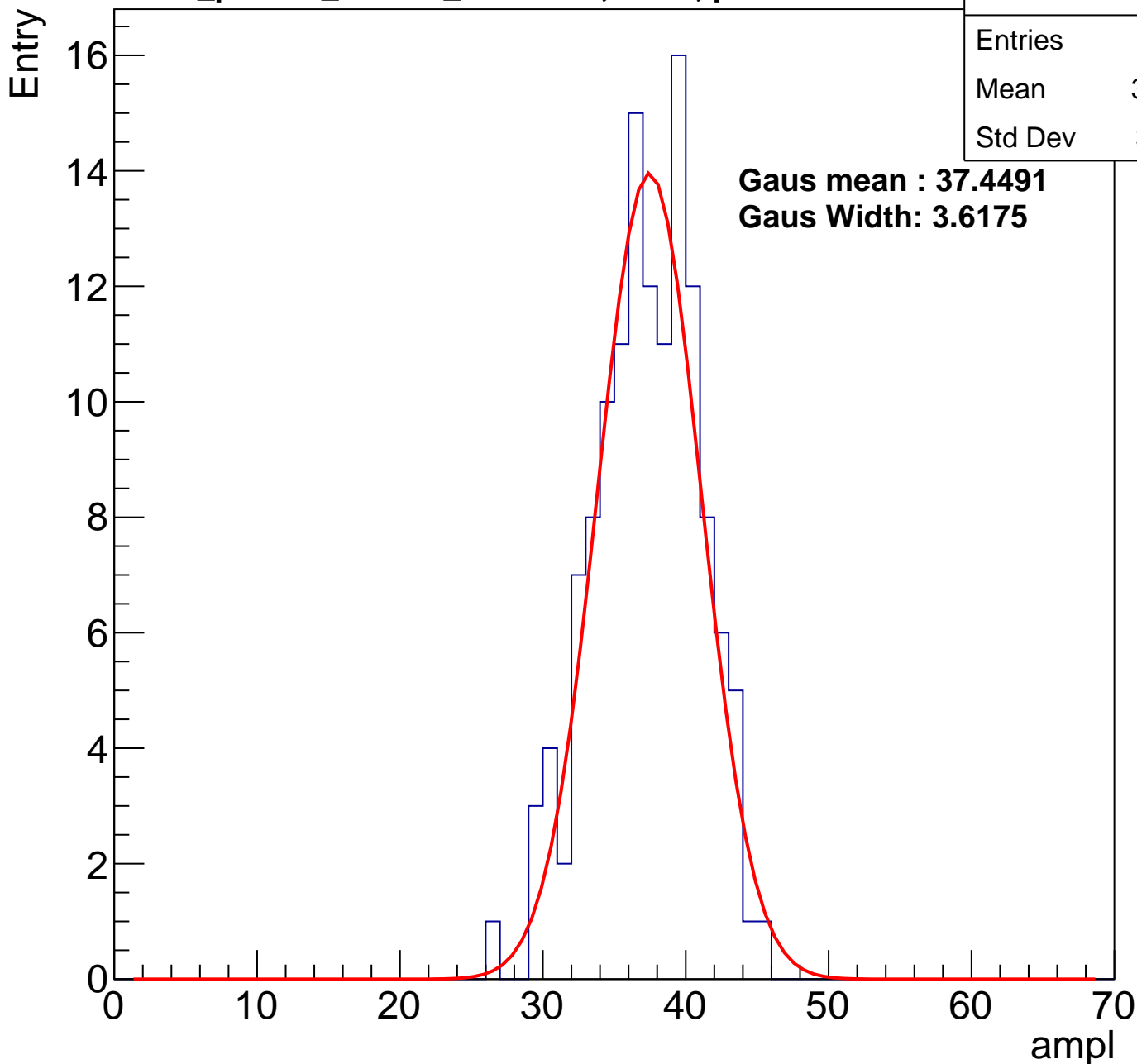
# B0L101S, U26-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	36.83
Std Dev	3.661

**Gaus mean : 37.4491**

**Gaus Width: 3.6175**



# B0L101S, U26-ch34, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

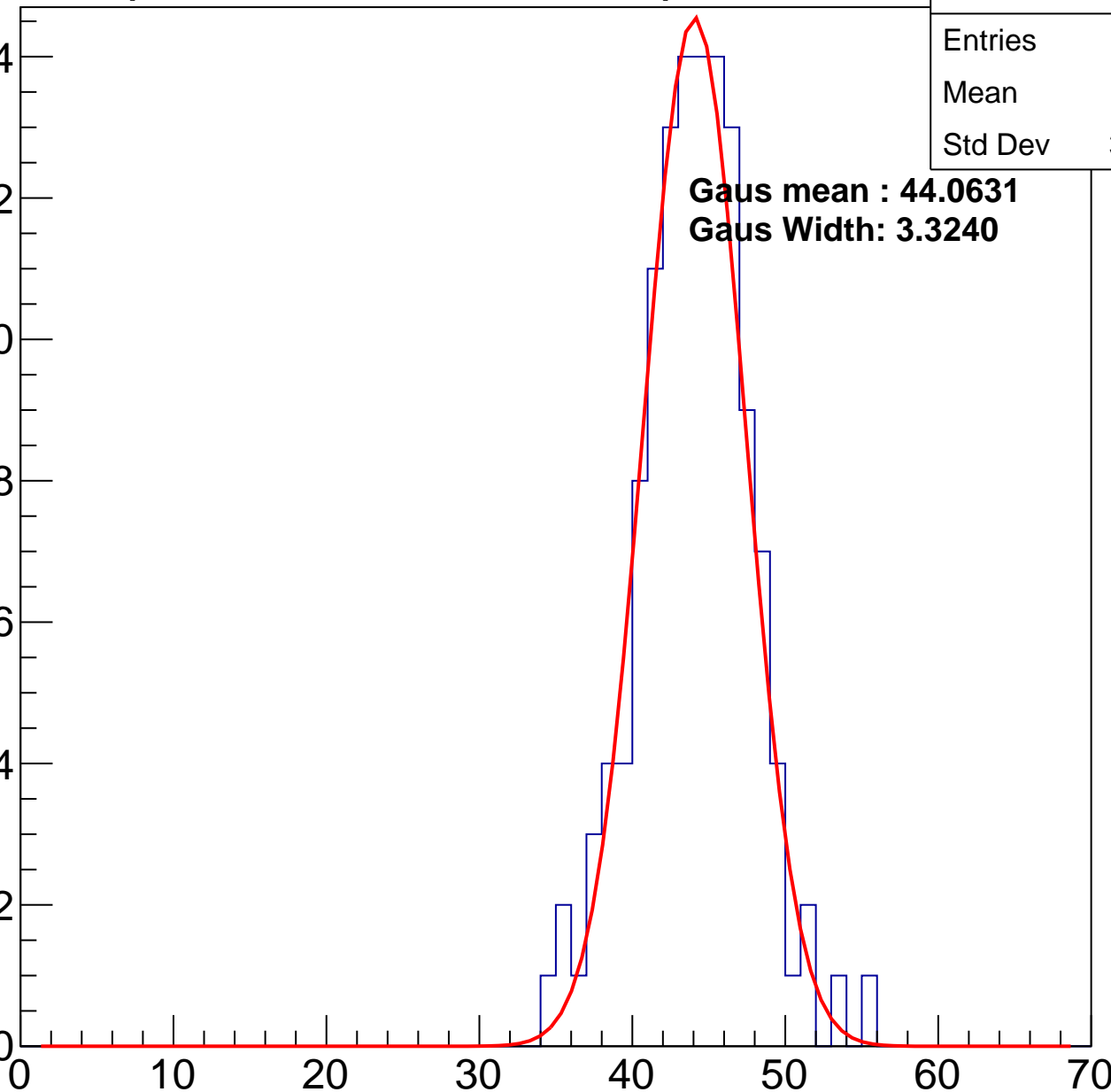
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	127
Mean	43.6
Std Dev	3.642

**Gaus mean : 44.0631**  
**Gaus Width: 3.3240**

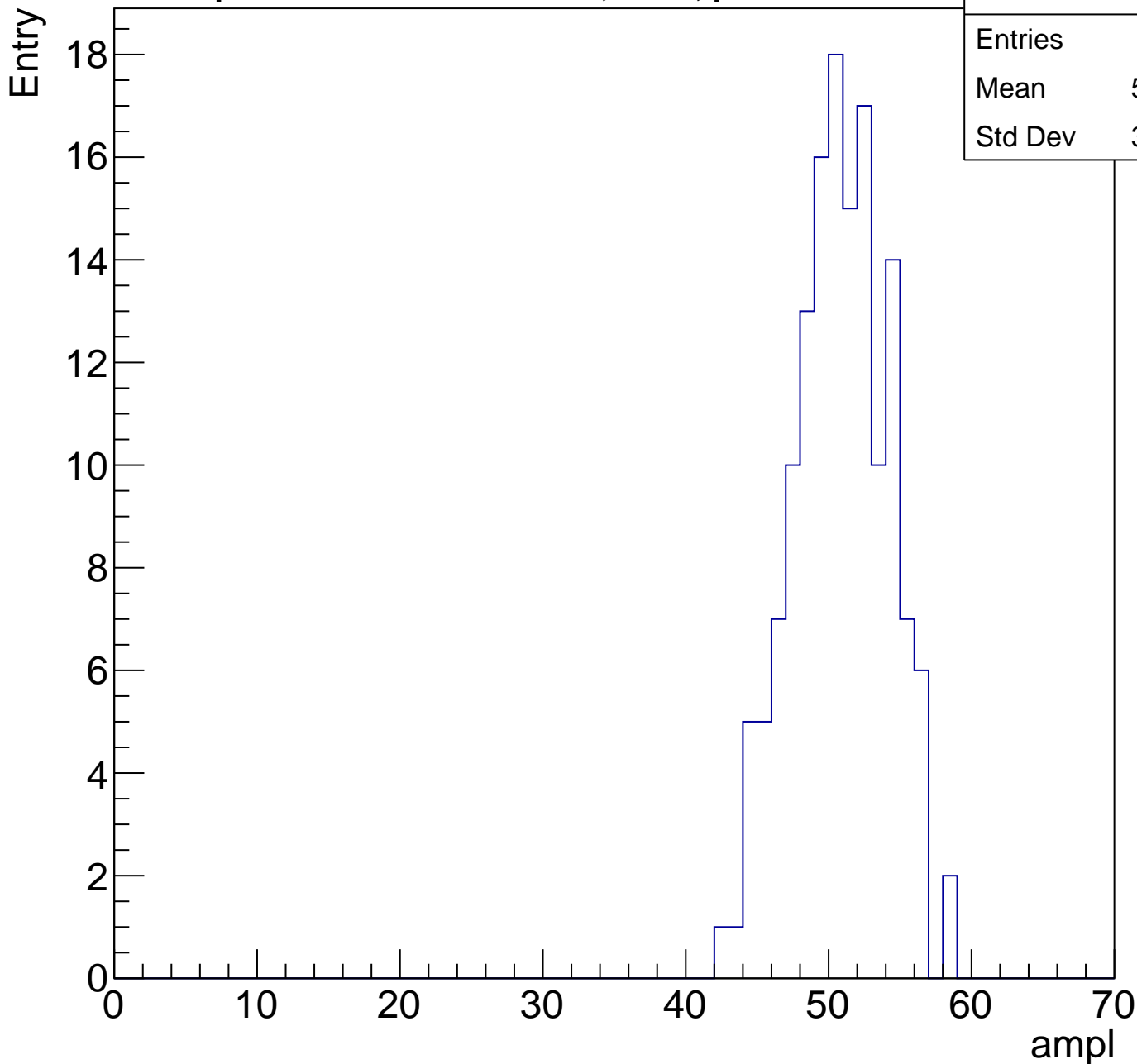
ampl



# B0L101S, U26-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	50.35
Std Dev	3.284



# B0L101S, U26-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

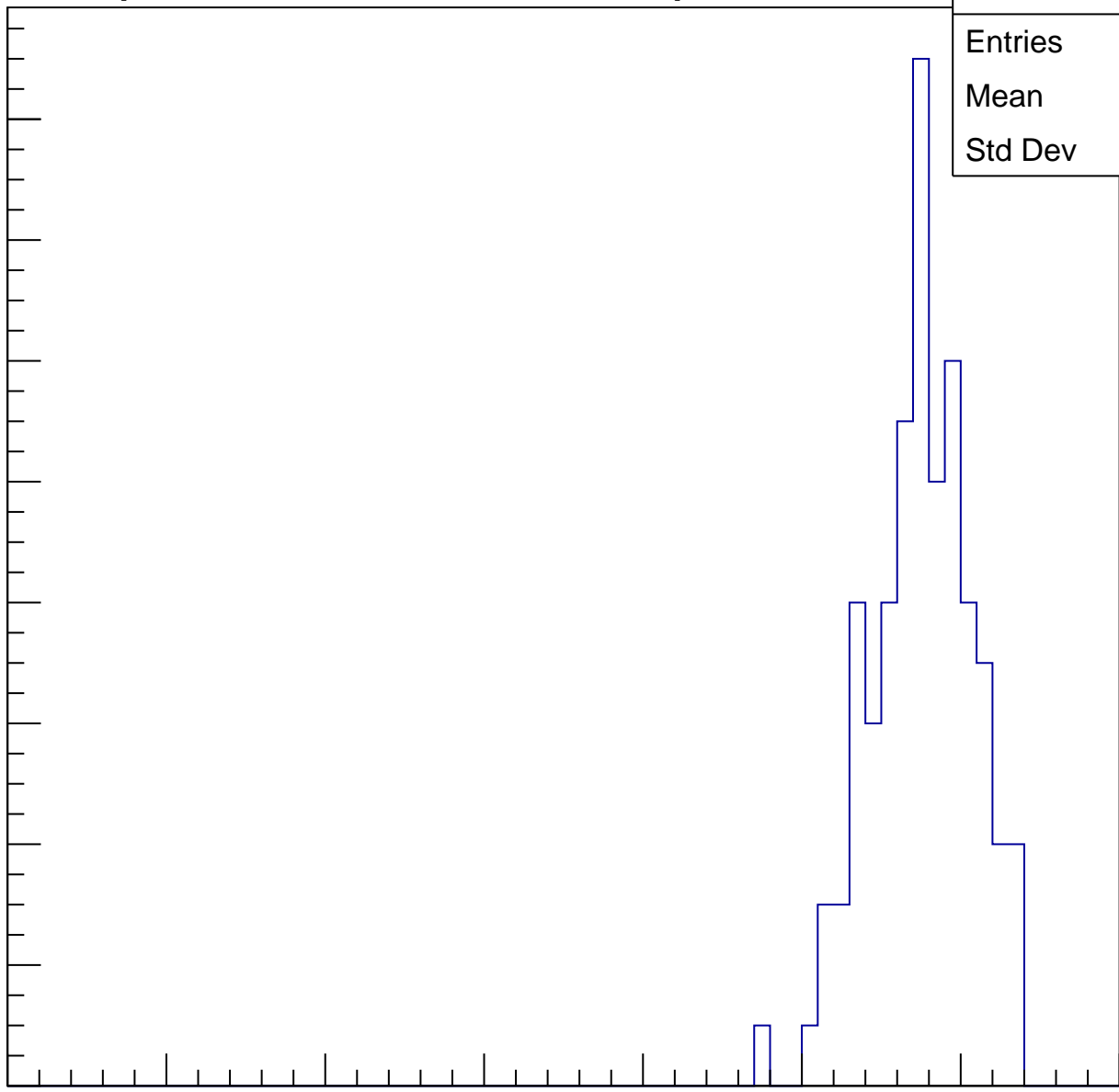
Entries	103
Mean	57.03
Std Dev	3.182

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch34, adc5

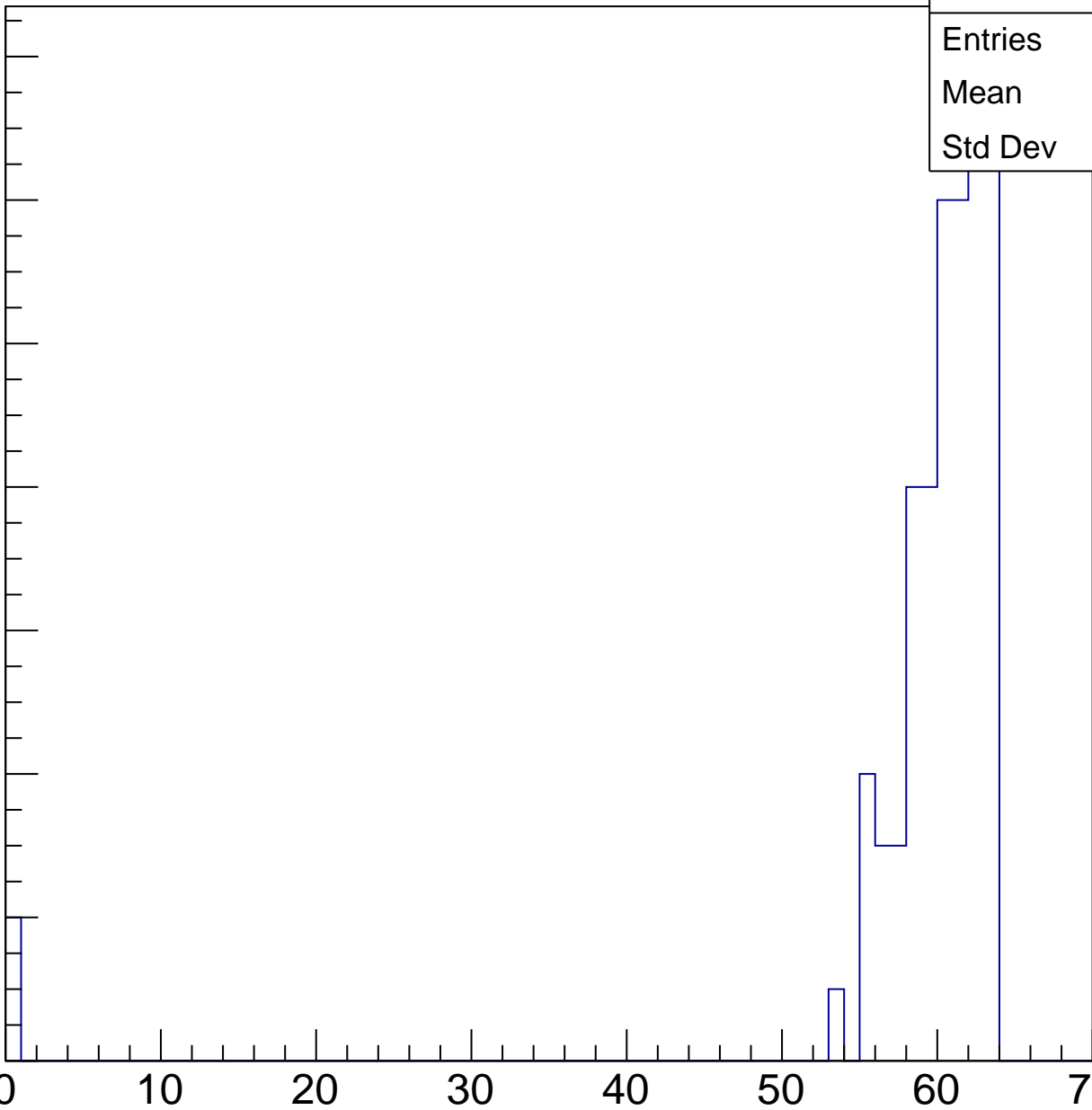
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	80
Mean	58.59
Std Dev	9.676

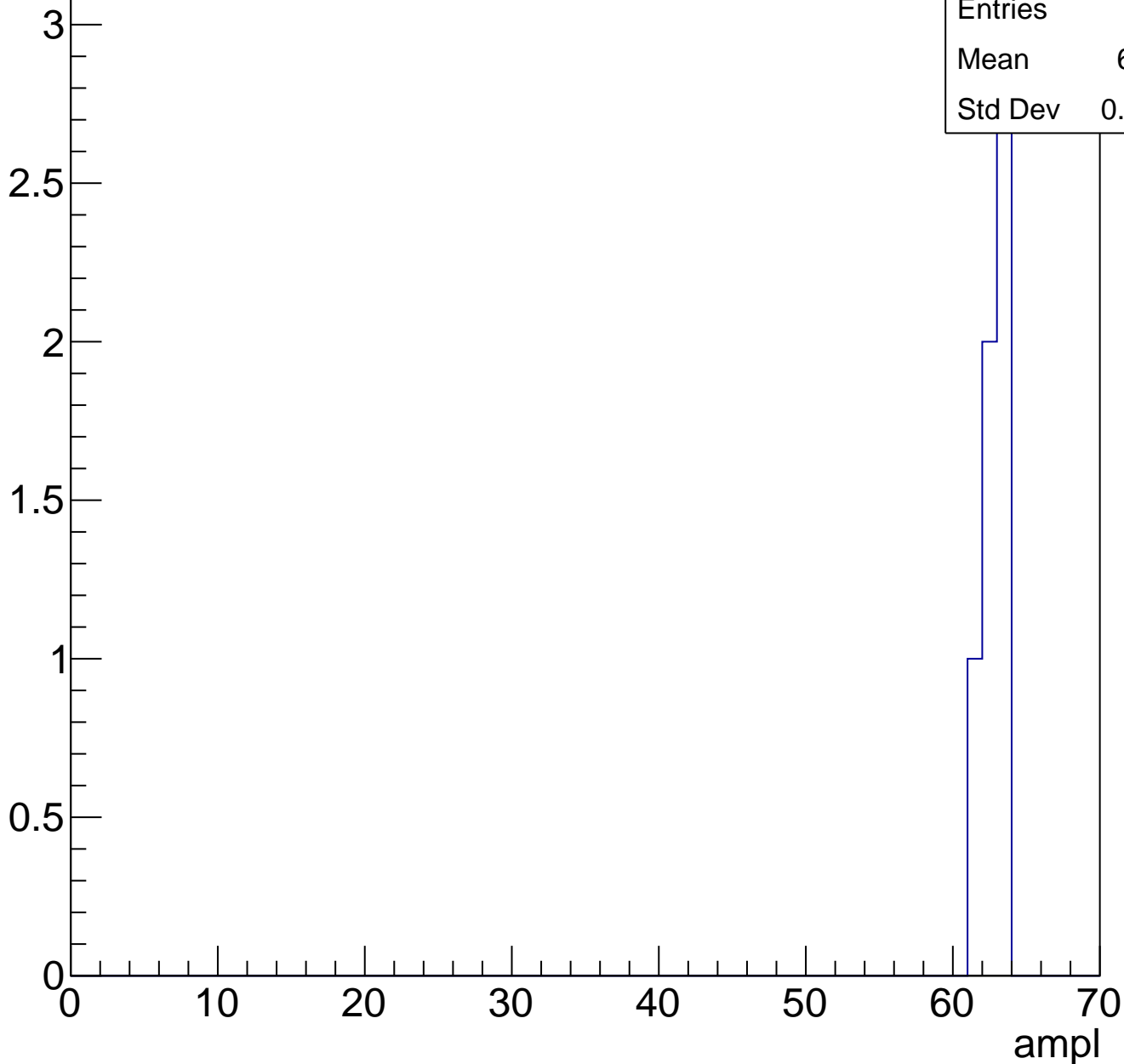
ampl



# B0L101S, U26-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch35, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

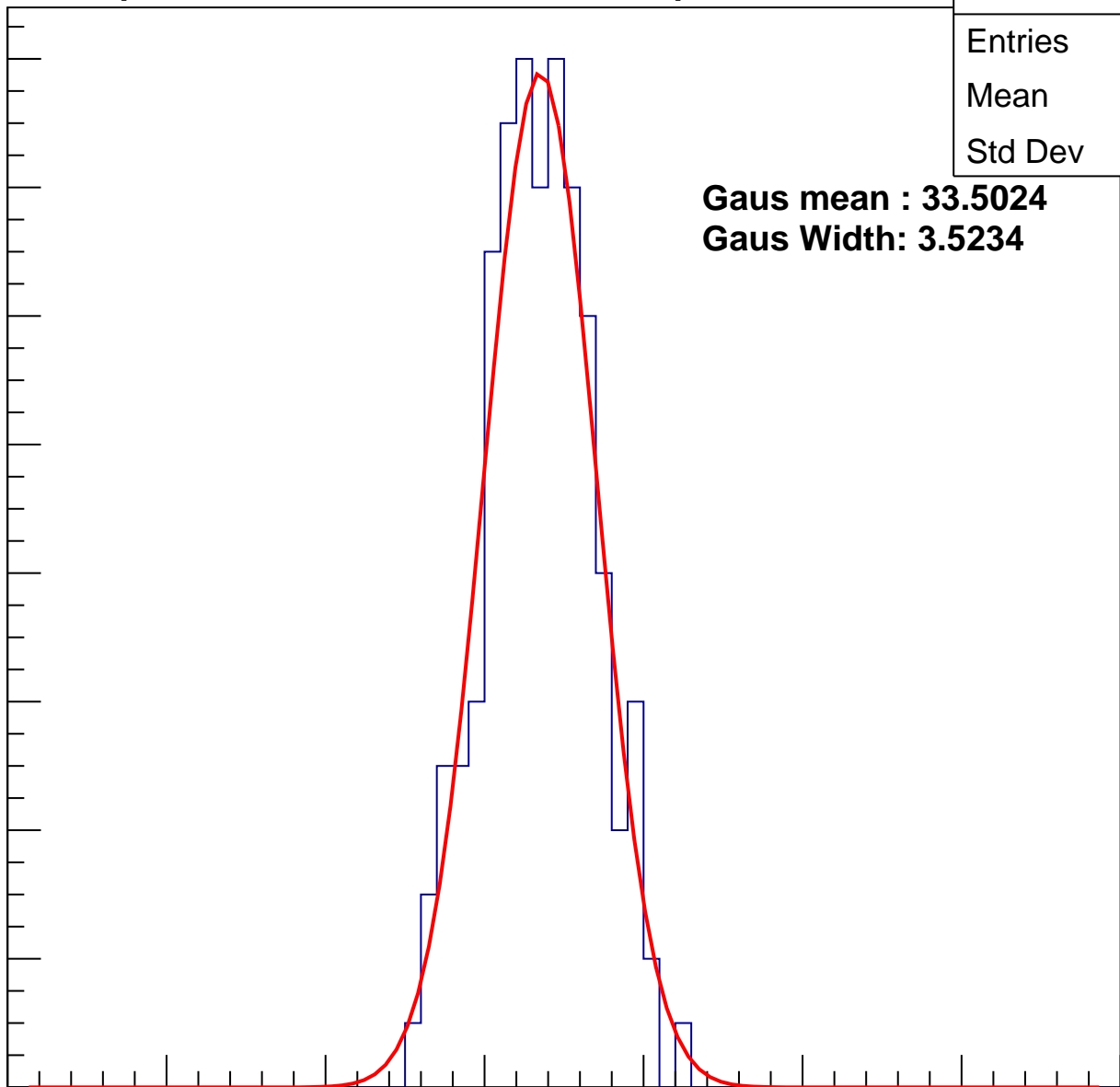
Entries	141
Mean	32.99
Std Dev	3.377

**Gaus mean : 33.5024**

**Gaus Width: 3.5234**

0 10 20 30 40 50 60 70

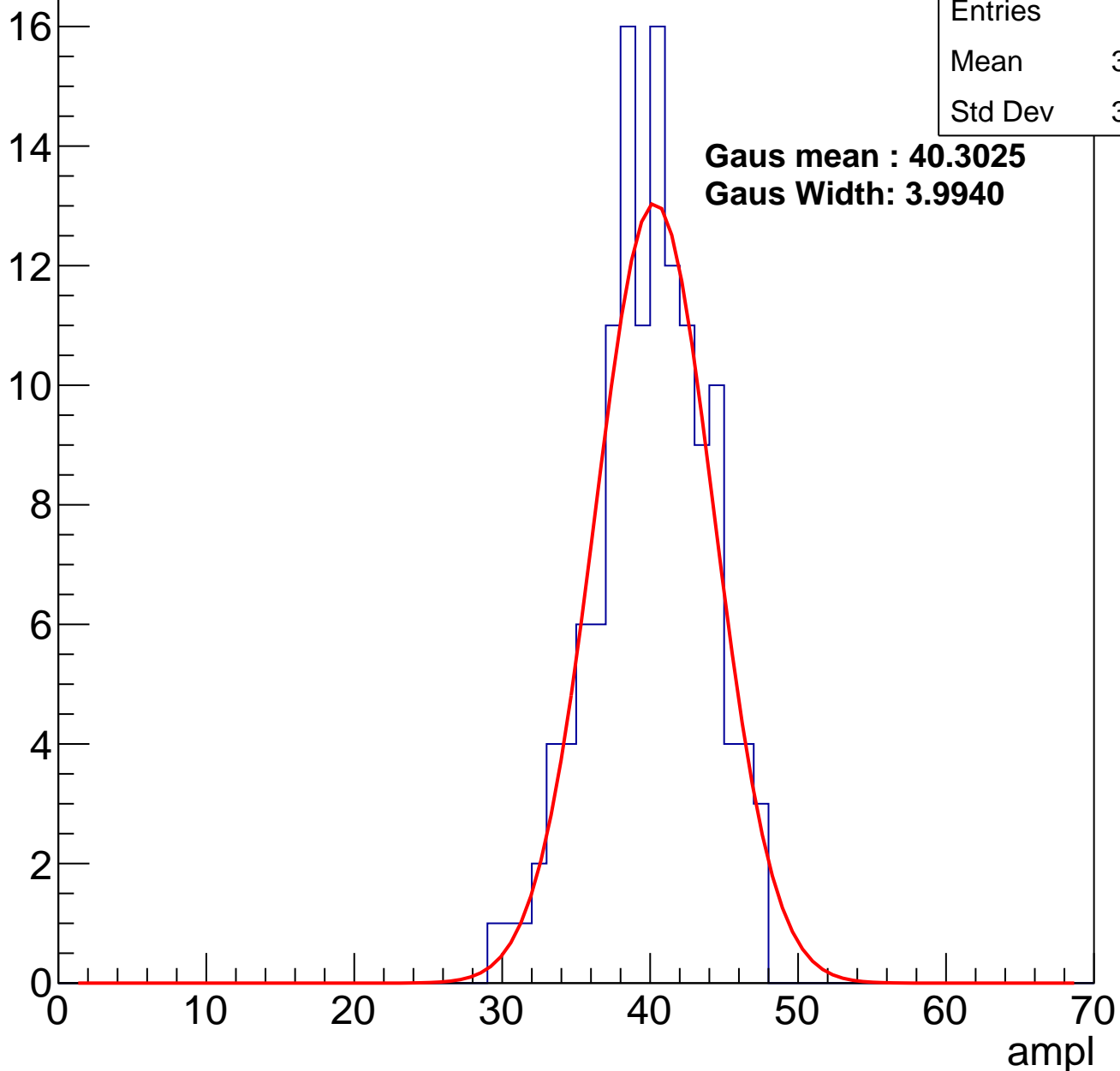
ampl



# B0L101S, U26-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	132
Mean	39.53
Std Dev	3.743

# B0L101S, U26-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

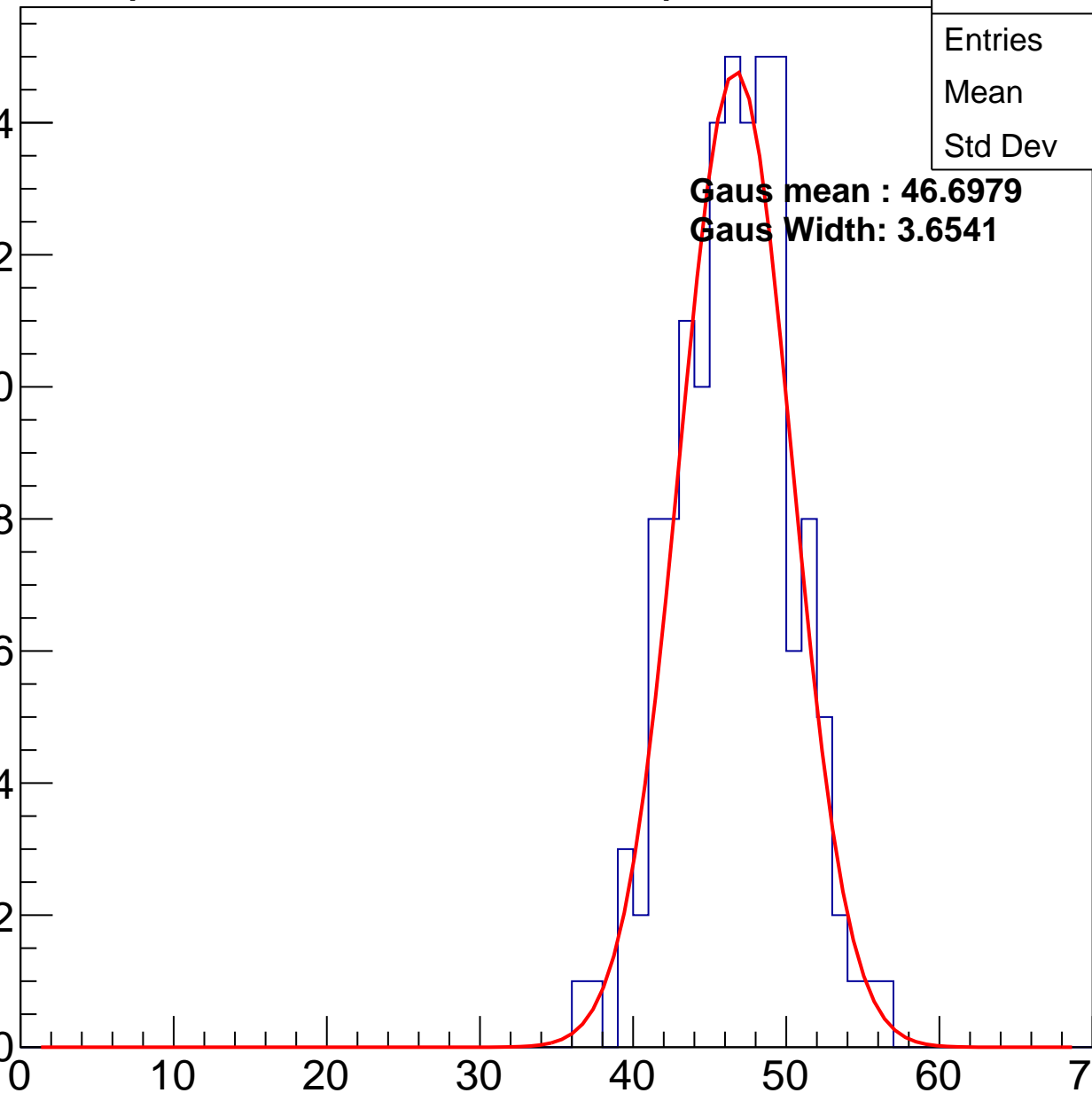
14  
12  
10  
8  
6  
4  
2  
0

Entries	141
Mean	46.23
Std Dev	3.672

**Gaus mean : 46.6979**

**Gaus Width: 3.6541**

ampl



# B0L101S, U26-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

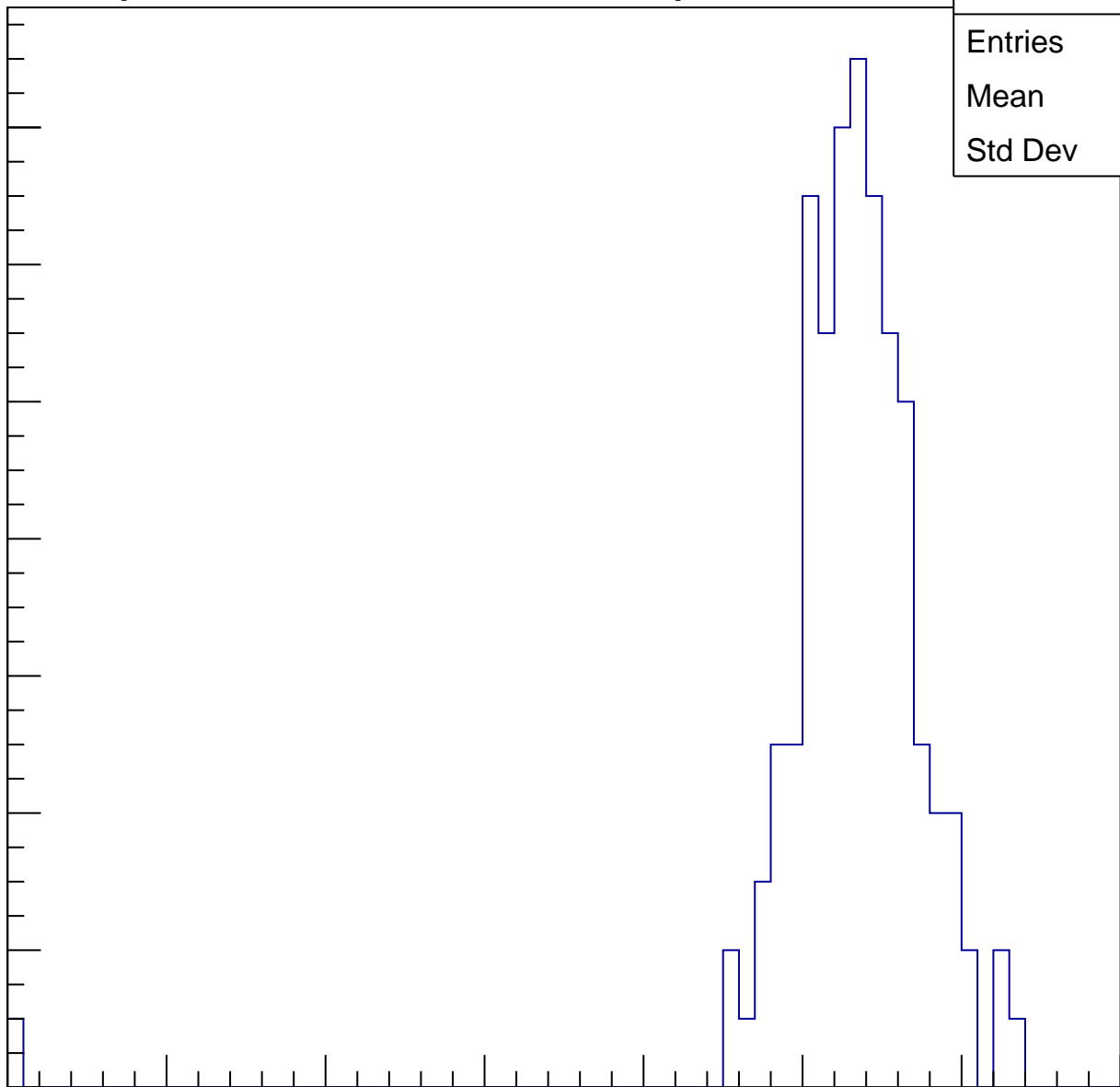
Entries	122
Mean	52.65
Std Dev	5.93

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

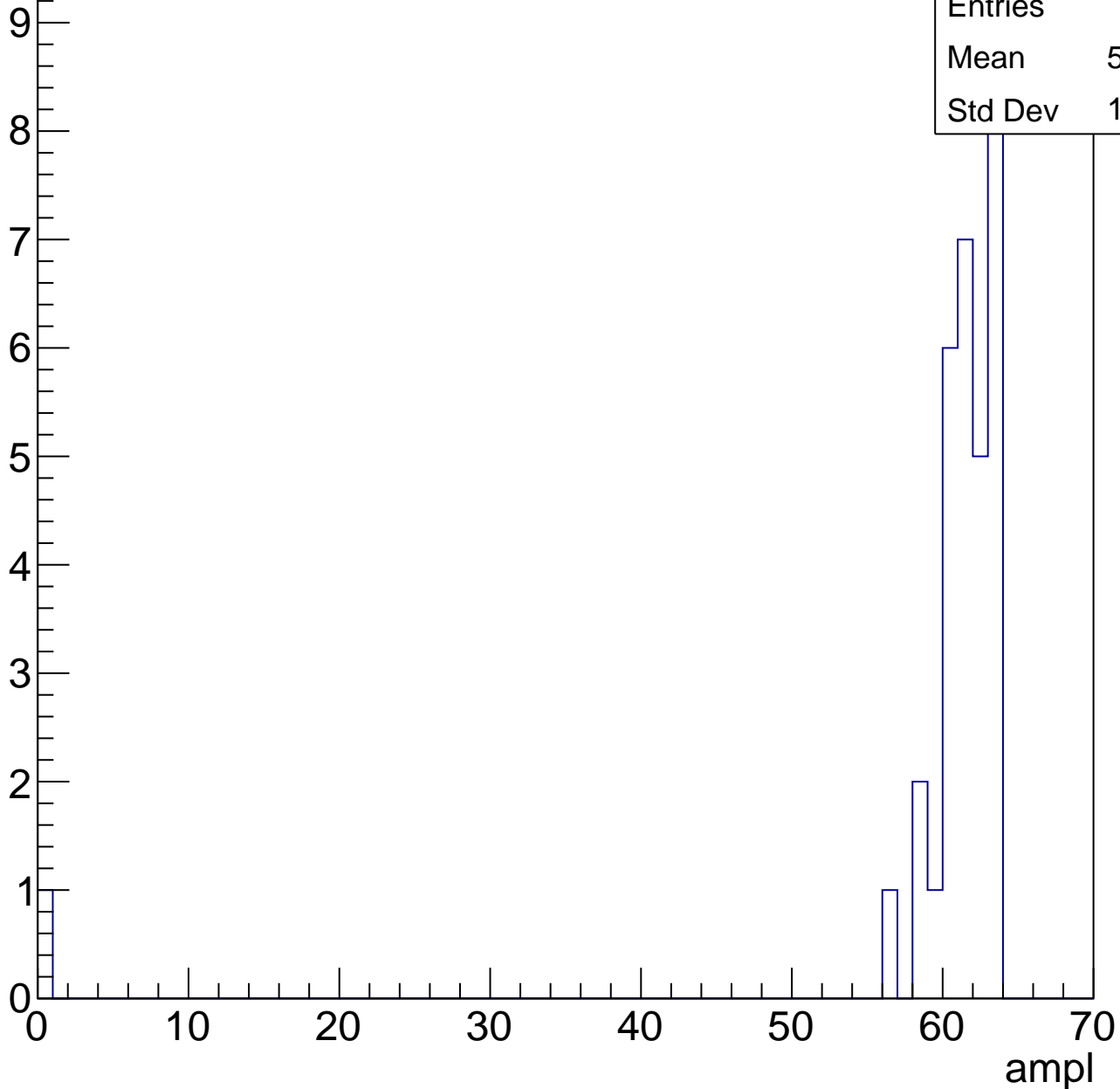
Entries	119
Mean	58.5
Std Dev	2.93

# B0L101S, U26-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	32
Mean	59.22
Std Dev	10.77



# B0L101S, U26-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

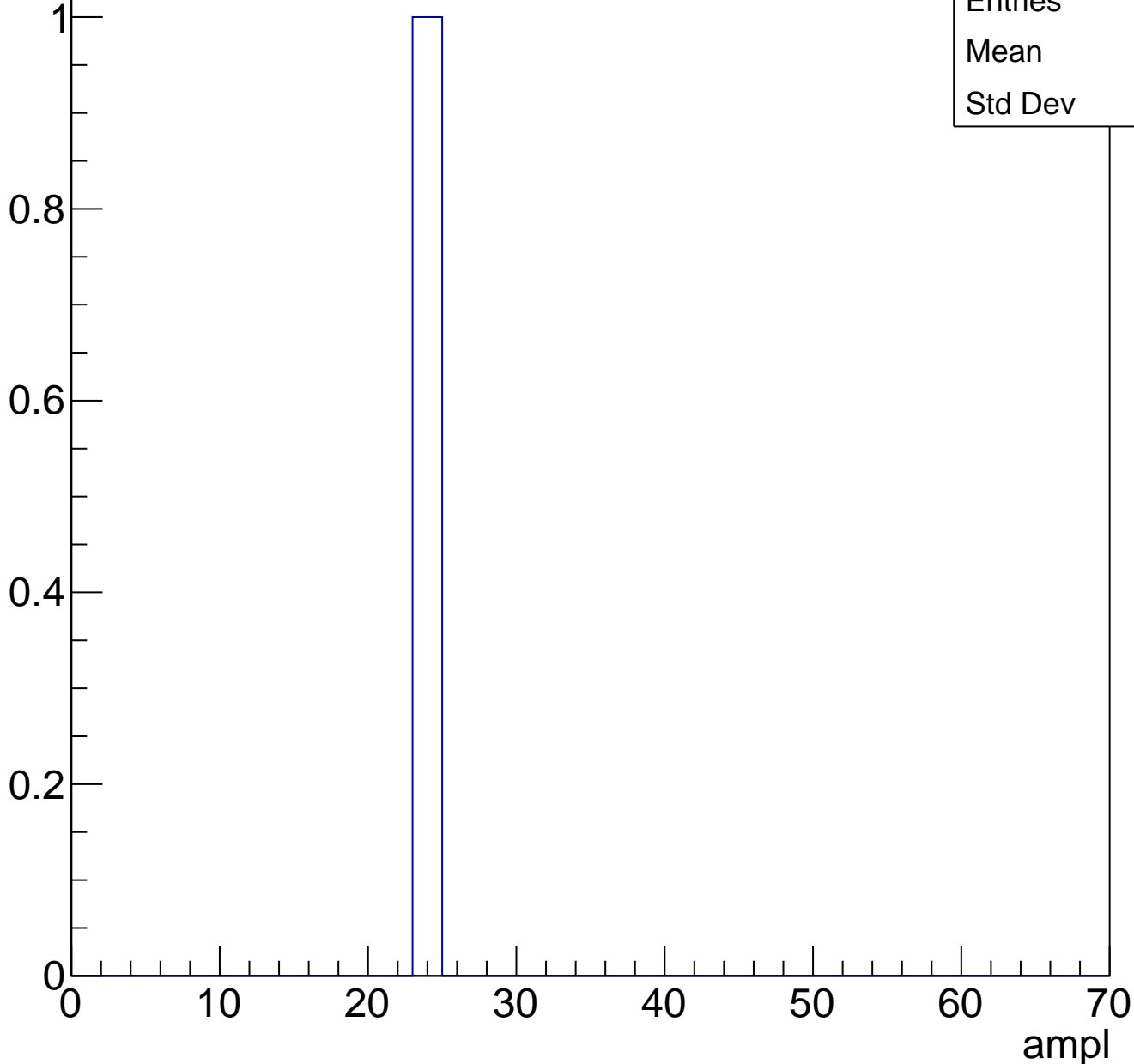




# B0L101S, U26-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	29.57
Std Dev	3.329

**Gaus mean : 29.7389**

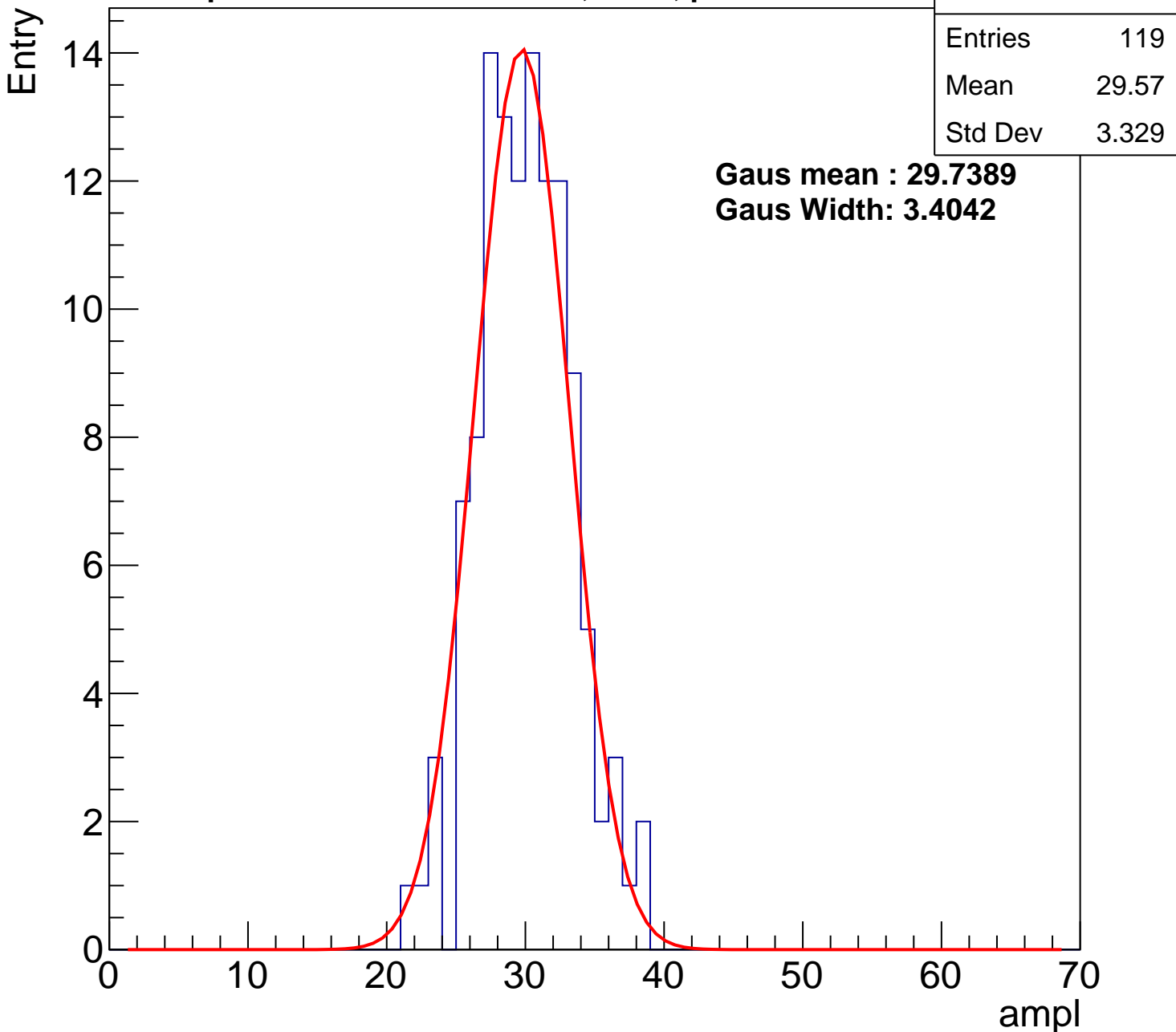
**Gaus Width: 3.4042**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



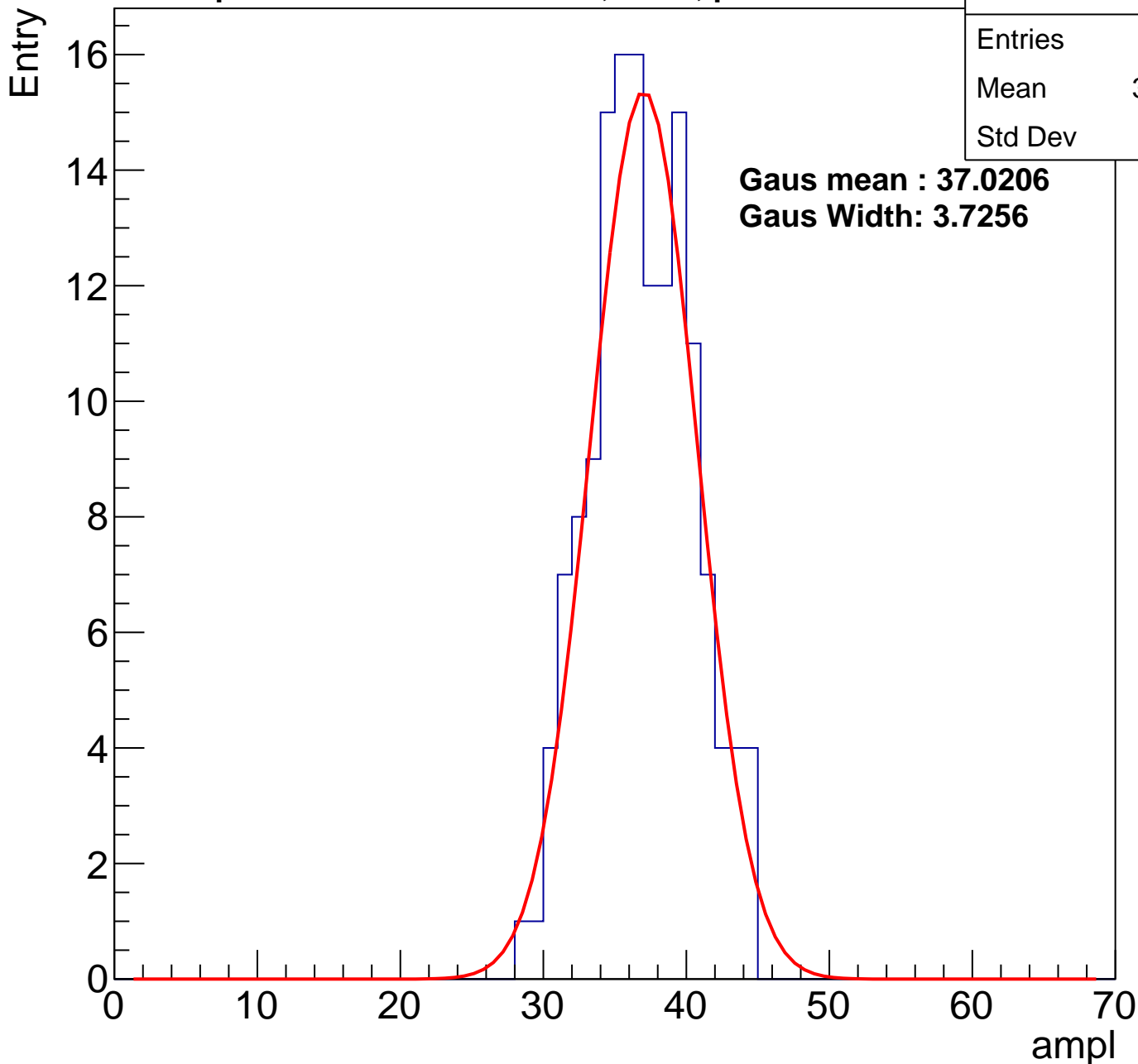
# B0L101S, U26-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	36.45
Std Dev	3.54

**Gaus mean : 37.0206**

**Gaus Width: 3.7256**



# B0L101S, U26-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

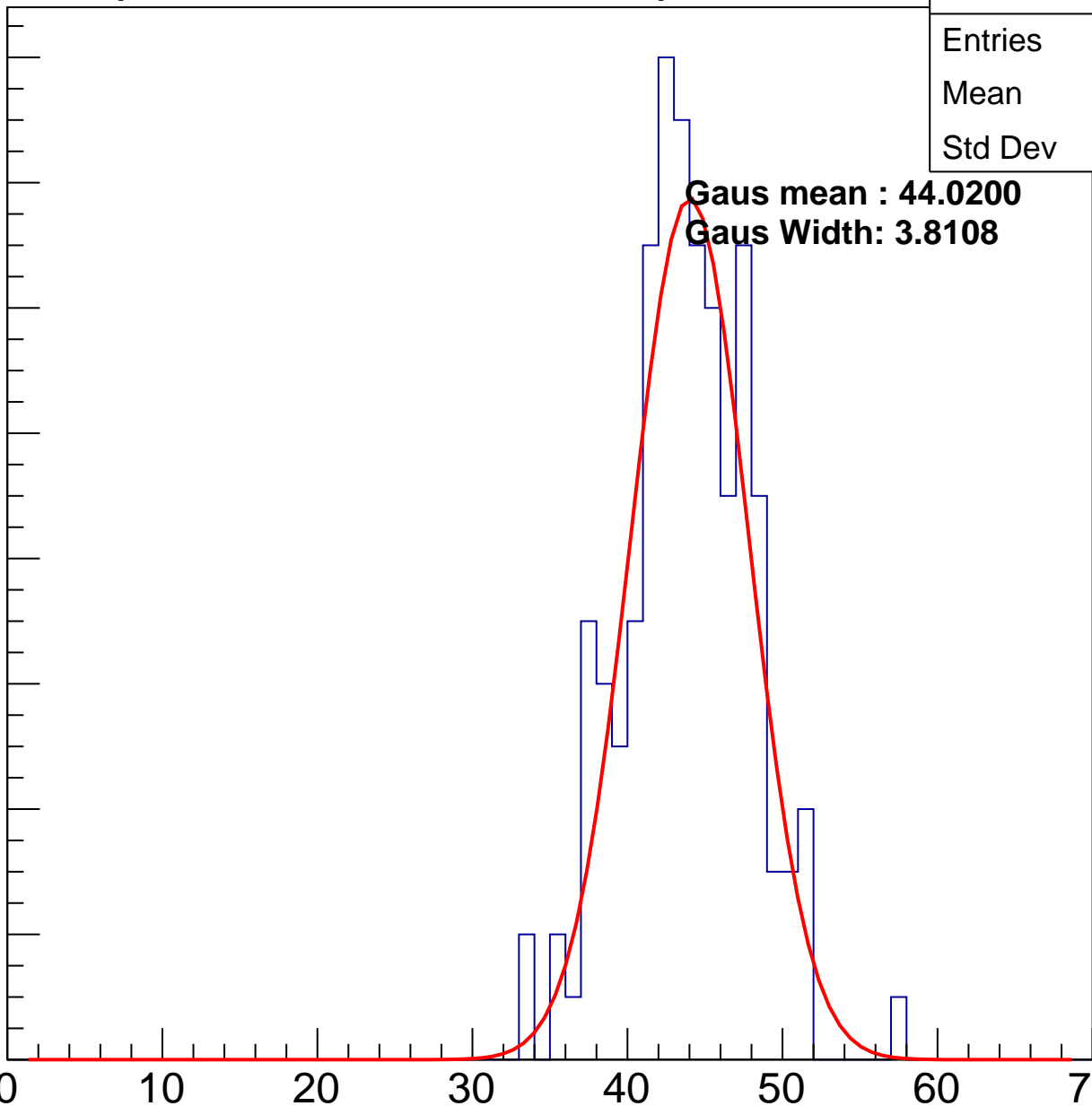
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	141
Mean	43.34
Std Dev	4

**Gaus mean : 44.0200**

**Gaus Width: 3.8108**

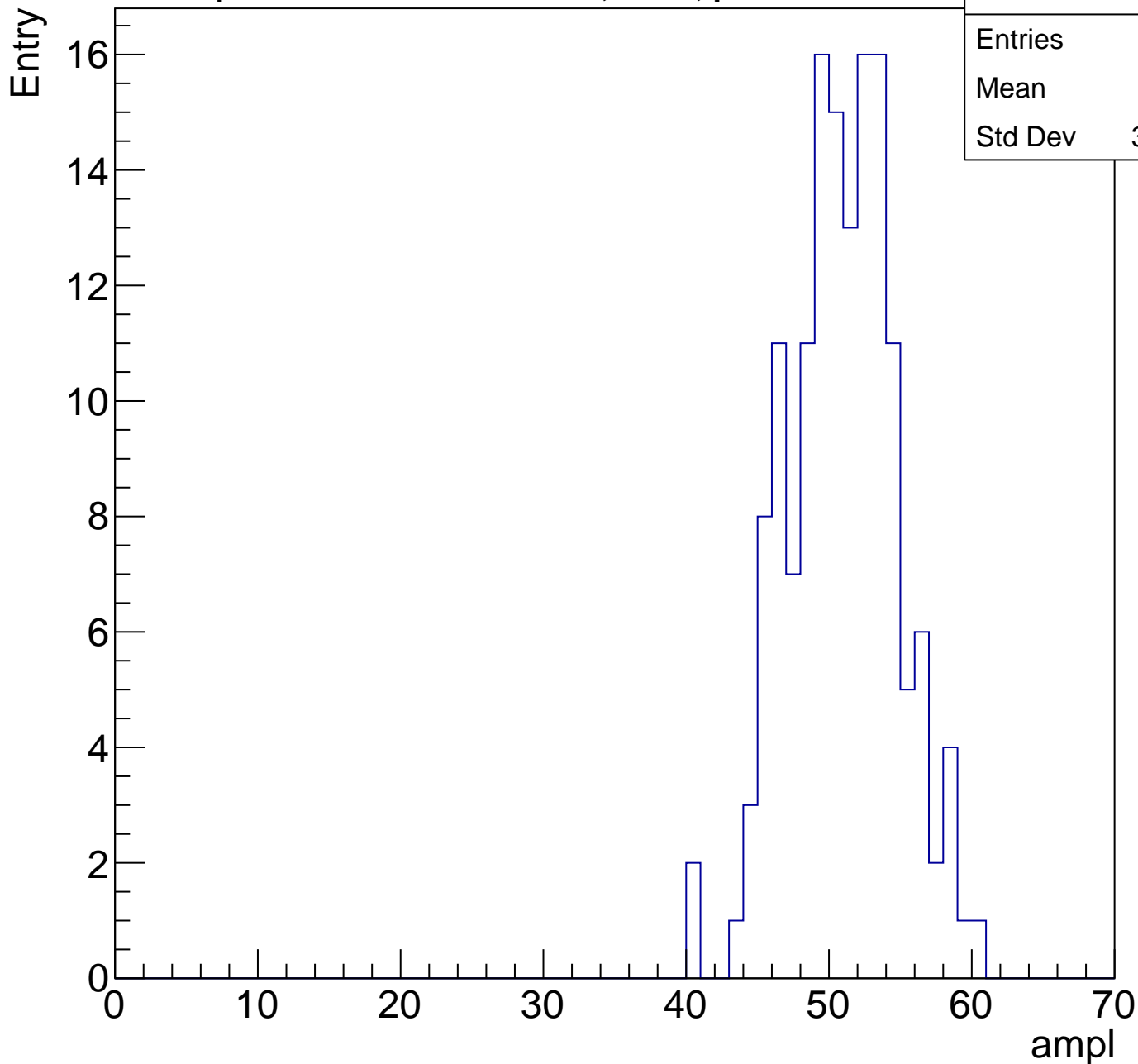
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U26-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

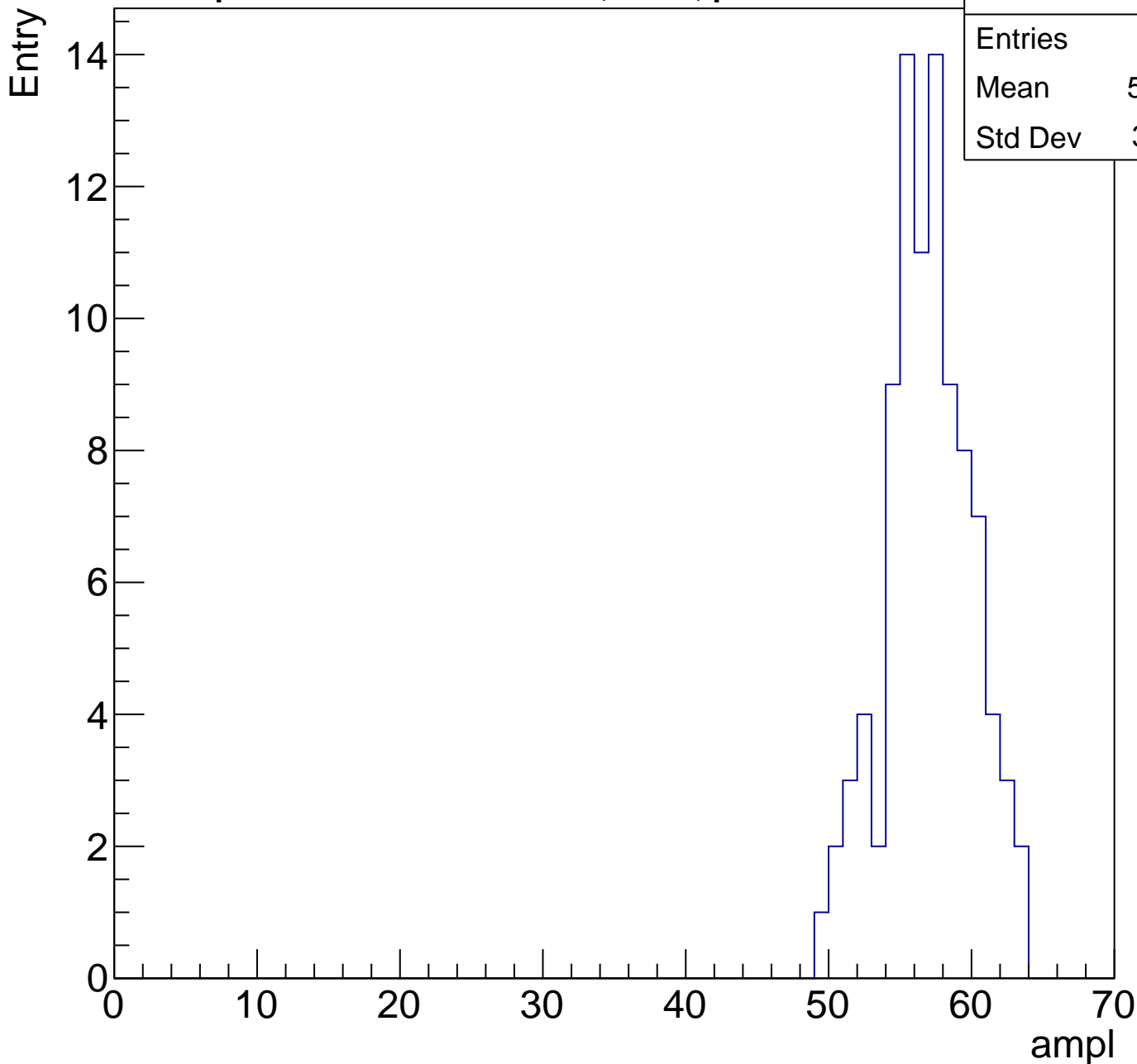
Entries	149
Mean	50.5
Std Dev	3.746



# B0L101S, U26-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	93
Mean	56.52
Std Dev	3.011



# B0L101S, U26-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	73
Mean	59.32
Std Dev	7.376

ampl

0

10

20

30

40

50

60

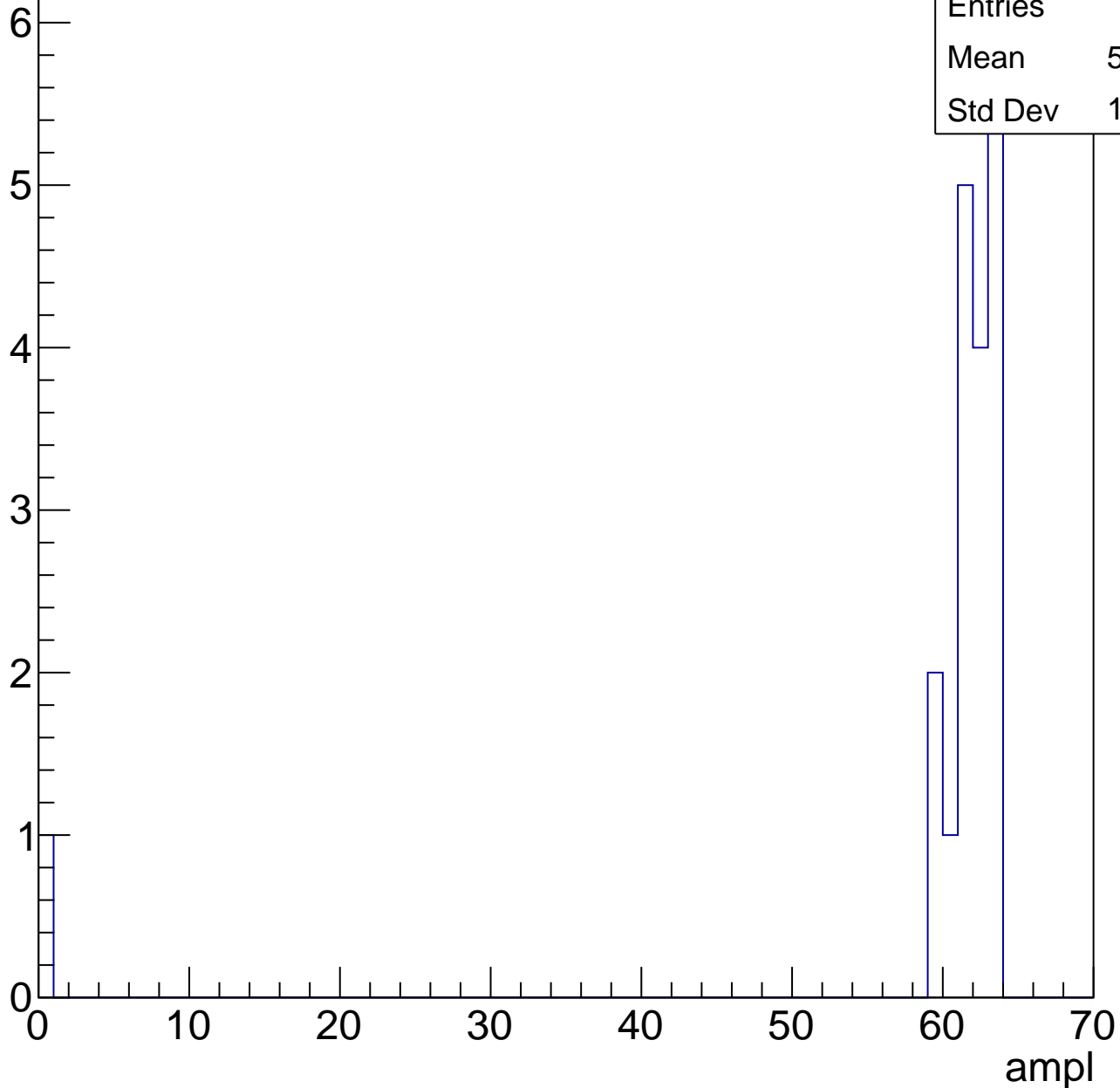
70

# B0L101S, U26-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	19
Mean	58.37
Std Dev	13.82





# B0L101S, U26-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch37, adc0

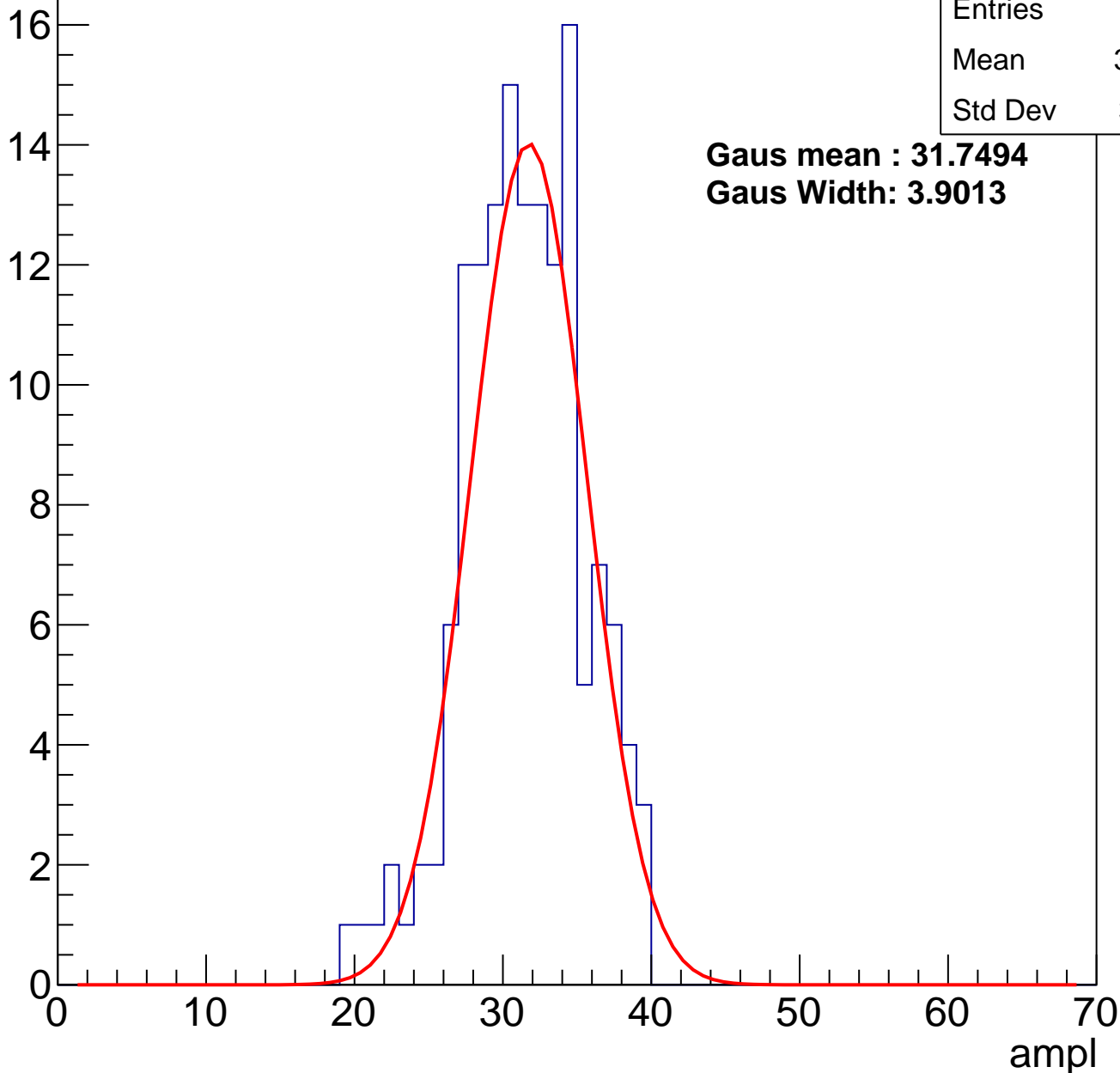
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	30.92
Std Dev	3.991

**Gaus mean : 31.7494**

**Gaus Width: 3.9013**

Entry

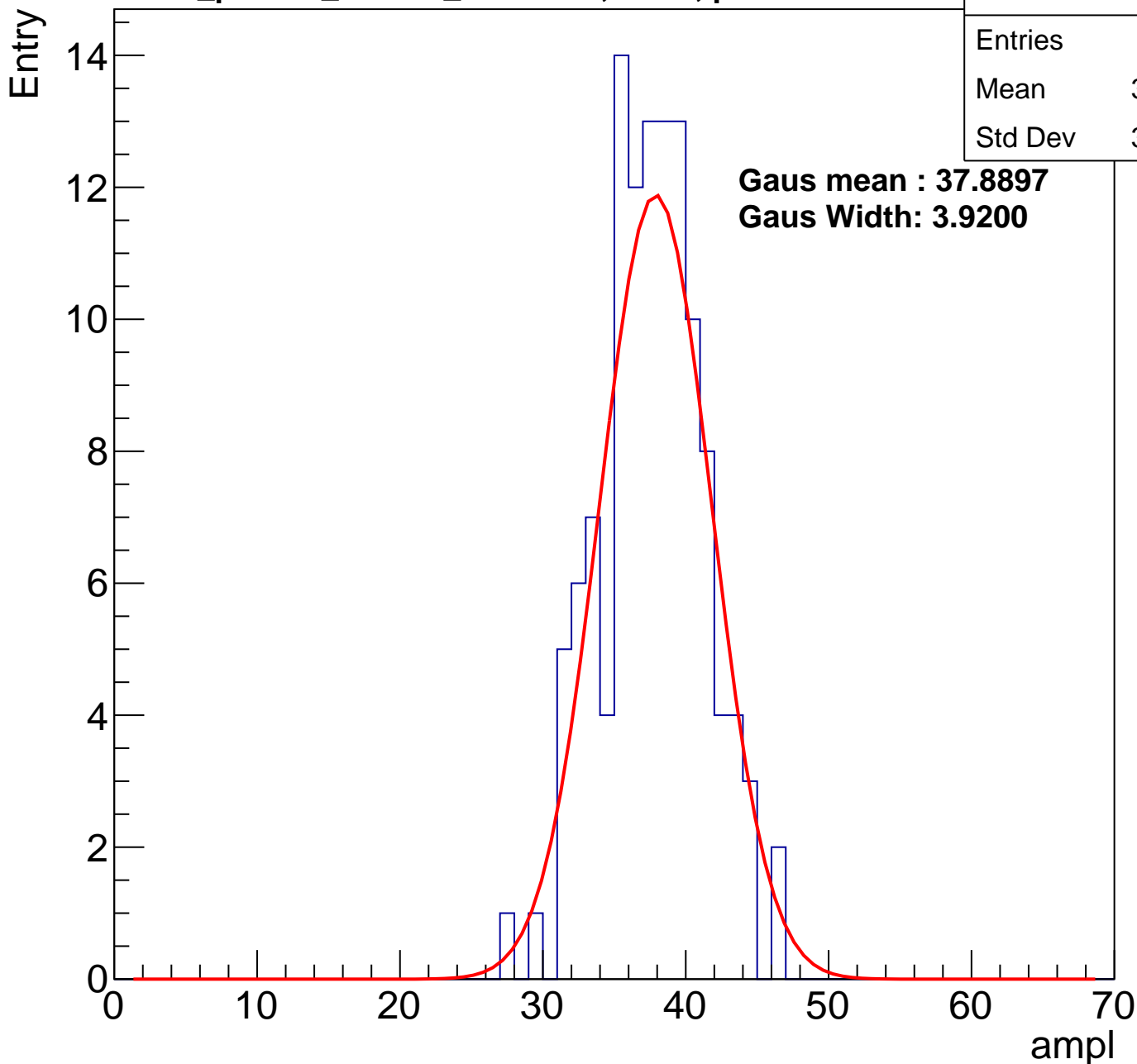


# B0L101S, U26-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	37.22
Std Dev	3.583

**Gaus mean : 37.8897**  
**Gaus Width: 3.9200**



# B0L101S, U26-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

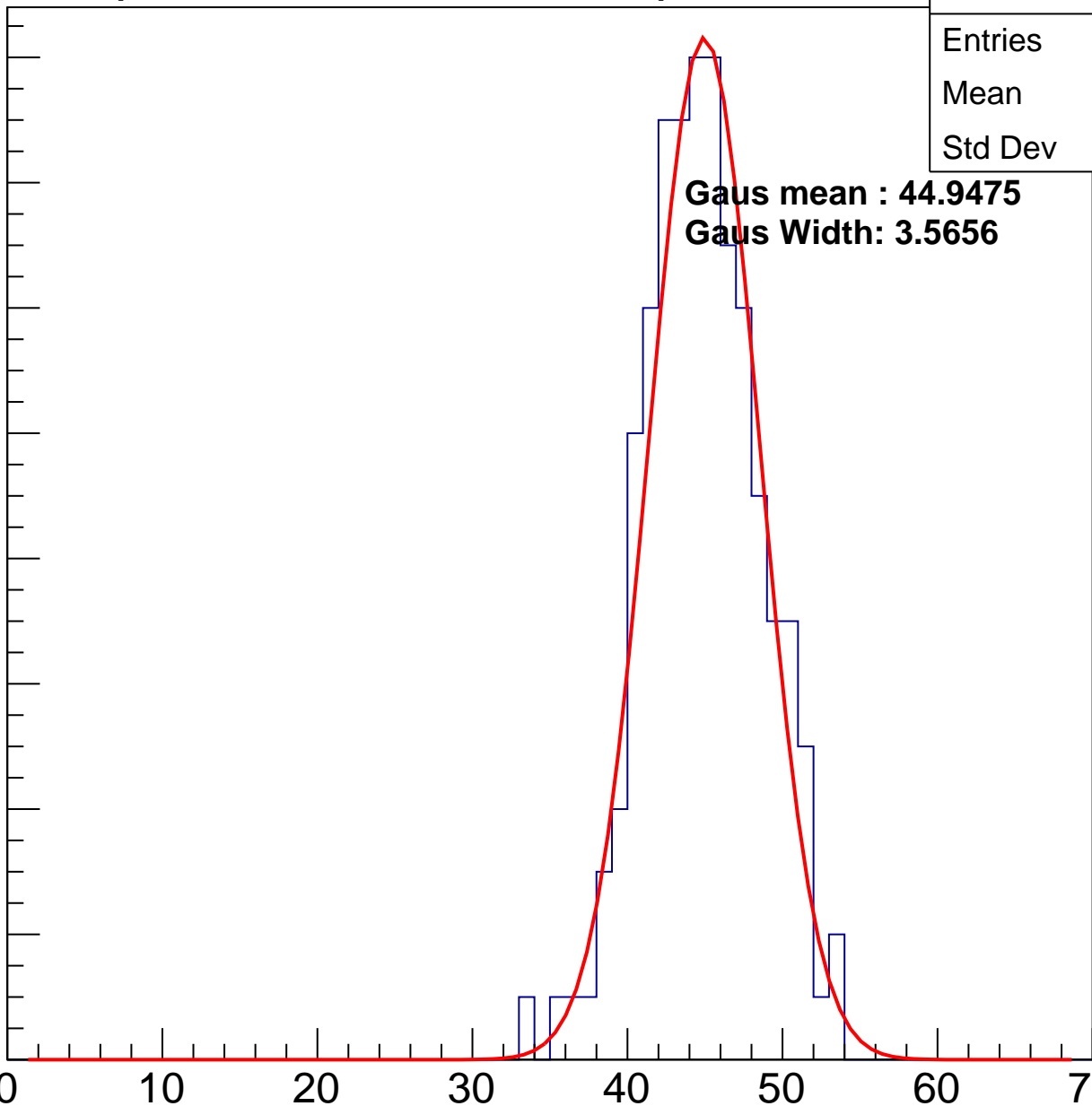
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	151
Mean	44.38
Std Dev	3.695

**Gaus mean : 44.9475**

**Gaus Width: 3.5656**

ampl



# B0L101S, U26-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	51.15
Std Dev	3.478

Entry

14

12

10

8

6

4

2

0

0

10

20

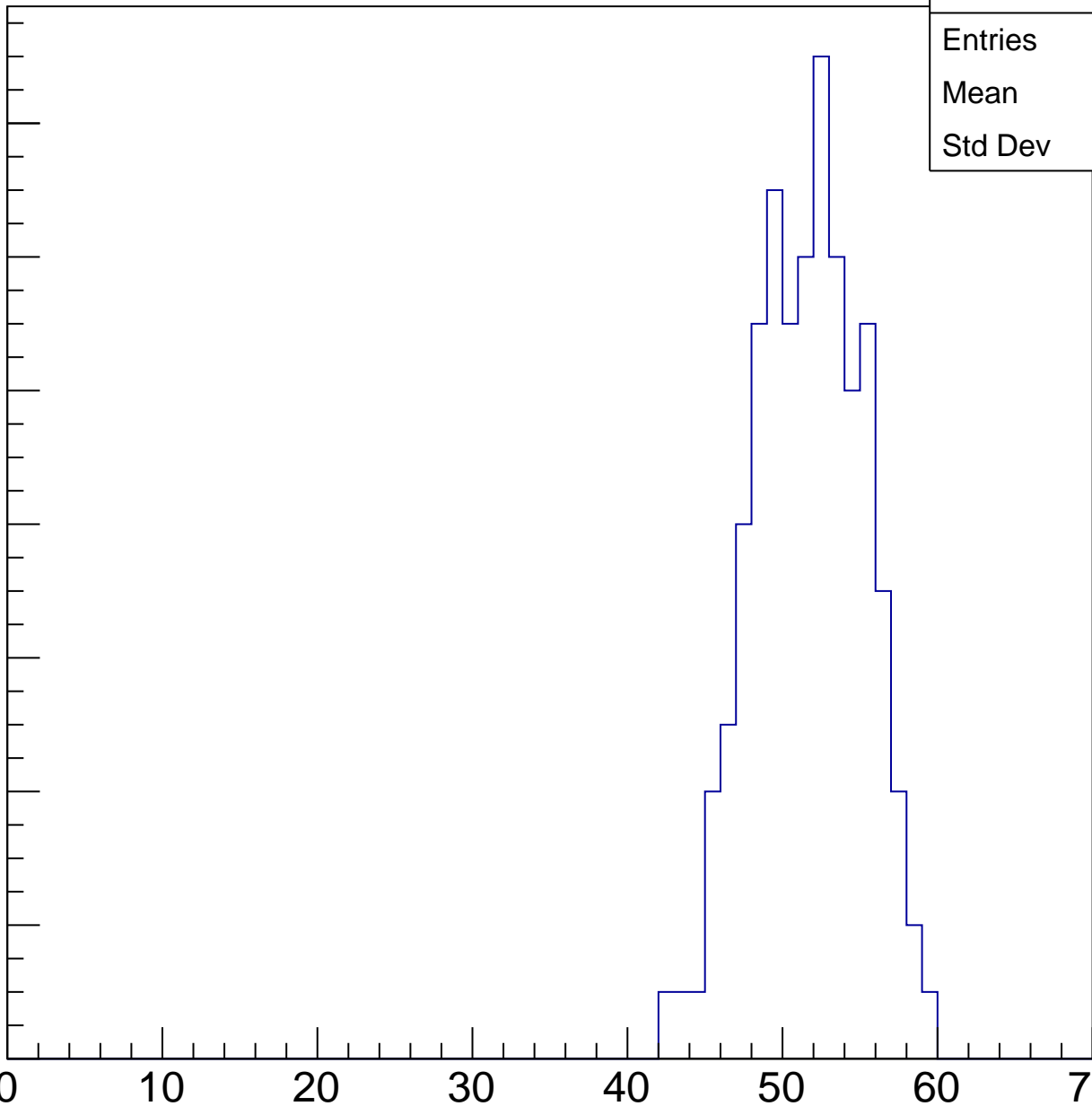
30

40

50

60

ampl



# B0L101S, U26-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

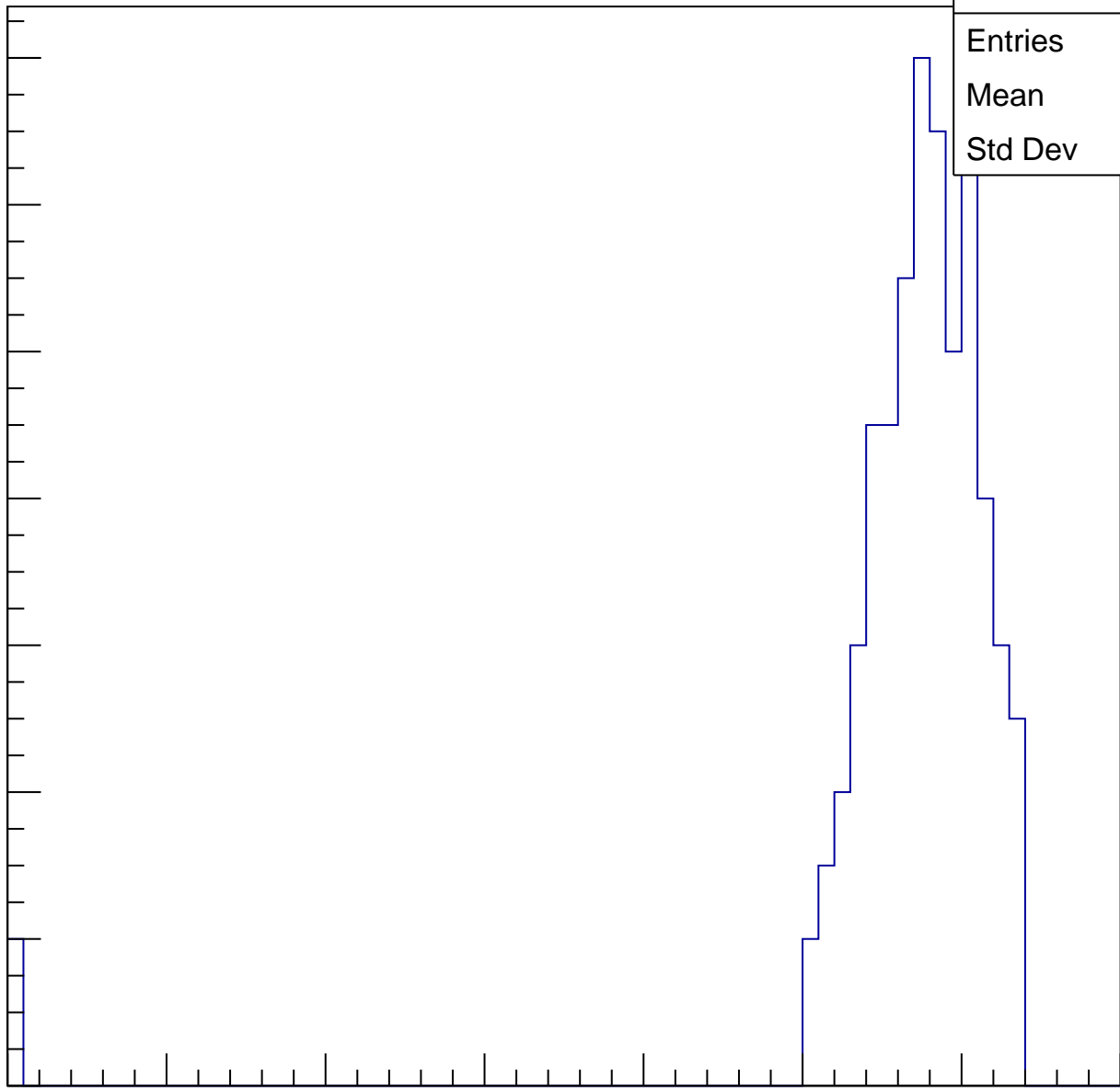
Entries	115
Mean	56.29
Std Dev	8.128

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch37, adc5

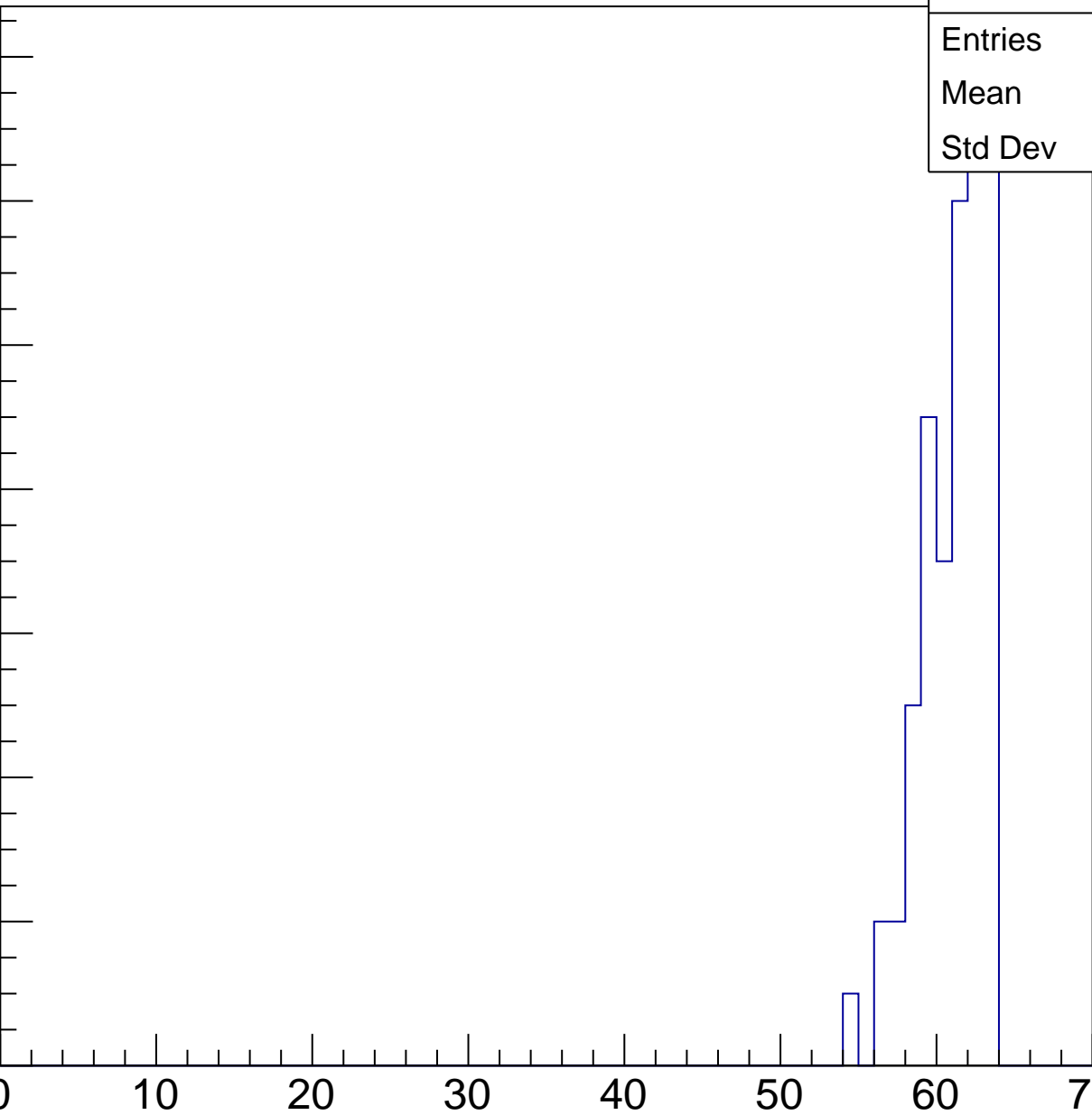
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	66
Mean	60.65
Std Dev	2.063

ampl



# B0L101S, U26-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch38, adc0

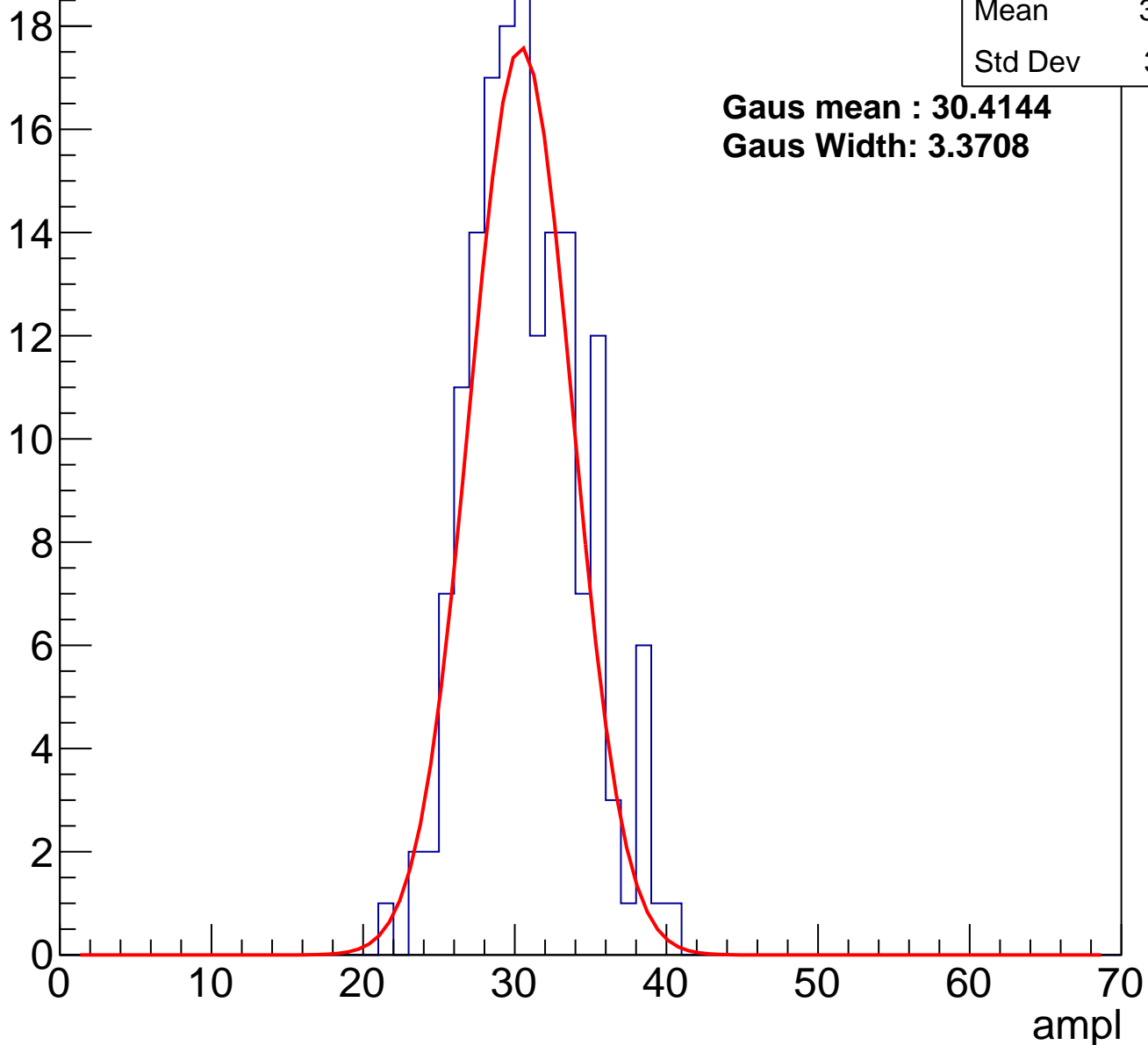
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	30.33
Std Dev	3.621

**Gaus mean : 30.4144**

**Gaus Width: 3.3708**

Entry



# B0L101S, U26-ch38, adc1

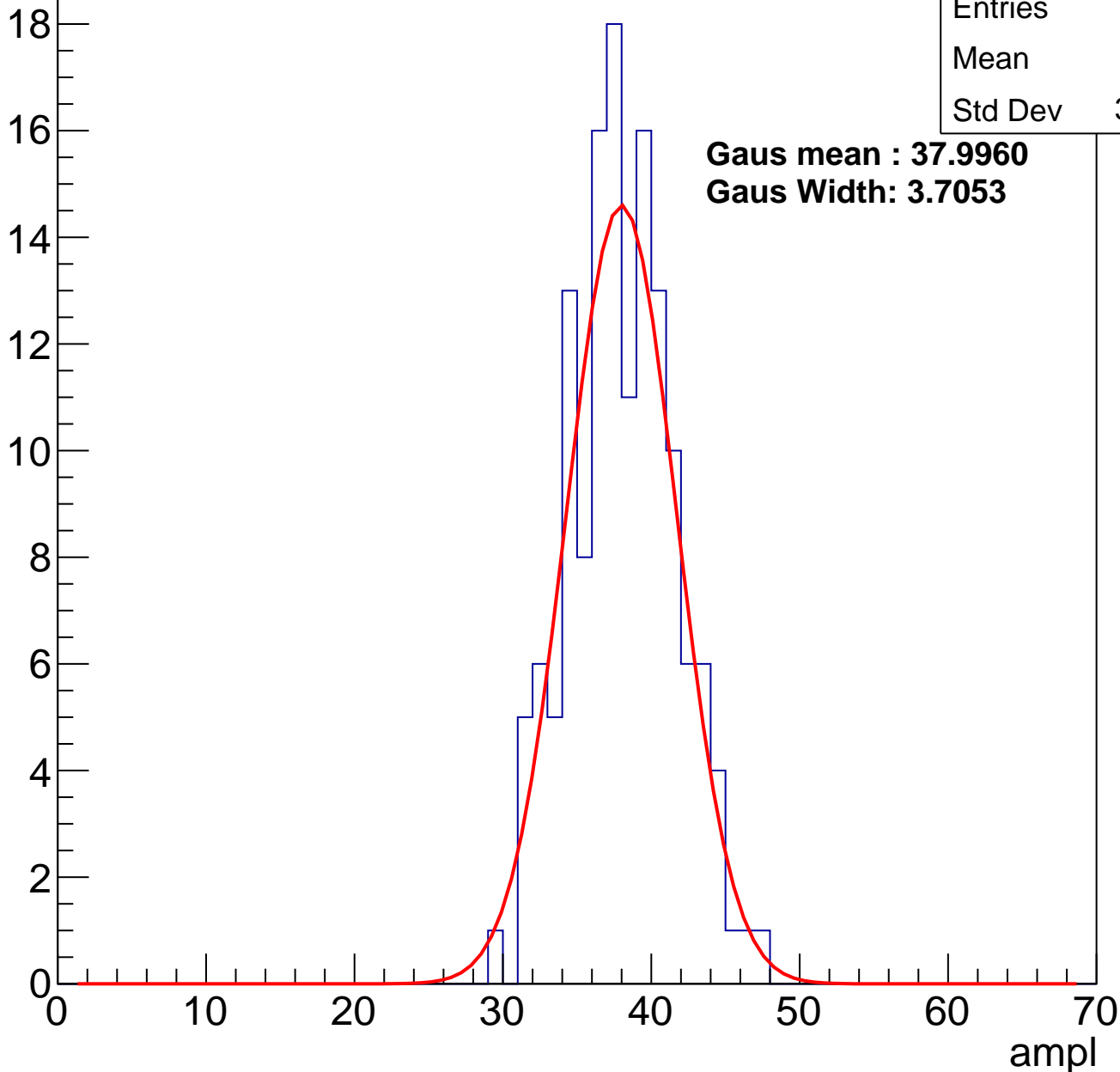
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	37.6
Std Dev	3.521

**Gaus mean : 37.9960**

**Gaus Width: 3.7053**

Entry



# B0L101S, U26-ch38, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

107

Mean

43.82

Std Dev

2.985

**Gaus mean : 44.3378**

**Gaus Width: 3.0095**

0

10

20

30

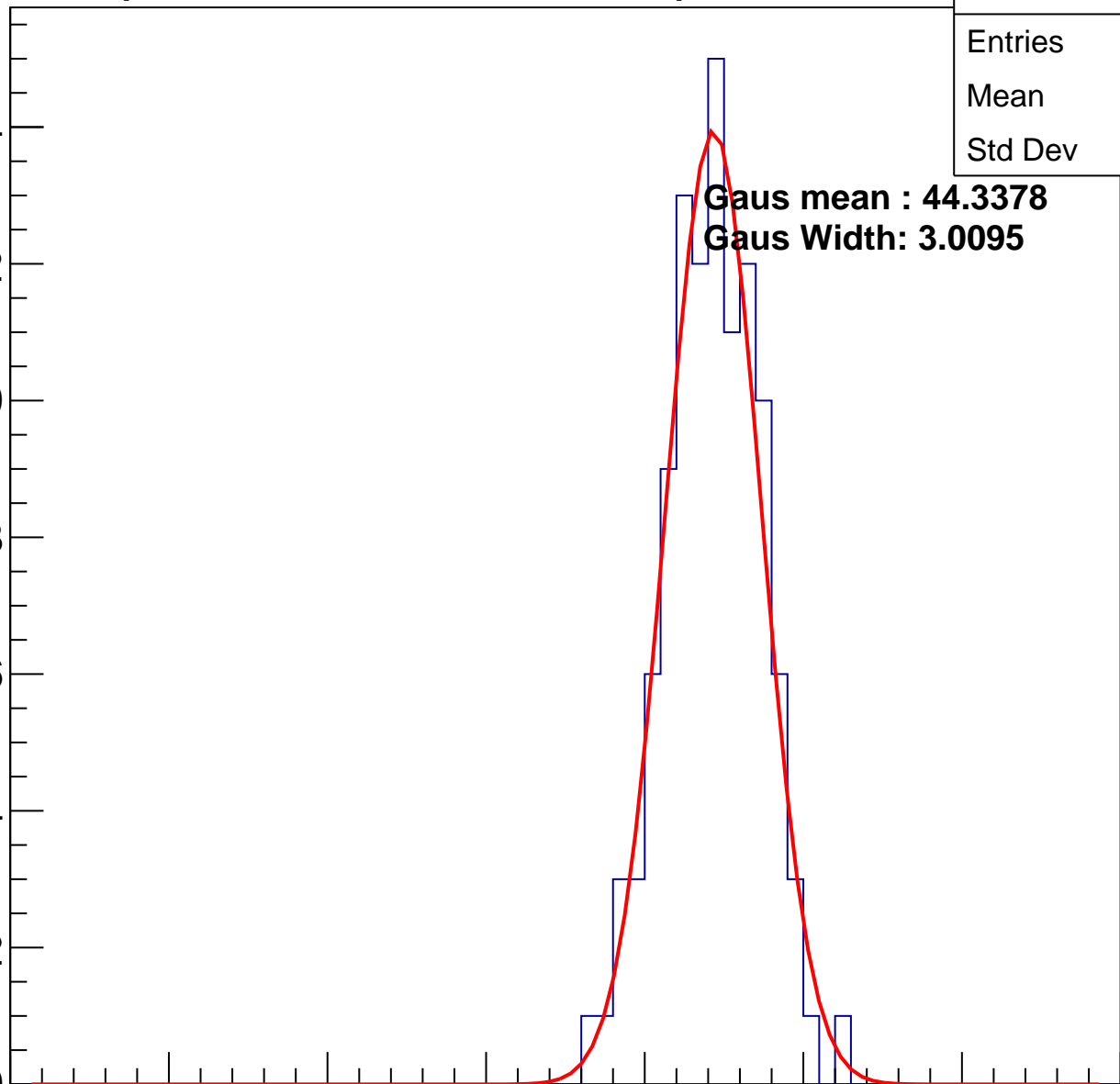
40

50

60

70

ampl

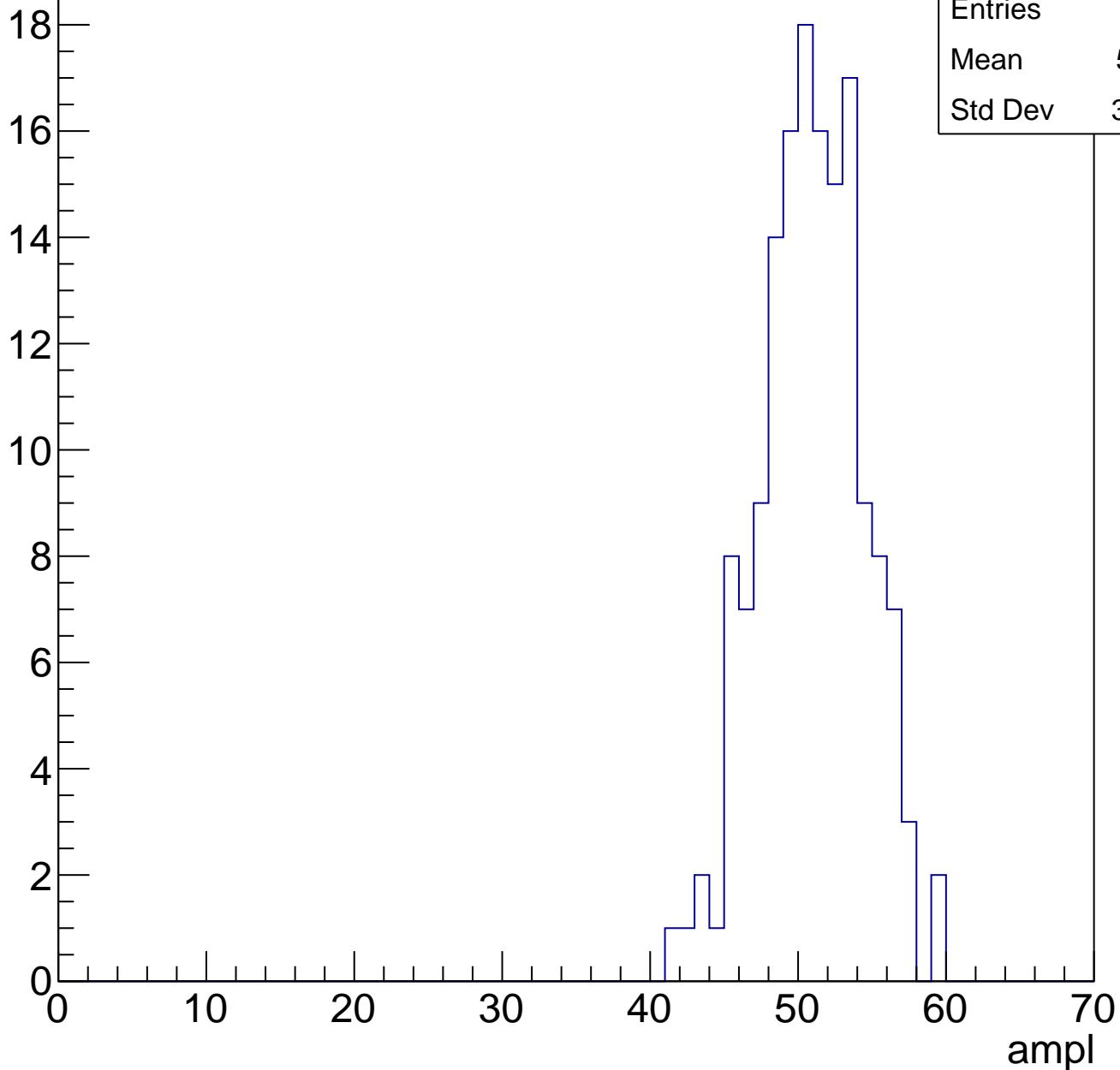


# B0L101S, U26-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	50.51
Std Dev	3.455

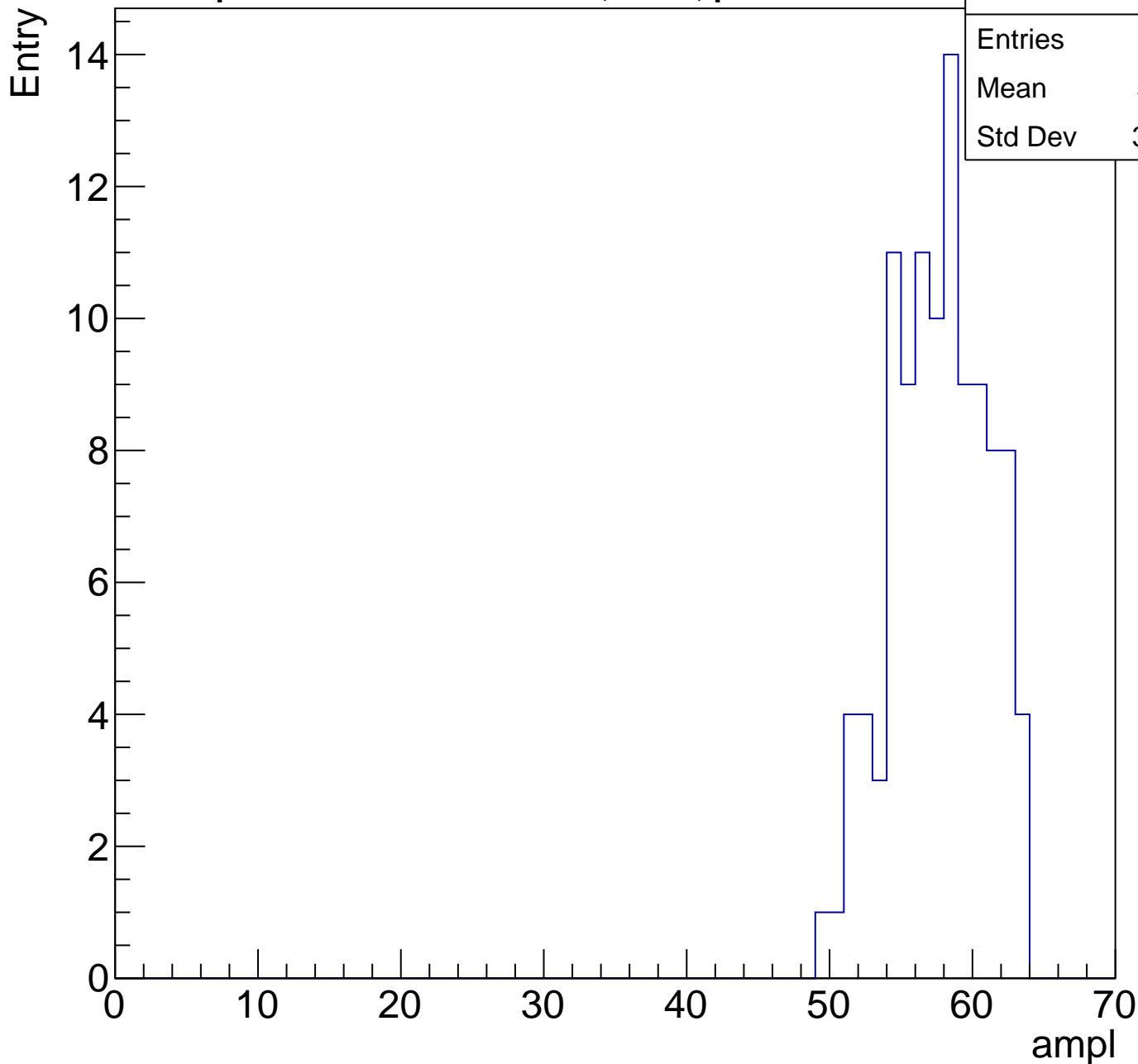
Entry



# B0L101S, U26-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	57.21
Std Dev	3.293



# B0L101S, U26-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

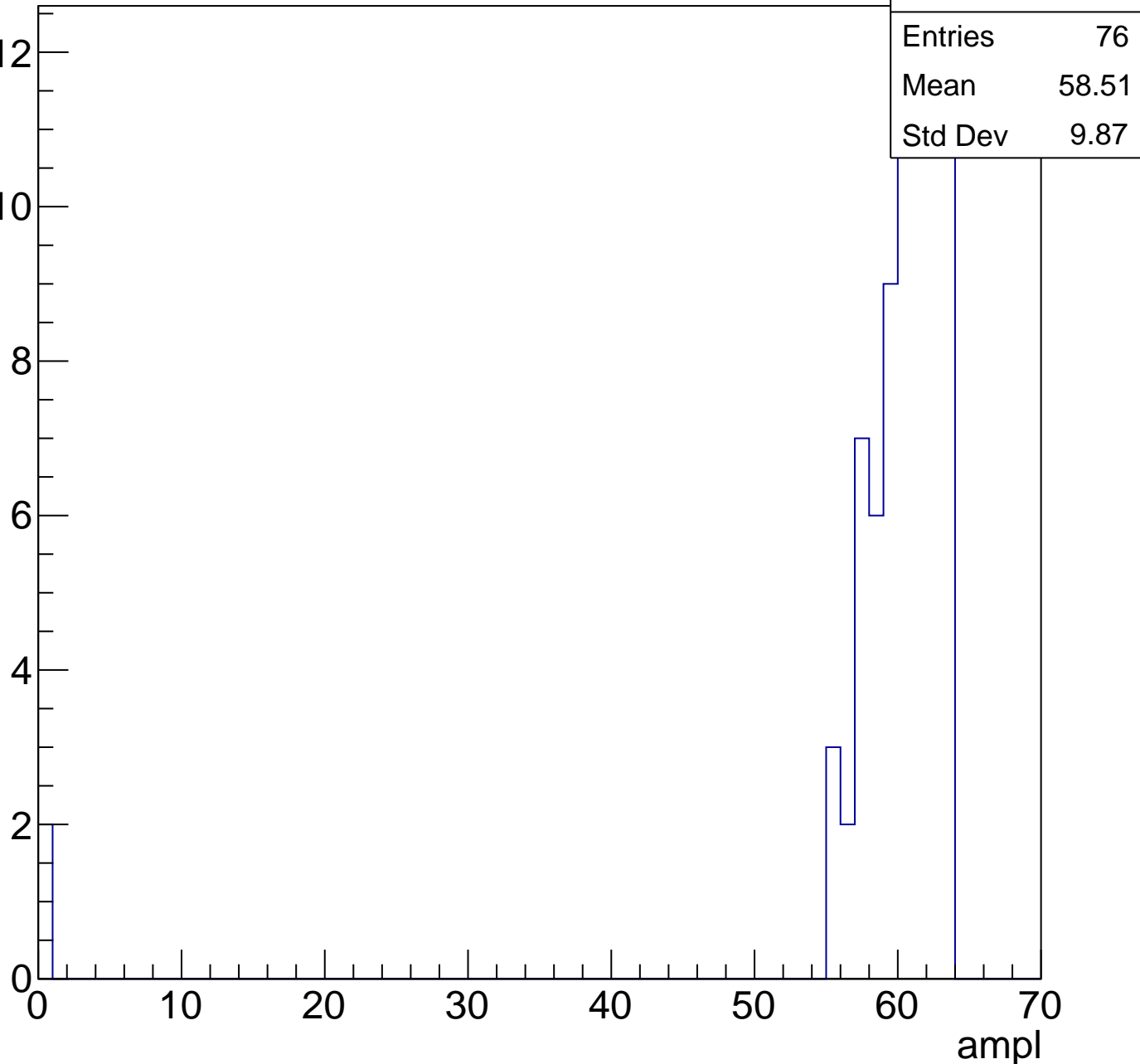
40

50

60

ampl

Entries	76
Mean	58.51
Std Dev	9.87



# B0L101S, U26-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	63
Std Dev	0



# B0L101S, U26-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

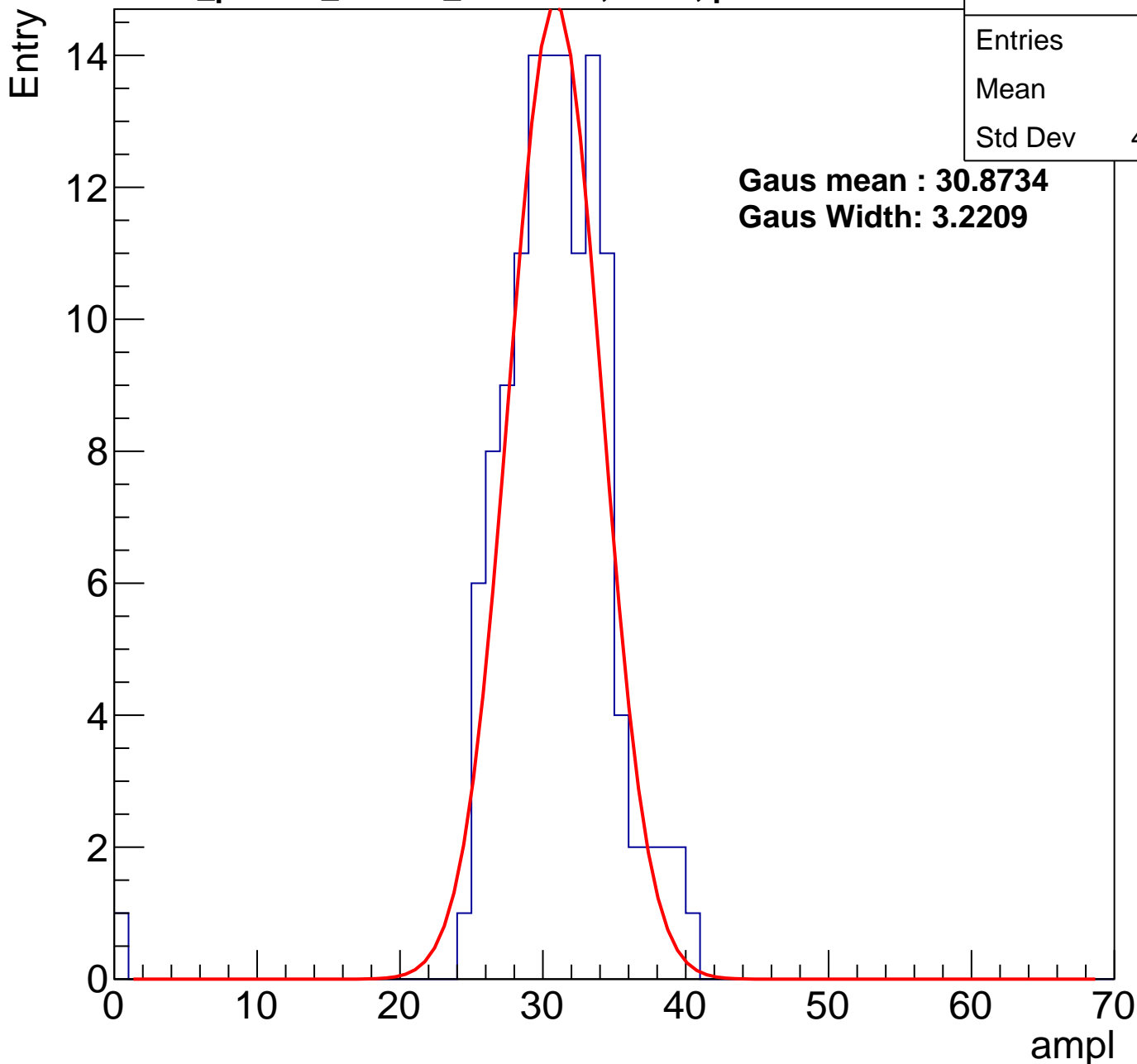
# B0L101S, U26-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	30.4
Std Dev	4.304

**Gaus mean : 30.8734**

**Gaus Width: 3.2209**



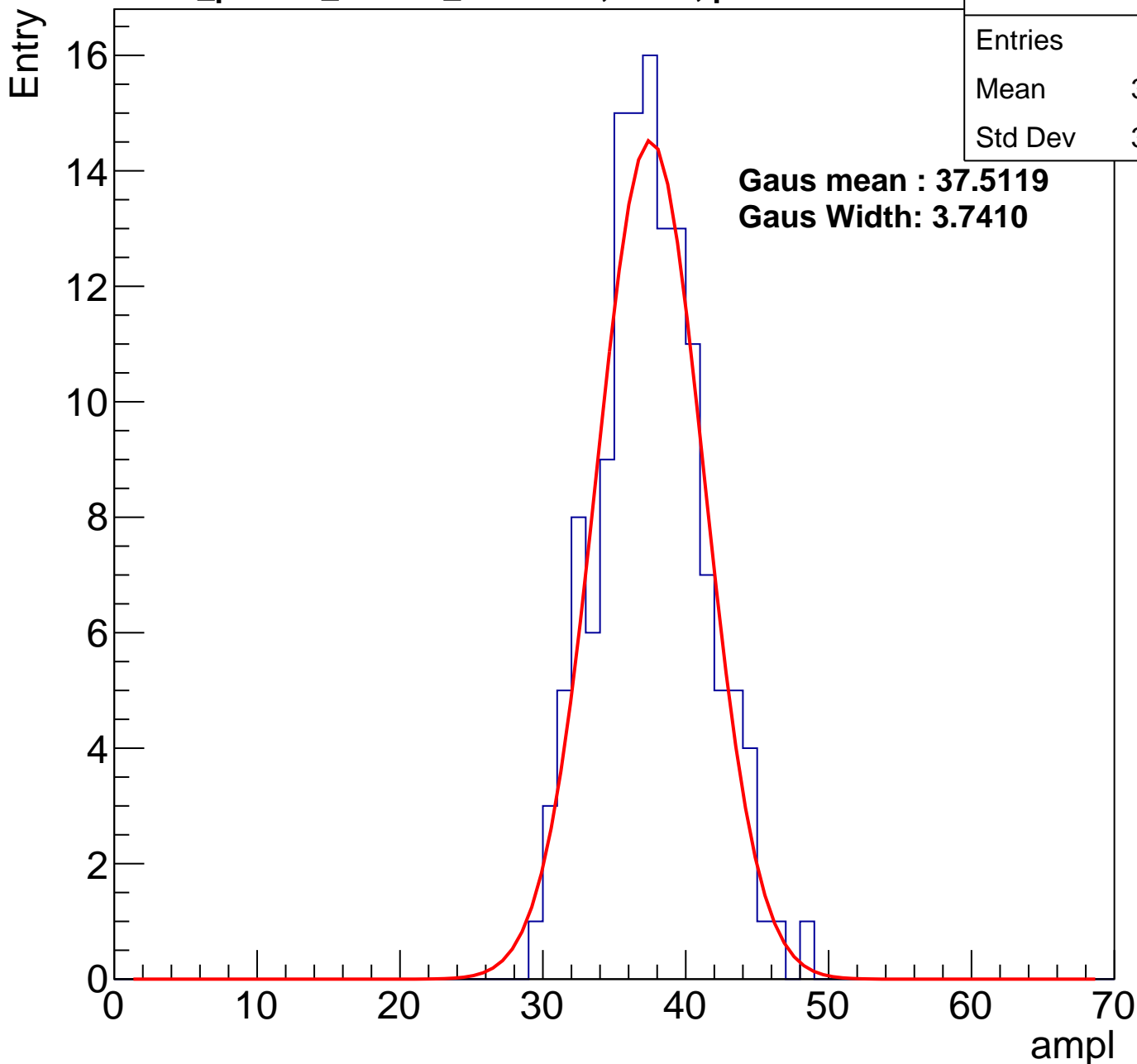
# B0L101S, U26-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	37.12
Std Dev	3.682

**Gaus mean : 37.5119**

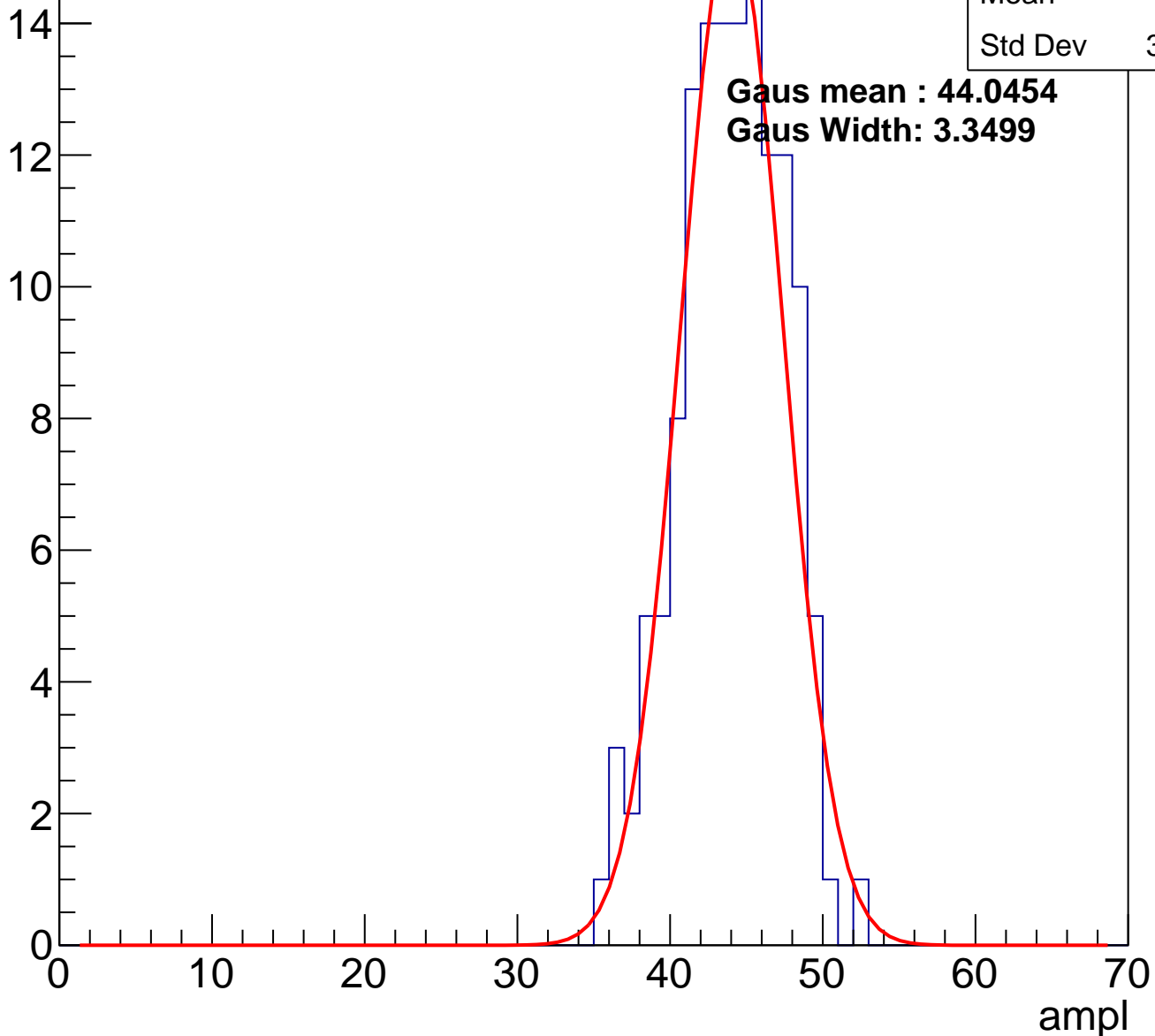
**Gaus Width: 3.7410**



# B0L101S, U26-ch39, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

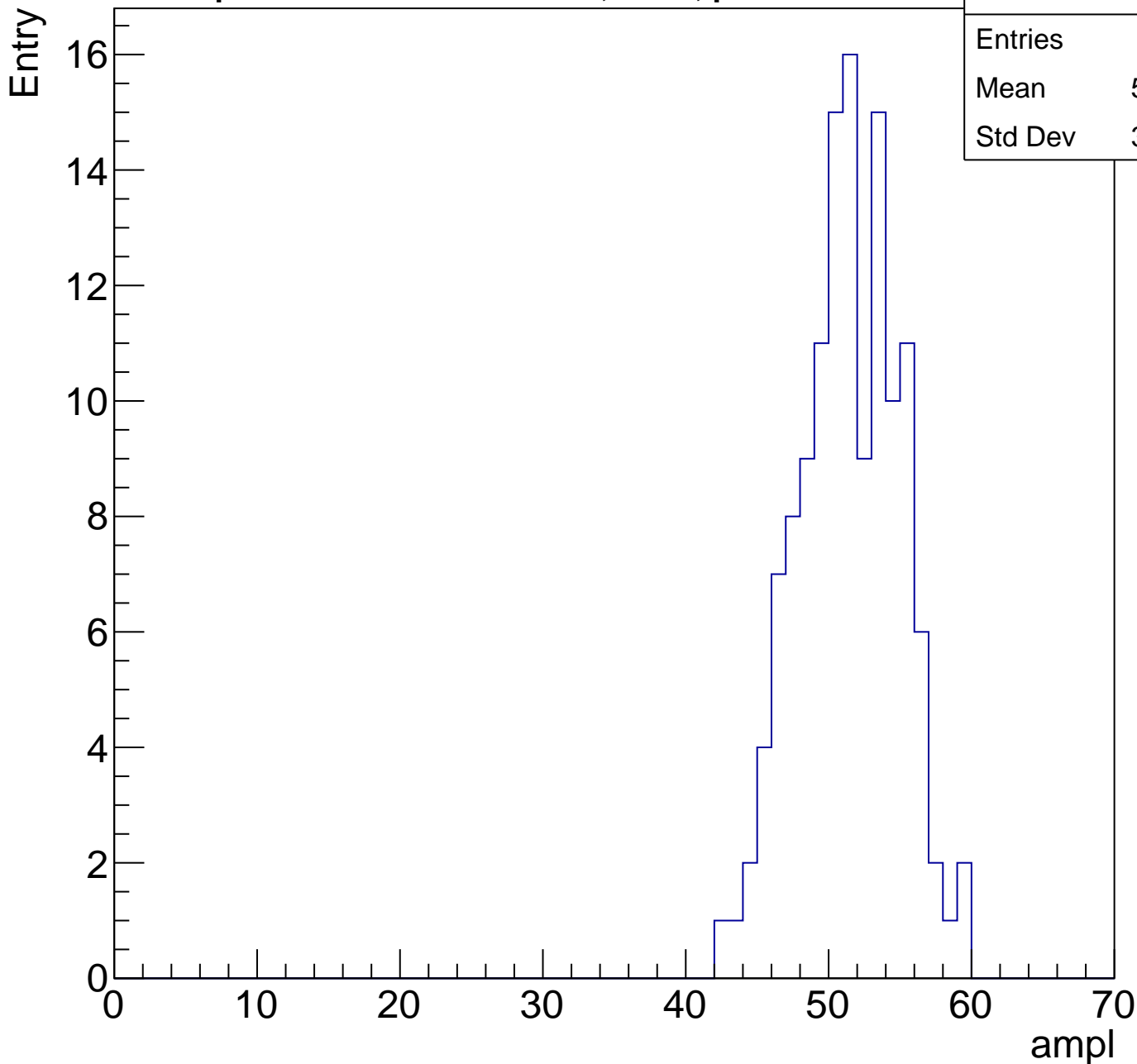
Entry



# B0L101S, U26-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	50.94
Std Dev	3.468



# B0L101S, U26-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

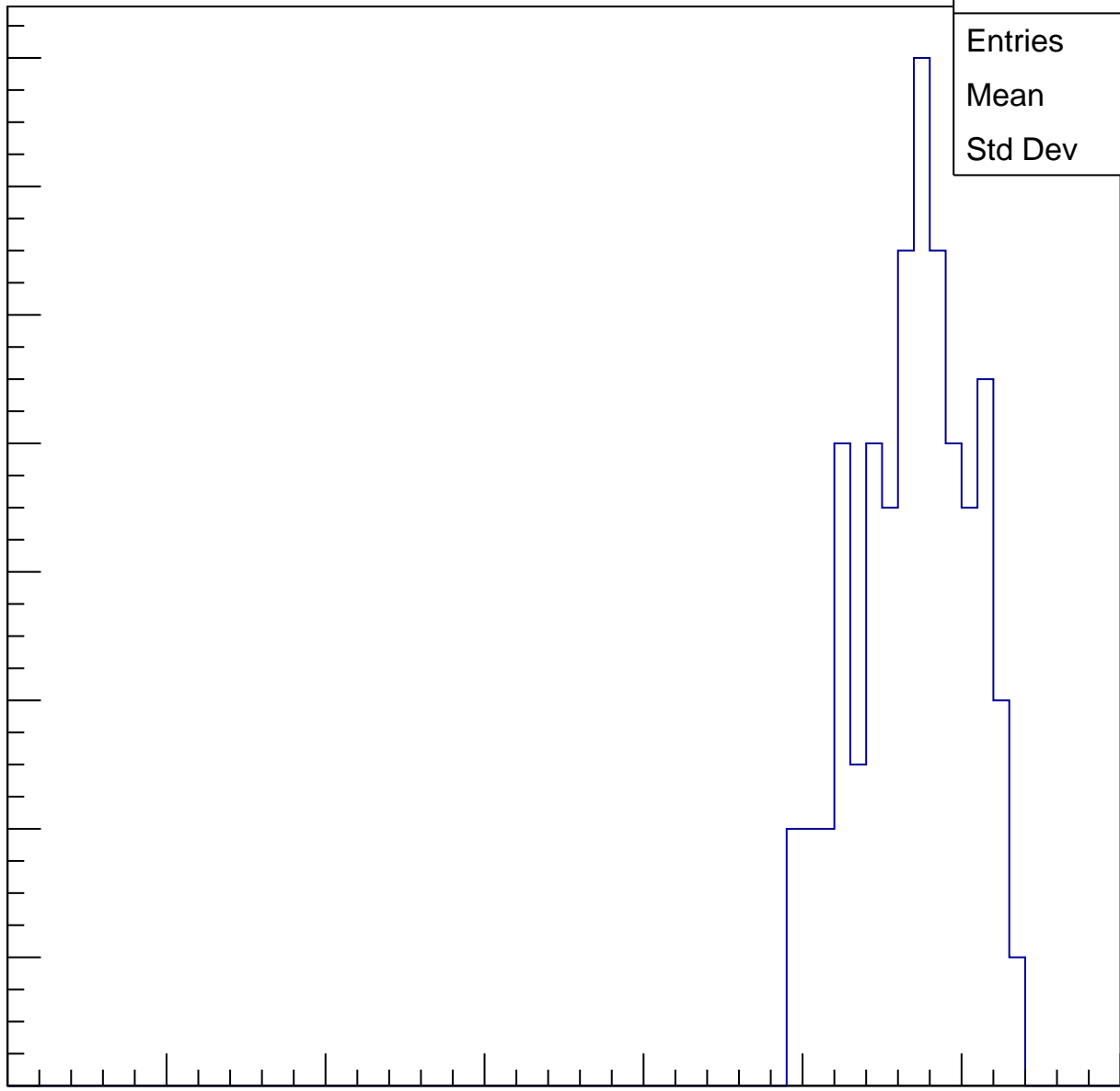
Entries	126
Mean	56.45
Std Dev	3.518

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch39, adc5

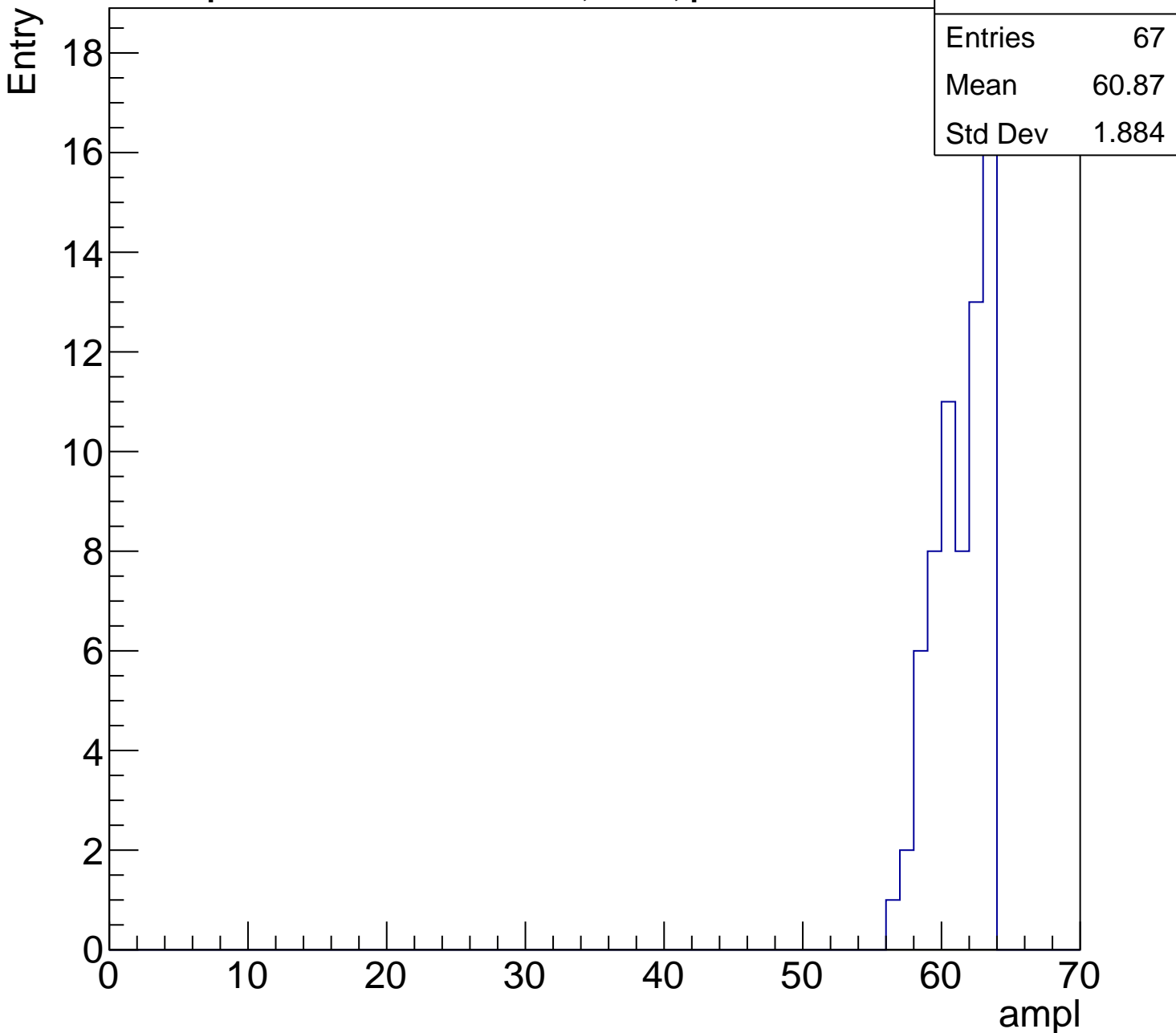
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	67
Mean	60.87
Std Dev	1.884

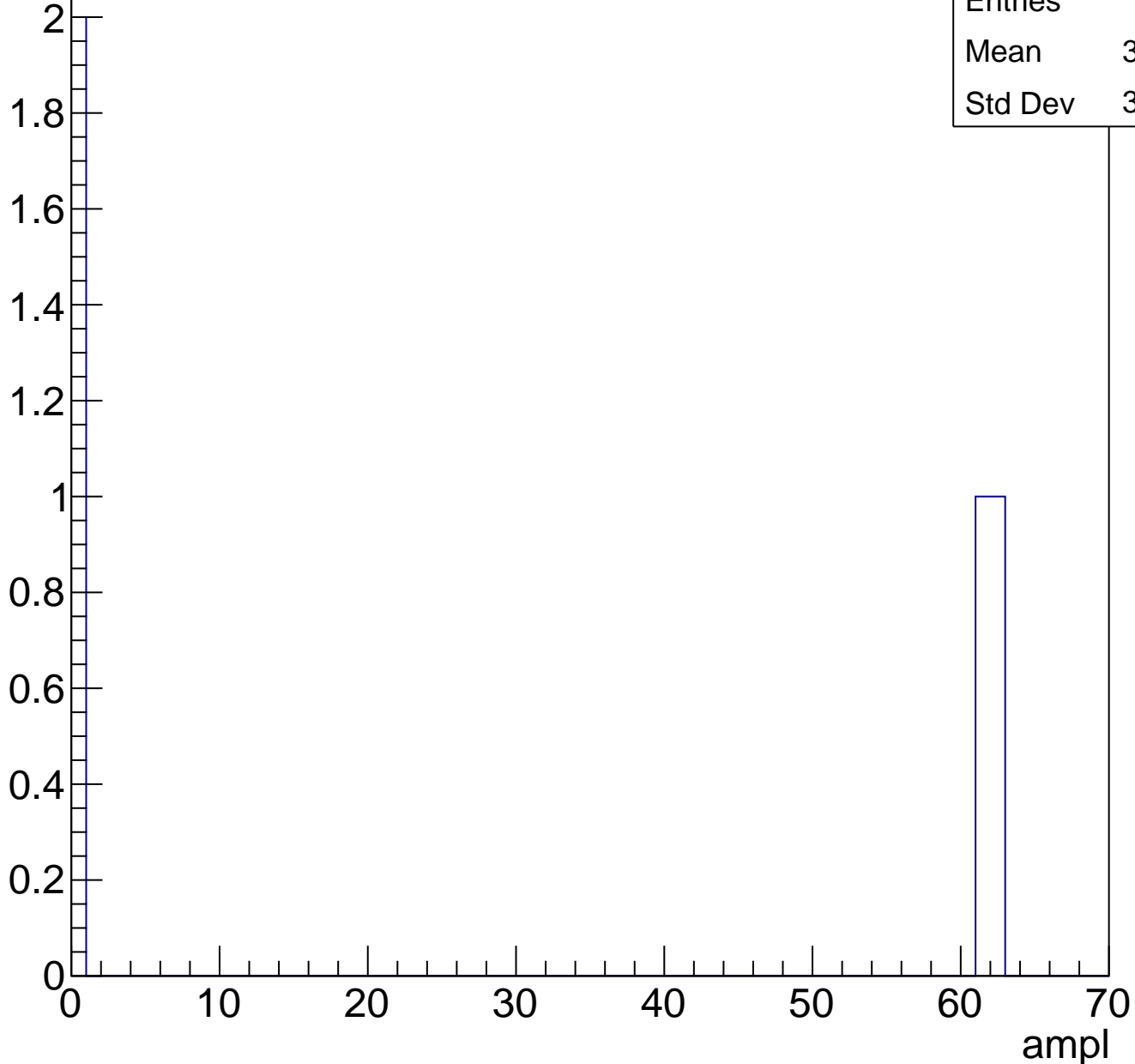
ampl



# B0L101S, U26-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	30.75
Std Dev	30.75



# B0L101S, U26-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch40, adc0

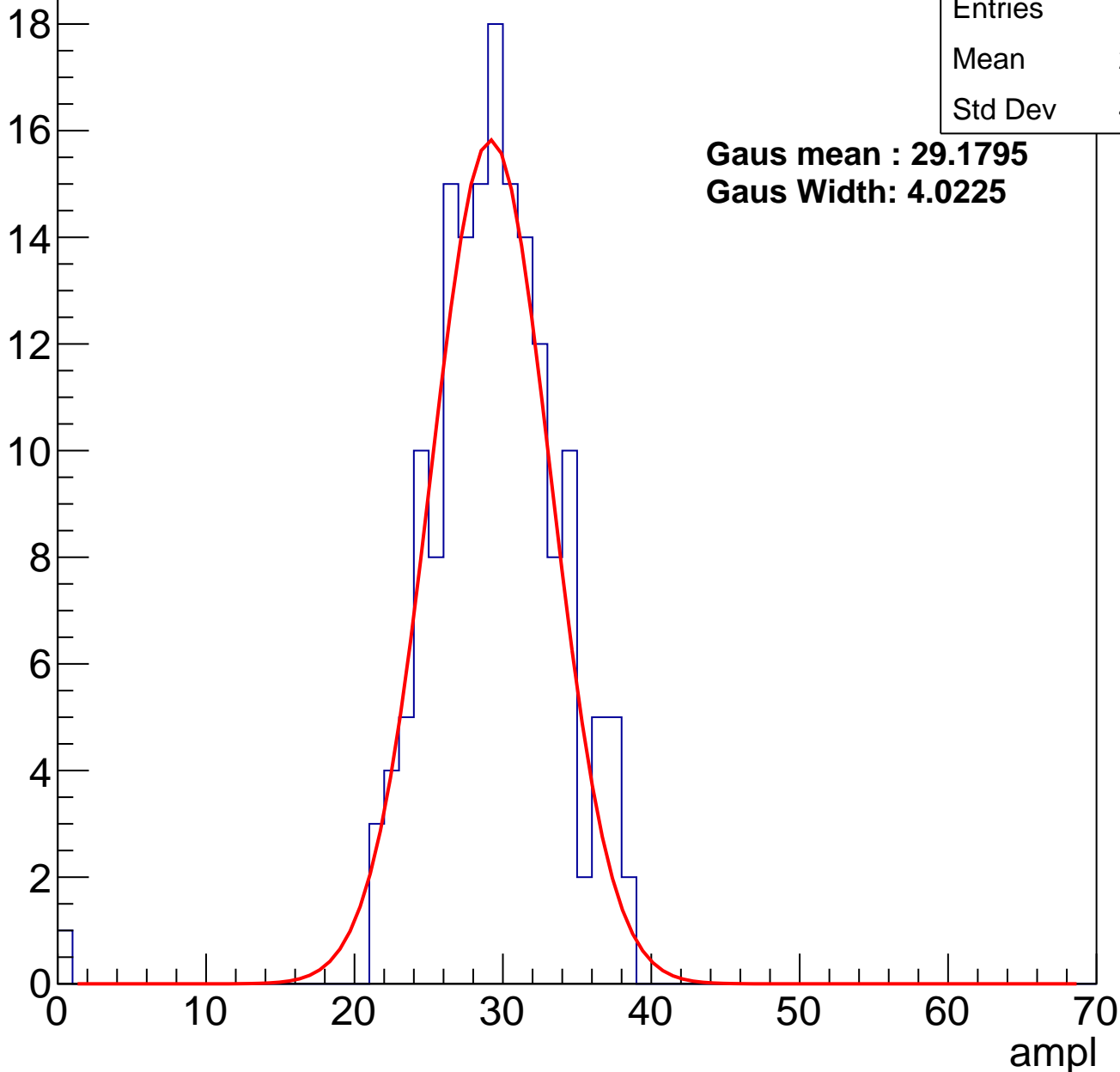
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	28.91
Std Dev	4.491

**Gaus mean : 29.1795**

**Gaus Width: 4.0225**

Entry



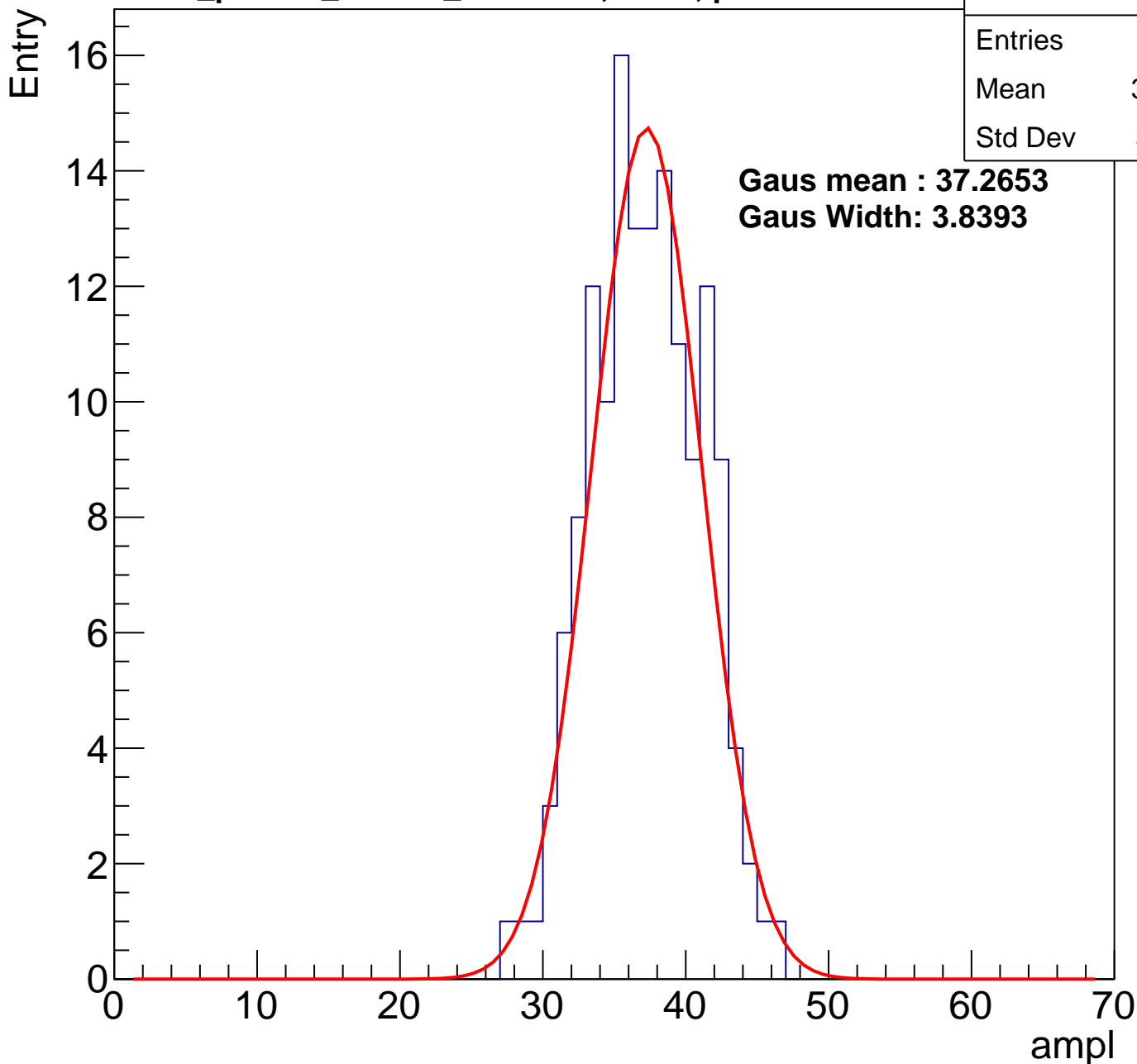
# B0L101S, U26-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	36.76
Std Dev	3.781

**Gaus mean : 37.2653**

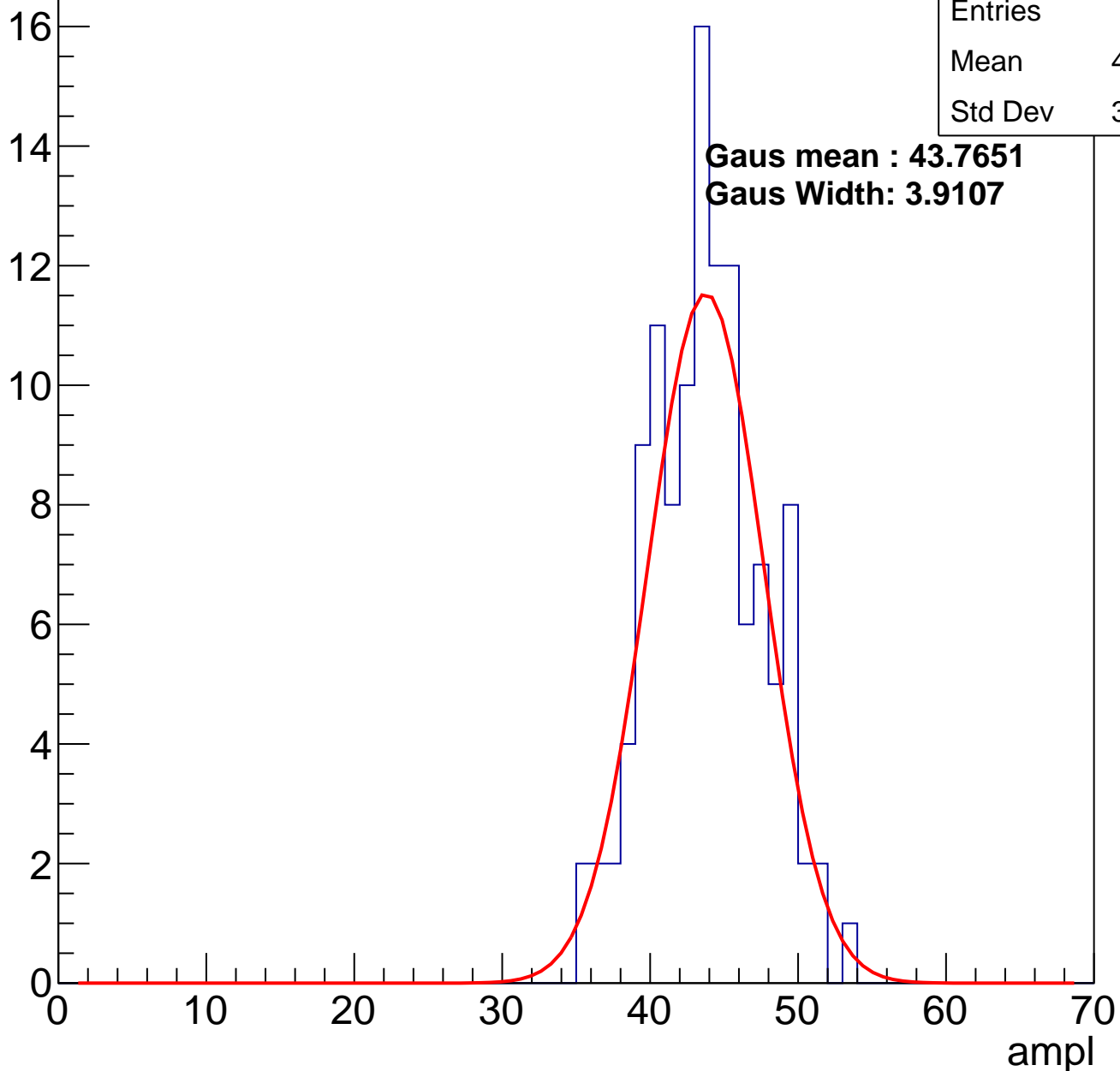
**Gaus Width: 3.8393**



# B0L101S, U26-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	49.65
Std Dev	3.397

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch40, adc4

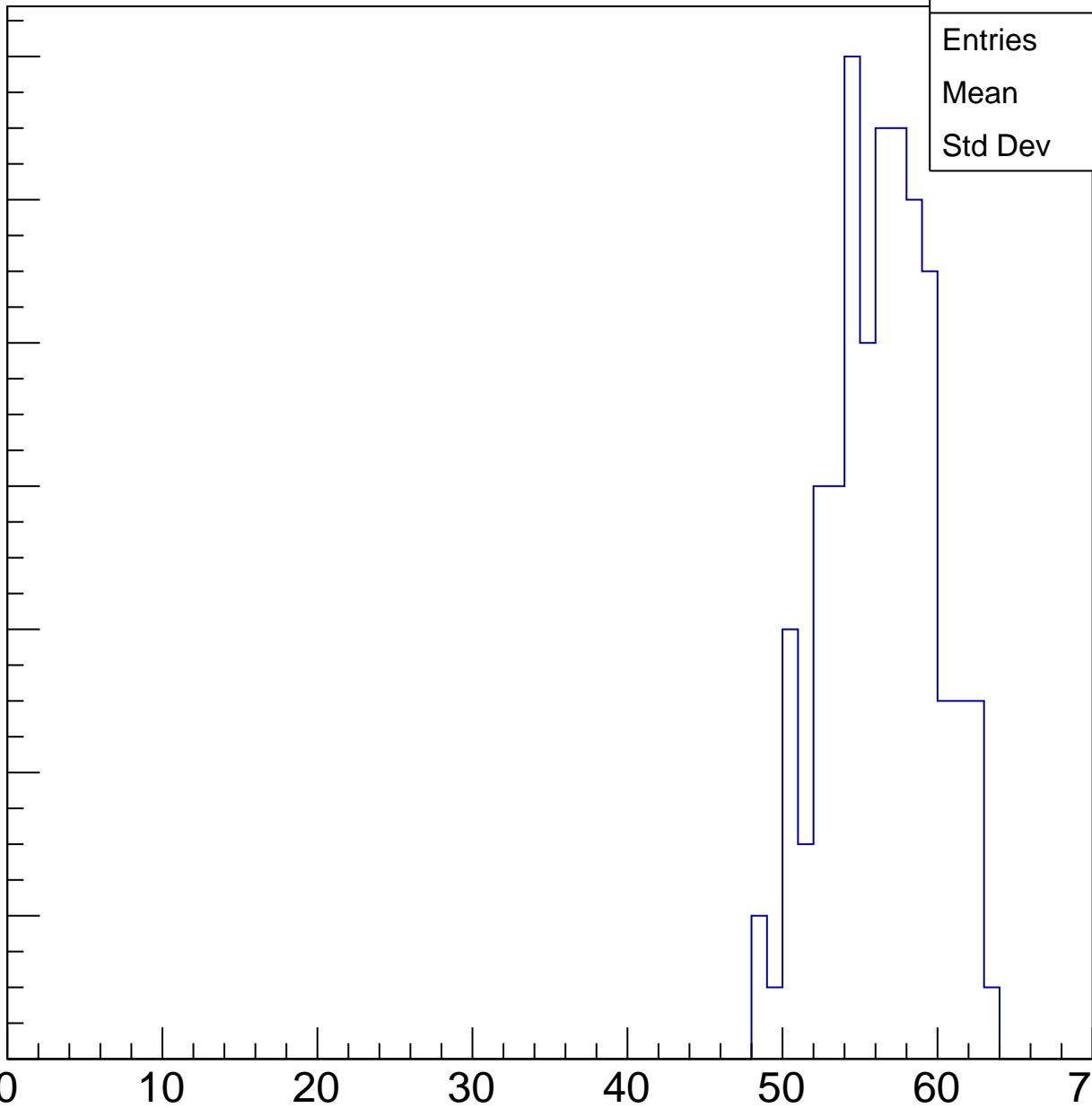
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	117
Mean	55.86
Std Dev	3.366

ampl



# B0L101S, U26-ch40, adc5

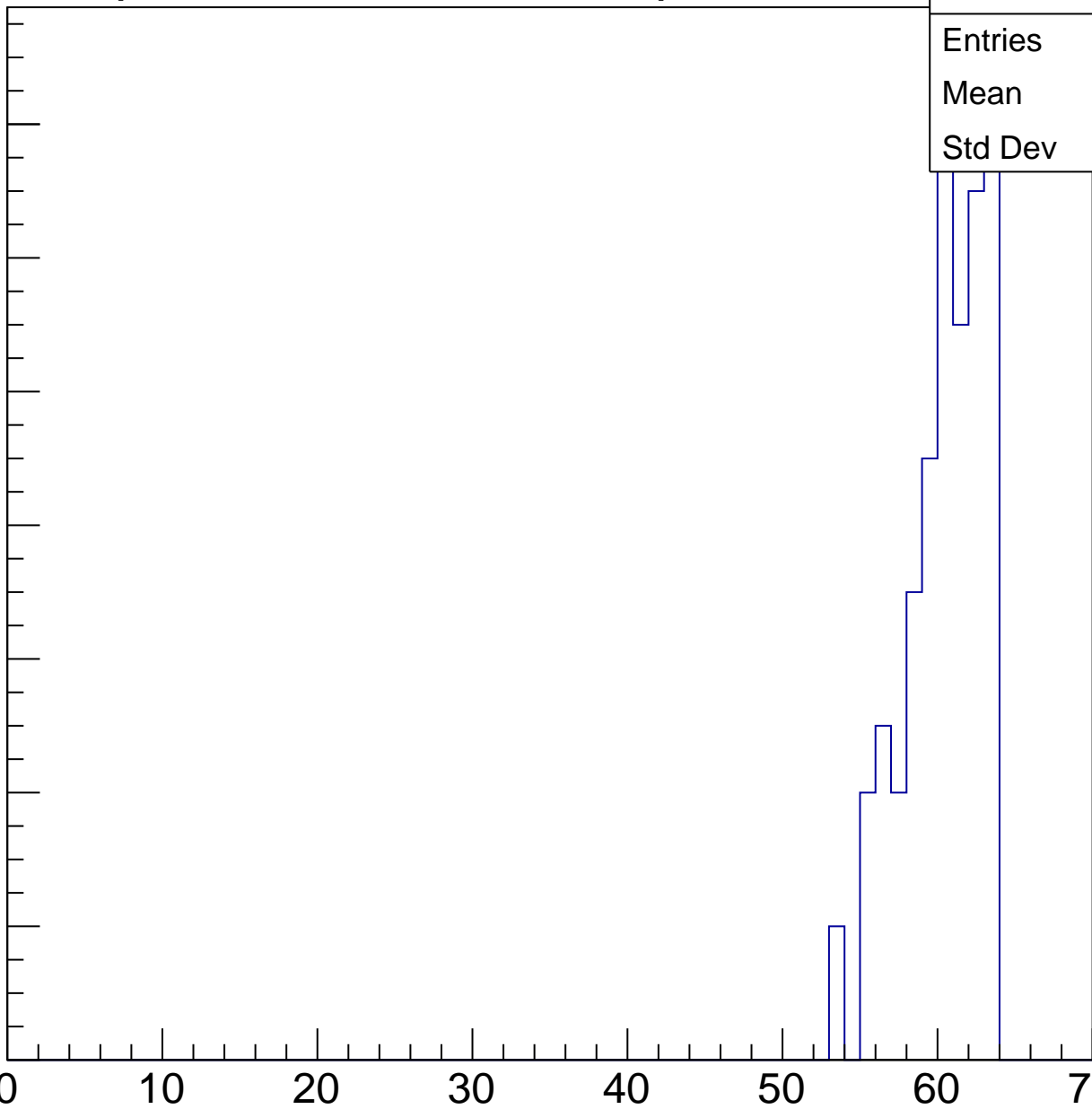
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	84
Mean	59.92
Std Dev	2.555

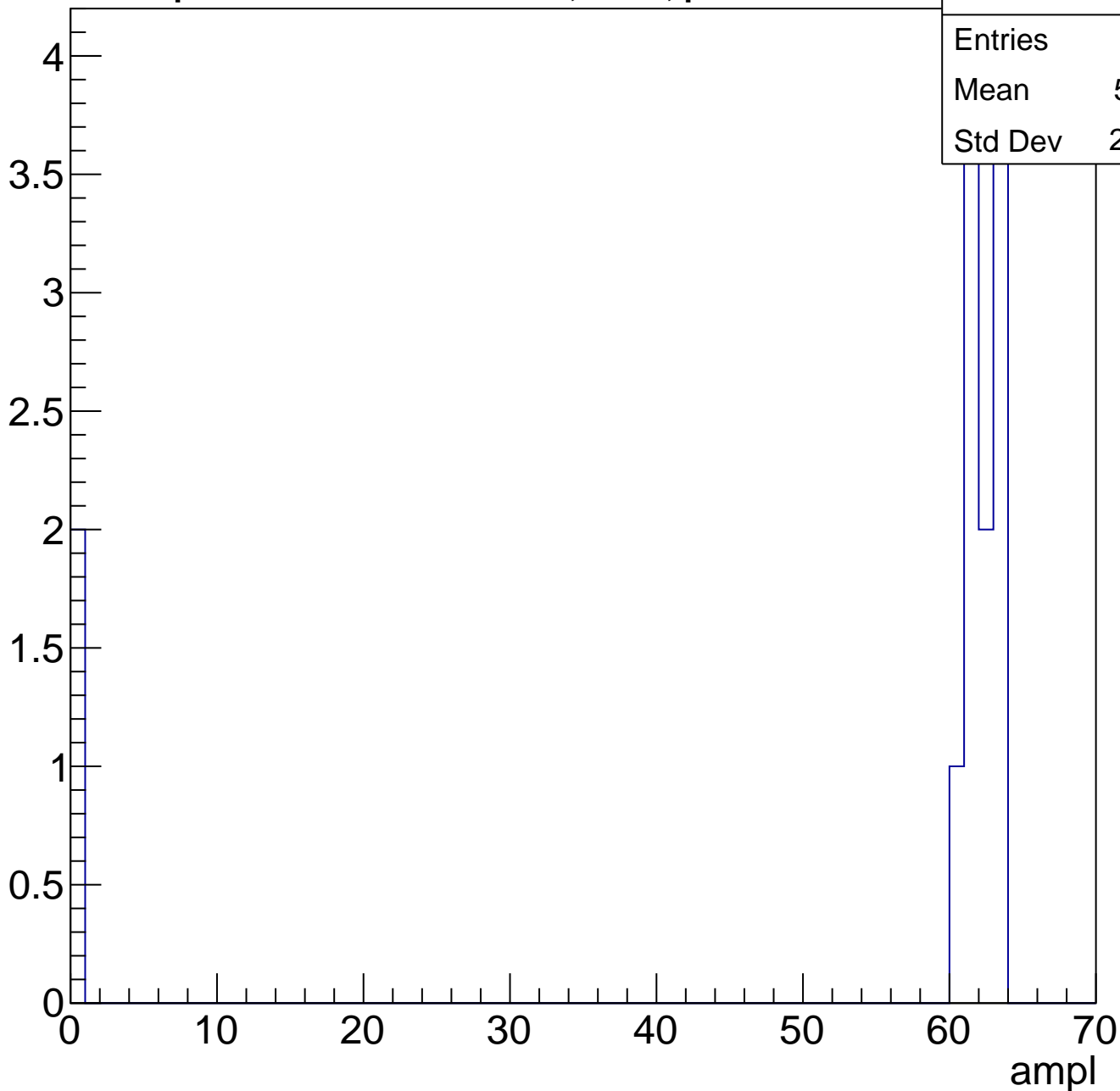
ampl



# B0L101S, U26-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

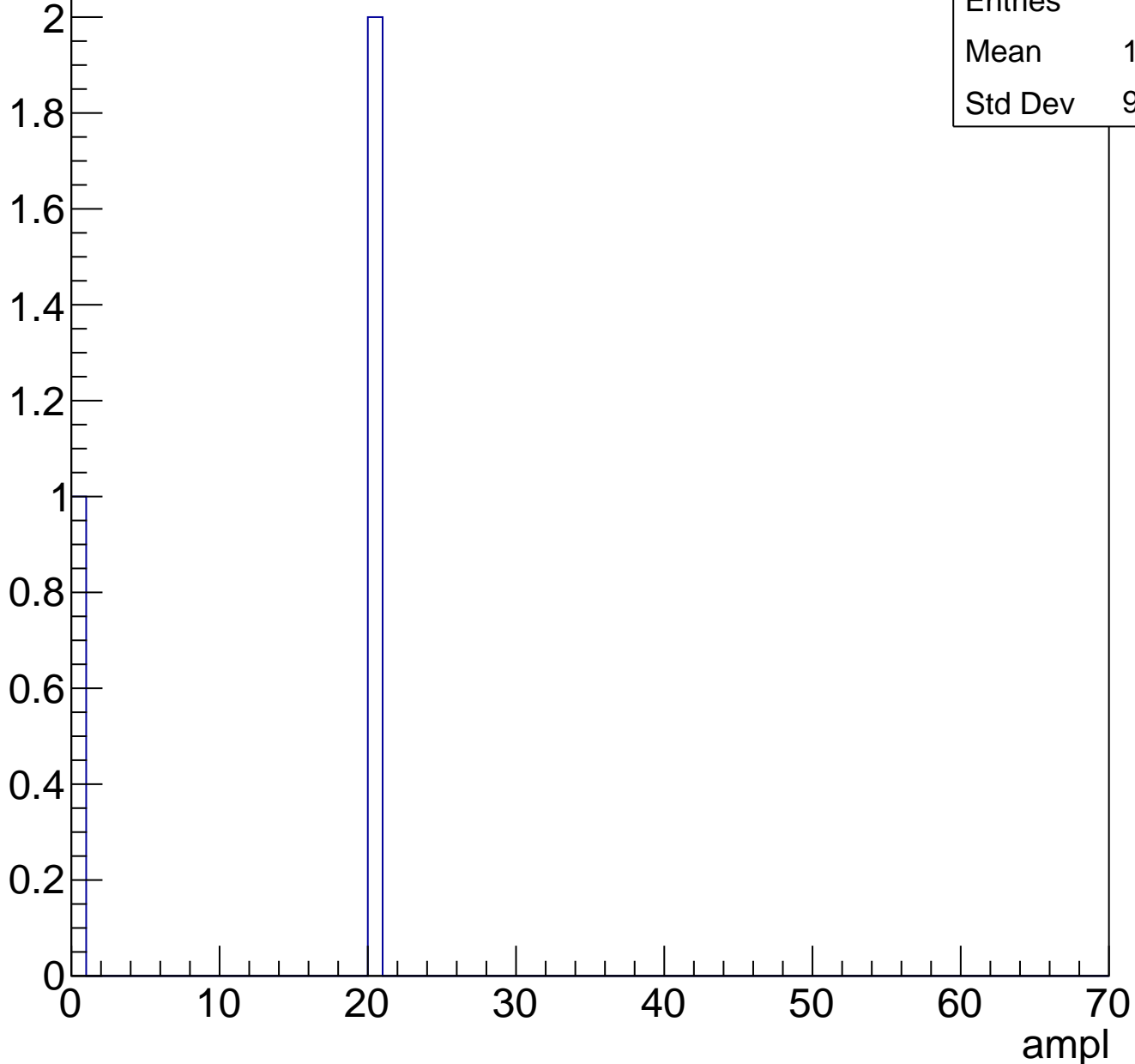




# B0L101S, U26-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	13.33
Std Dev	9.428

# B0L101S, U26-ch41, adc0

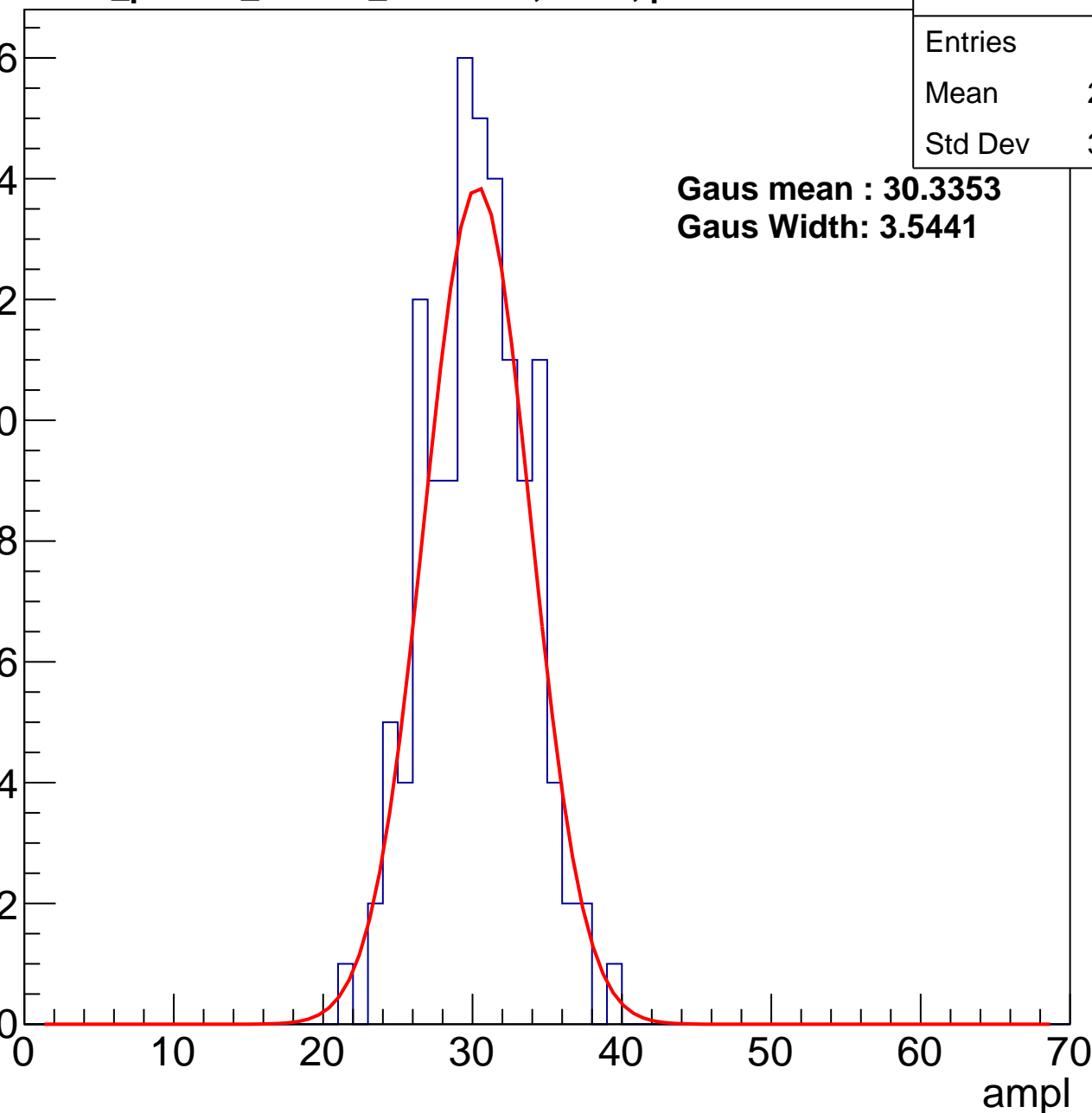
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	127
Mean	29.84
Std Dev	3.375

**Gaus mean : 30.3353**  
**Gaus Width: 3.5441**



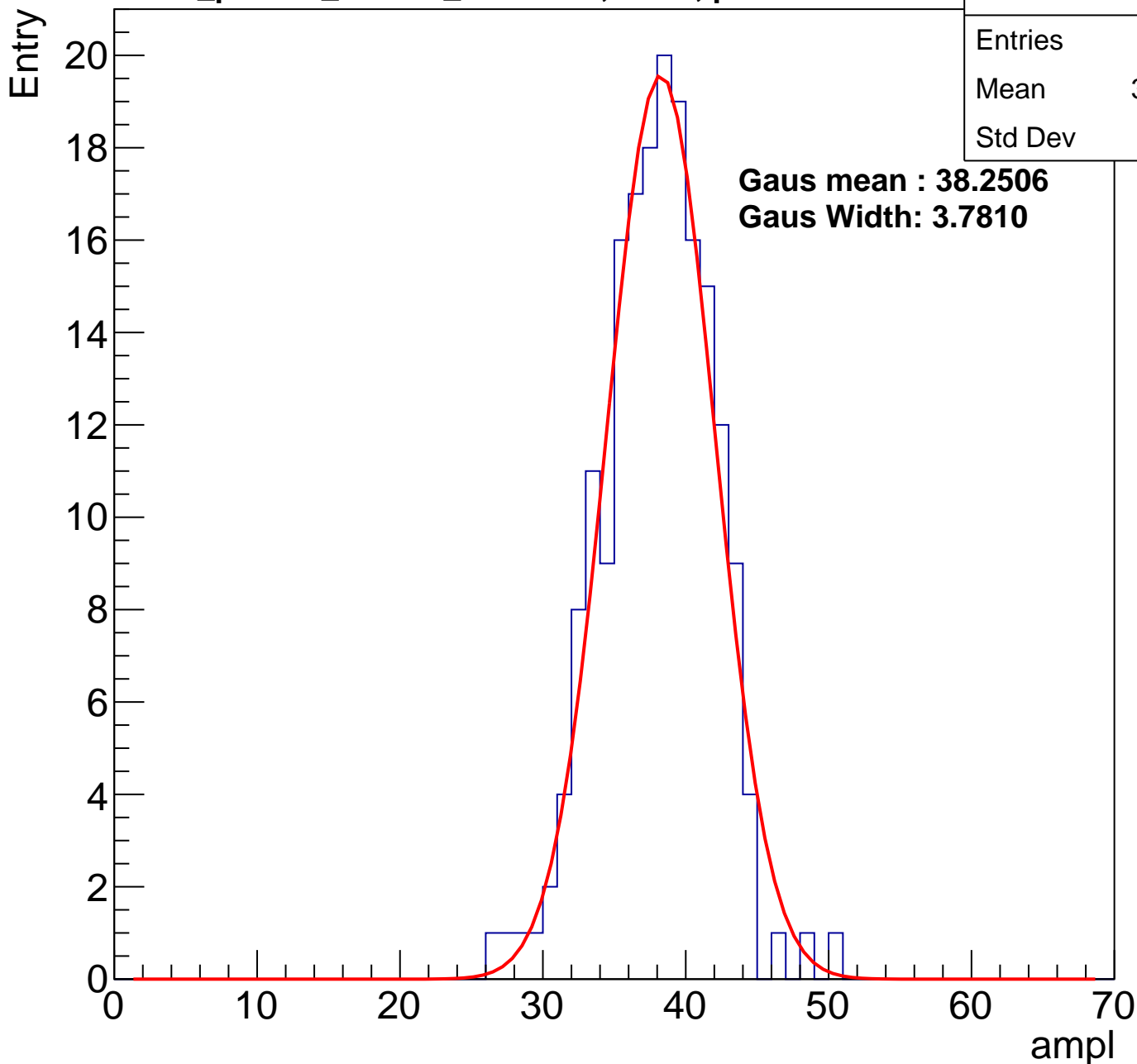
# B0L101S, U26-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	187
Mean	37.56
Std Dev	3.84

**Gaus mean : 38.2506**

**Gaus Width: 3.7810**



# B0L101S, U26-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

105

Mean

45.04

Std Dev

2.533

**Gaus mean : 45.5171**

**Gaus Width: 2.3766**

0

10

20

30

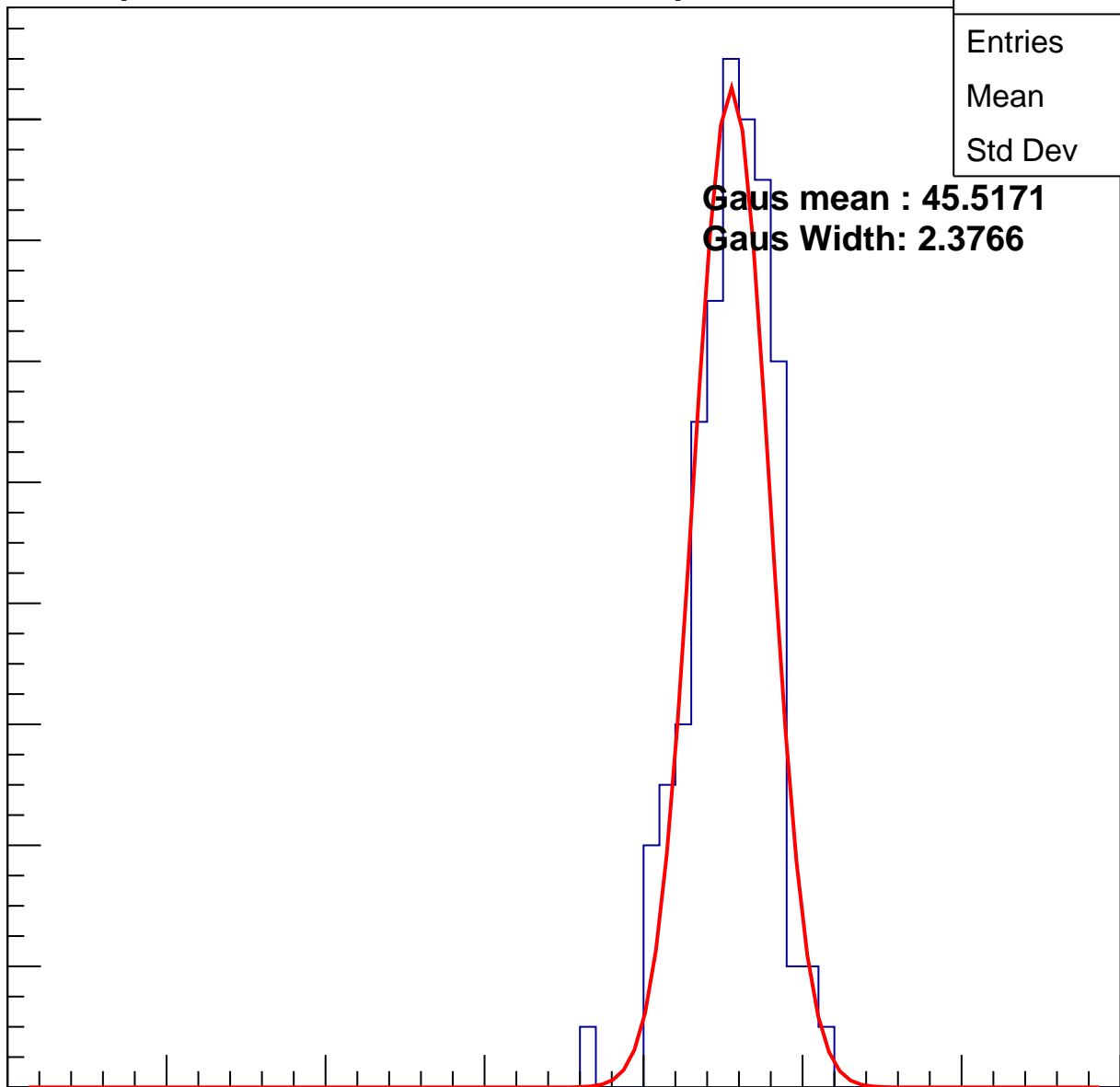
40

50

60

70

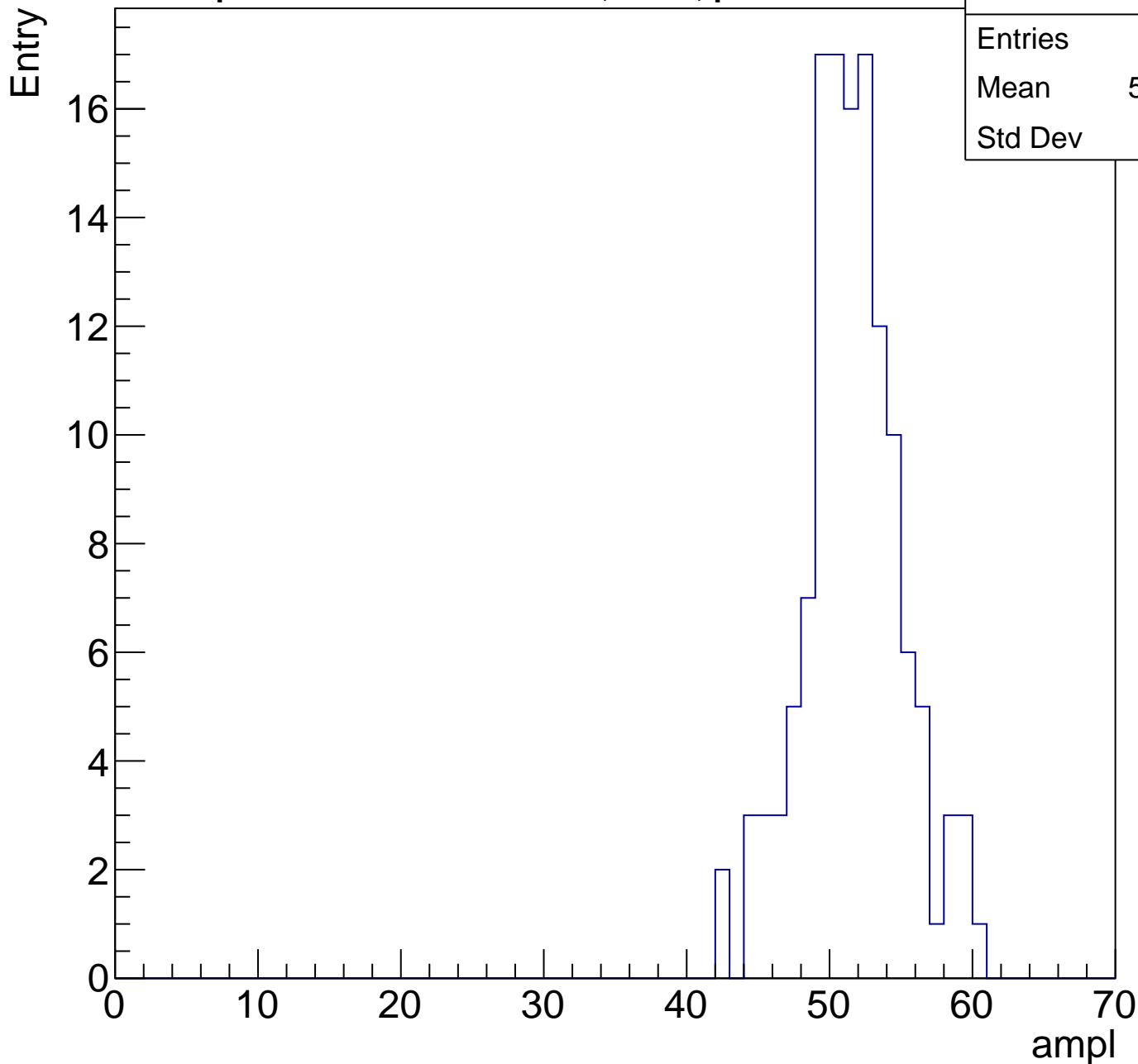
ampl



# B0L101S, U26-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

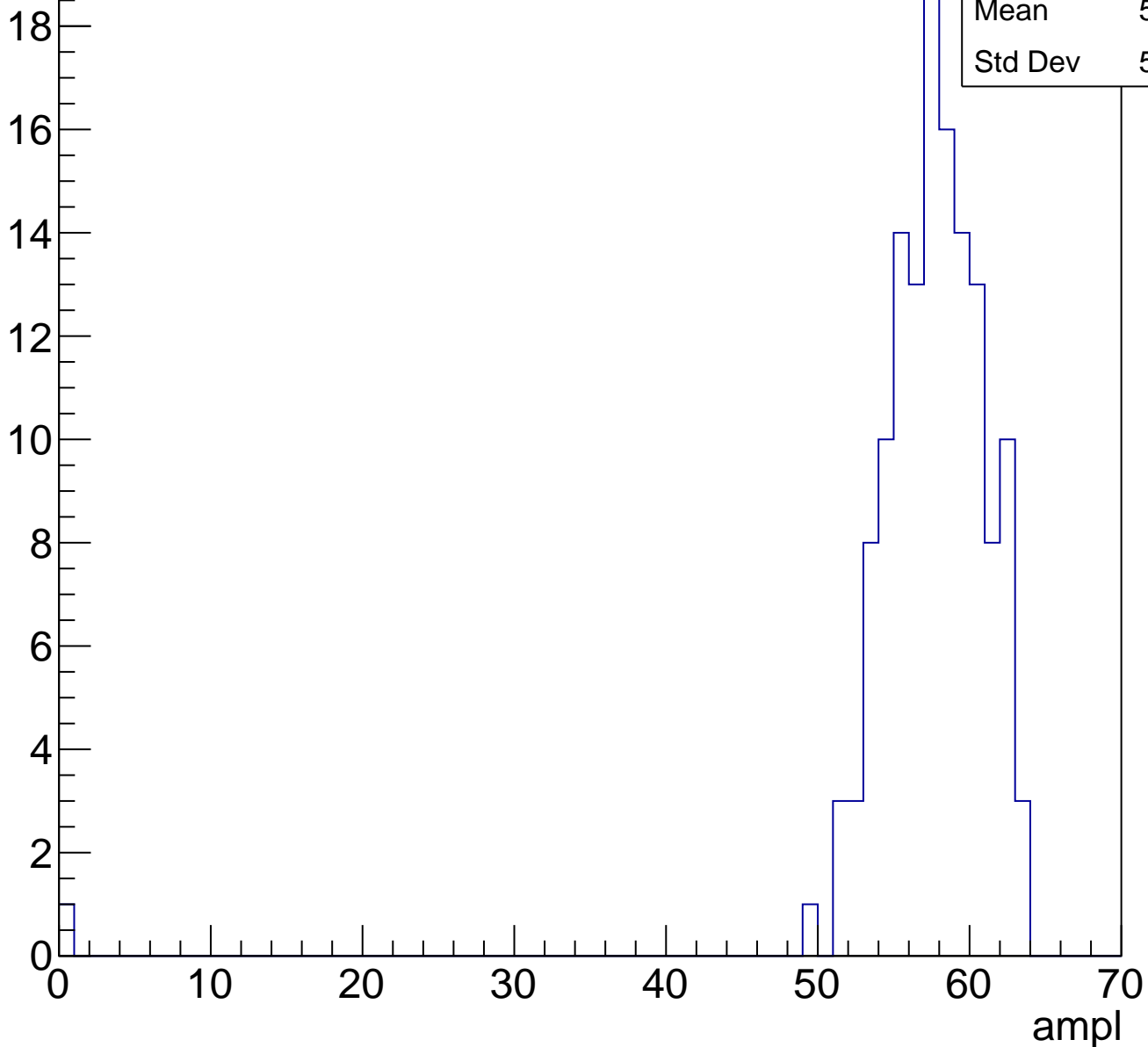
Entries	131
Mean	51.12
Std Dev	3.44



# B0L101S, U26-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	42
Mean	59.74
Std Dev	9.5

Entry

12

10

8

6

4

2

0

0

10

20

30

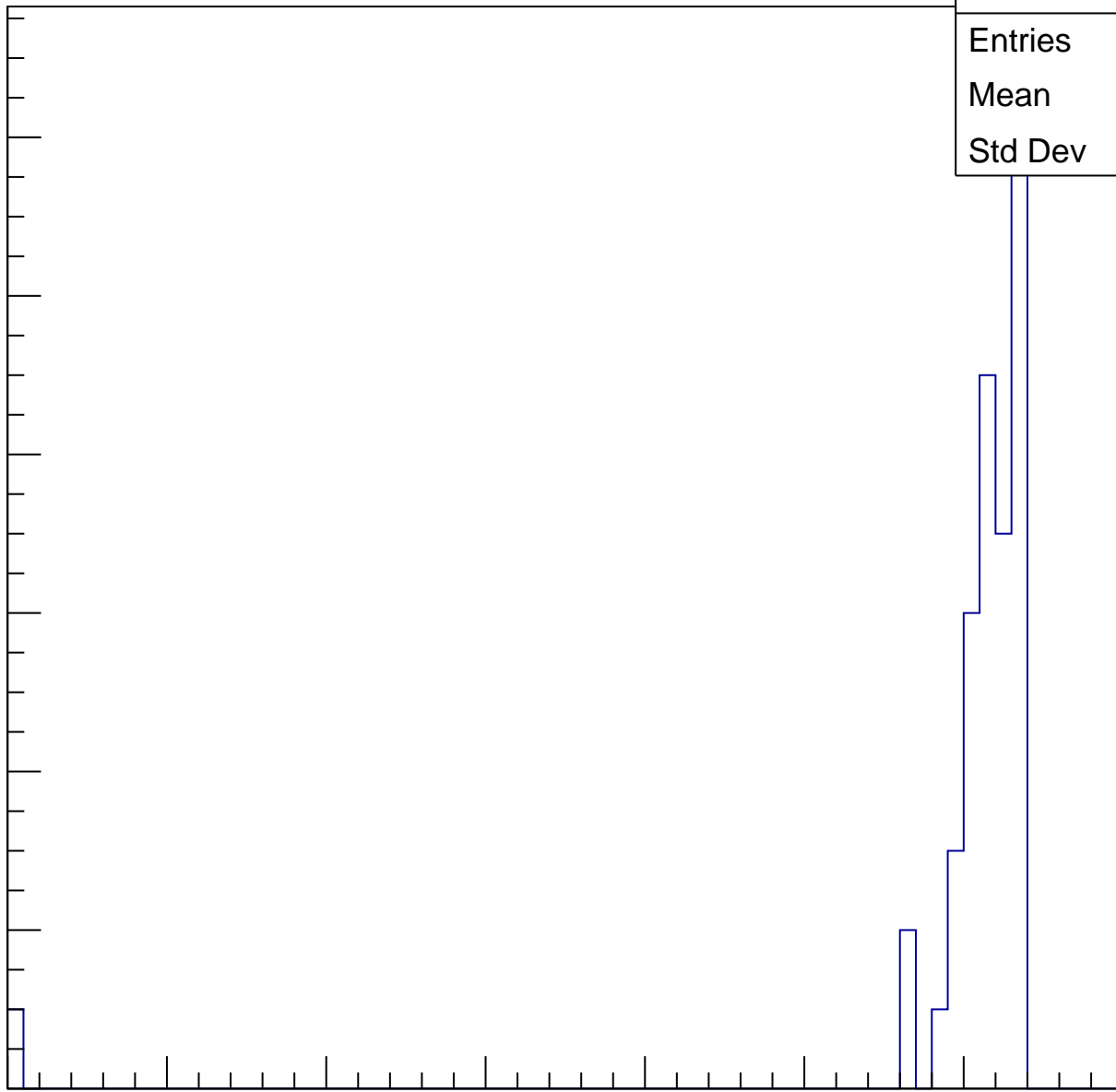
40

50

60

70

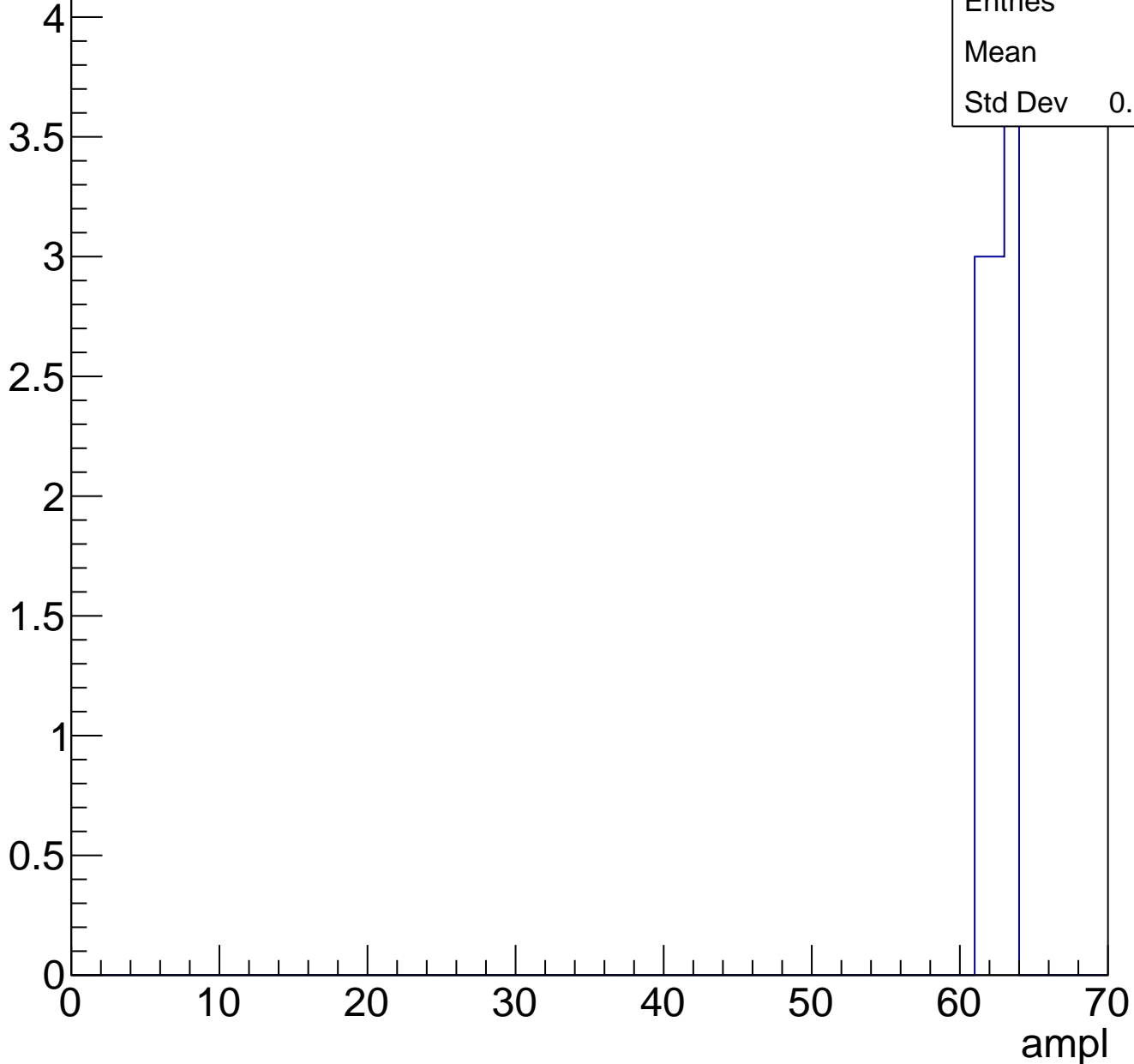
ampl



# B0L101S, U26-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch42, adc0

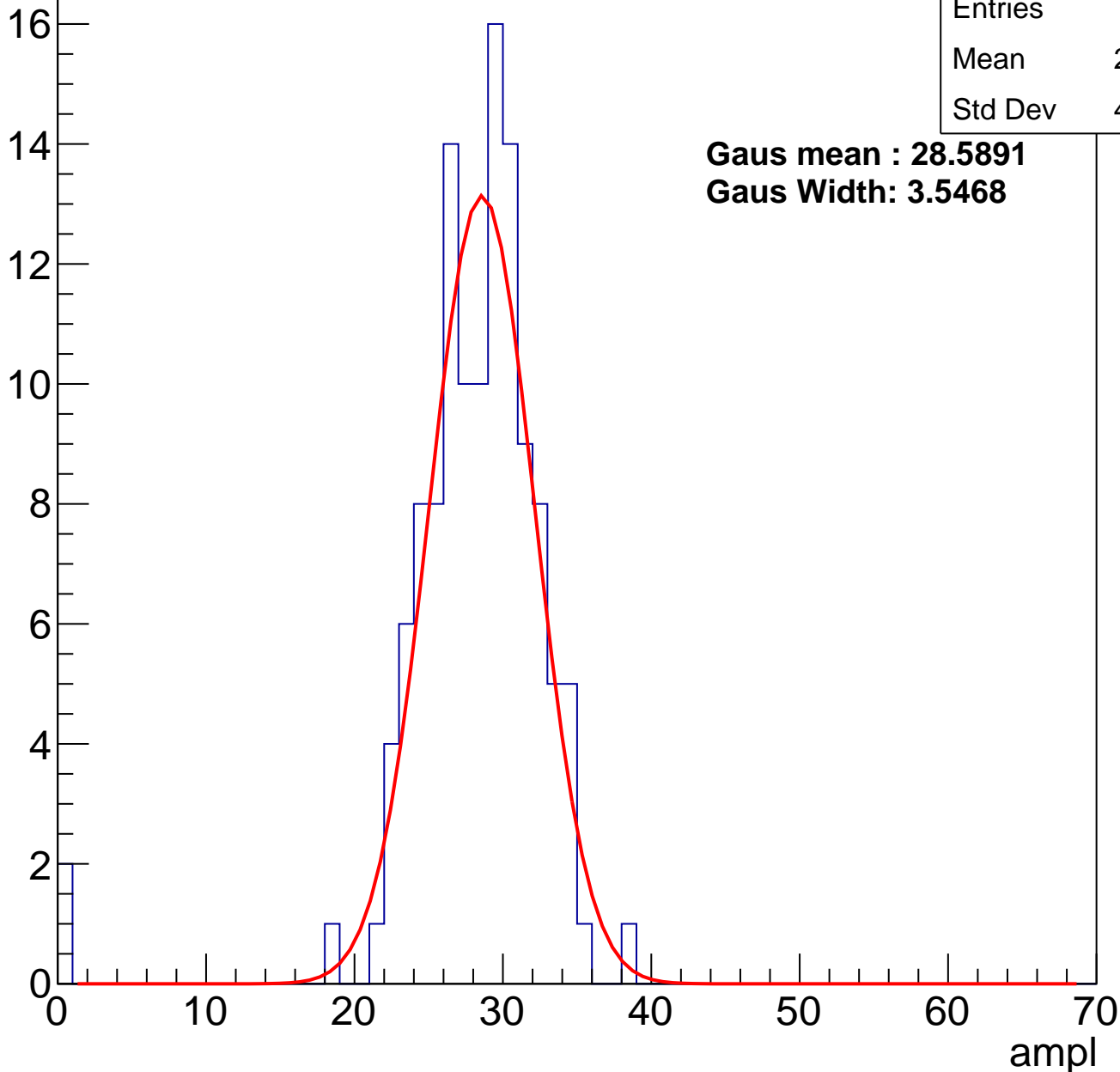
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	27.63
Std Dev	4.934

**Gaus mean : 28.5891**

**Gaus Width: 3.5468**

Entry



# B0L101S, U26-ch42, adc1

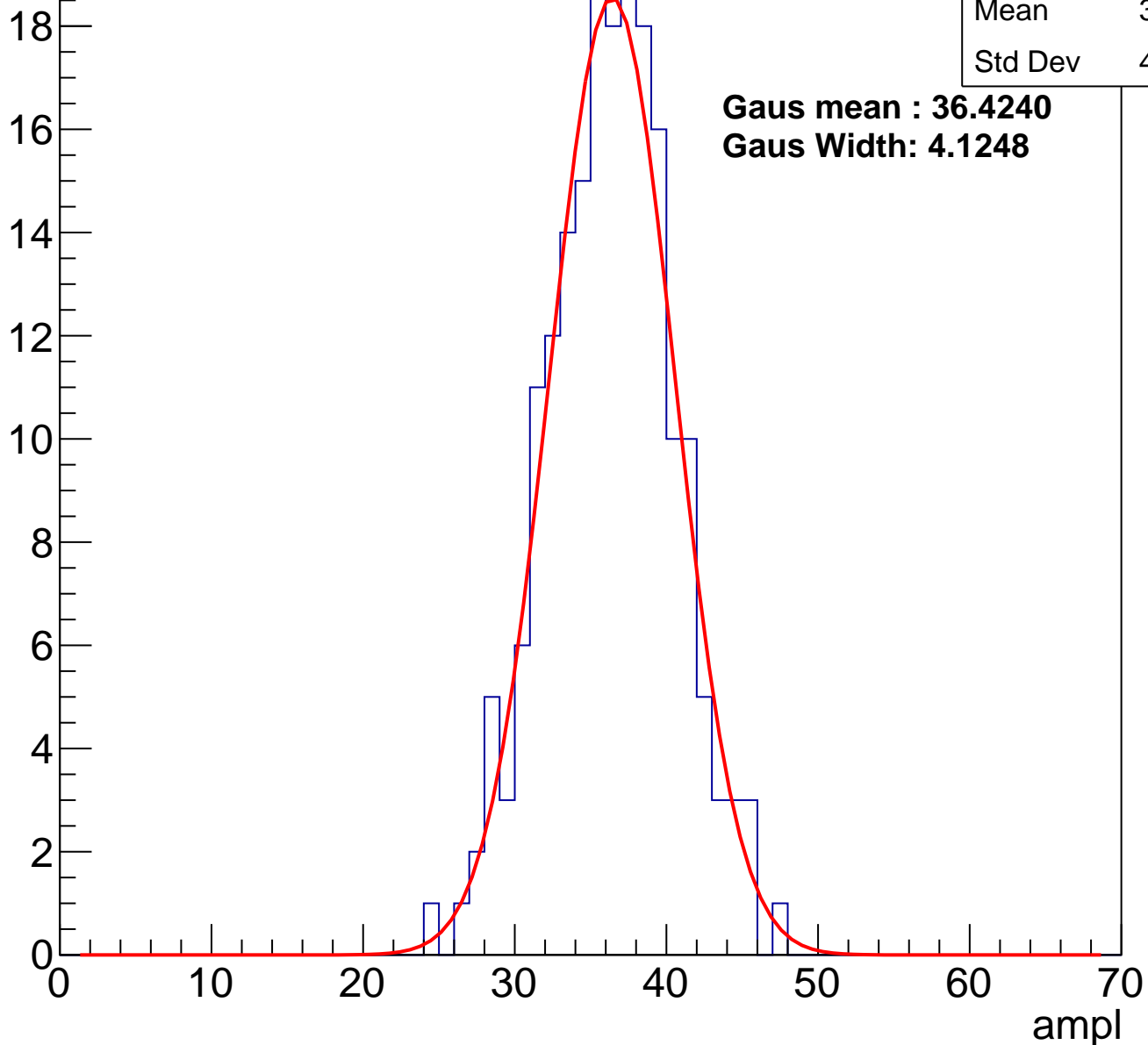
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	195
Mean	35.87
Std Dev	4.105

**Gaus mean : 36.4240**

**Gaus Width: 4.1248**

Entry



# B0L101S, U26-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

102

Mean

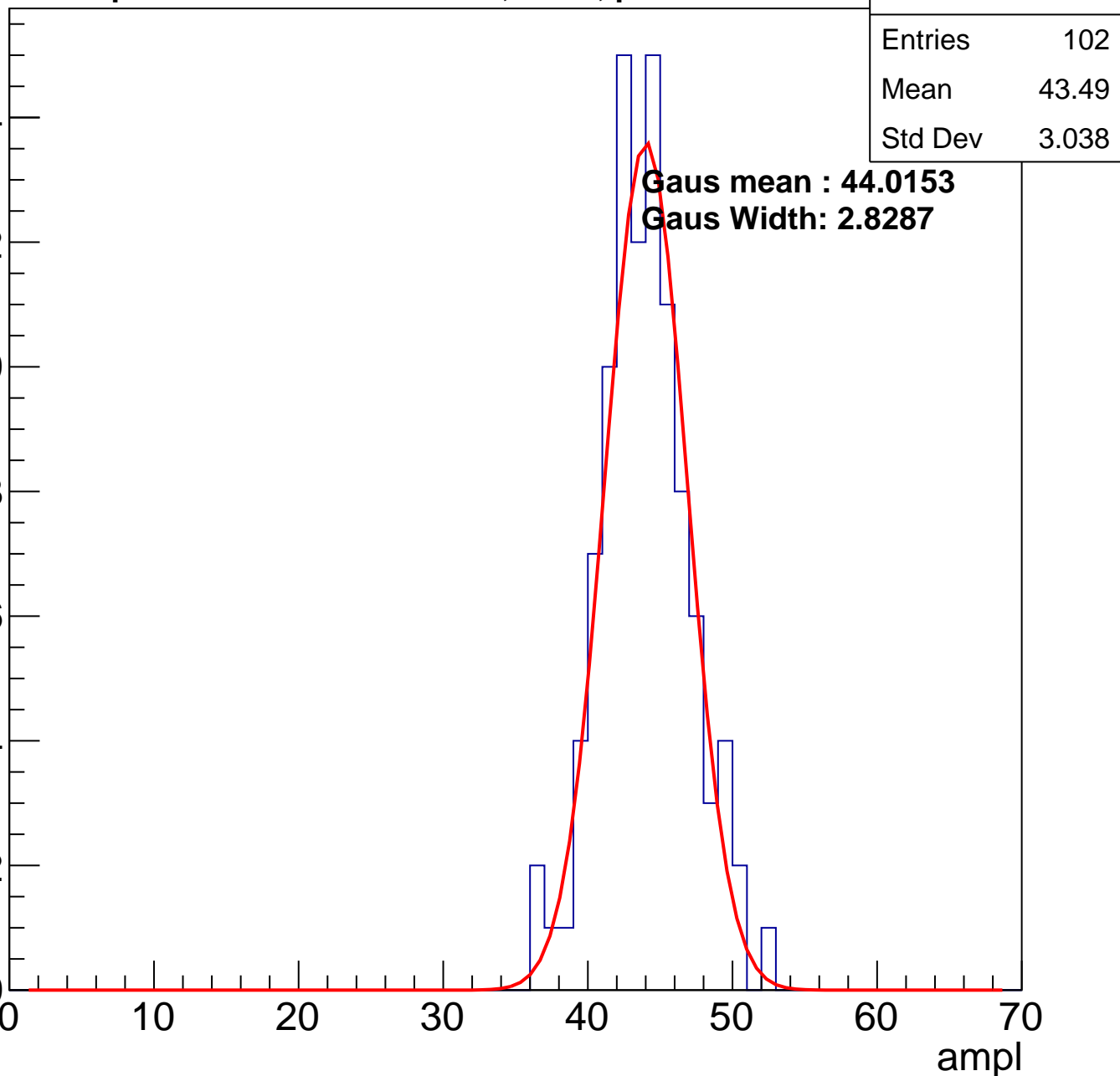
43.49

Std Dev

3.038

**Gaus mean : 44.0153**

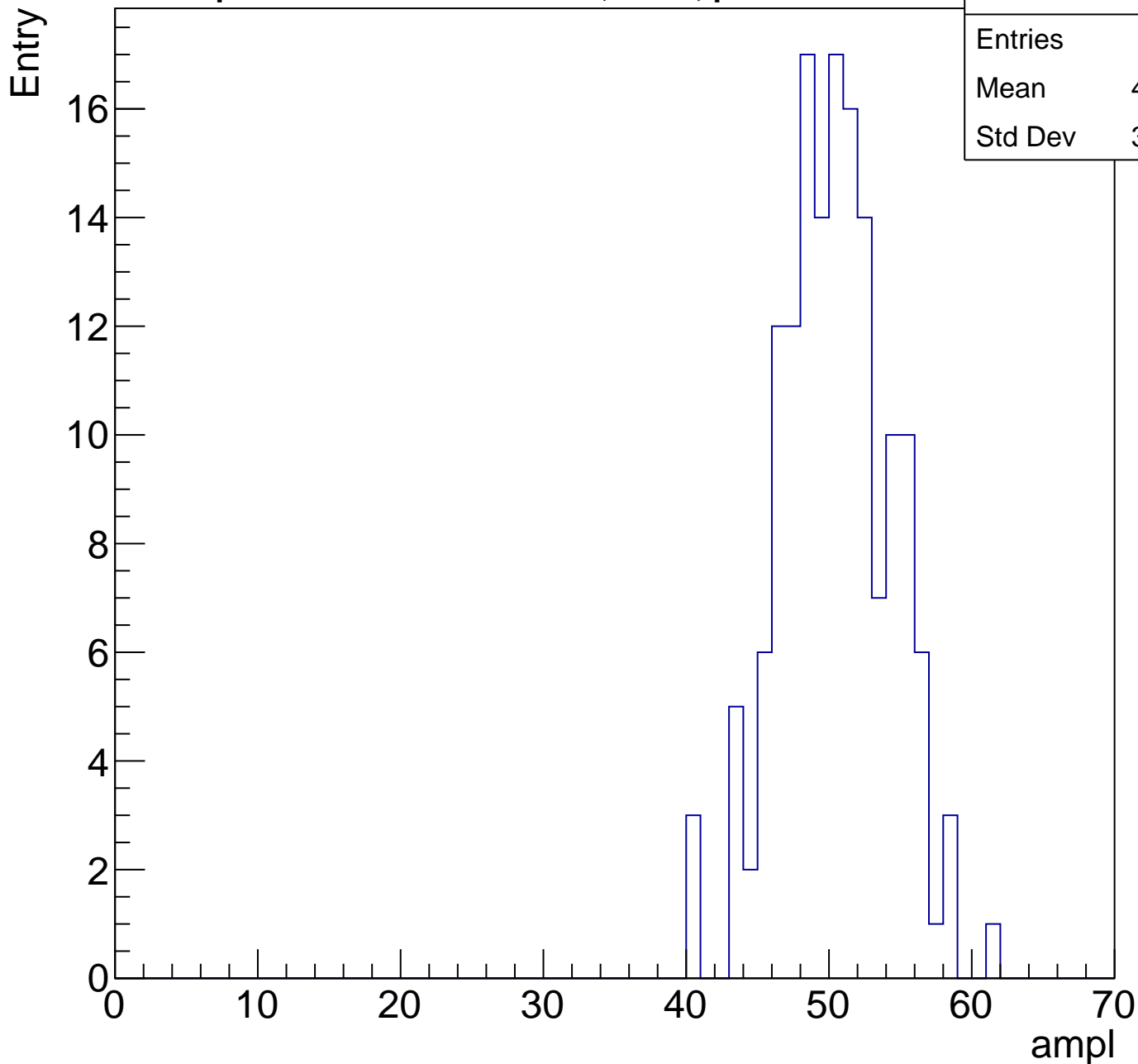
**Gaus Width: 2.8287**



# B0L101S, U26-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

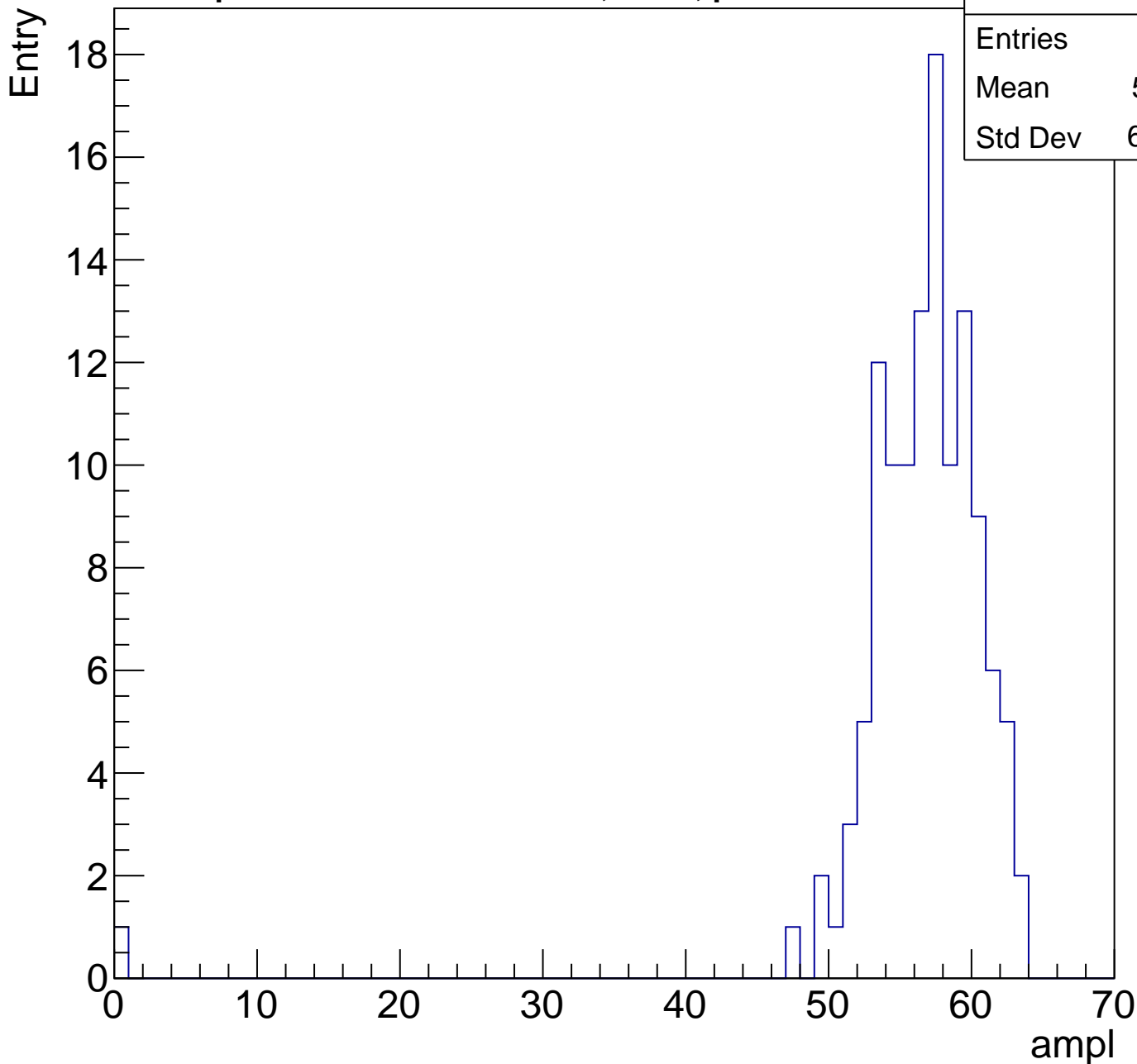
Entries	156
Mean	49.96
Std Dev	3.816



# B0L101S, U26-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	56.01
Std Dev	6.038



# B0L101S, U26-ch42, adc5

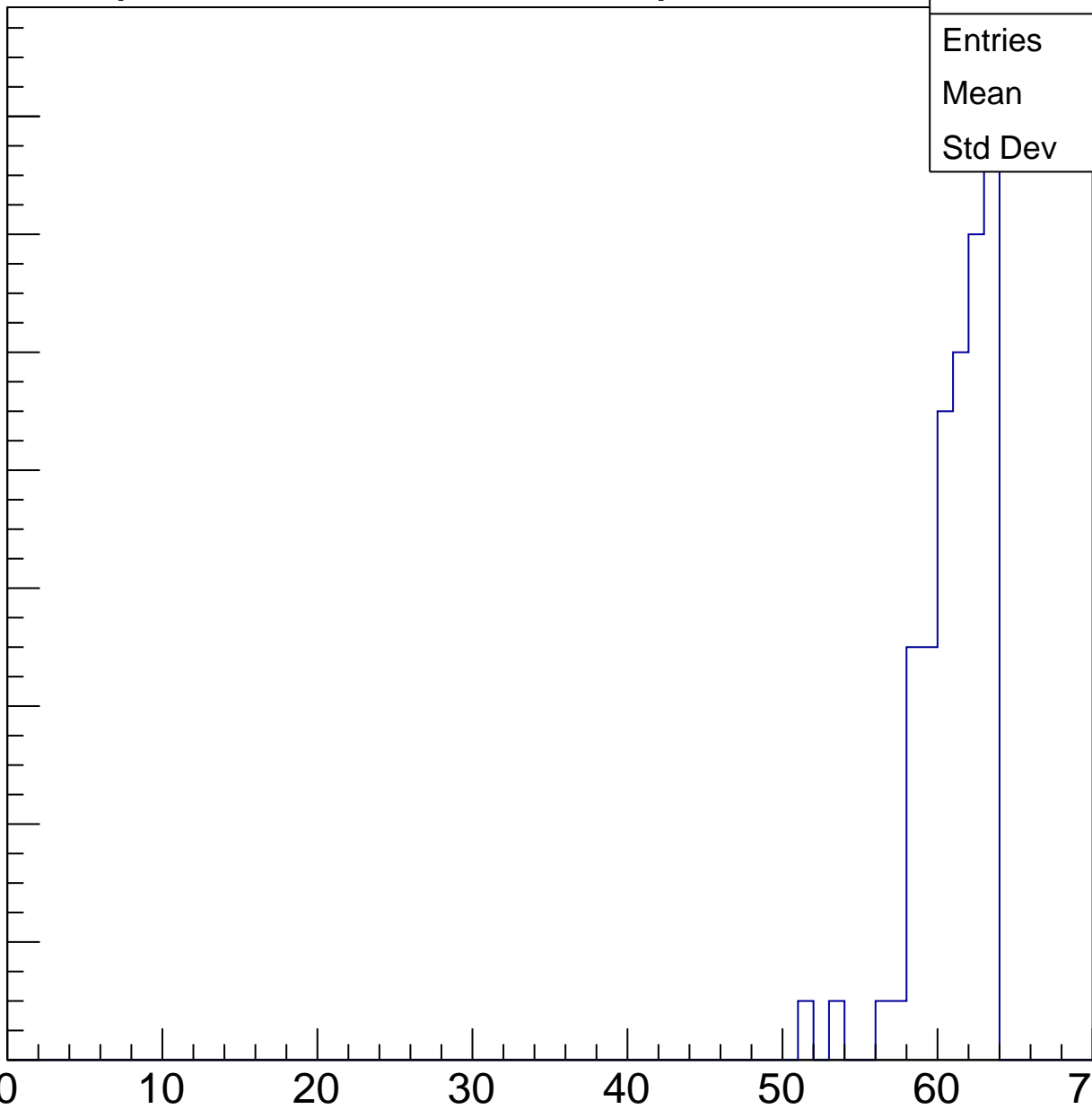
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	60.65
Std Dev	2.298

ampl



# B0L101S, U26-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch43, adc0

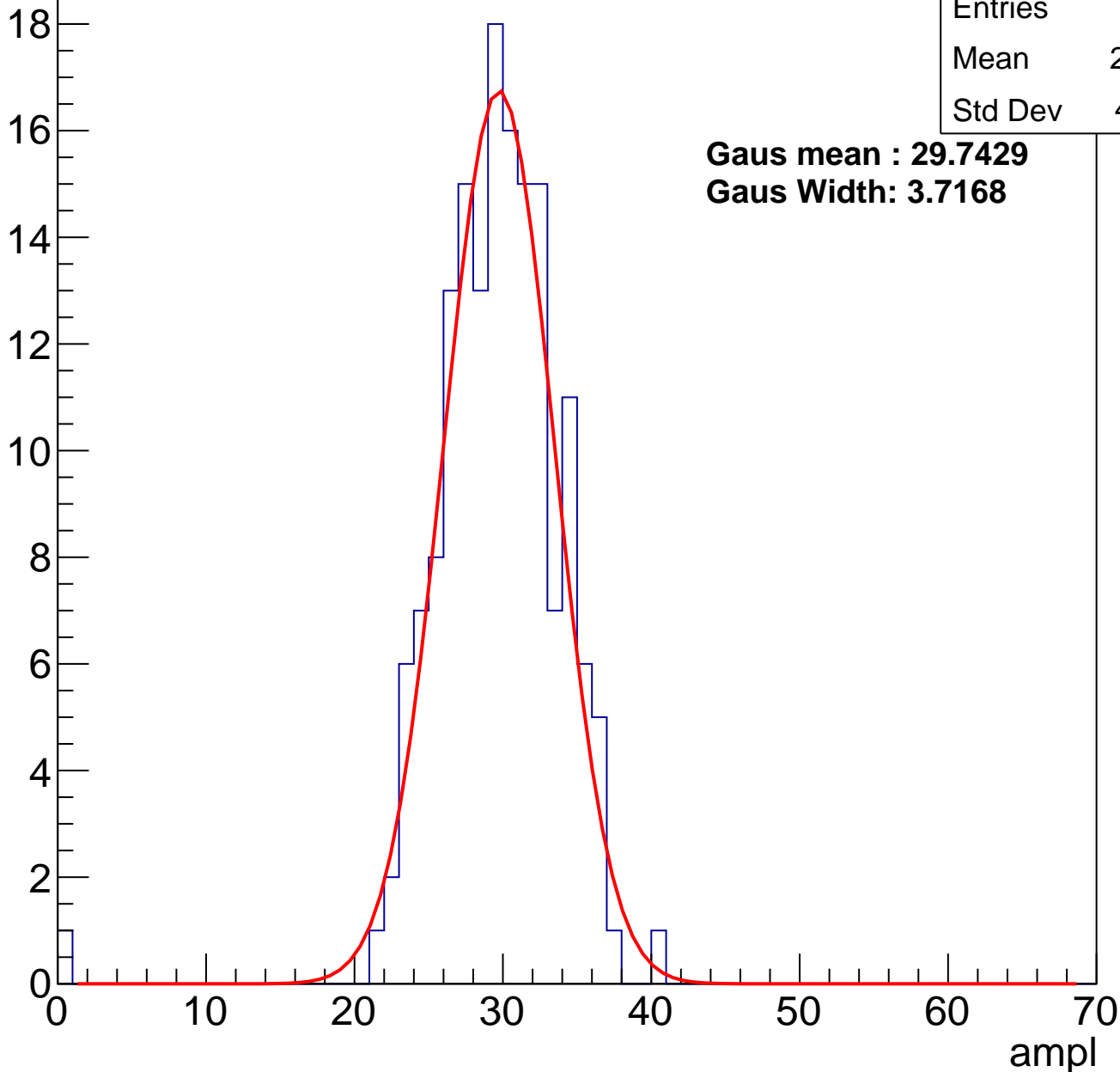
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	29.17
Std Dev	4.281

**Gaus mean : 29.7429**

**Gaus Width: 3.7168**

Entry



# B0L101S, U26-ch43, adc1

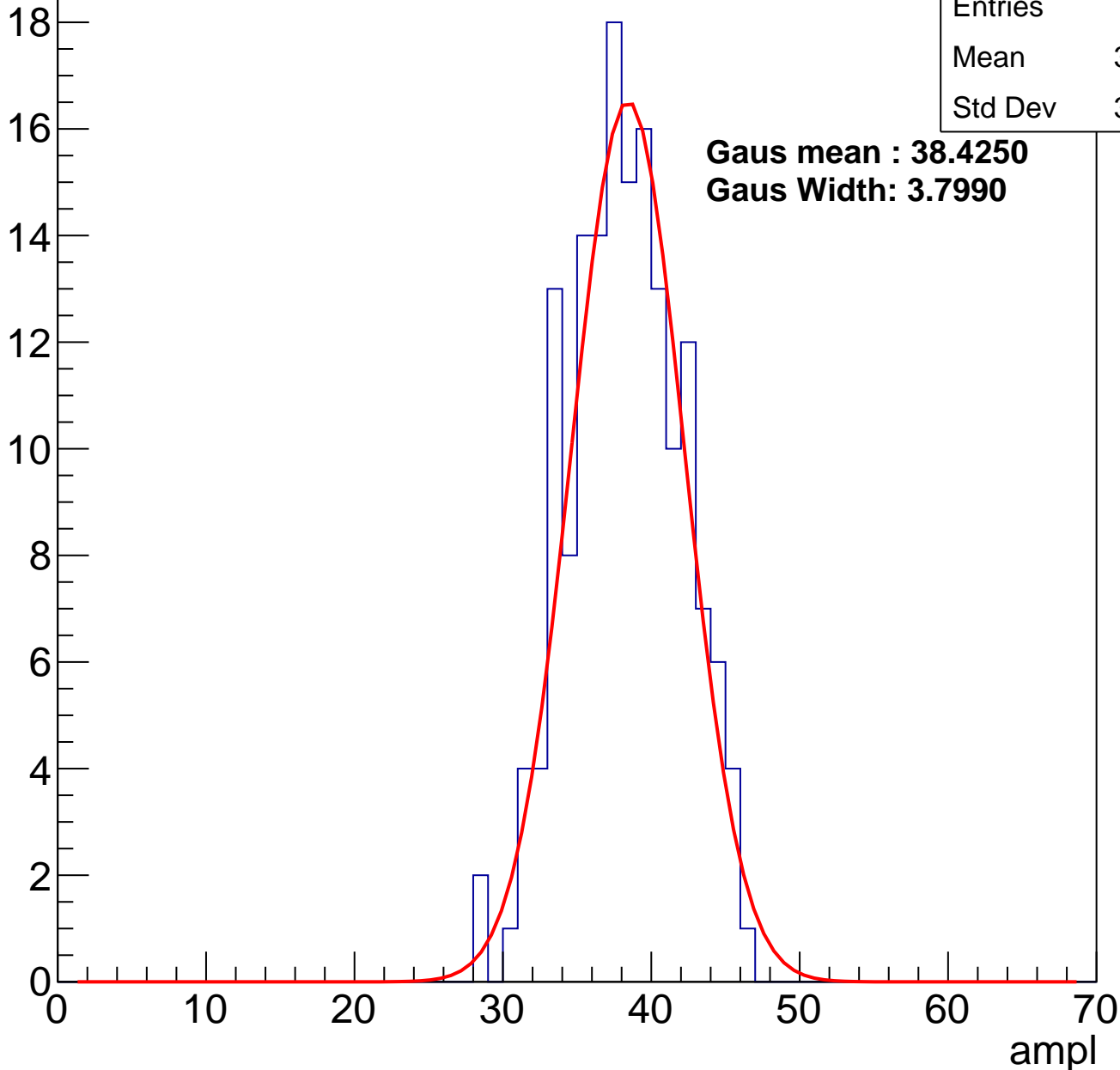
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	37.77
Std Dev	3.726

**Gaus mean : 38.4250**

**Gaus Width: 3.7990**

Entry



# B0L101S, U26-ch43, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

118

Mean

44.13

Std Dev

3.667

Gaus mean : 44.8318

Gaus Width: 3.4915

0

10

20

30

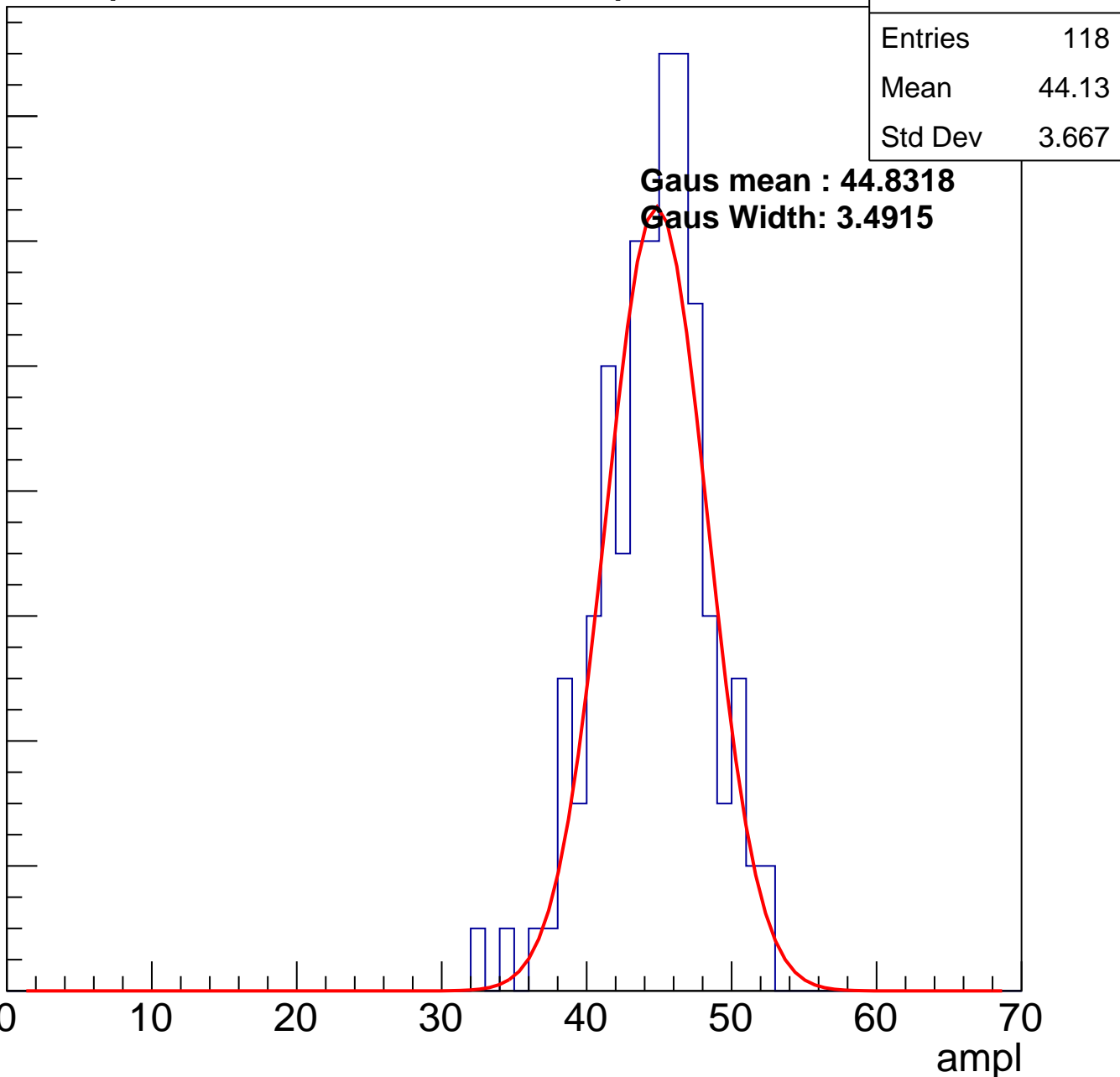
40

50

60

70

ampl



# B0L101S, U26-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

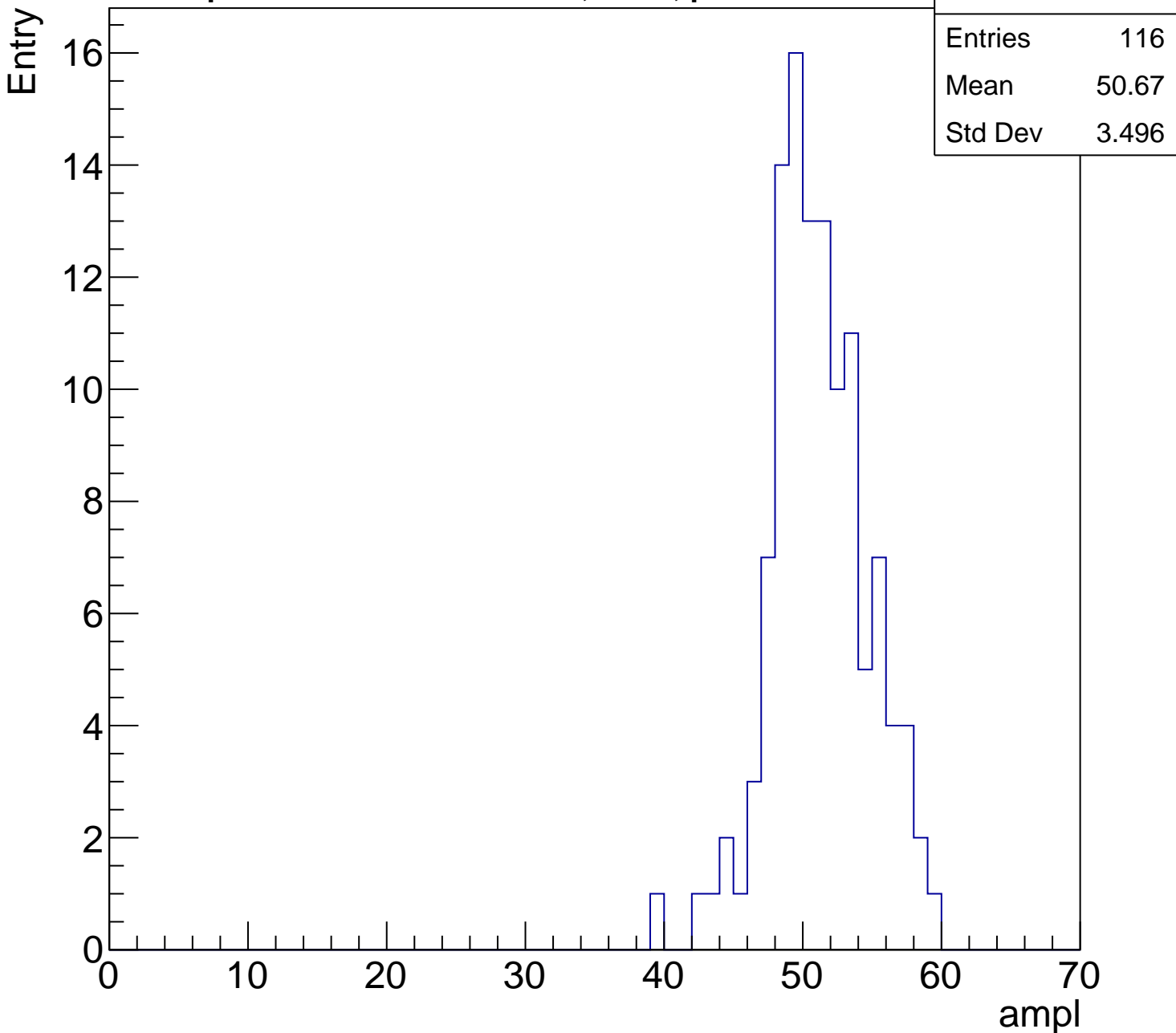
Entries	116
Mean	50.67
Std Dev	3.496

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

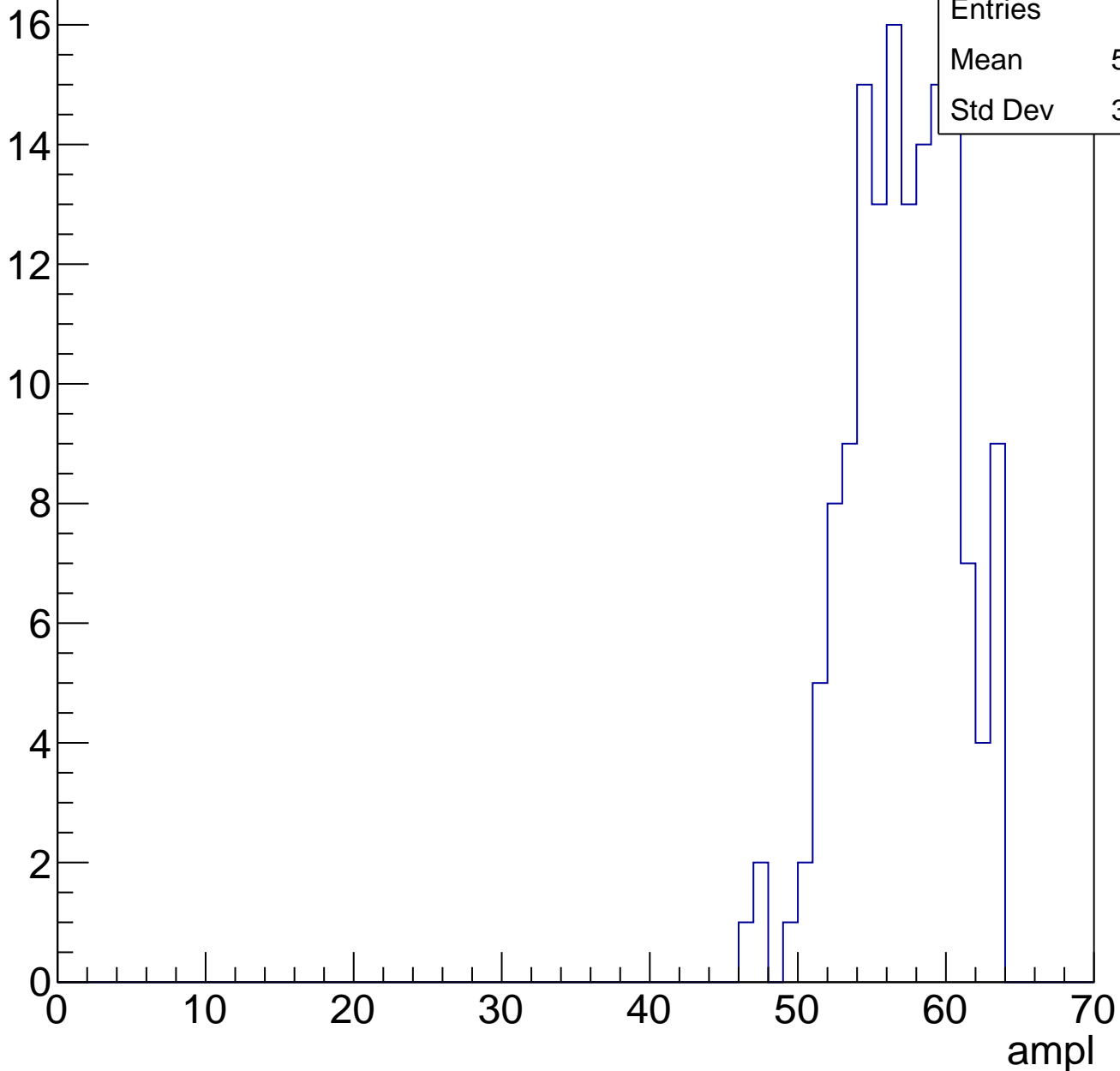
ampl



# B0L101S, U26-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

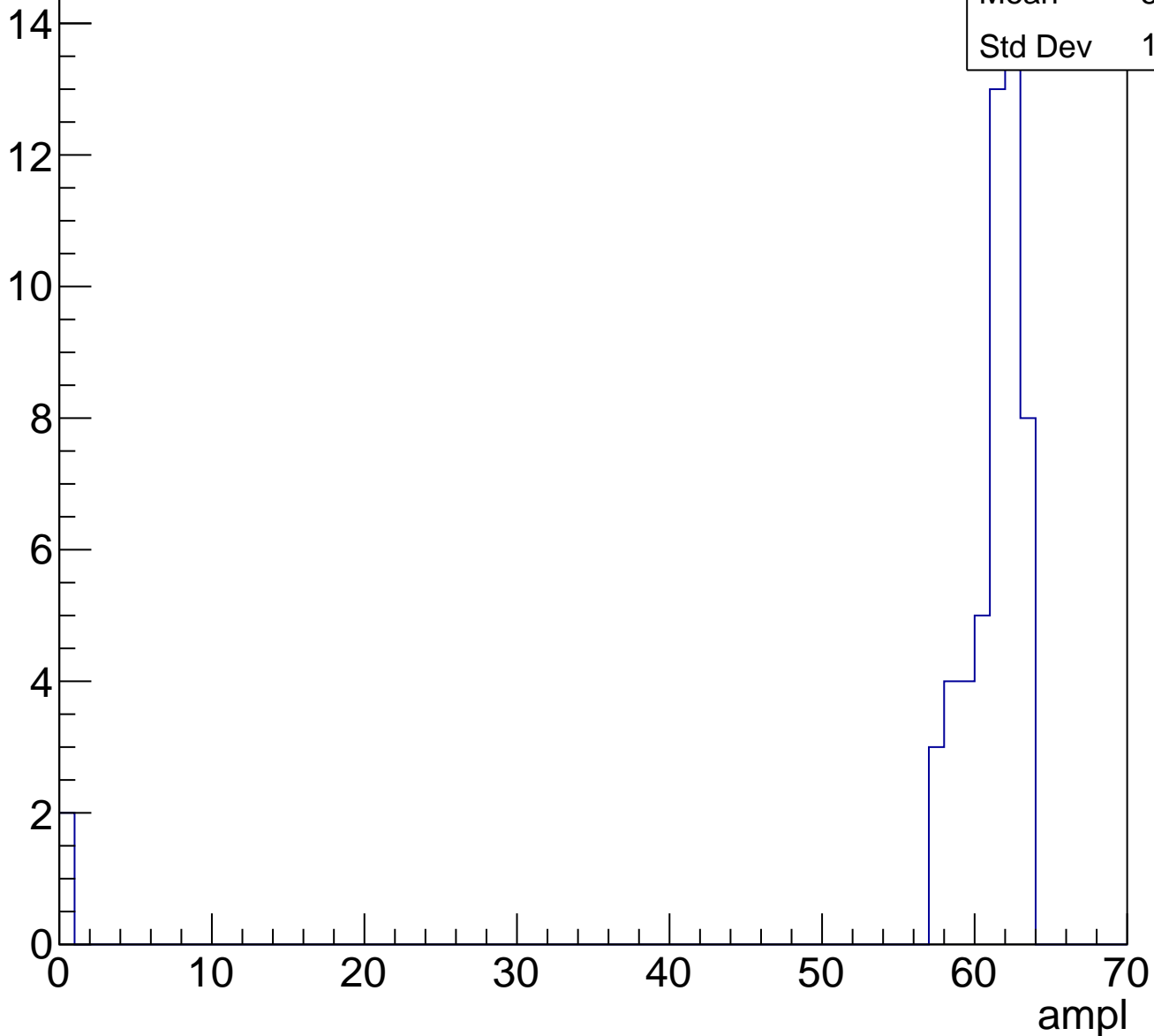
Entry



# B0L101S, U26-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

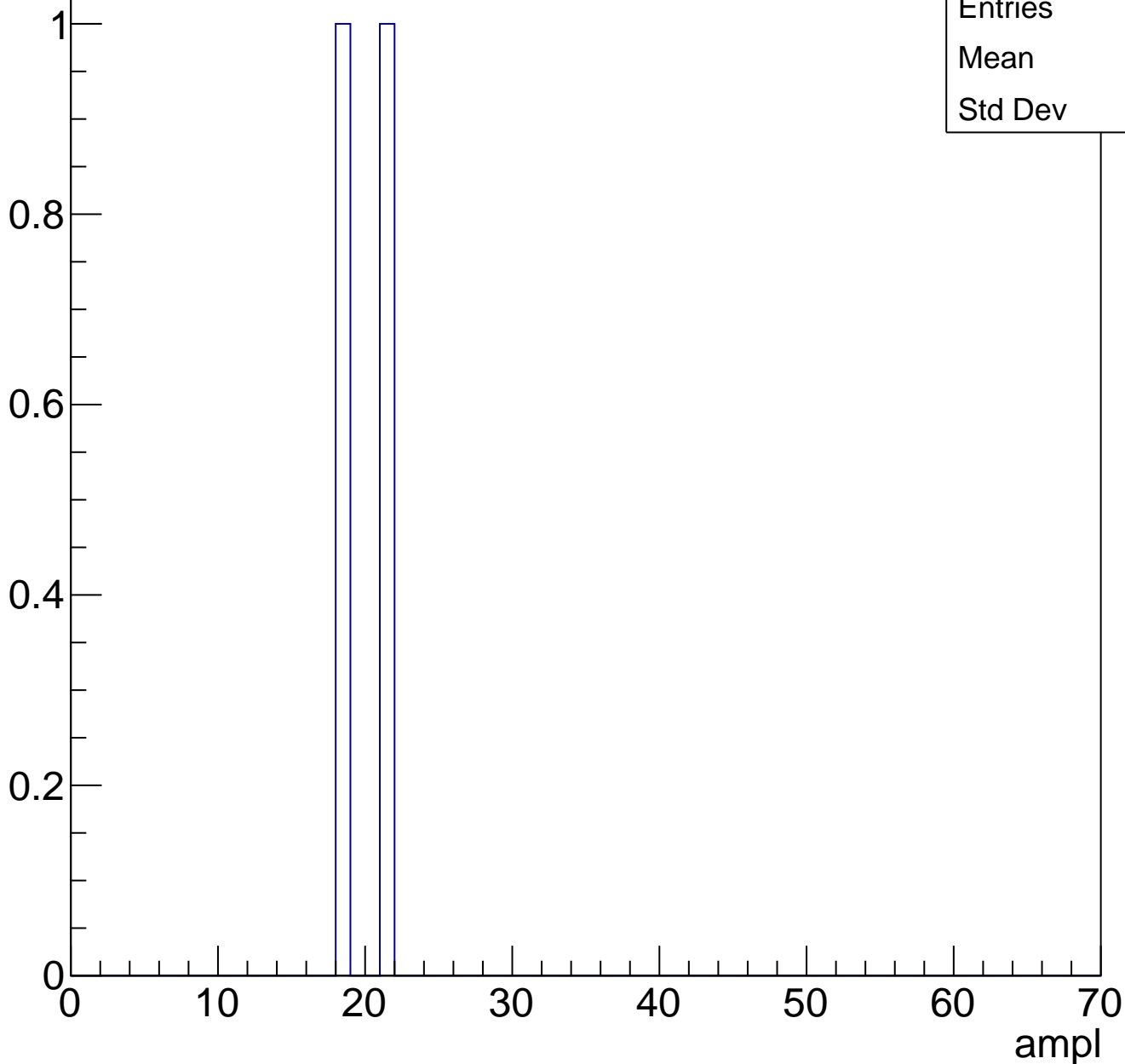




# B0L101S, U26-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch44, adc0

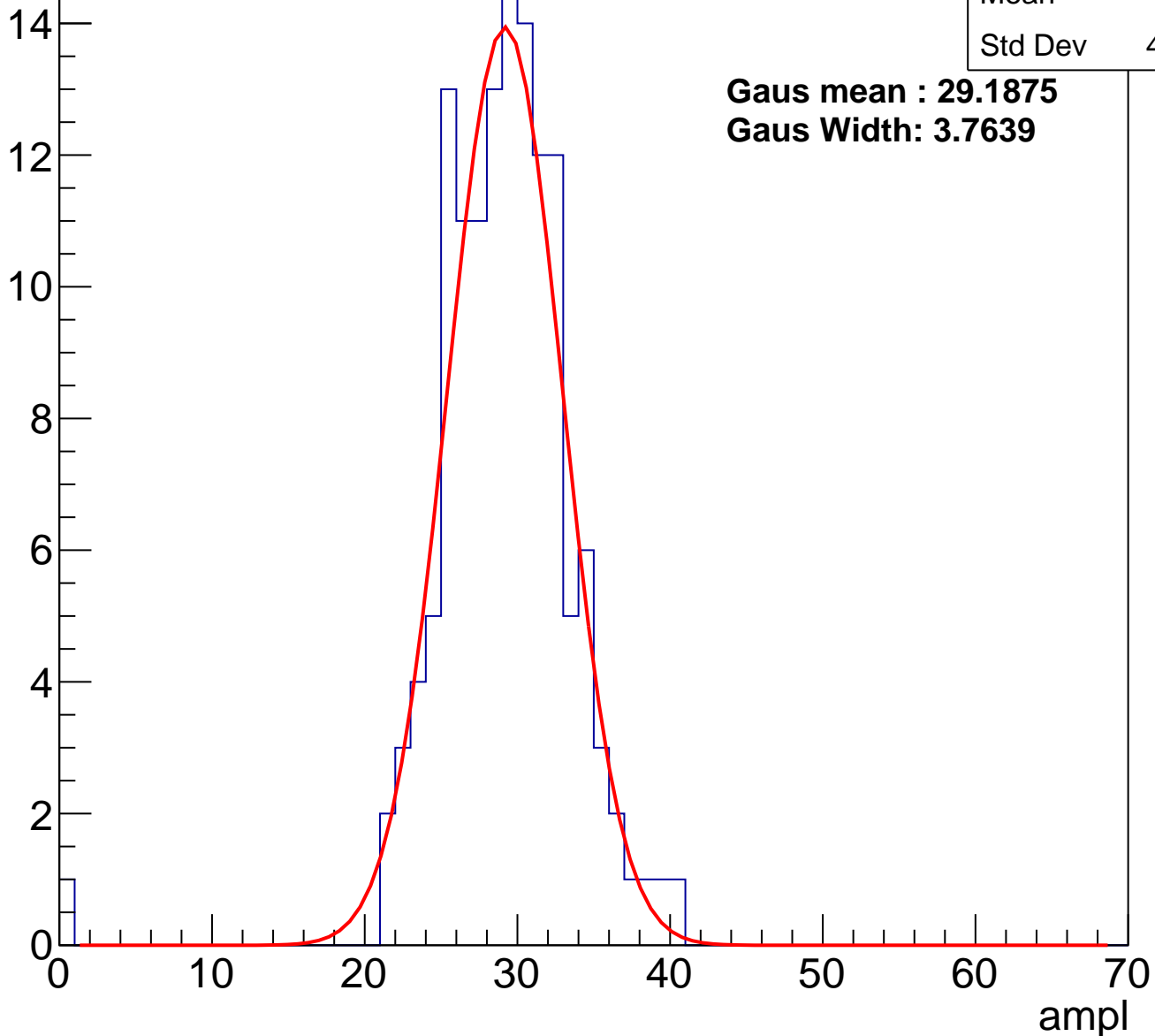
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	28.7
Std Dev	4.463

**Gaus mean : 29.1875**

**Gaus Width: 3.7639**

Entry



# B0L101S, U26-ch44, adc1

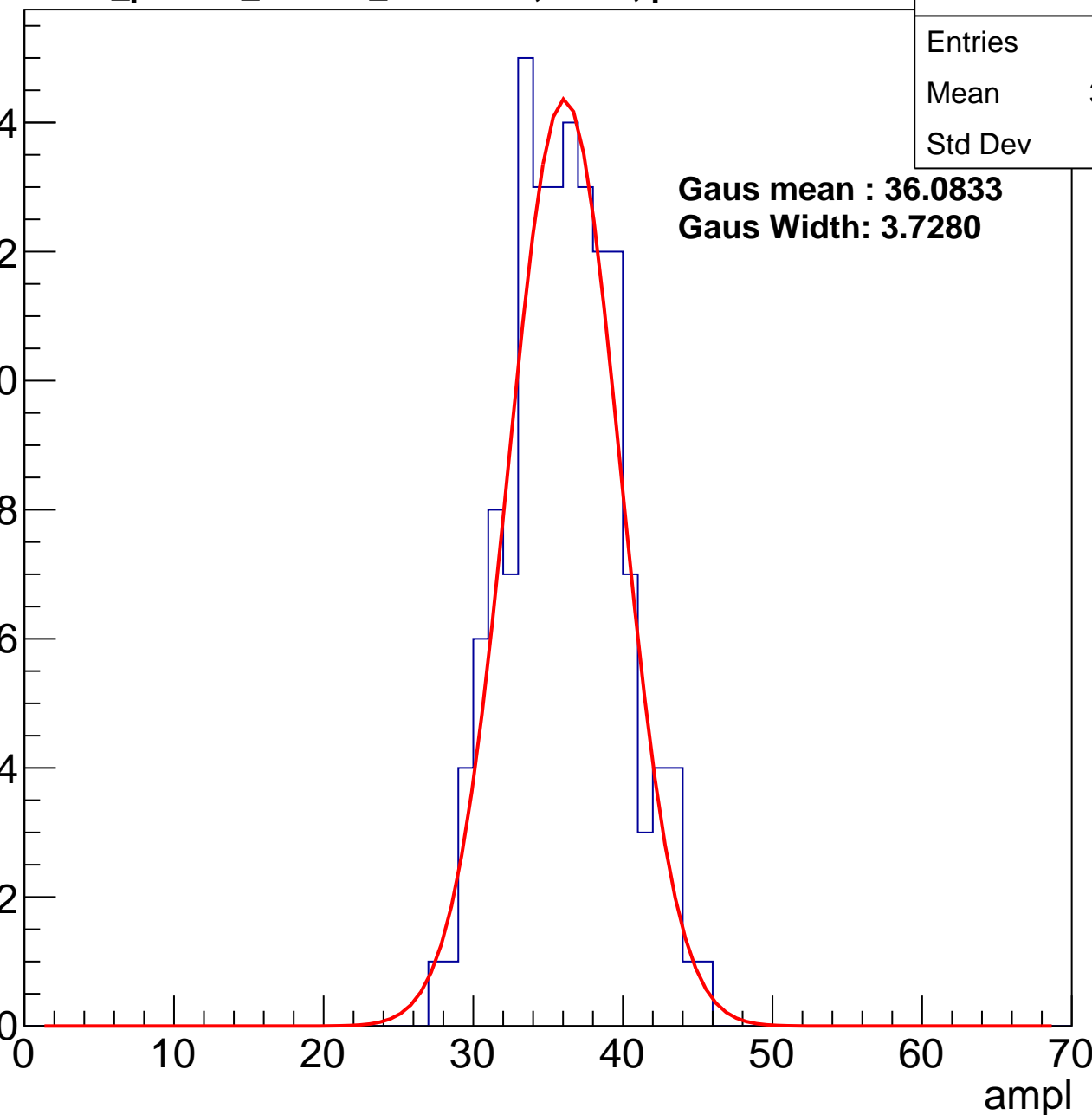
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	139
Mean	35.65
Std Dev	3.691

**Gaus mean : 36.0833**  
**Gaus Width: 3.7280**



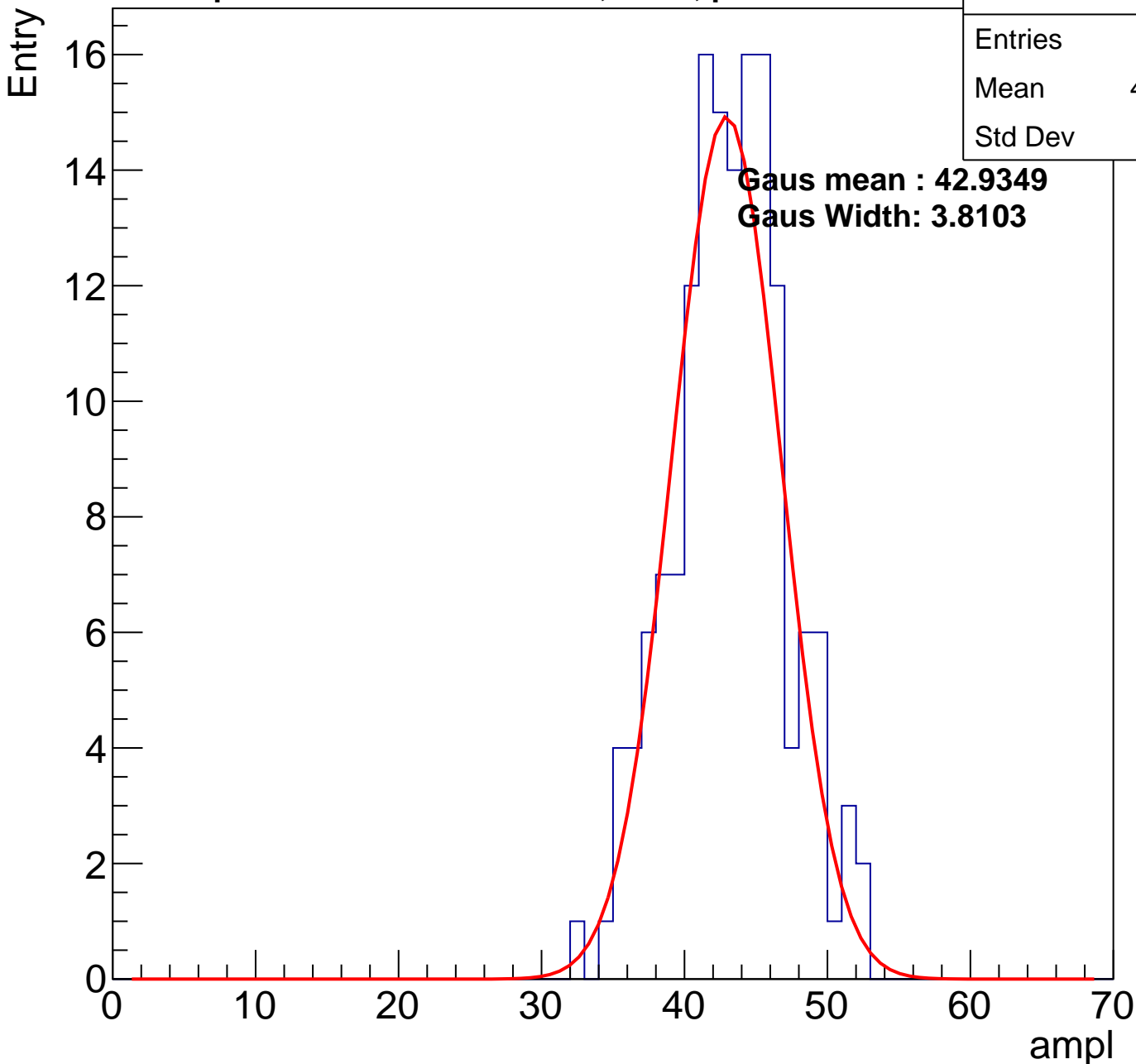
# B0L101S, U26-ch44, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	42.69
Std Dev	3.92

**Gaus mean : 42.9349**

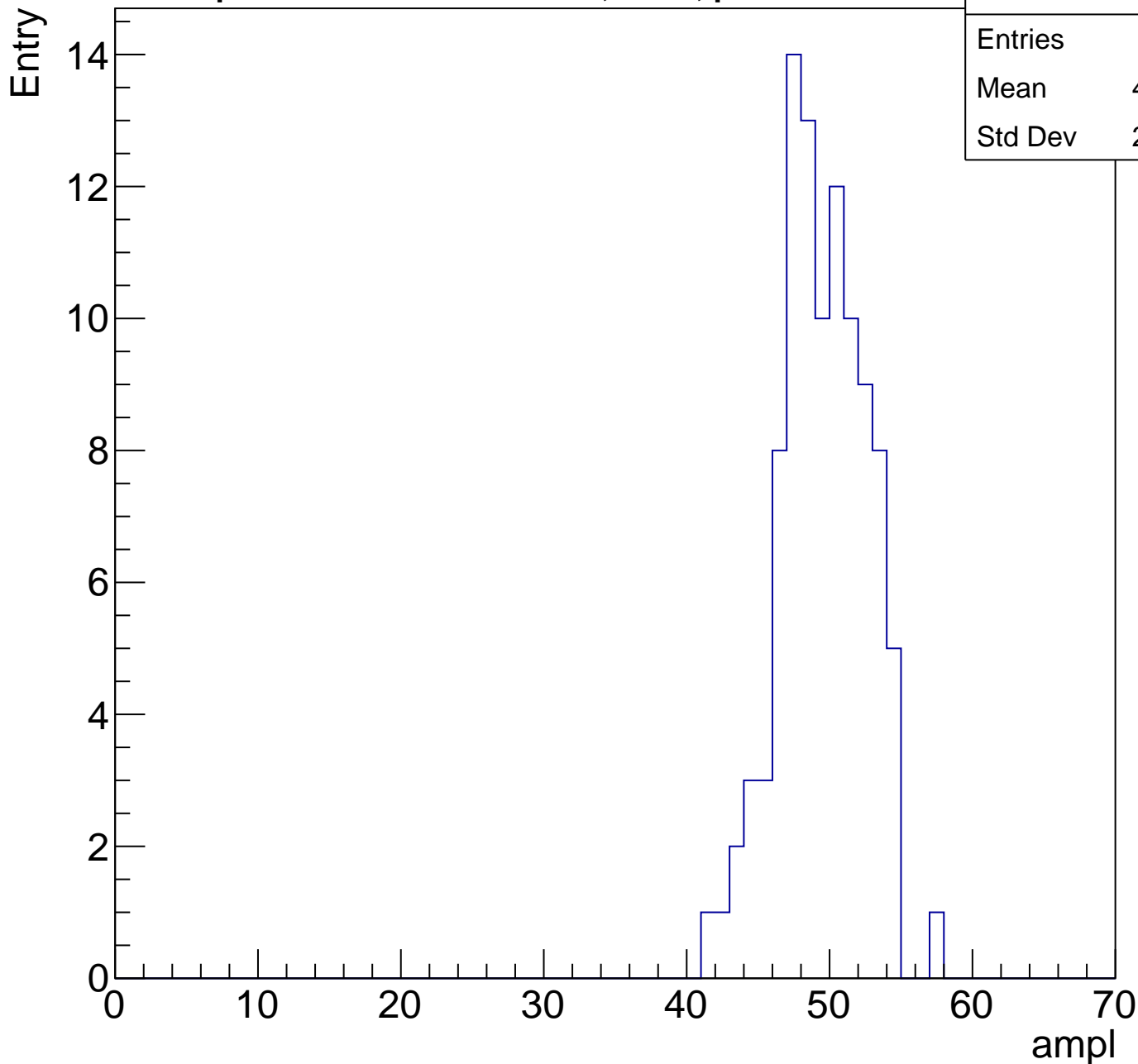
**Gaus Width: 3.8103**



# B0L101S, U26-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	49.05
Std Dev	2.998

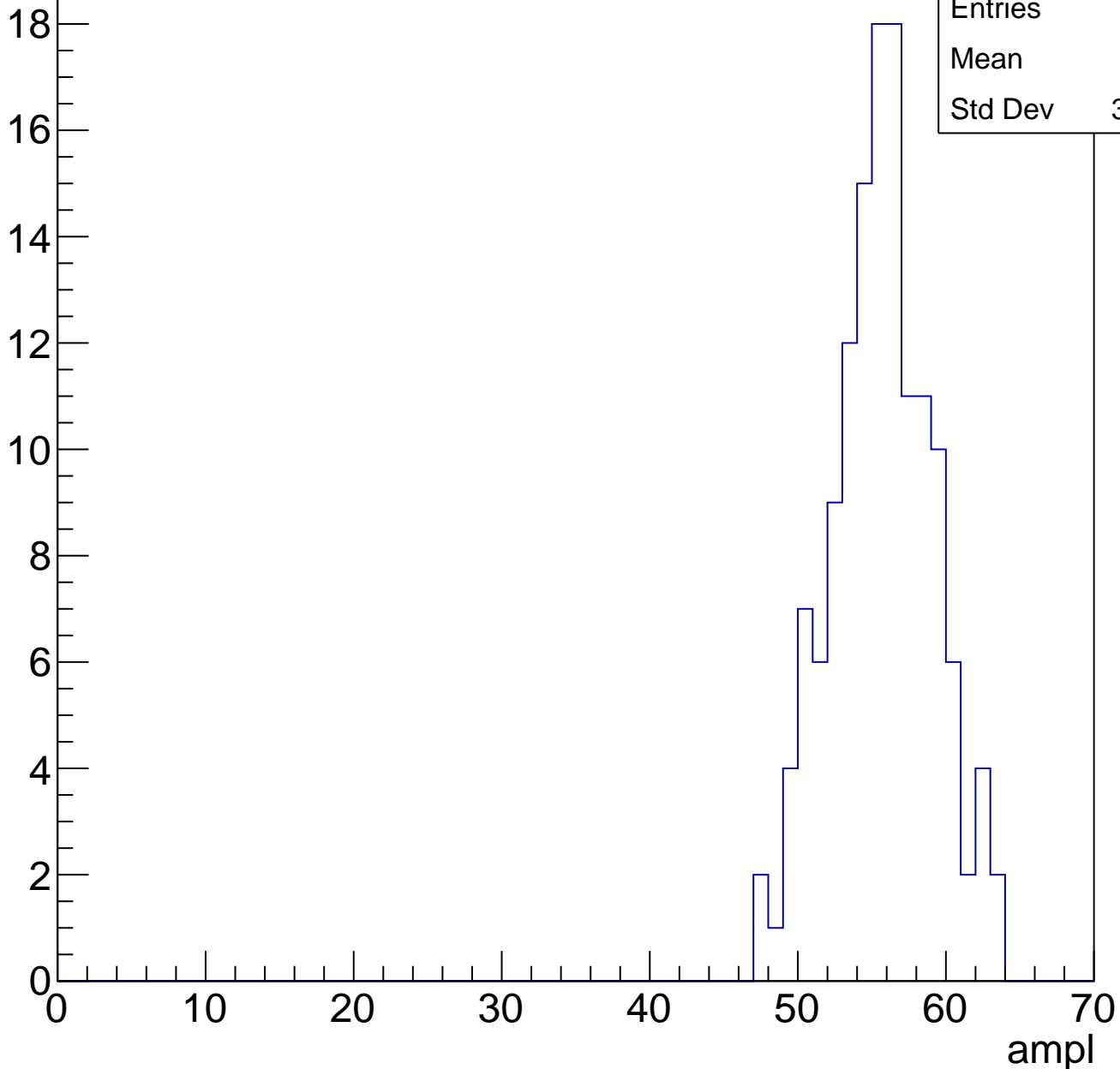


# B0L101S, U26-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	55.2
Std Dev	3.403

Entry



# B0L101S, U26-ch44, adc5

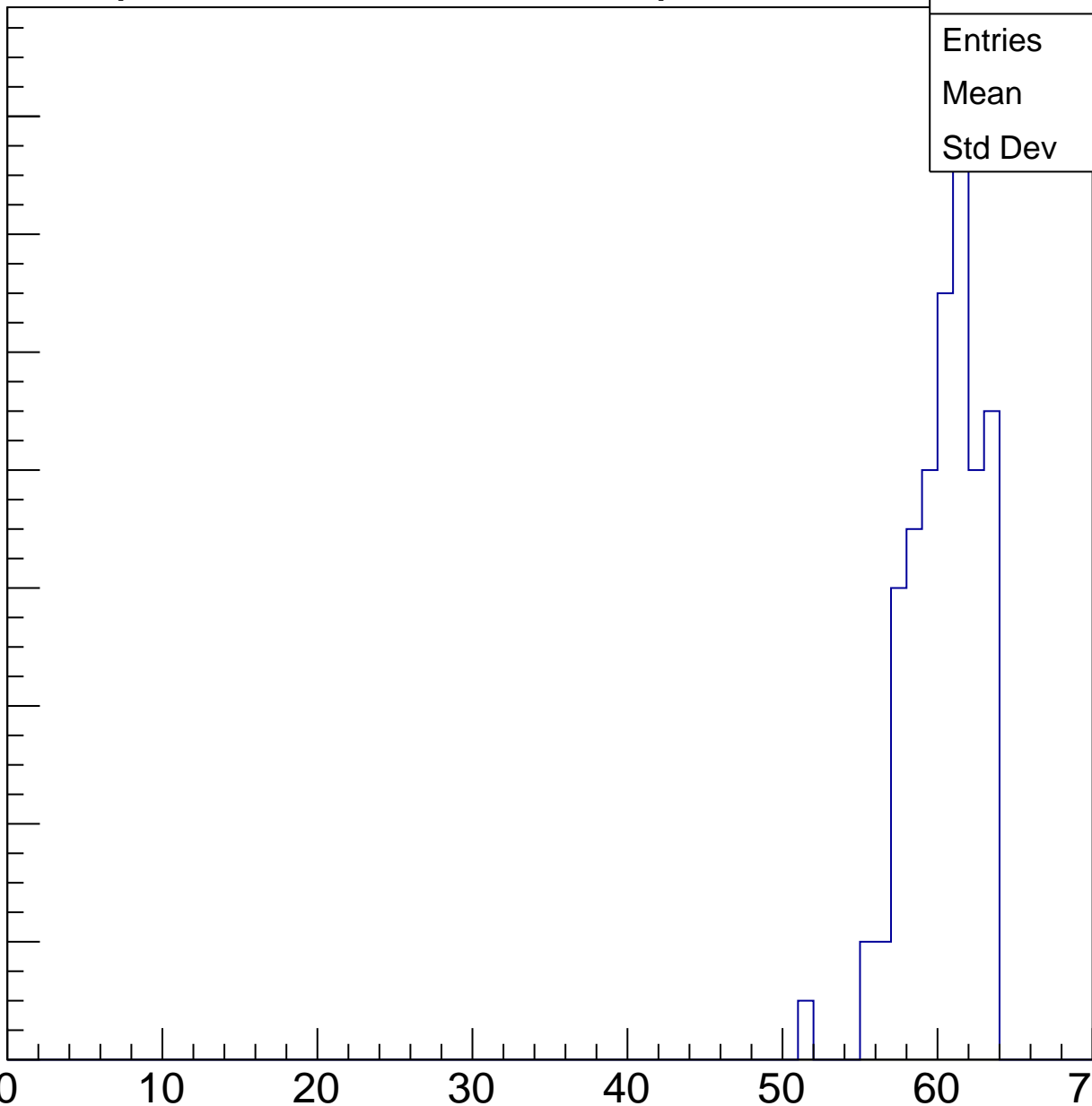
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	59.89
Std Dev	2.292

ampl

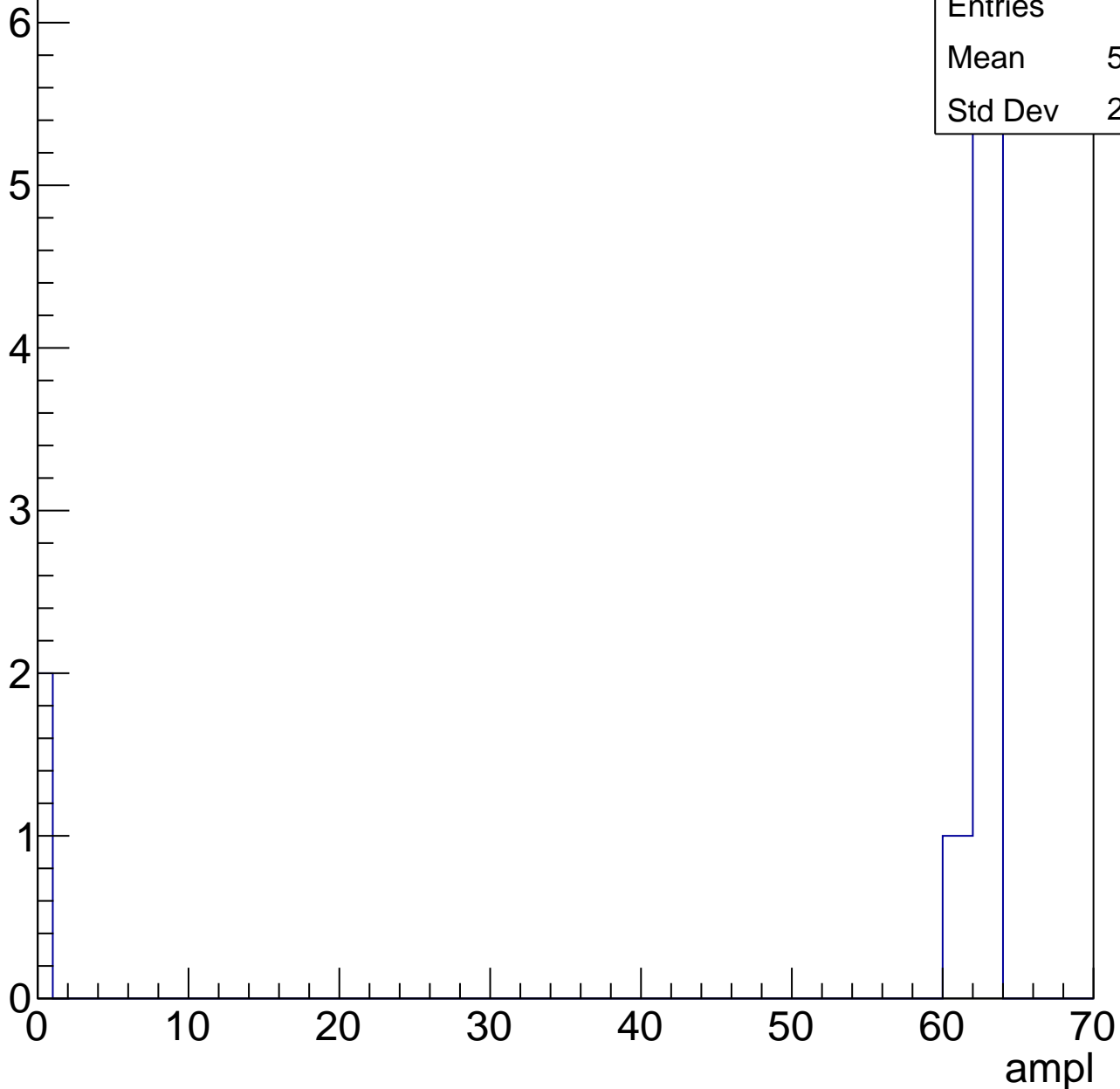


# B0L101S, U26-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	16
Mean	54.44
Std Dev	20.59





# B0L101S, U26-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch45, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	28.95
Std Dev	3.685

**Gaus mean : 29.7427**

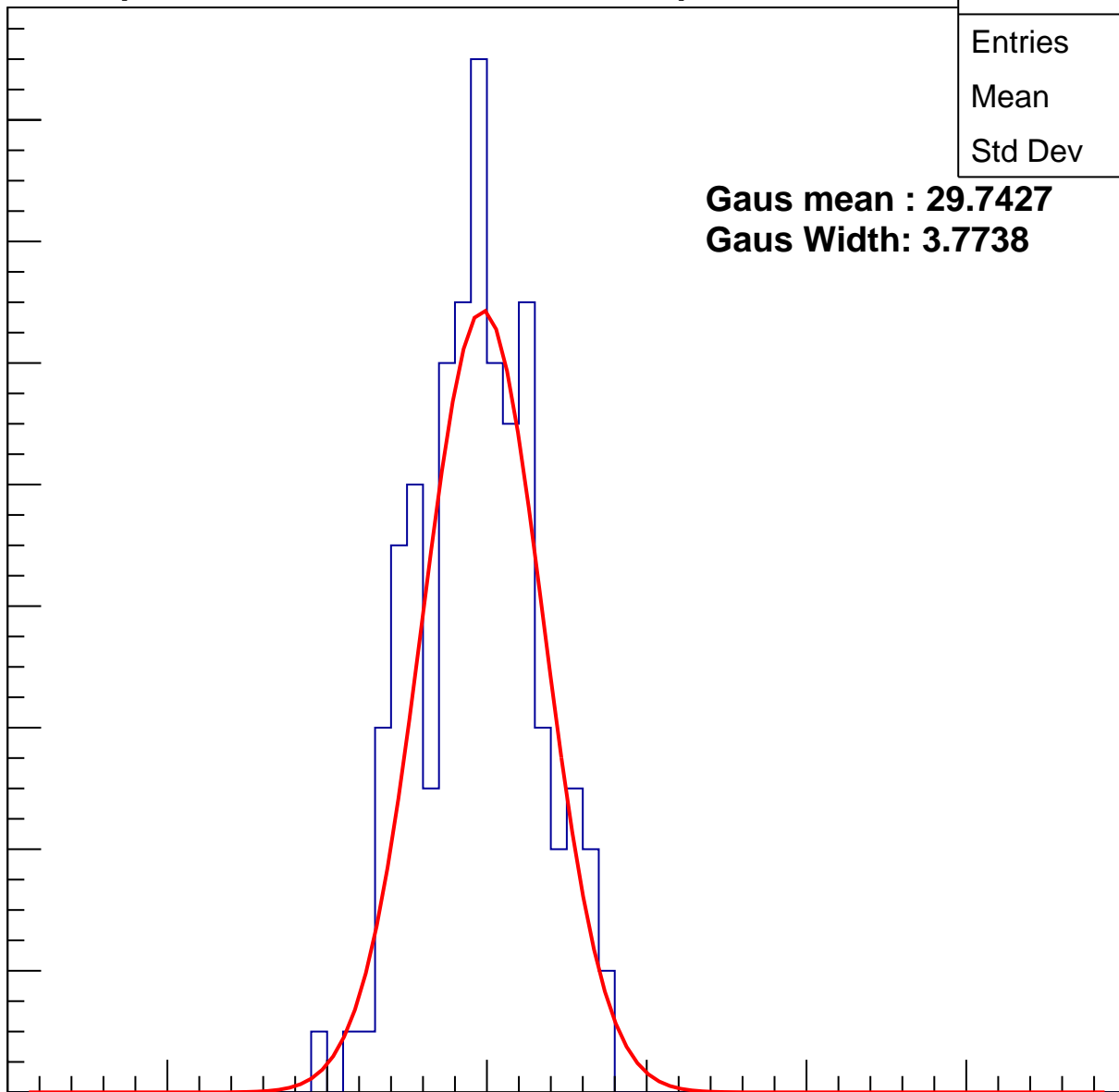
**Gaus Width: 3.7738**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	35.56
Std Dev	3.785

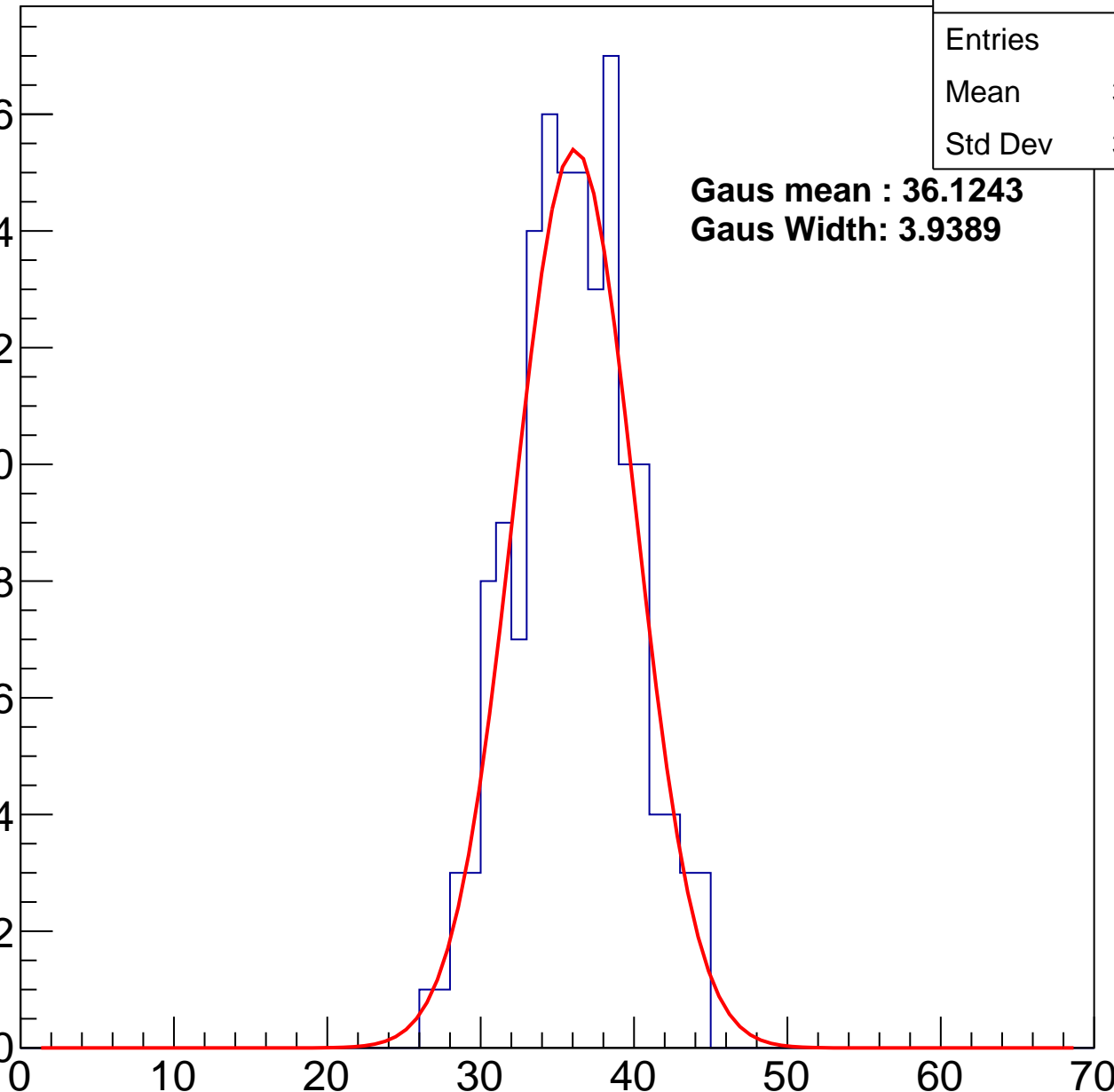
**Gaus mean : 36.1243**

**Gaus Width: 3.9389**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

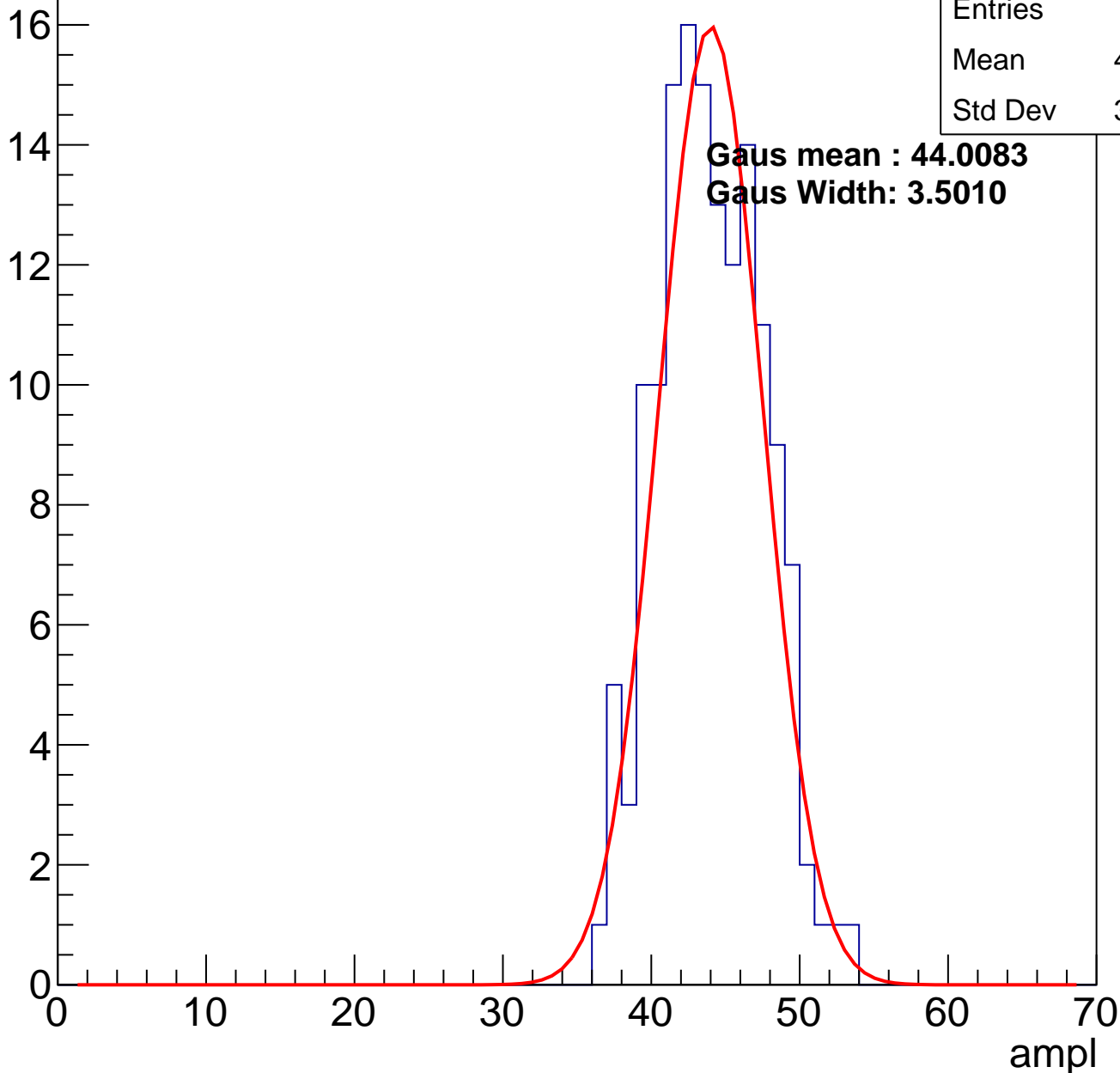
ampl



# B0L101S, U26-ch45, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

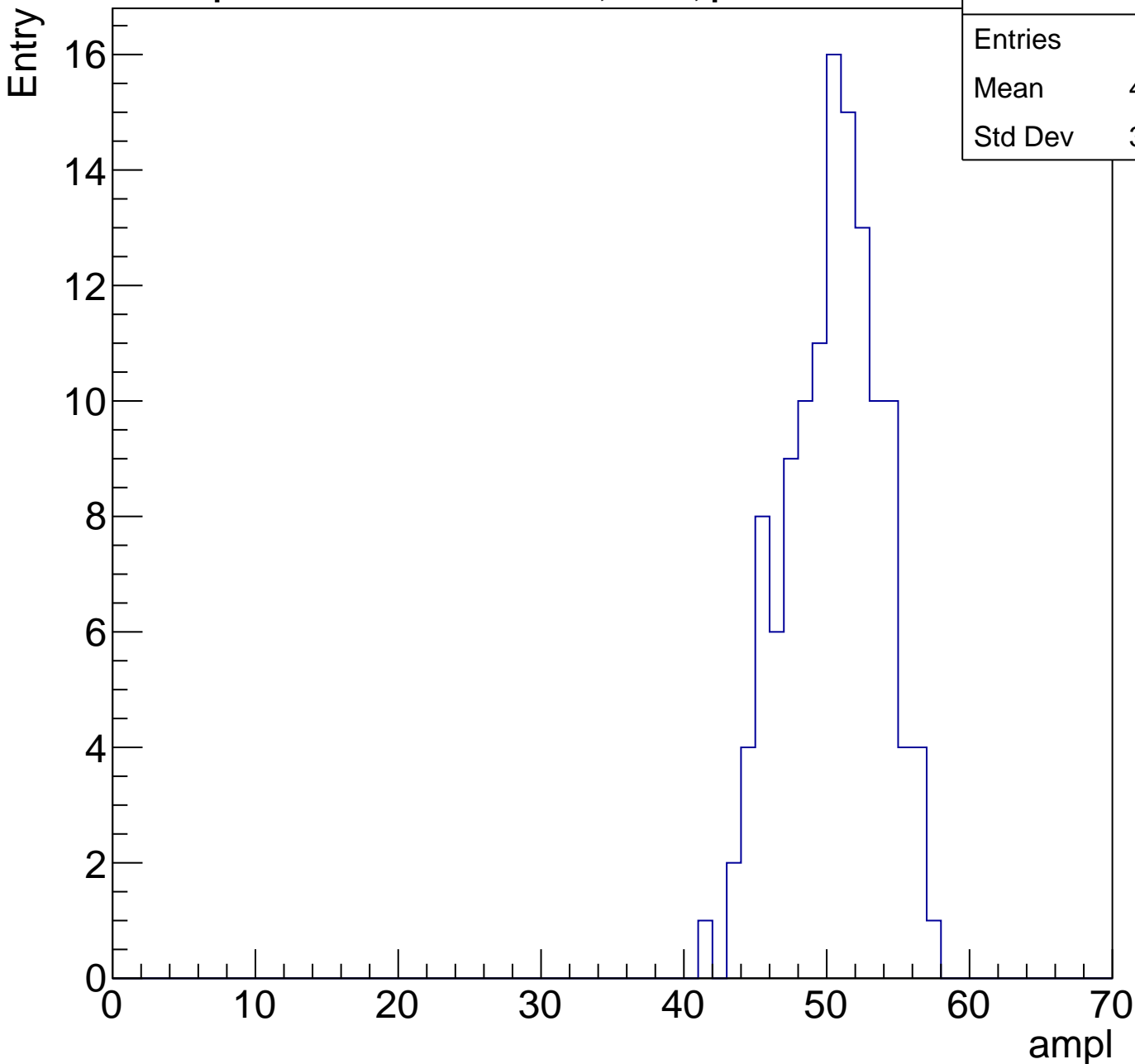
Entry



# B0L101S, U26-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	49.94
Std Dev	3.312



# B0L101S, U26-ch45, adc4

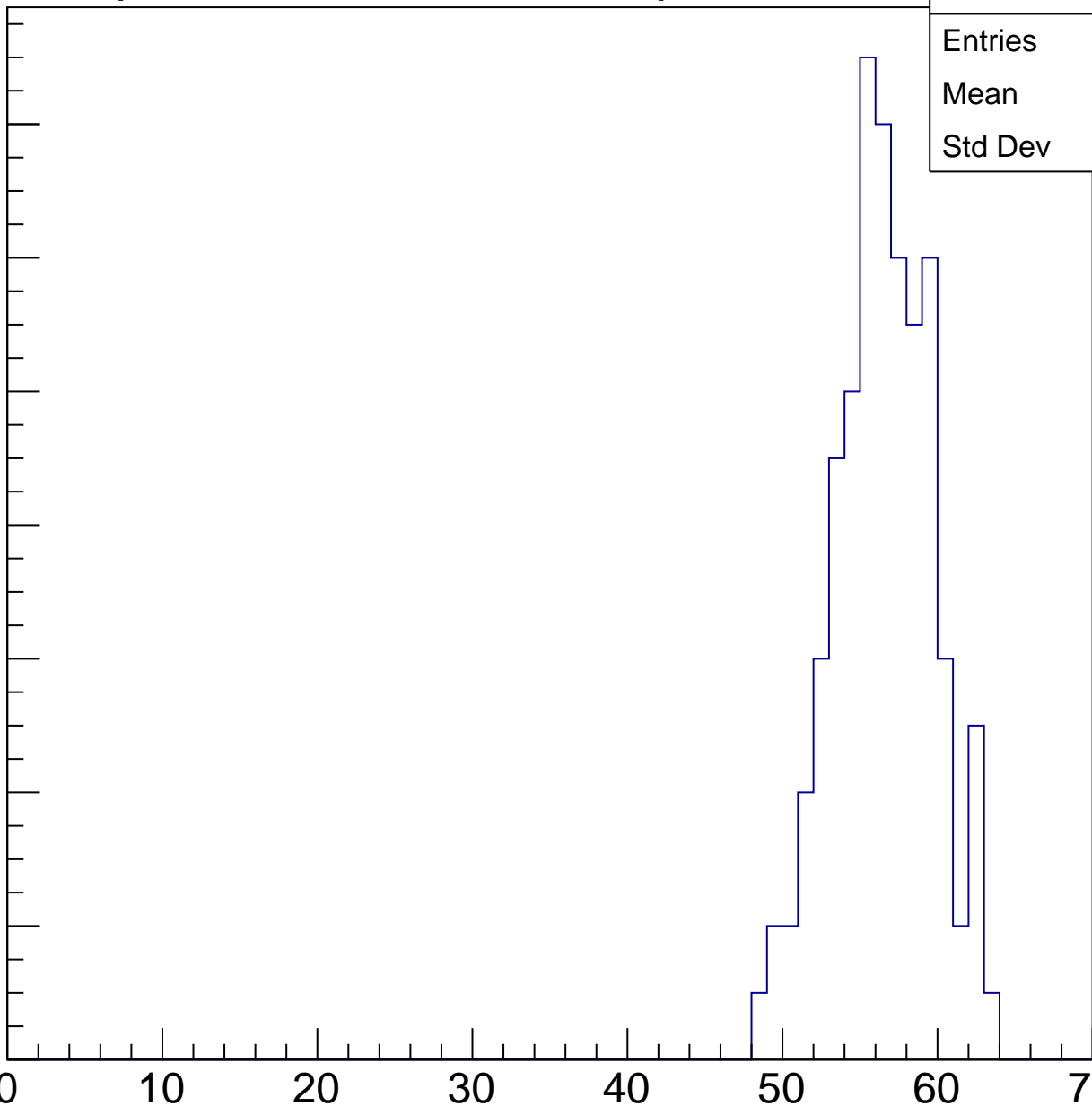
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	112
Mean	56.01
Std Dev	3.141

ampl



# B0L101S, U26-ch45, adc5

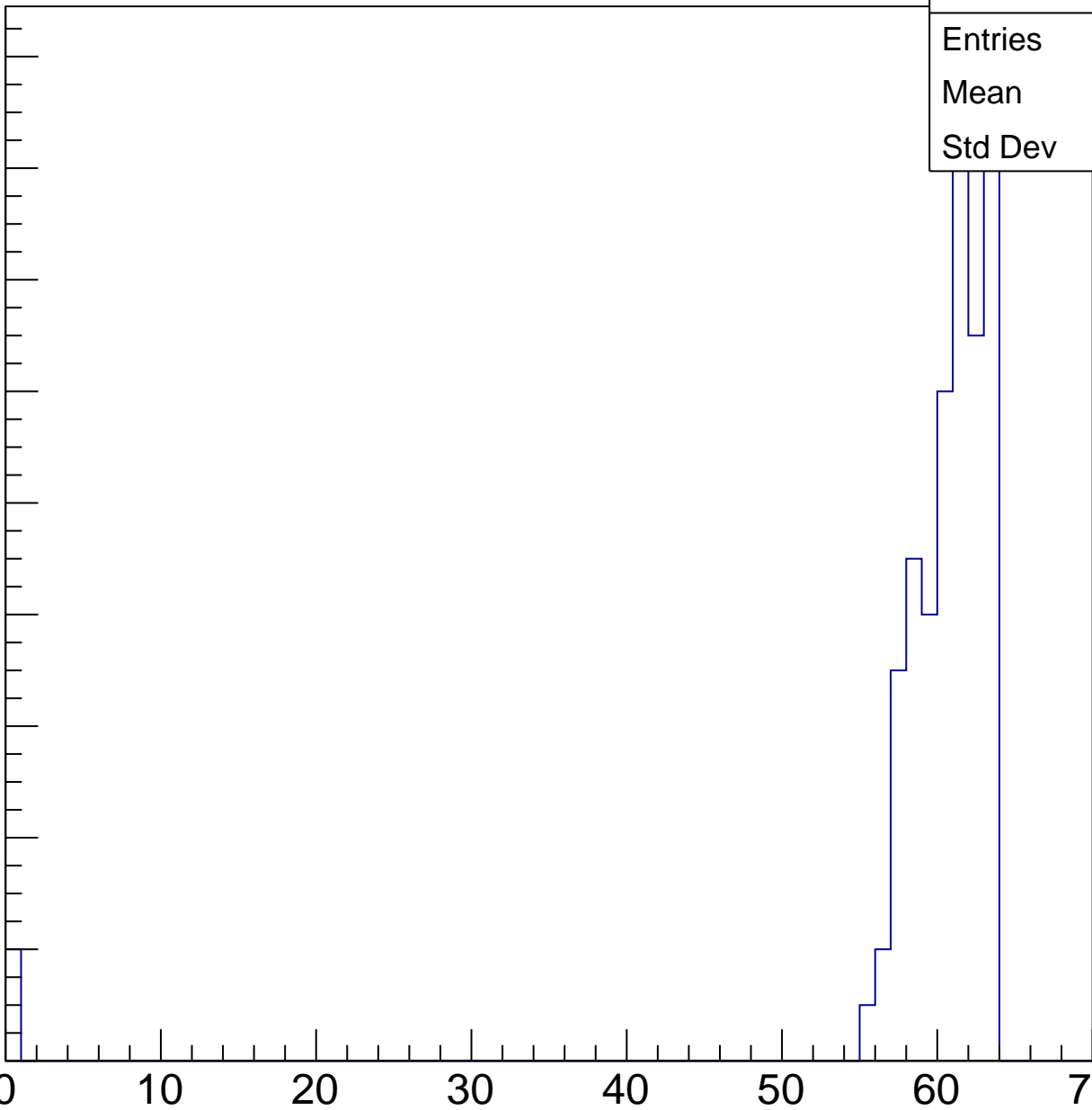
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	59
Std Dev	9.226

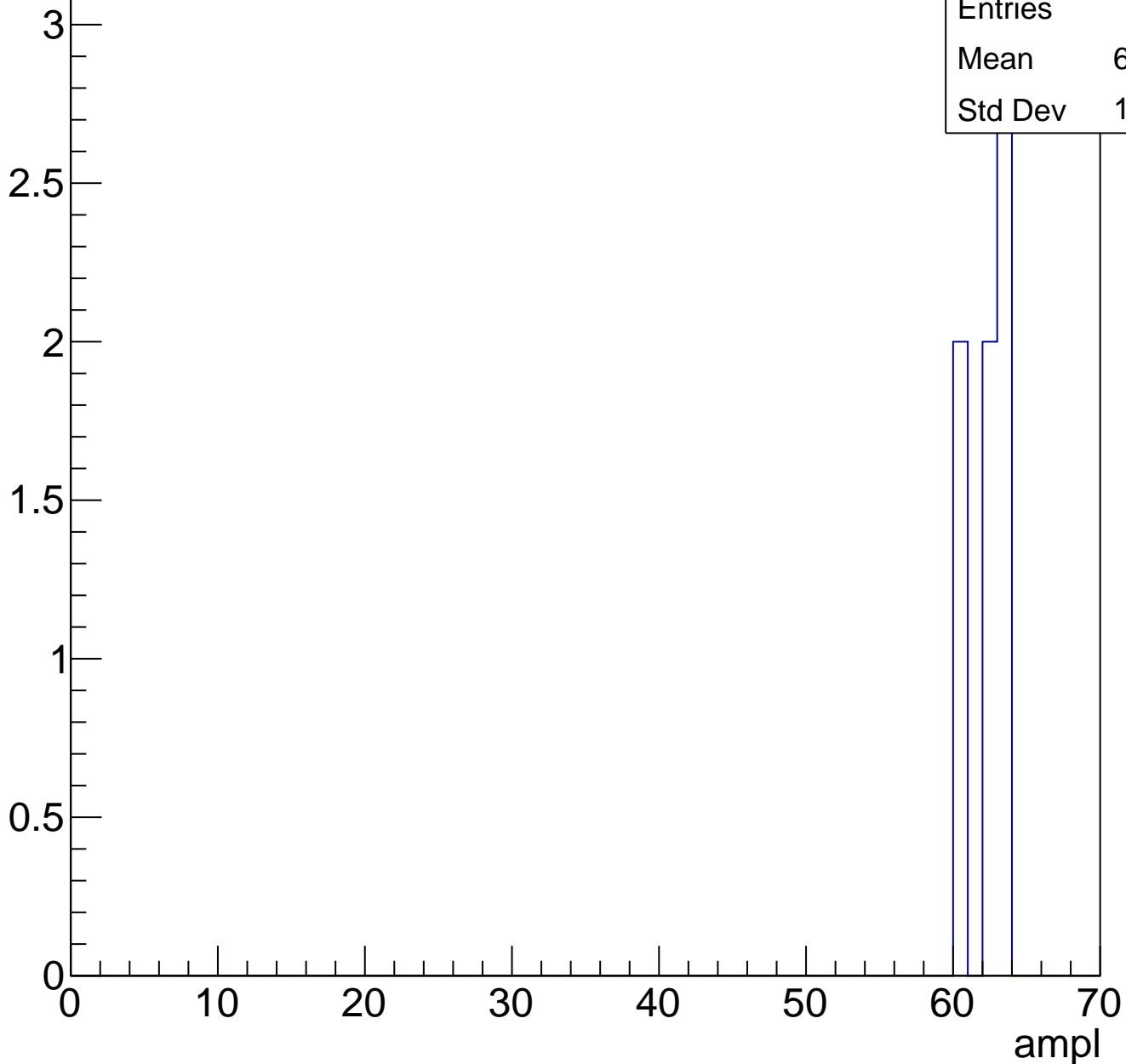
ampl



# B0L101S, U26-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch46, adc0

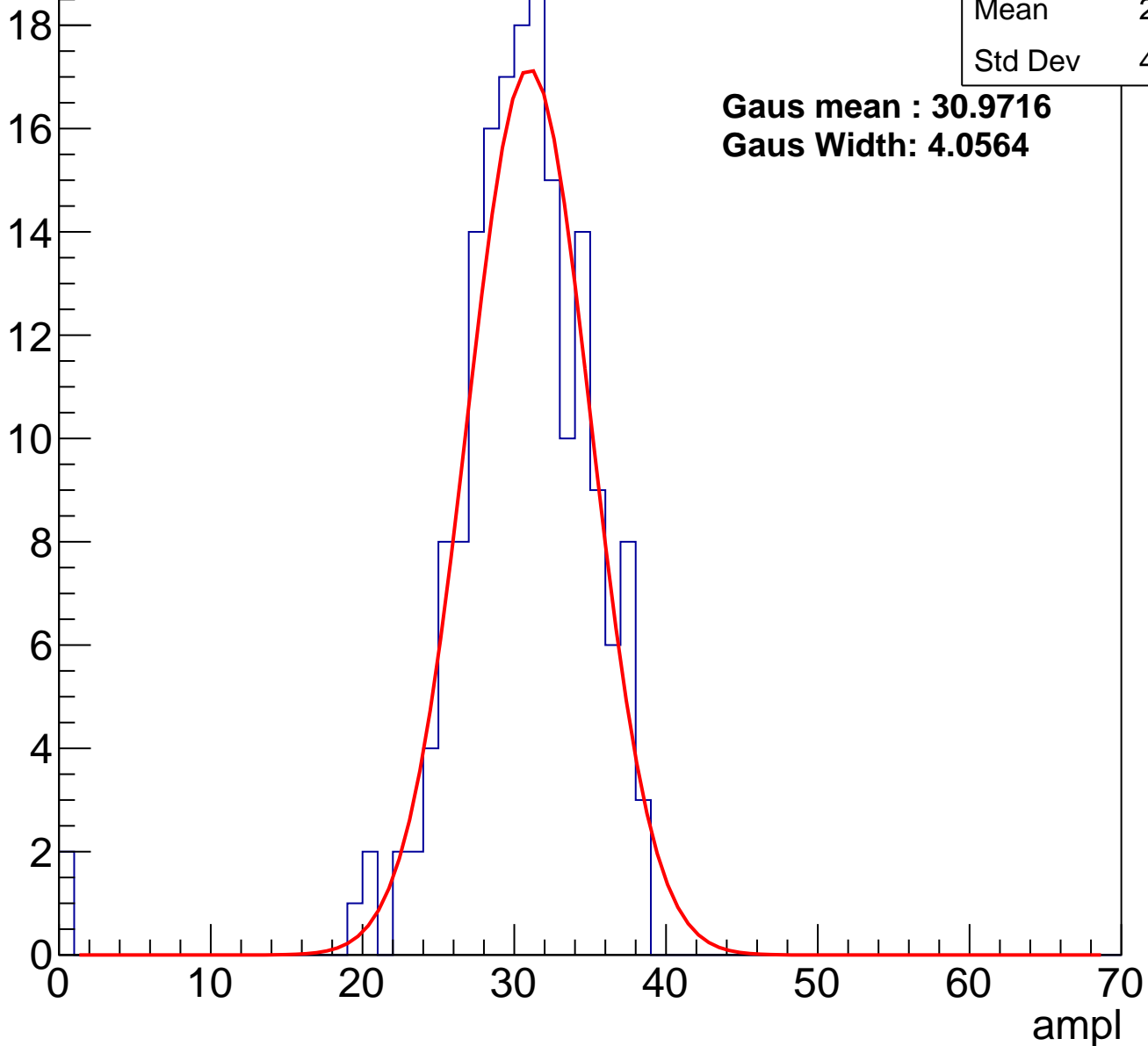
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	178
Mean	29.93
Std Dev	4.996

**Gaus mean : 30.9716**

**Gaus Width: 4.0564**

Entry



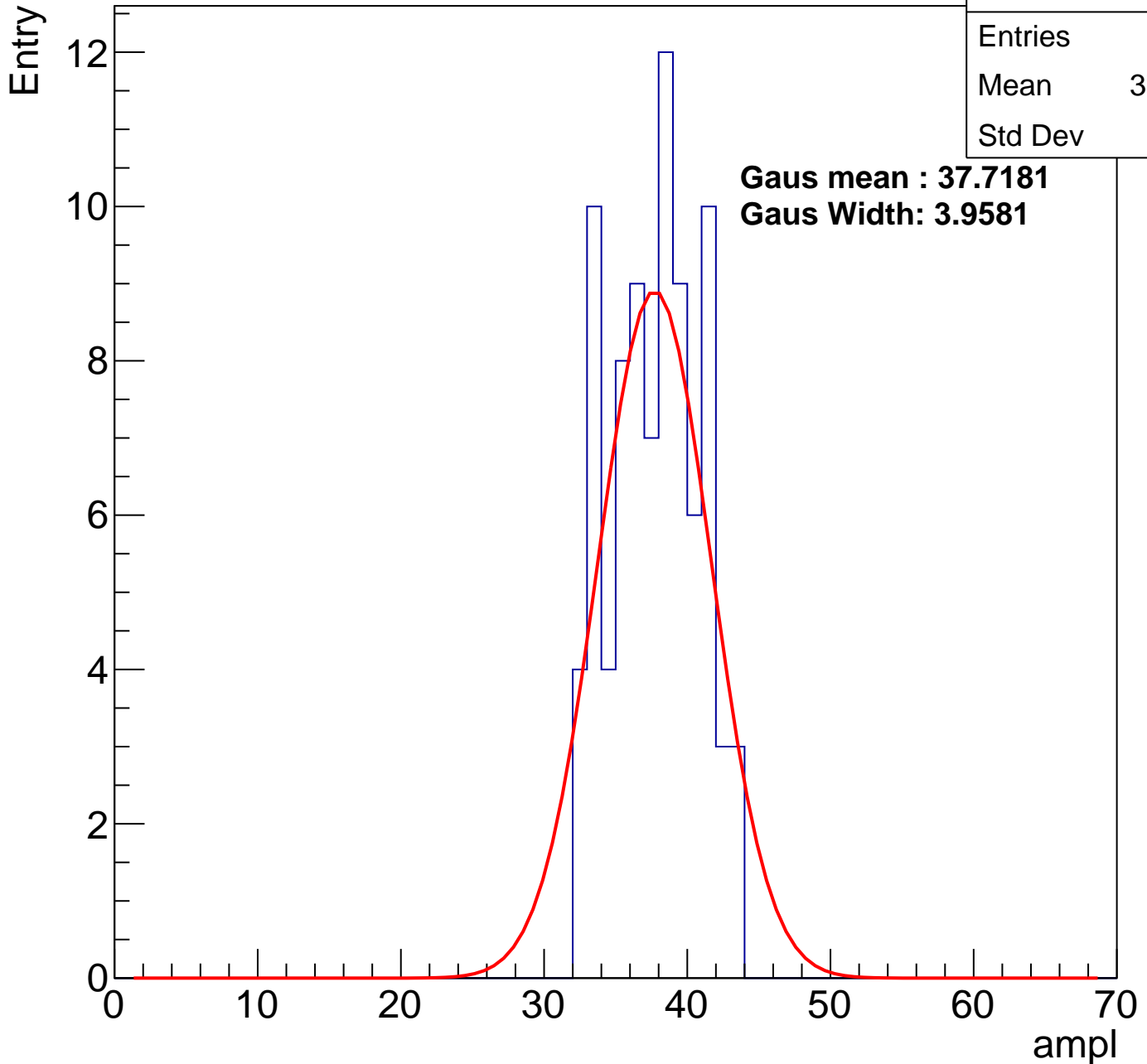
# B0L101S, U26-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	85
Mean	37.28
Std Dev	3.02

**Gaus mean : 37.7181**

**Gaus Width: 3.9581**



# B0L101S, U26-ch46, adc2

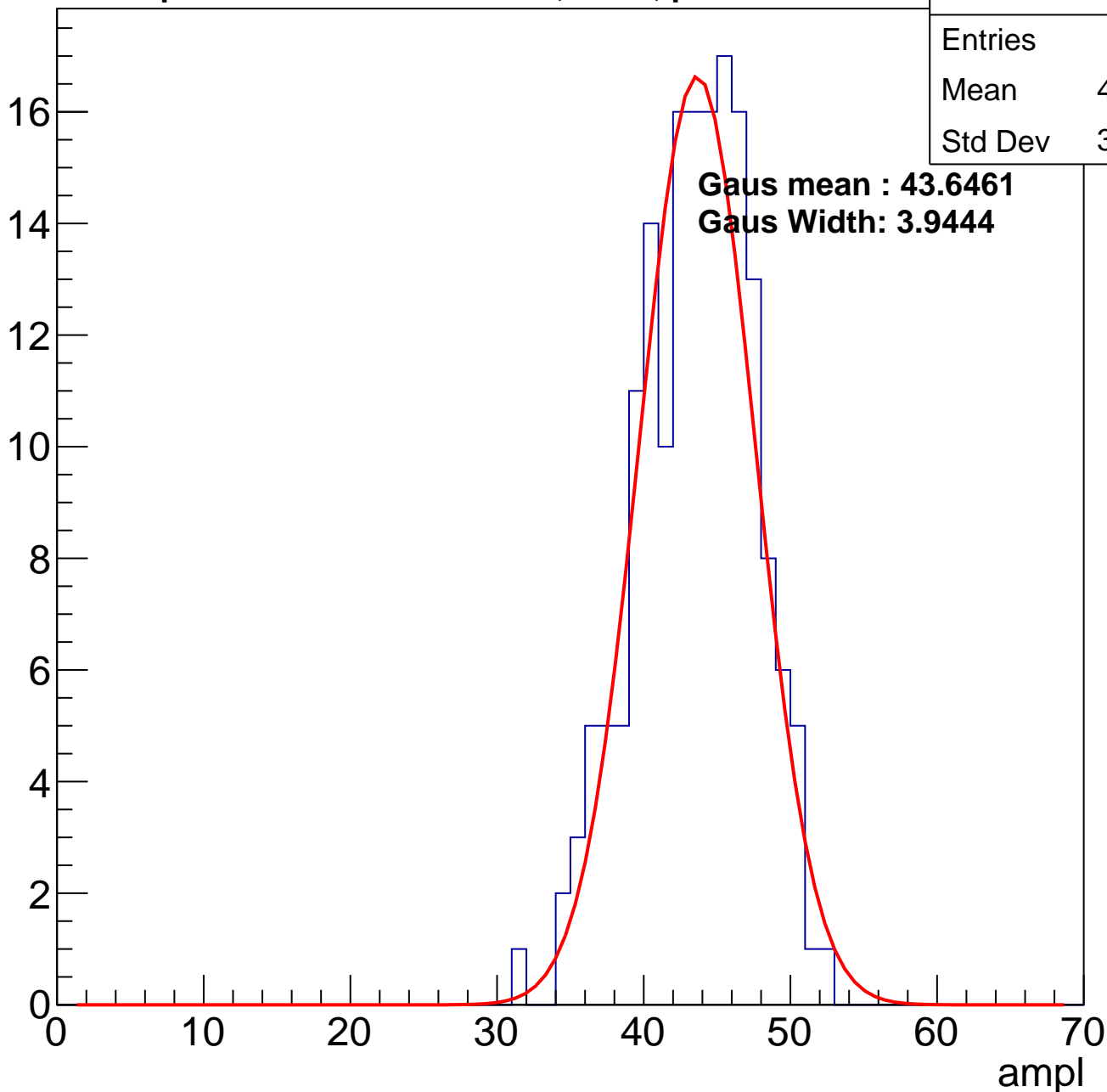
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	43.07
Std Dev	3.935

**Gaus mean : 43.6461**

**Gaus Width: 3.9444**

Entry

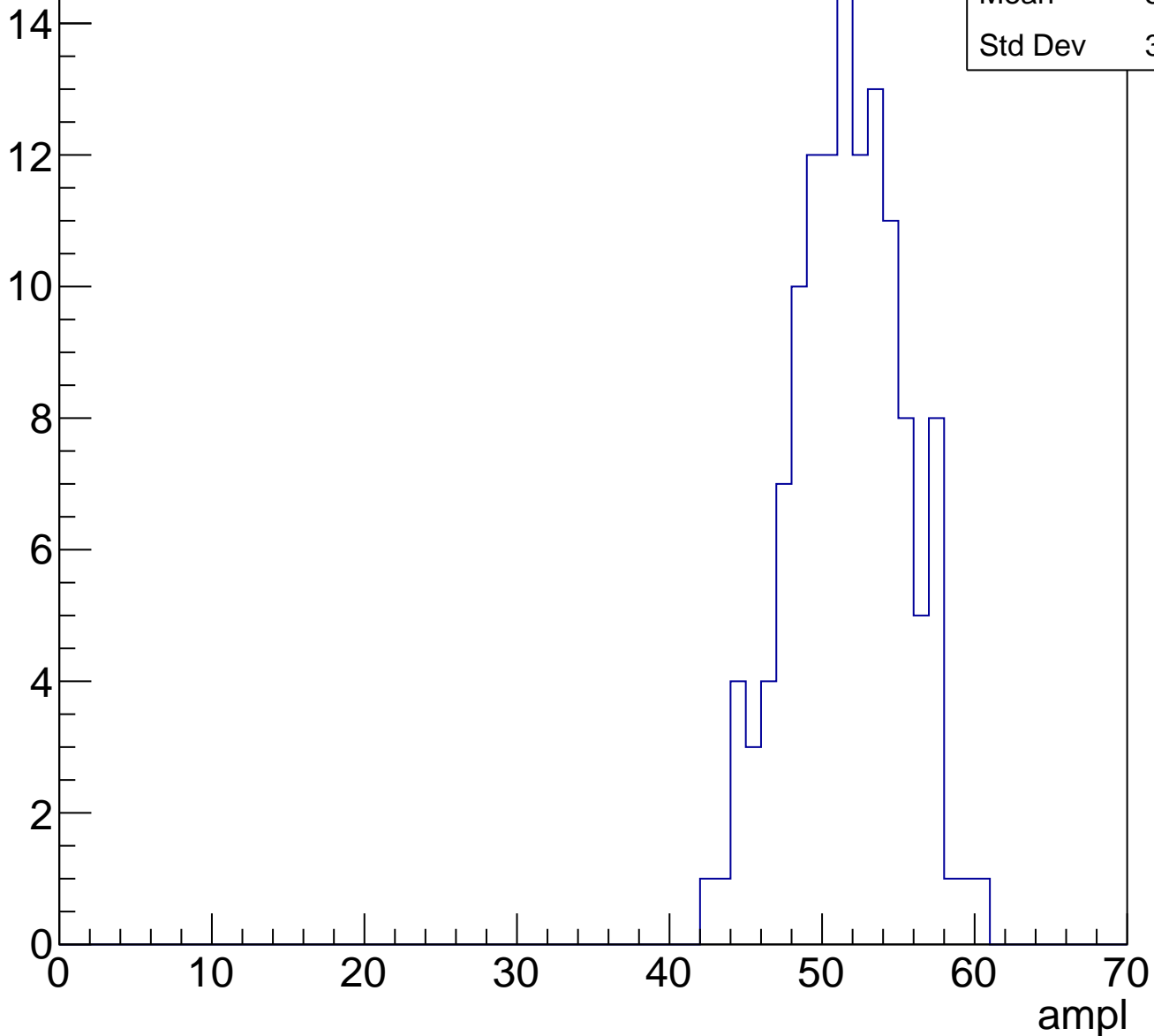


# B0L101S, U26-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	51.18
Std Dev	3.635

Entry



# B0L101S, U26-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	56.83
Std Dev	3.493

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

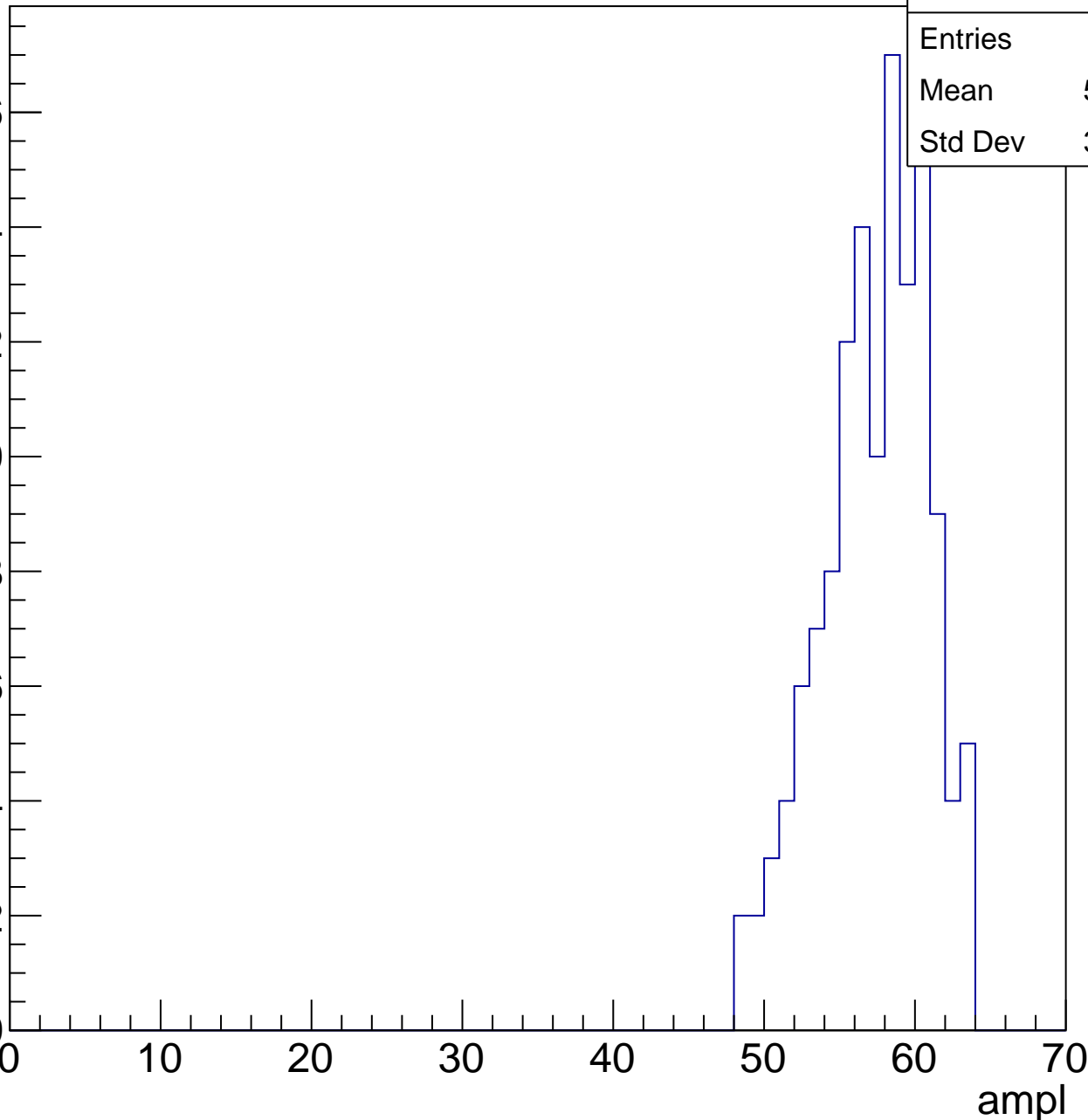
40

50

60

70

ampl



# B0L101S, U26-ch46, adc5

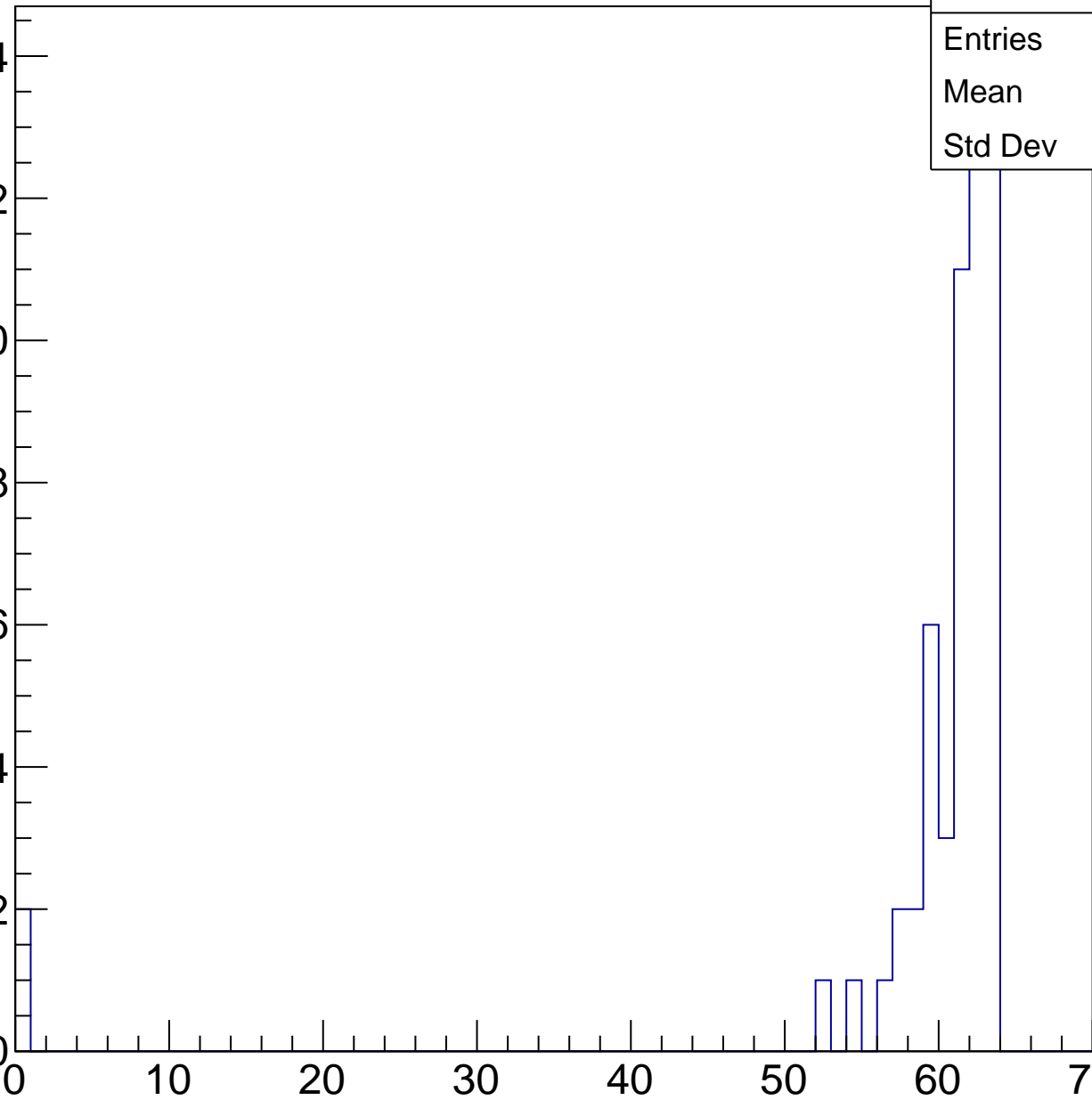
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	56
Mean	58.64
Std Dev	11.52

ampl



# B0L101S, U26-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

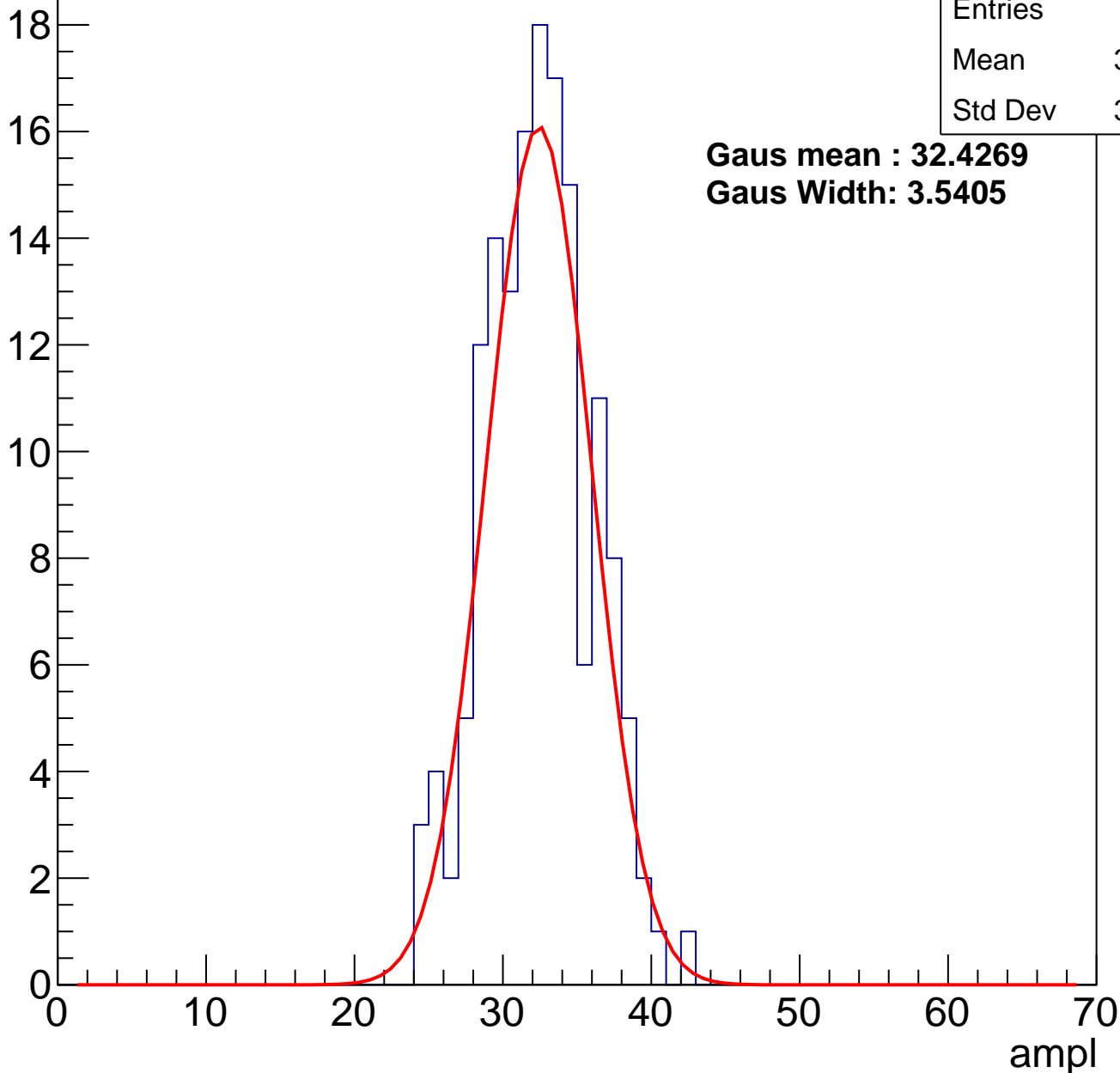
Entry



# B0L101S, U26-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



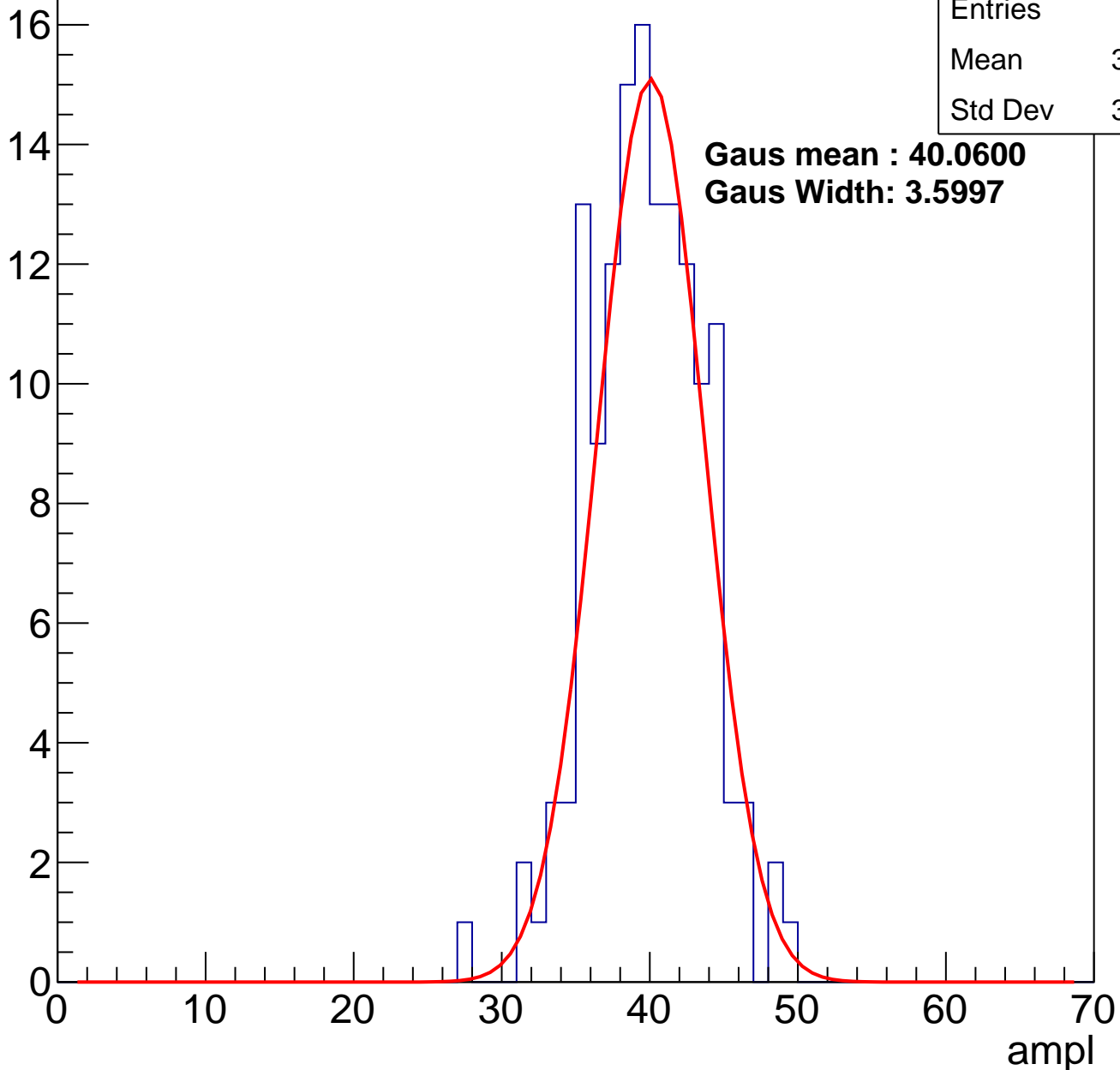
# B0L101S, U26-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	39.36
Std Dev	3.705

**Gaus mean : 40.0600**  
**Gaus Width: 3.5997**

Entry



# B0L101S, U26-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

140

Mean

46.12

Std Dev

3.604

**Gaus mean : 46.4993**

**Gaus Width: 3.5556**

0

10

20

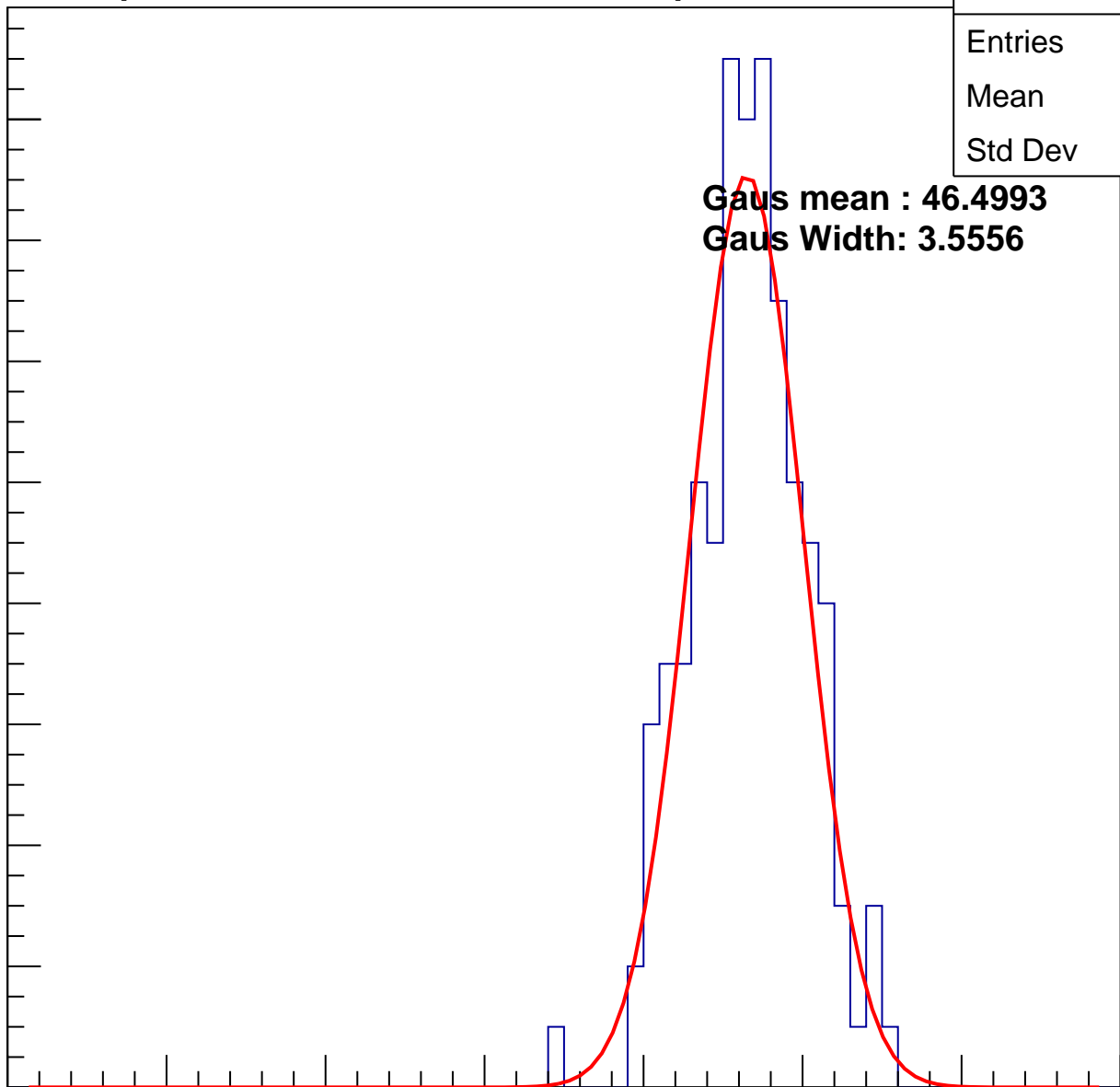
30

40

50

60

ampl



# B0L101S, U26-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

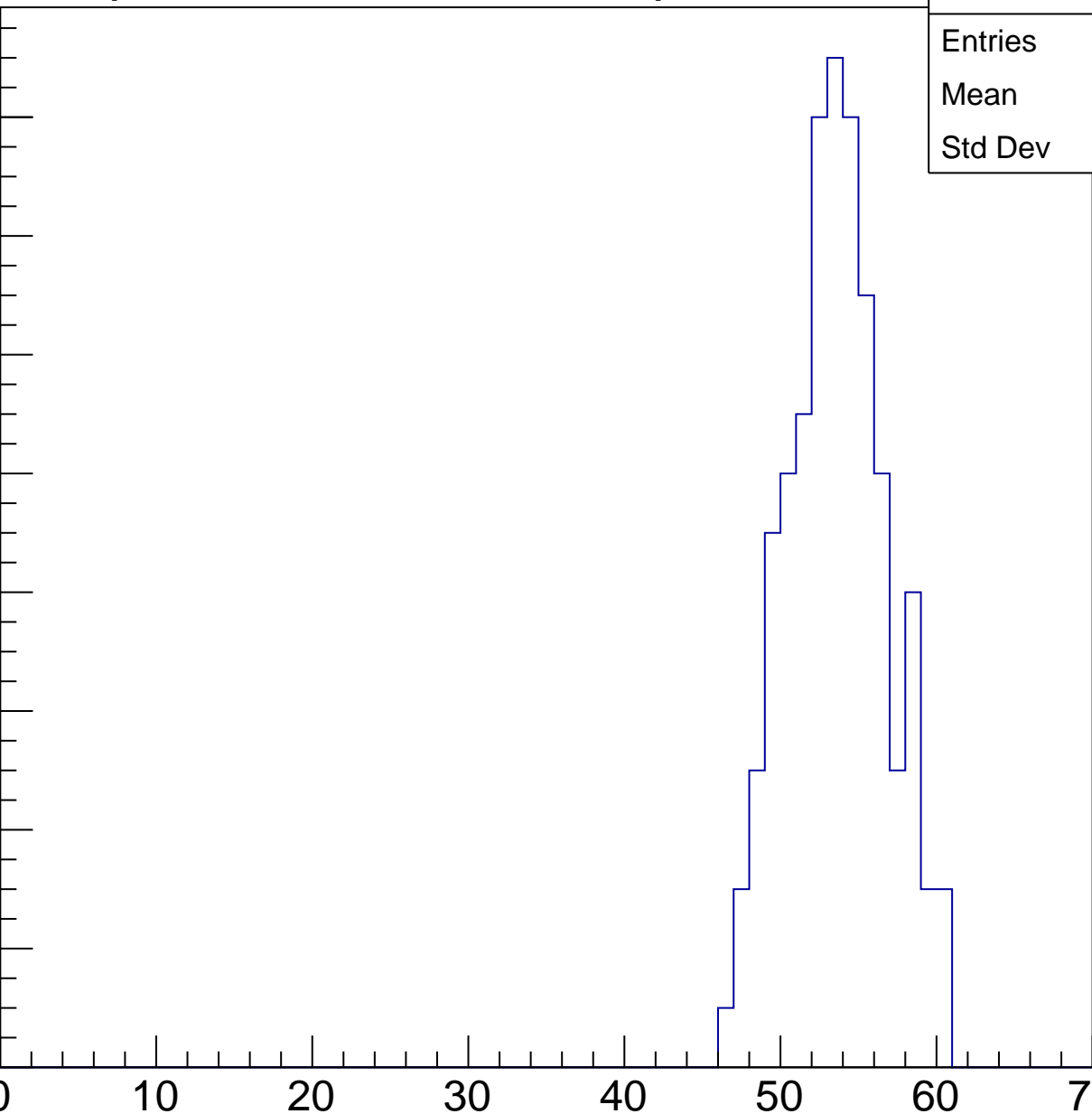
Entries	130
Mean	53.13
Std Dev	3.122

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch47, adc4

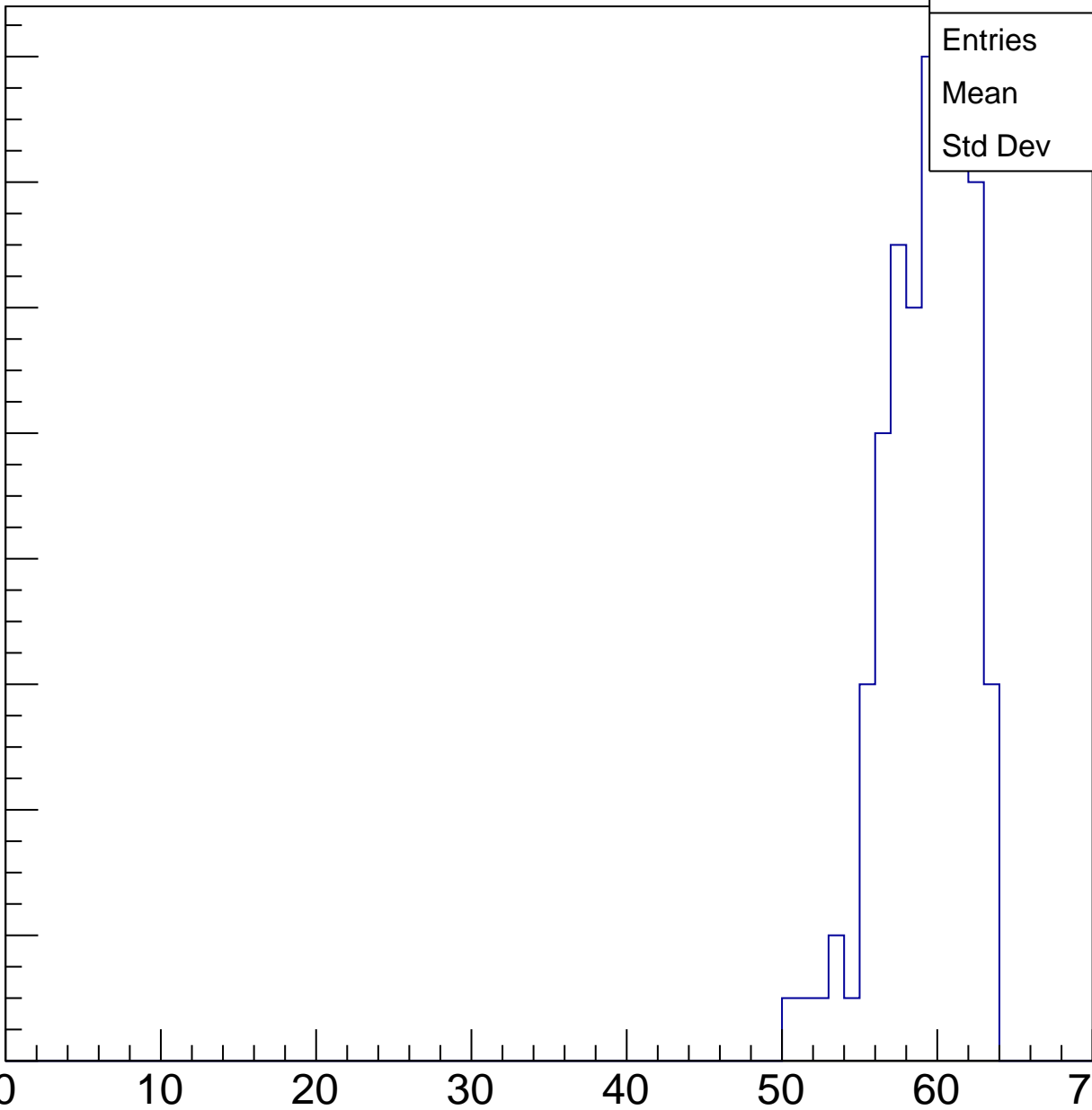
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	114
Mean	58.82
Std Dev	2.707

ampl



# B0L101S, U26-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

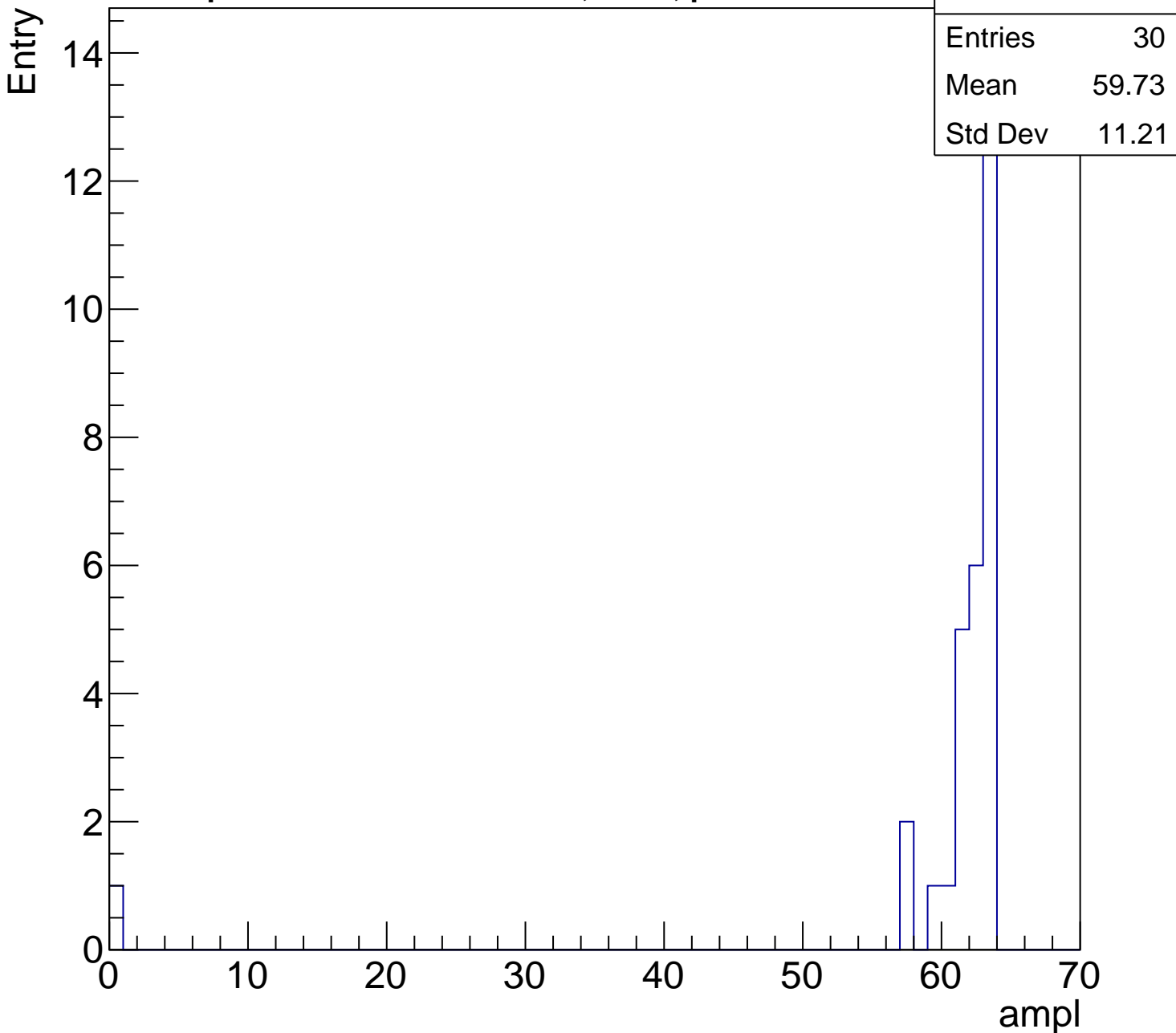
Entries	30
Mean	59.73
Std Dev	11.21

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

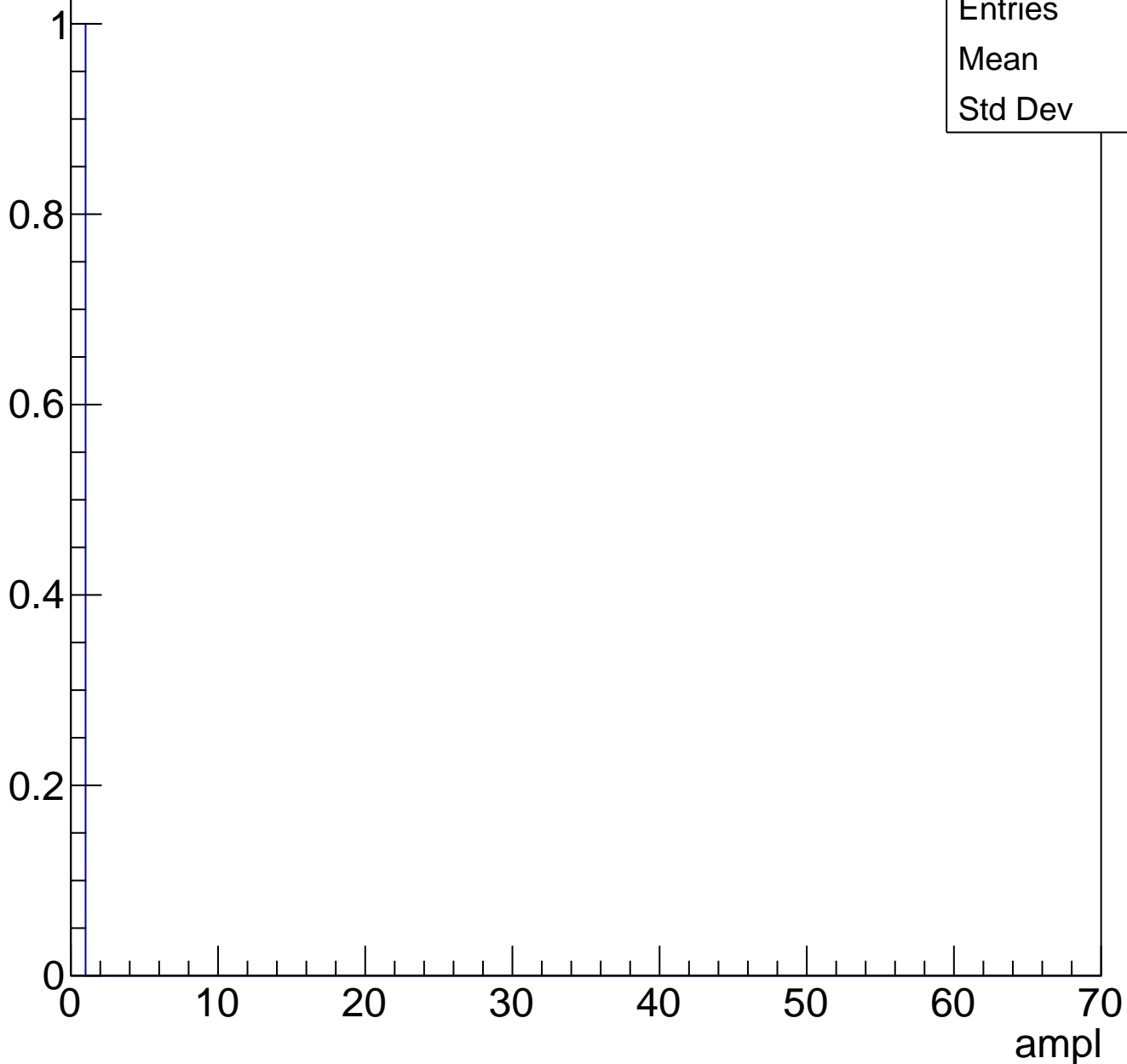
ampl



# B0L101S, U26-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch48, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	30.21
Std Dev	3.798

**Gaus mean : 30.3532**

**Gaus Width: 4.3892**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

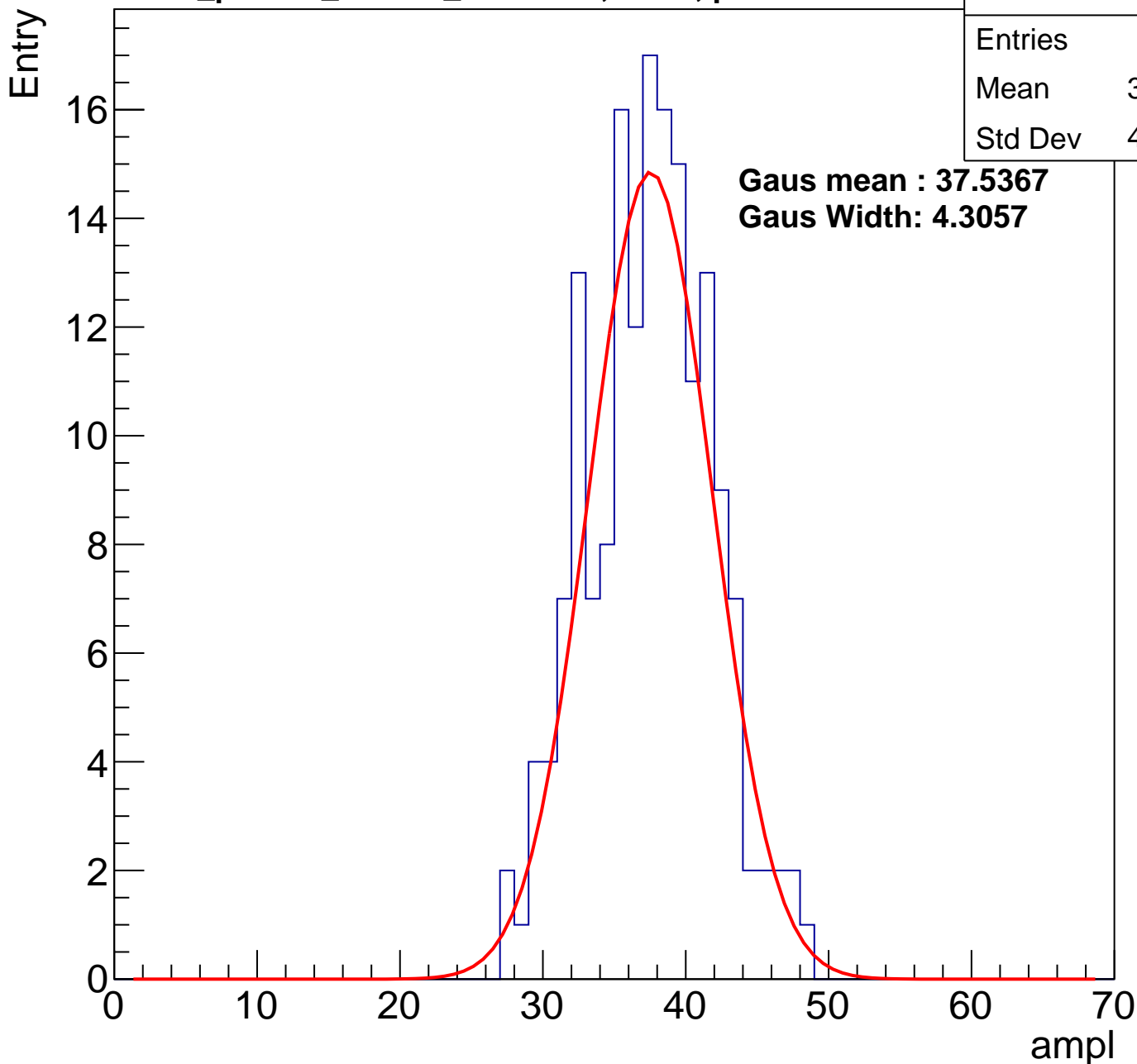
70

# B0L101S, U26-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	37.03
Std Dev	4.284

**Gaus mean : 37.5367**  
**Gaus Width: 4.3057**



# B0L101S, U26-ch48, adc2

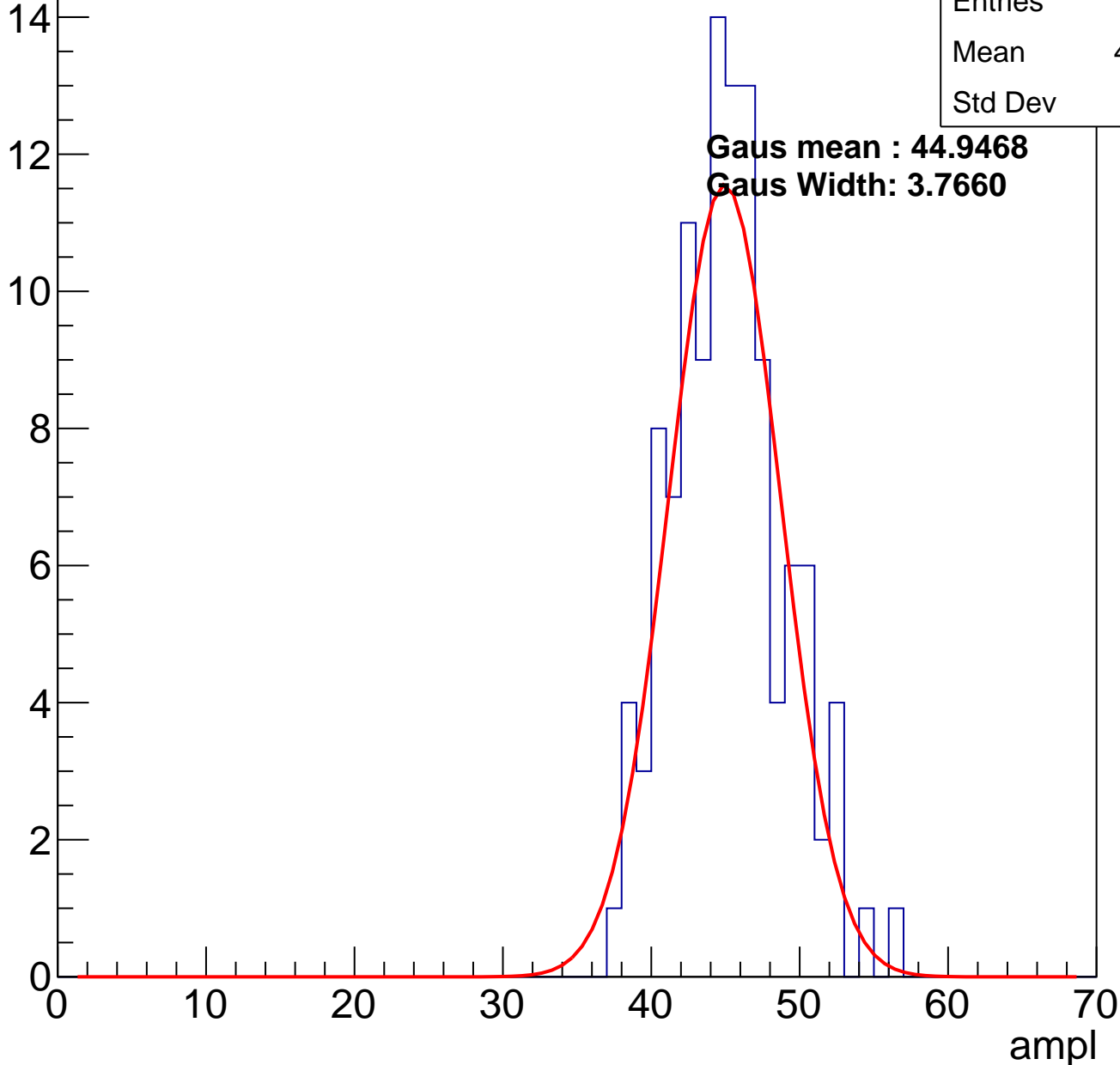
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	116
Mean	44.74
Std Dev	3.74

**Gaus mean : 44.9468**

**Gaus Width: 3.7660**



# B0L101S, U26-ch48, adc3

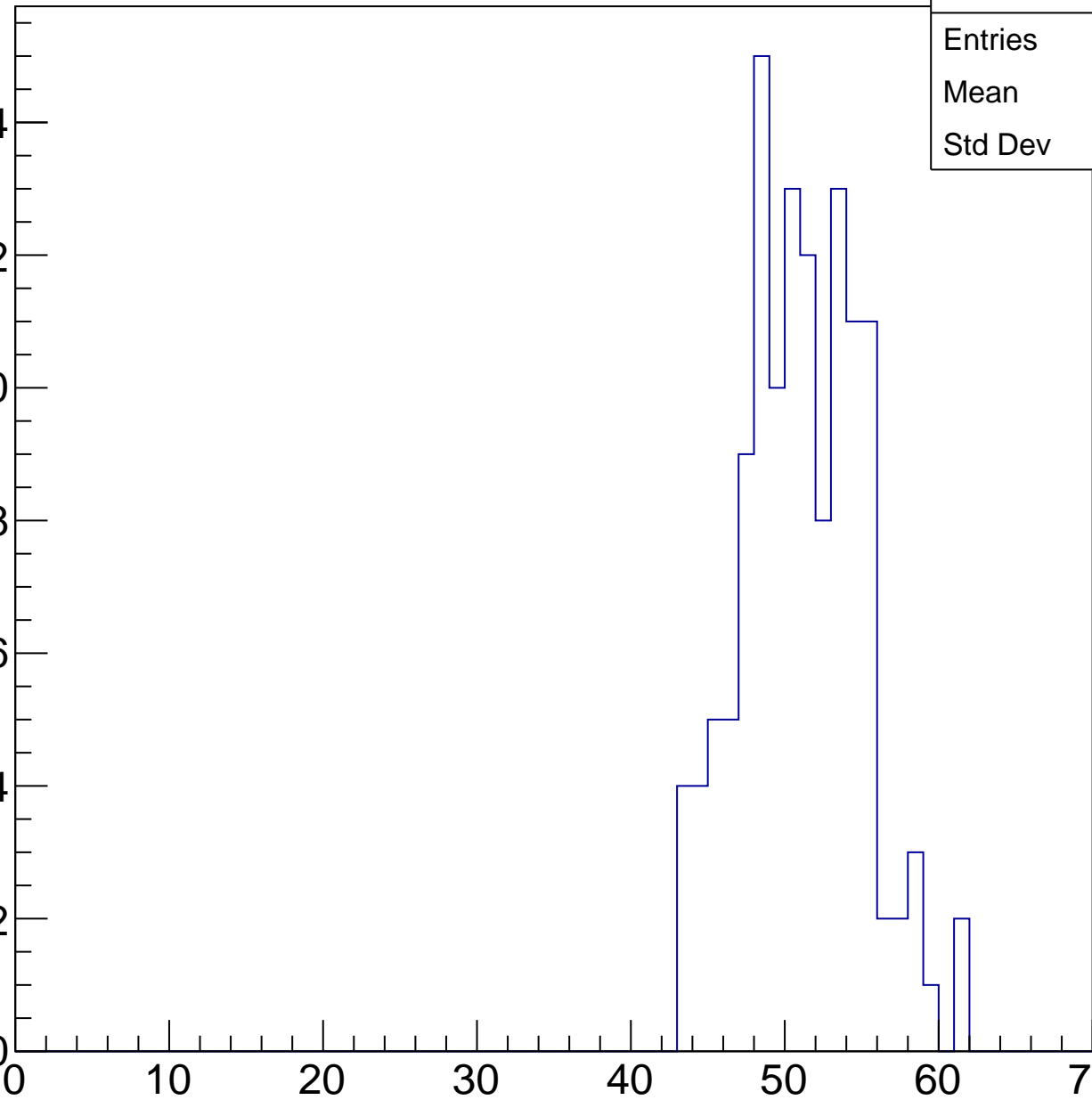
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	50.64
Std Dev	3.863

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch48, adc4

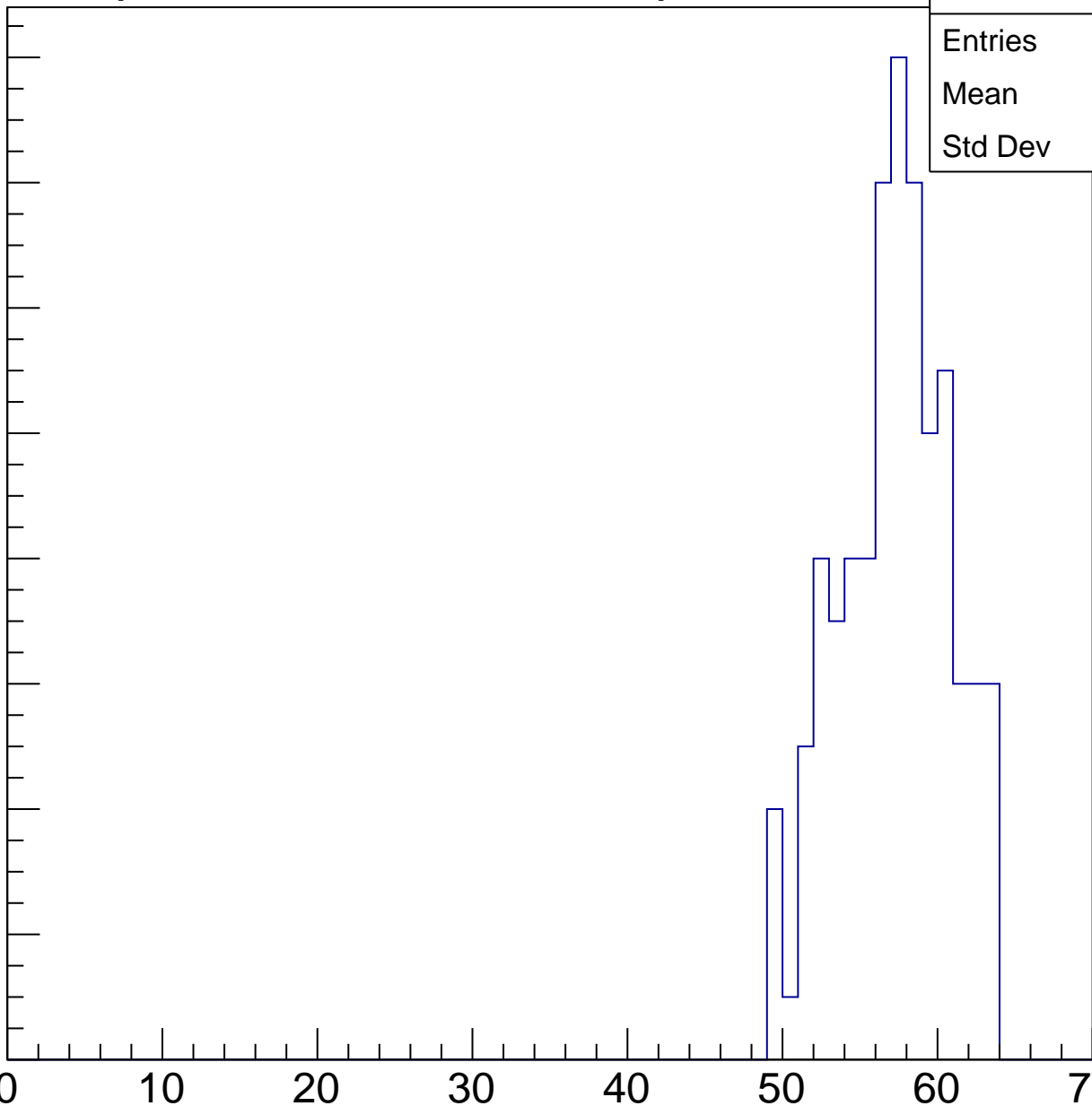
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	124
Mean	56.73
Std Dev	3.502

ampl



# B0L101S, U26-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	65
Mean	59.54
Std Dev	7.804

ampl

0

10

20

30

40

50

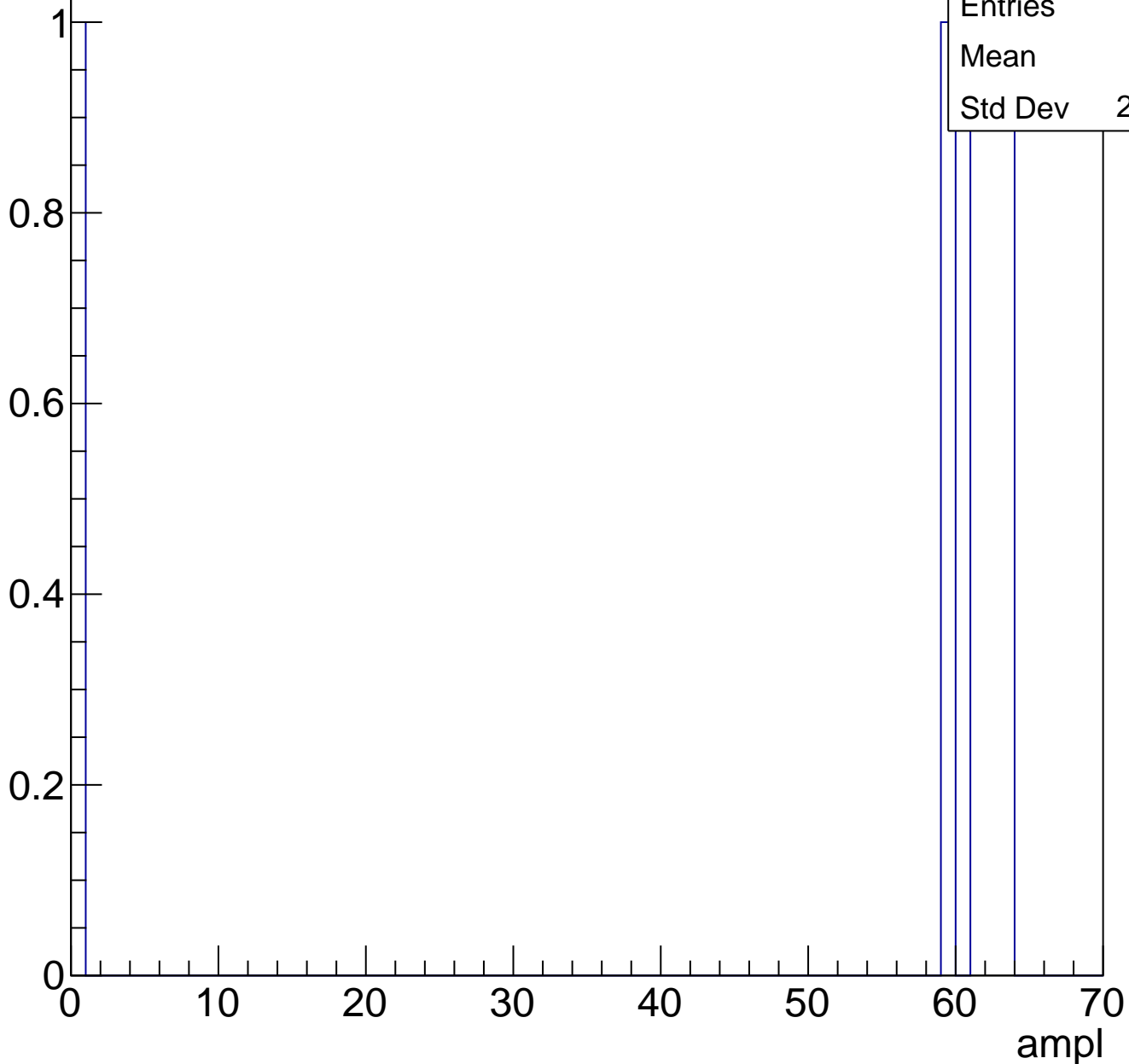
60

70

# B0L101S, U26-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L101S, U26-ch49, adc0

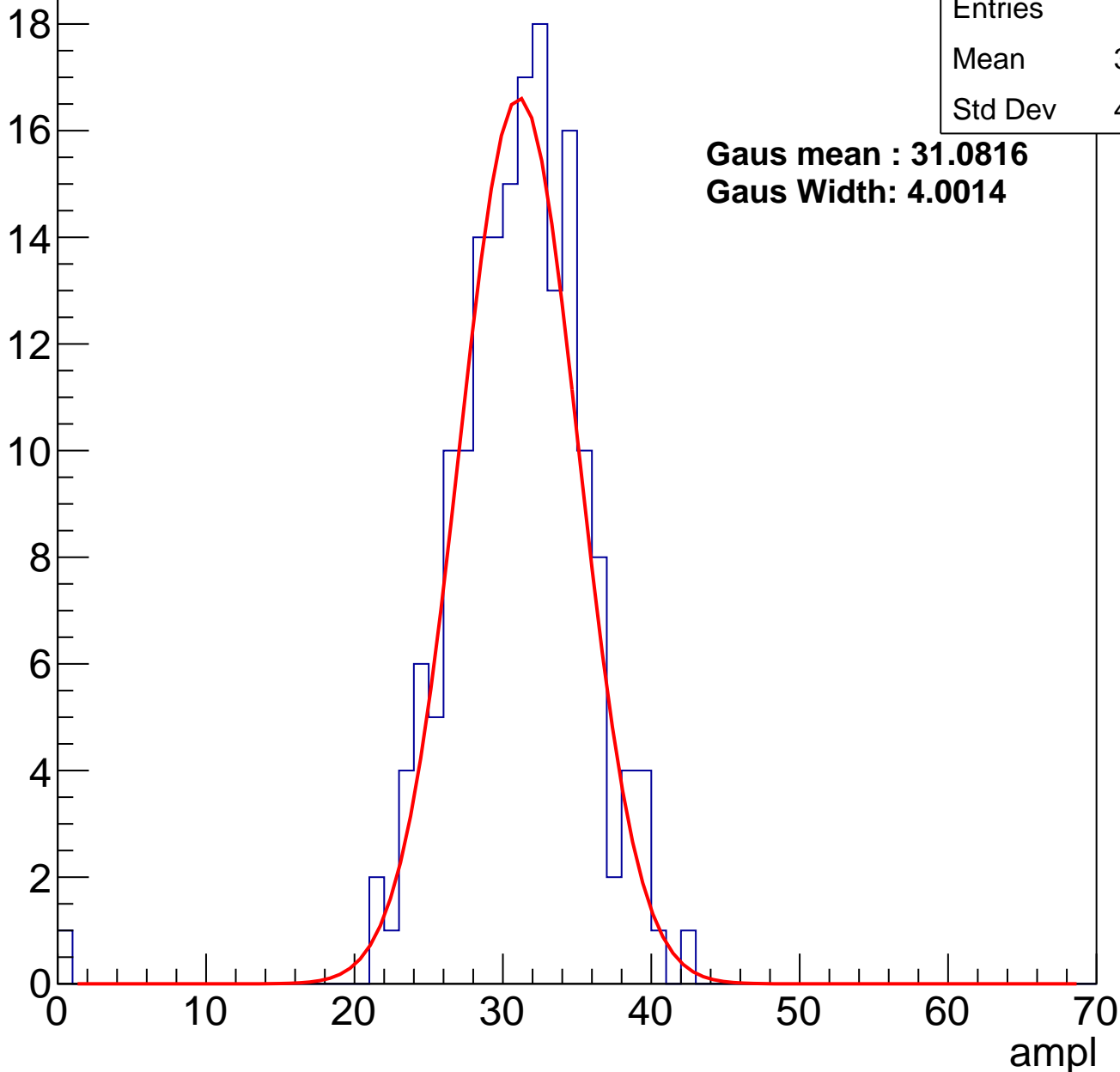
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	176
Mean	30.57
Std Dev	4.675

**Gaus mean : 31.0816**

**Gaus Width: 4.0014**

Entry

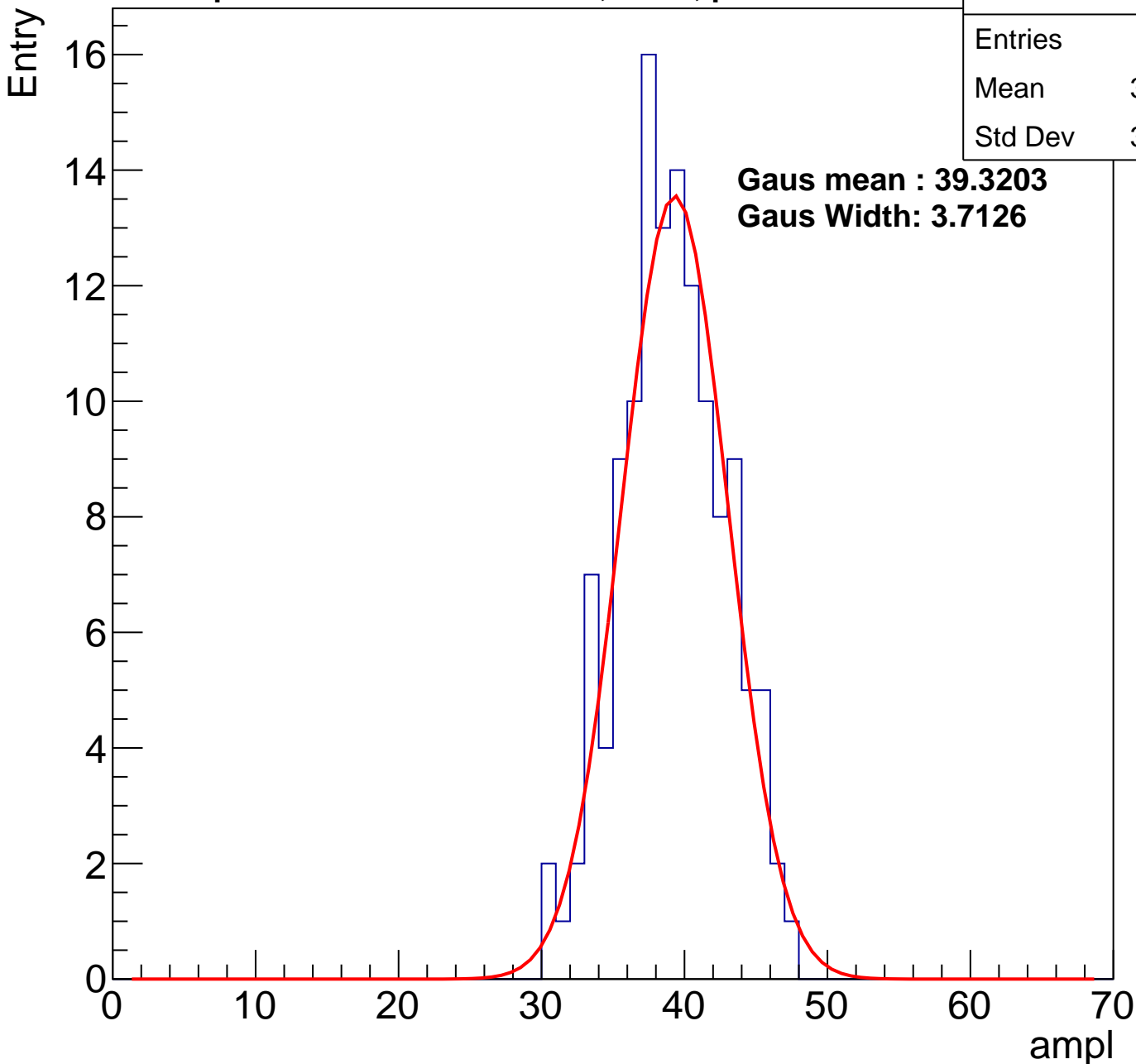


# B0L101S, U26-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	38.66
Std Dev	3.624

**Gaus mean : 39.3203**  
**Gaus Width: 3.7126**



# B0L101S, U26-ch49, adc2

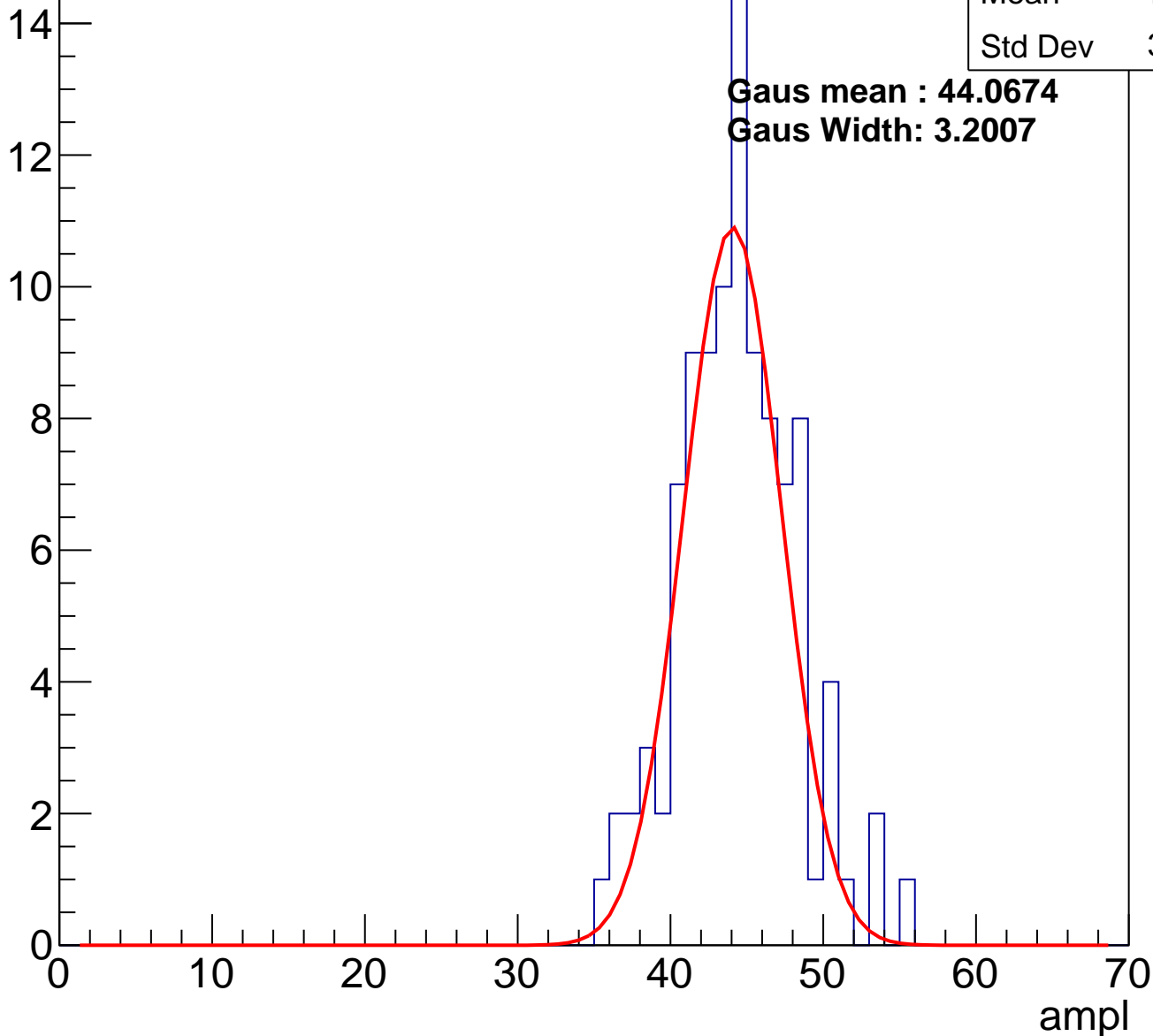
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	101
Mean	43.93
Std Dev	3.771

**Gaus mean : 44.0674**

**Gaus Width: 3.2007**

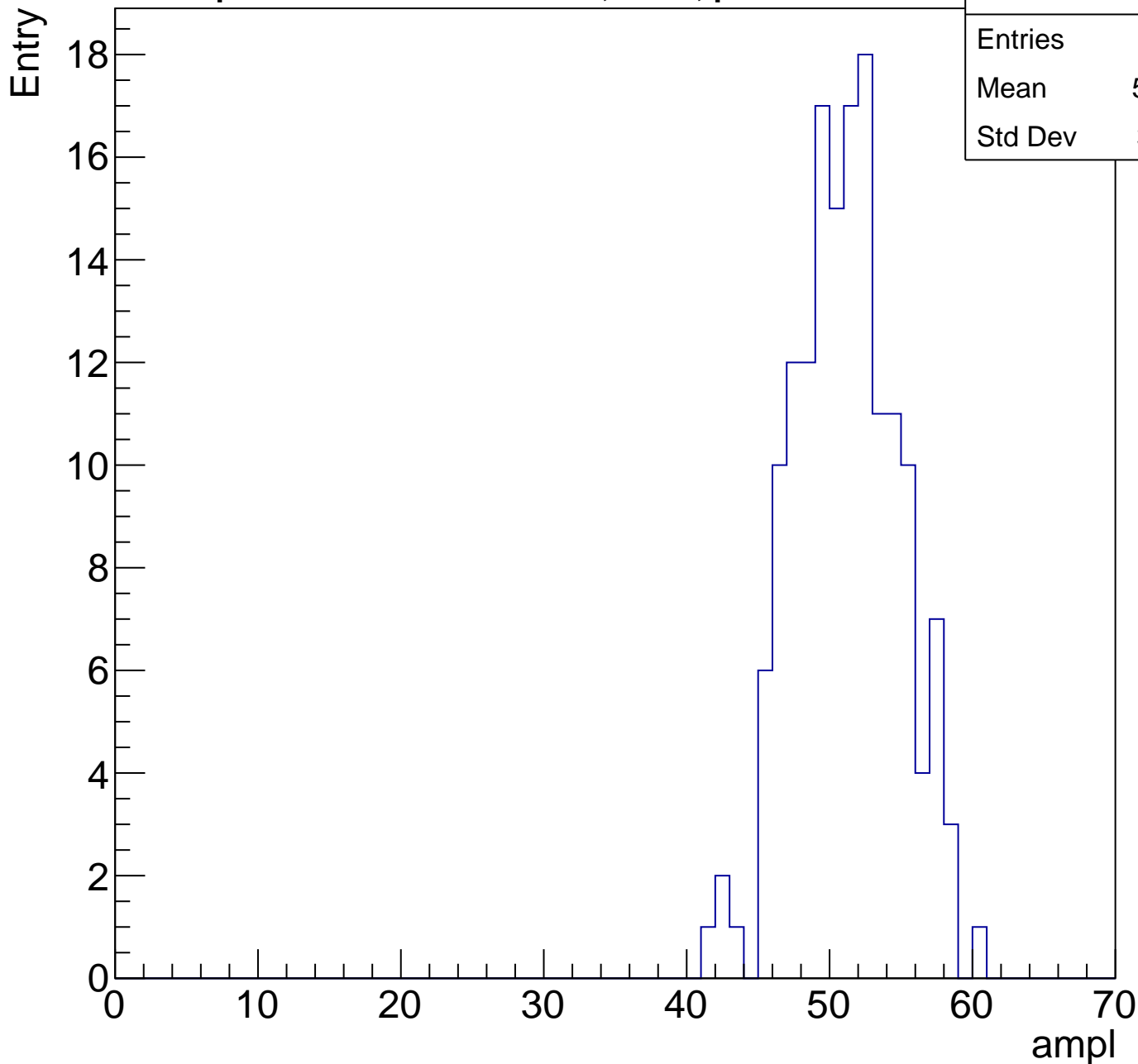
Entry



# B0L101S, U26-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	50.68
Std Dev	3.611



# B0L101S, U26-ch49, adc4

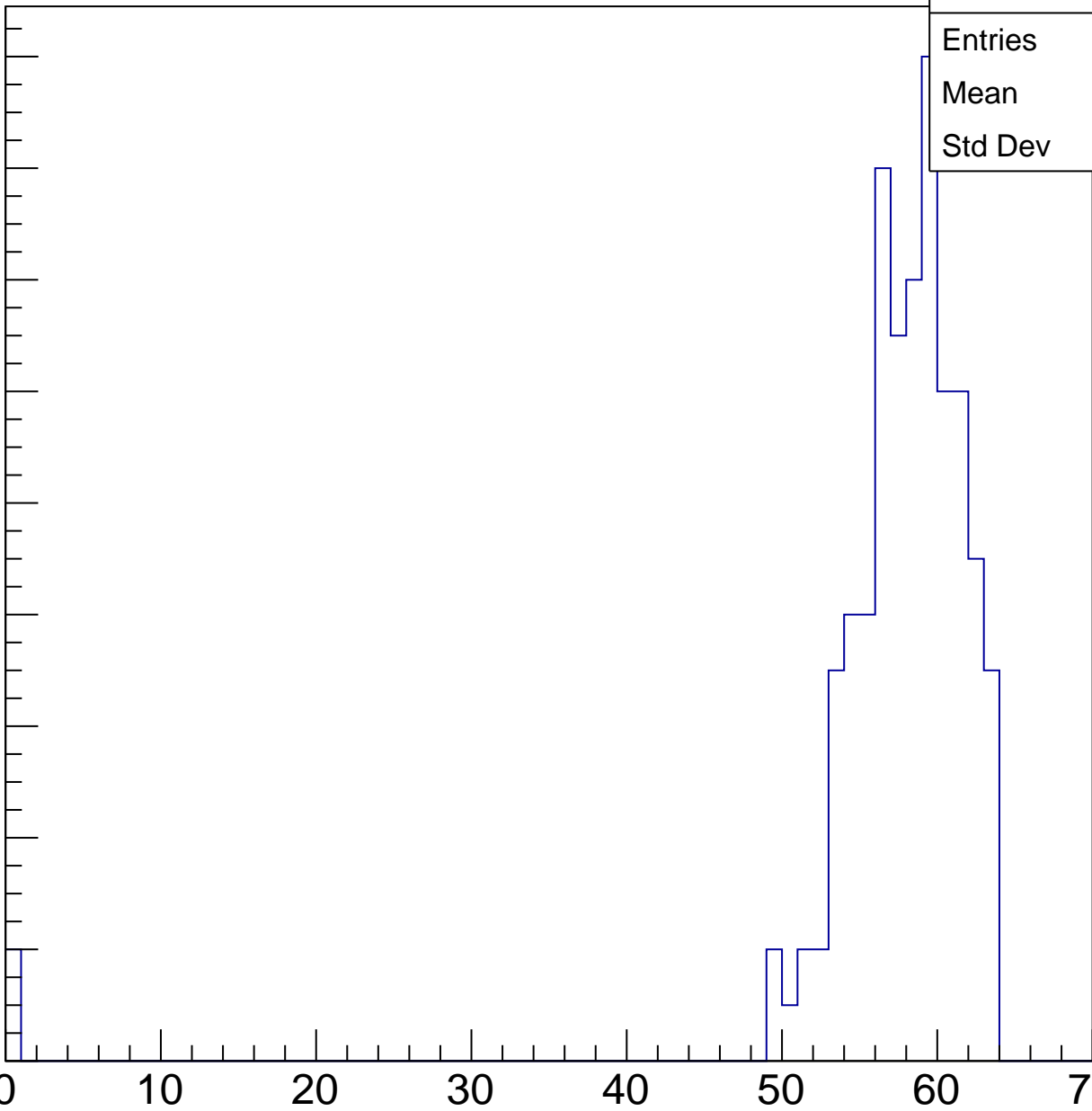
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	133
Mean	56.83
Std Dev	7.701

ampl



# B0L101S, U26-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	61
Std Dev	2.057

# B0L101S, U26-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	26.59
Std Dev	4.655

**Gaus mean : 27.5294**

**Gaus Width: 3.4605**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

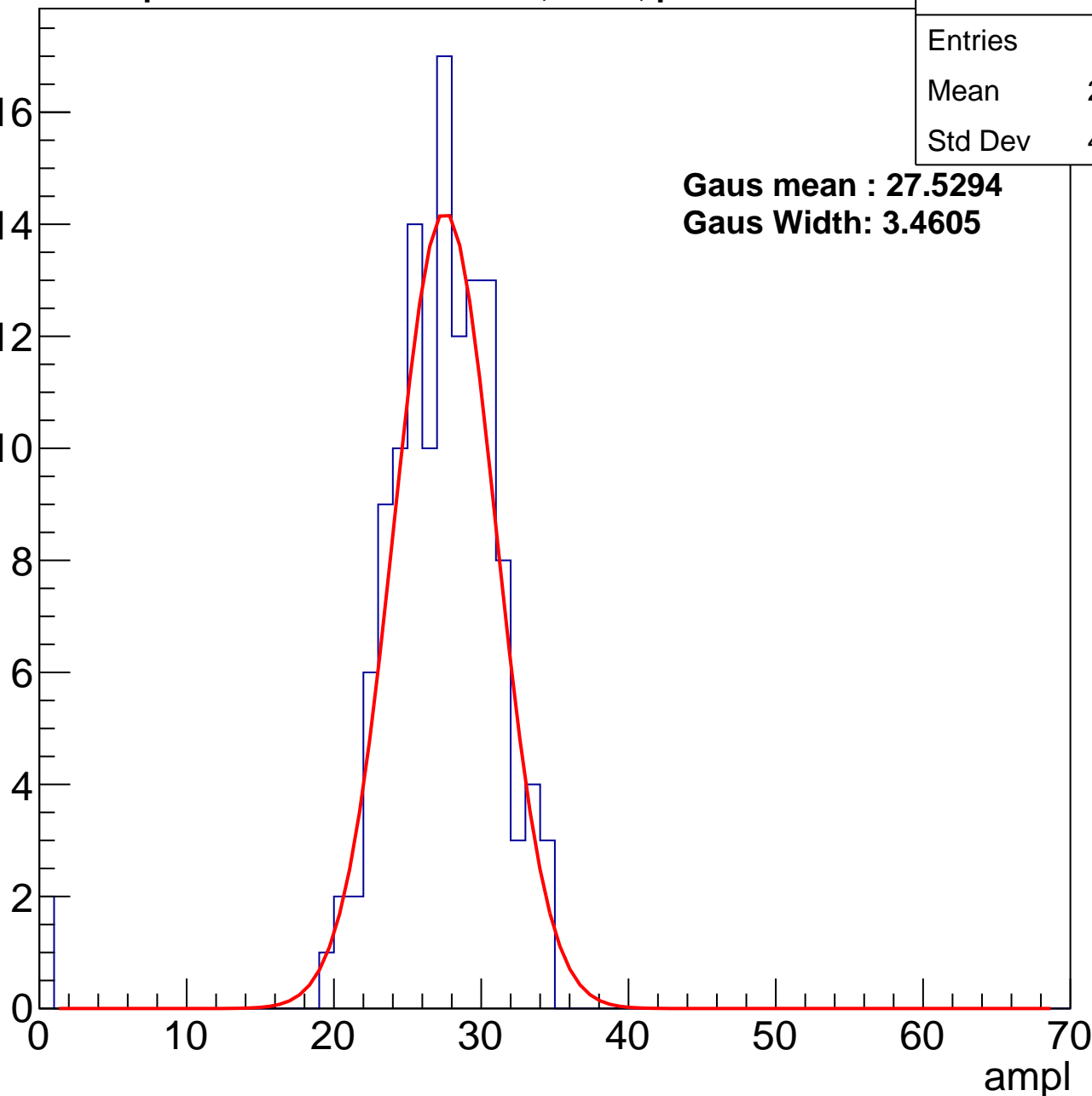
40

50

60

70

ampl



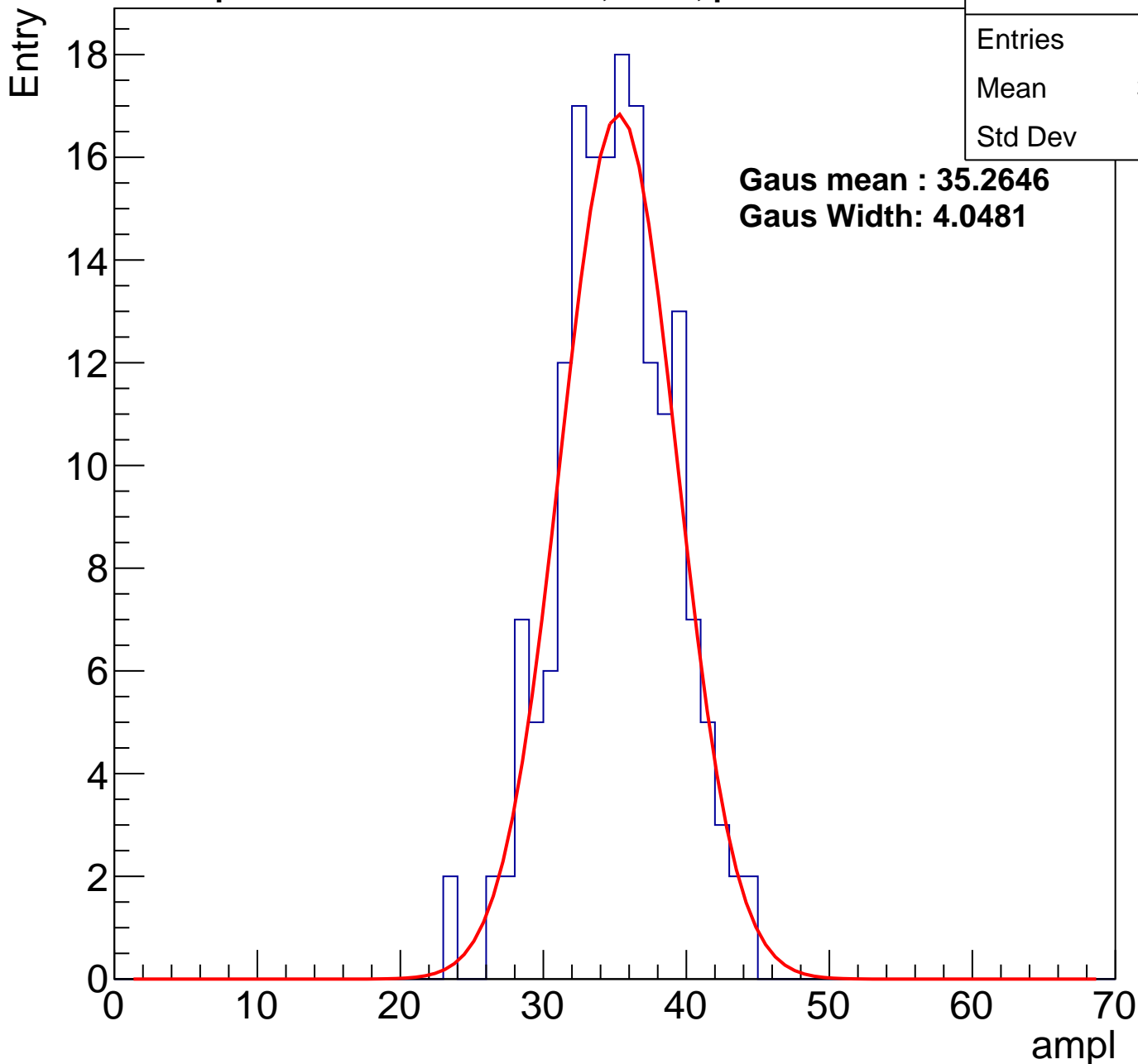
# B0L101S, U26-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	34.61
Std Dev	4.01

**Gaus mean : 35.2646**

**Gaus Width: 4.0481**



# B0L101S, U26-ch50, adc2

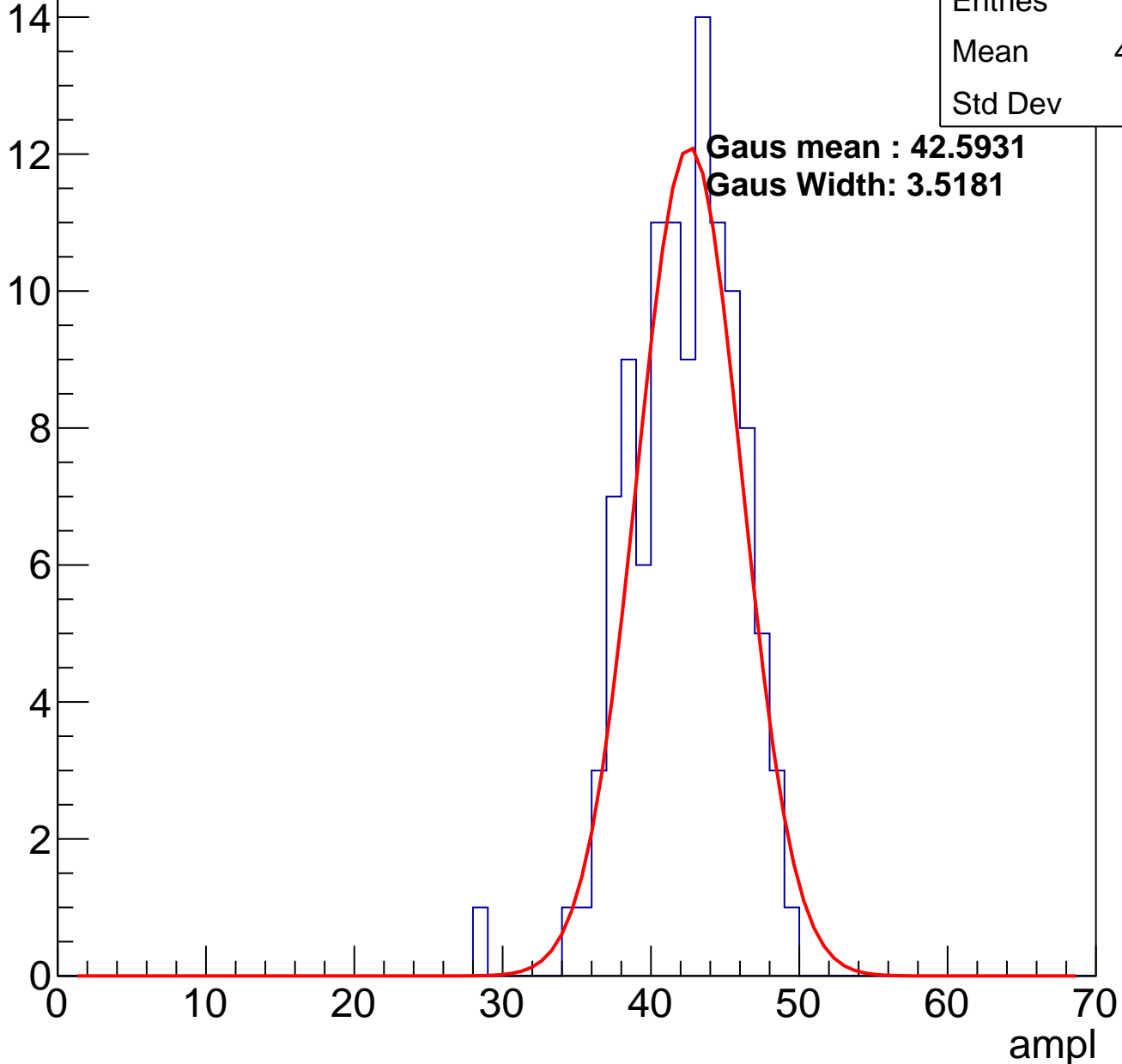
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	111
Mean	41.81
Std Dev	3.55

**Gaus mean : 42.5931**

**Gaus Width: 3.5181**

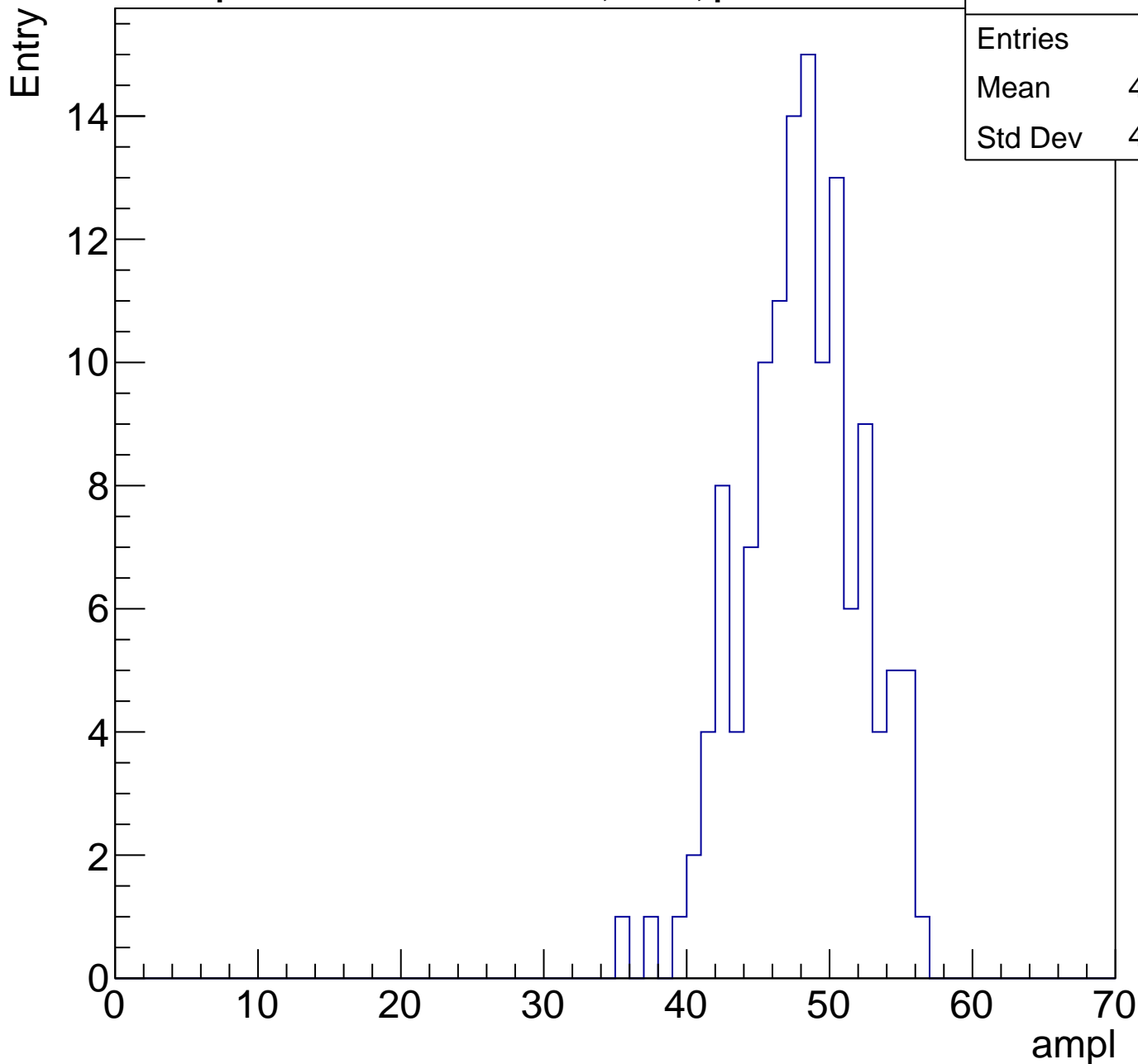
Entry



# B0L101S, U26-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

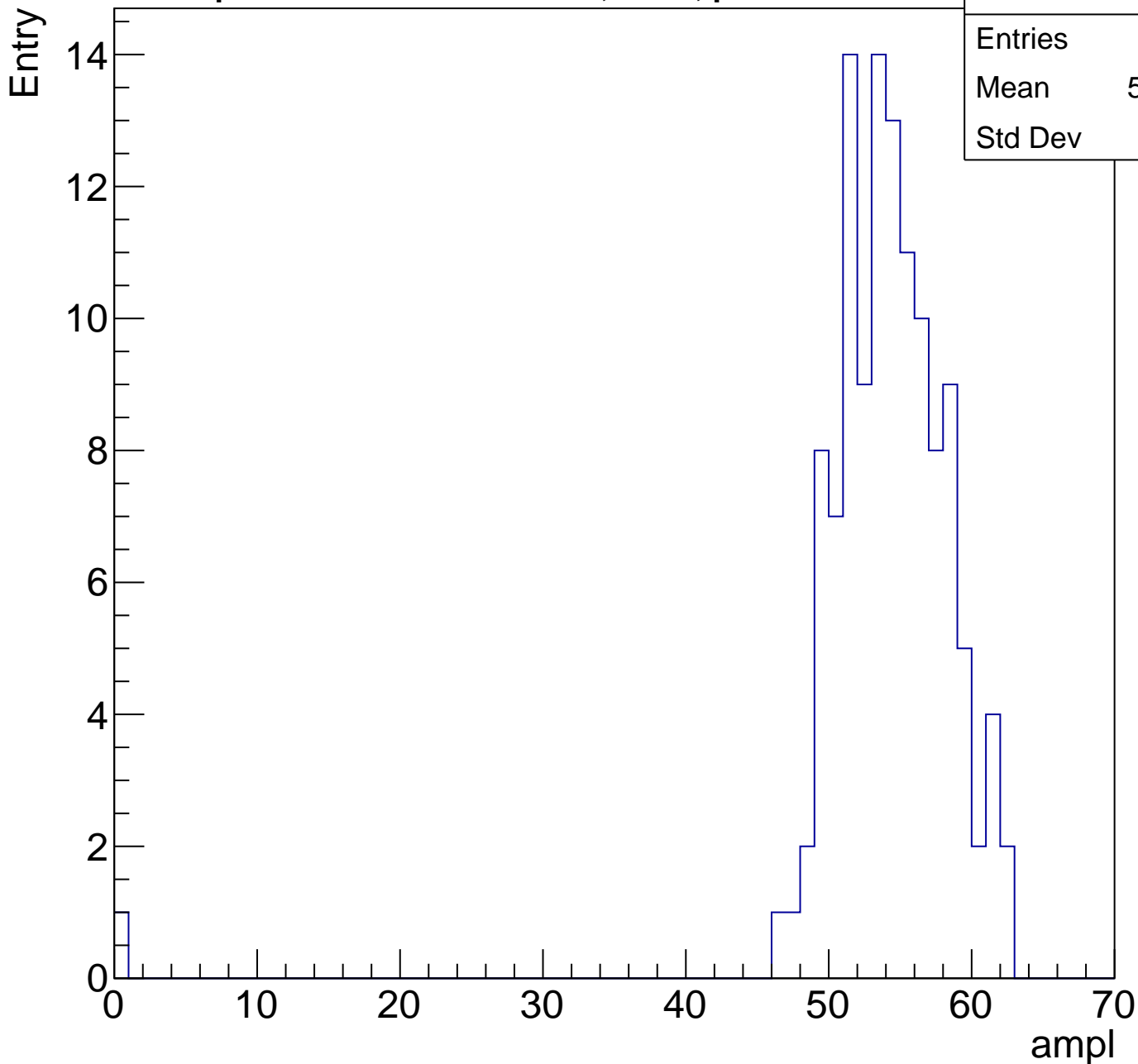
Entries	131
Mean	47.57
Std Dev	4.068



# B0L101S, U26-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	53.58
Std Dev	6



# B0L101S, U26-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries	109
Mean	58.61
Std Dev	6.359

ampl

0

10

20

30

40

50

60

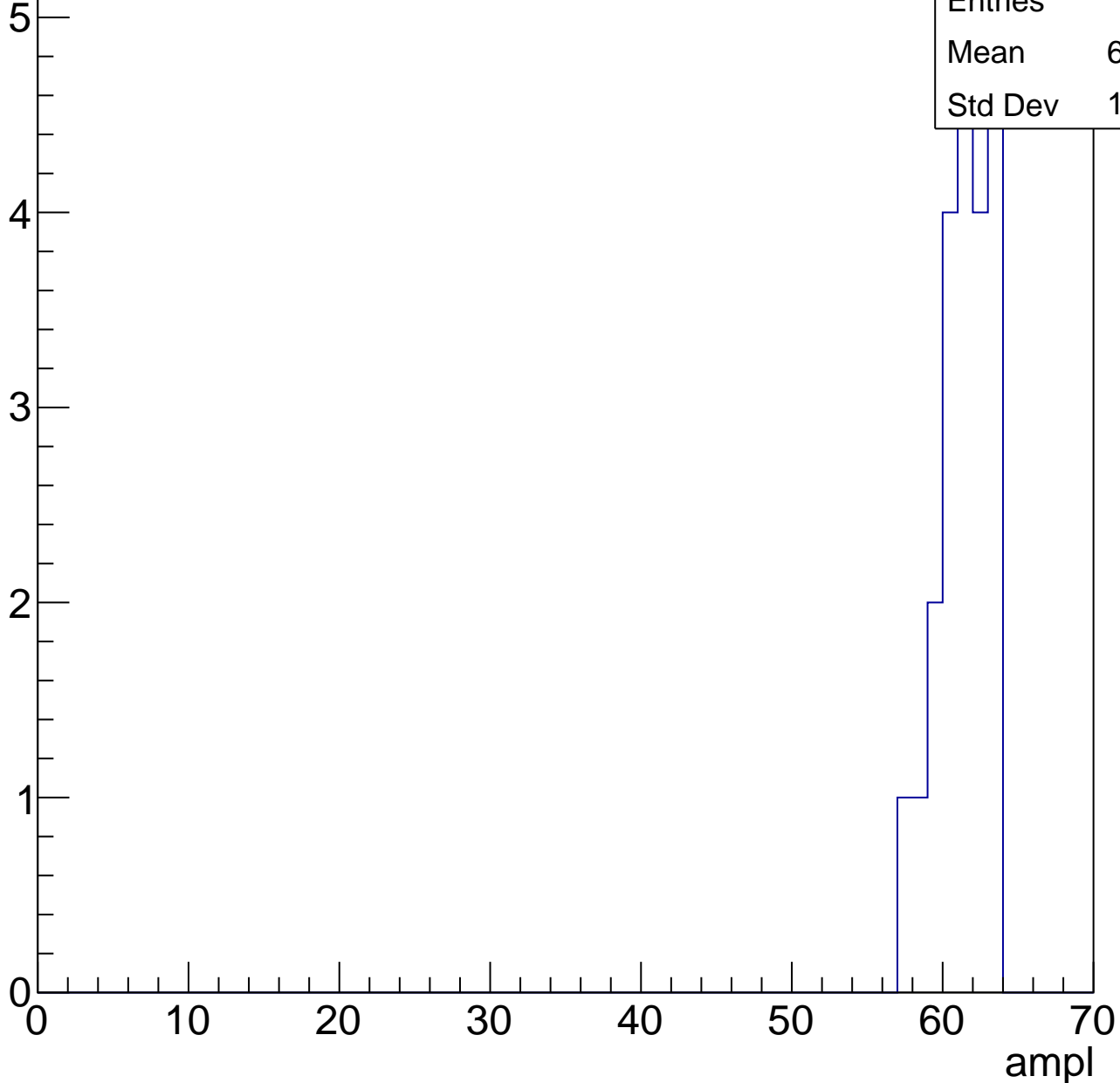
70

# B0L101S, U26-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	22
Mean	60.95
Std Dev	1.665





# B0L101S, U26-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch51, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

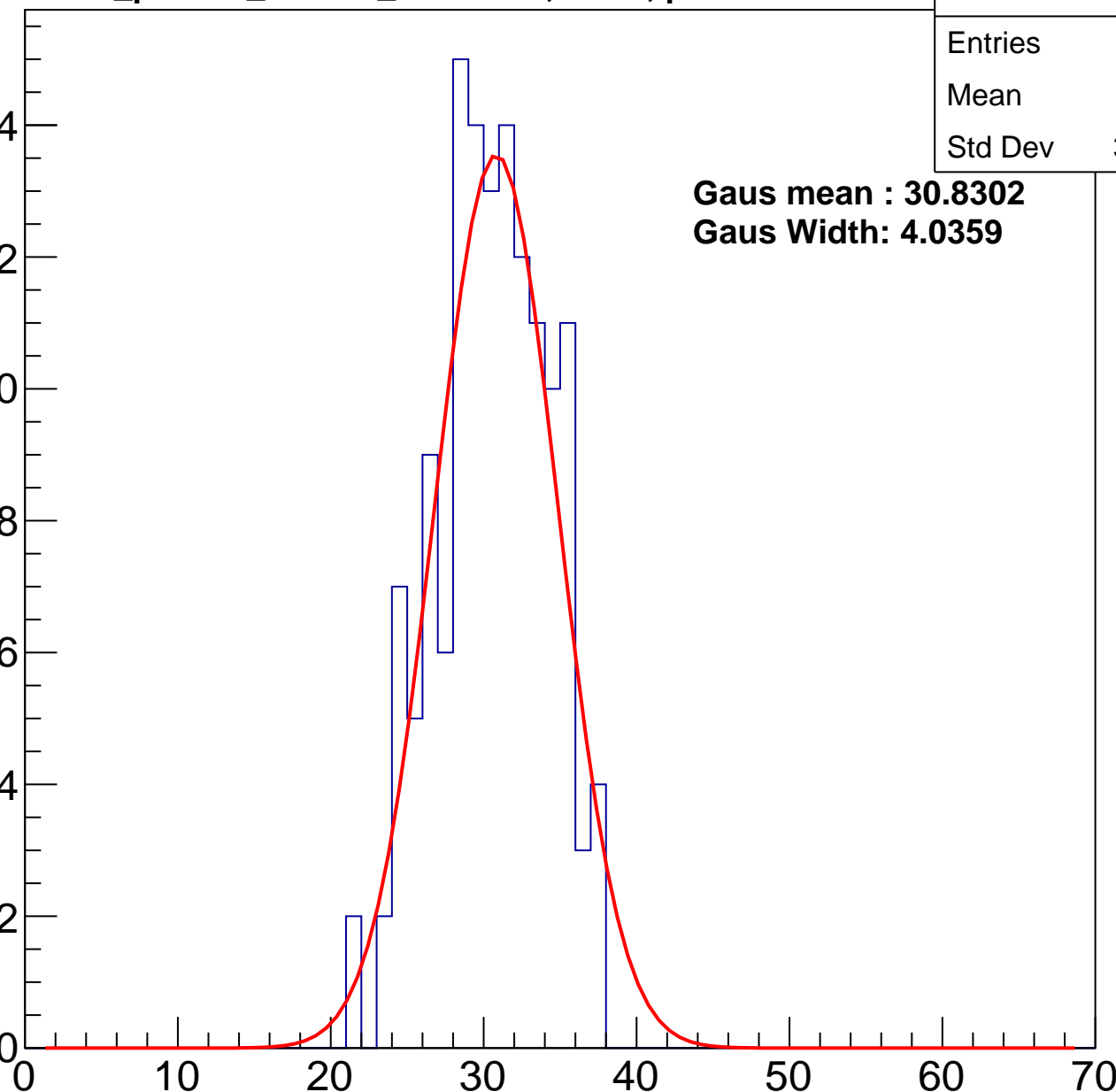
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	138
Mean	30.11
Std Dev	3.619

**Gaus mean : 30.8302**

**Gaus Width: 4.0359**



ampl

# B0L101S, U26-ch51, adc1

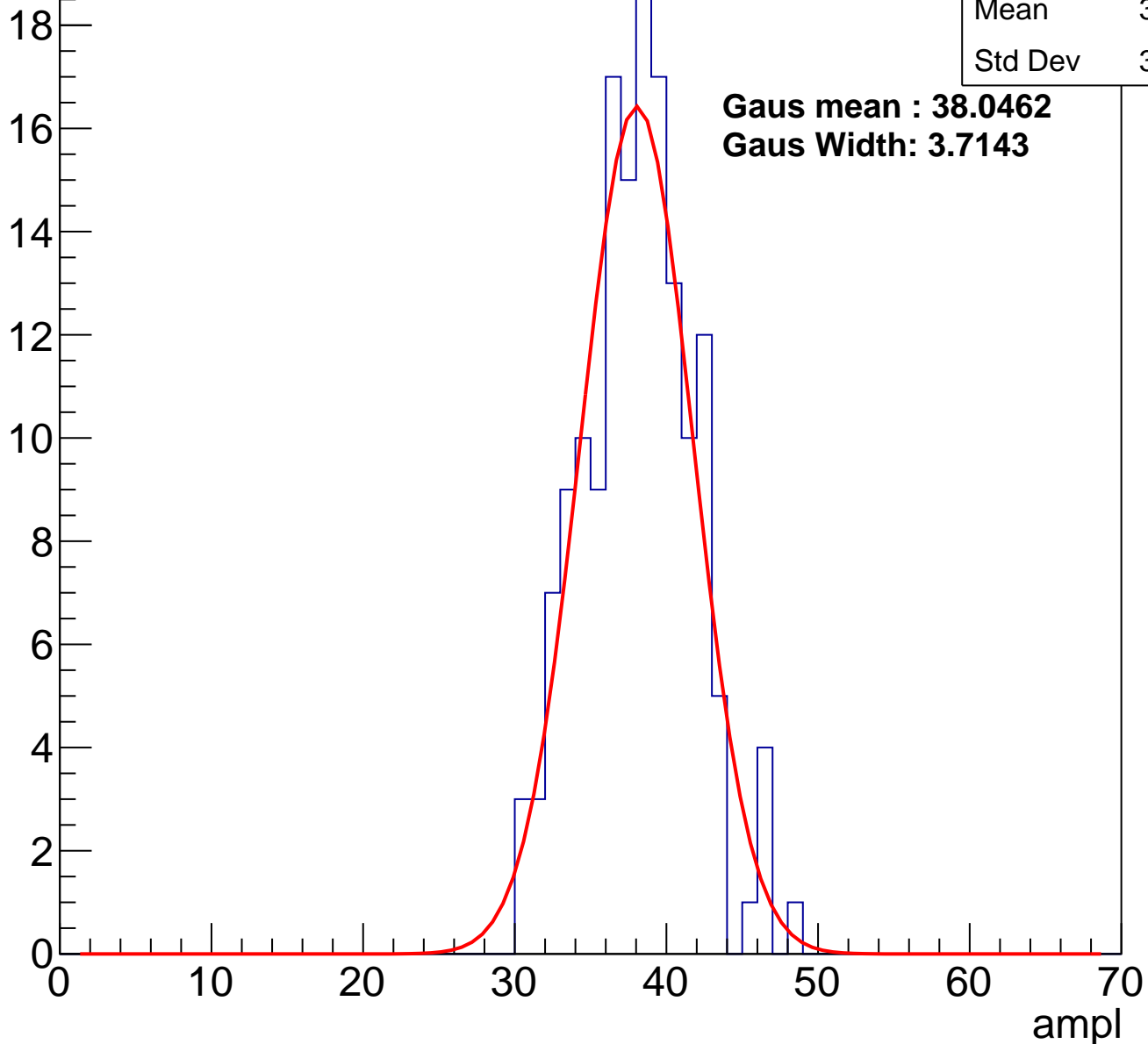
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	37.66
Std Dev	3.595

**Gaus mean : 38.0462**

**Gaus Width: 3.7143**

Entry



# B0L101S, U26-ch51, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	45.09
Std Dev	3.31

**Gaus mean : 45.6472**

**Gaus Width: 3.4850**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

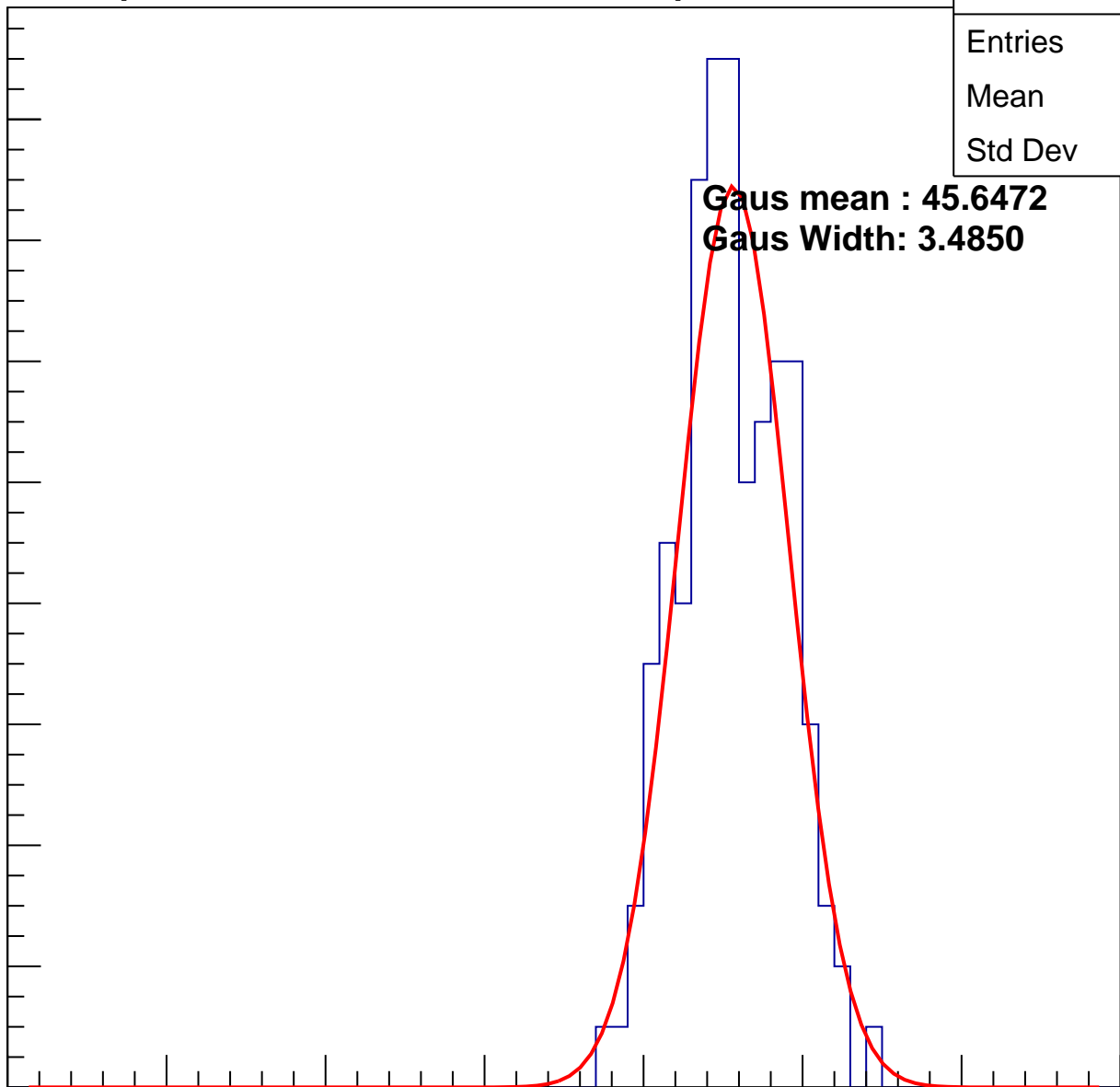
40

50

60

70

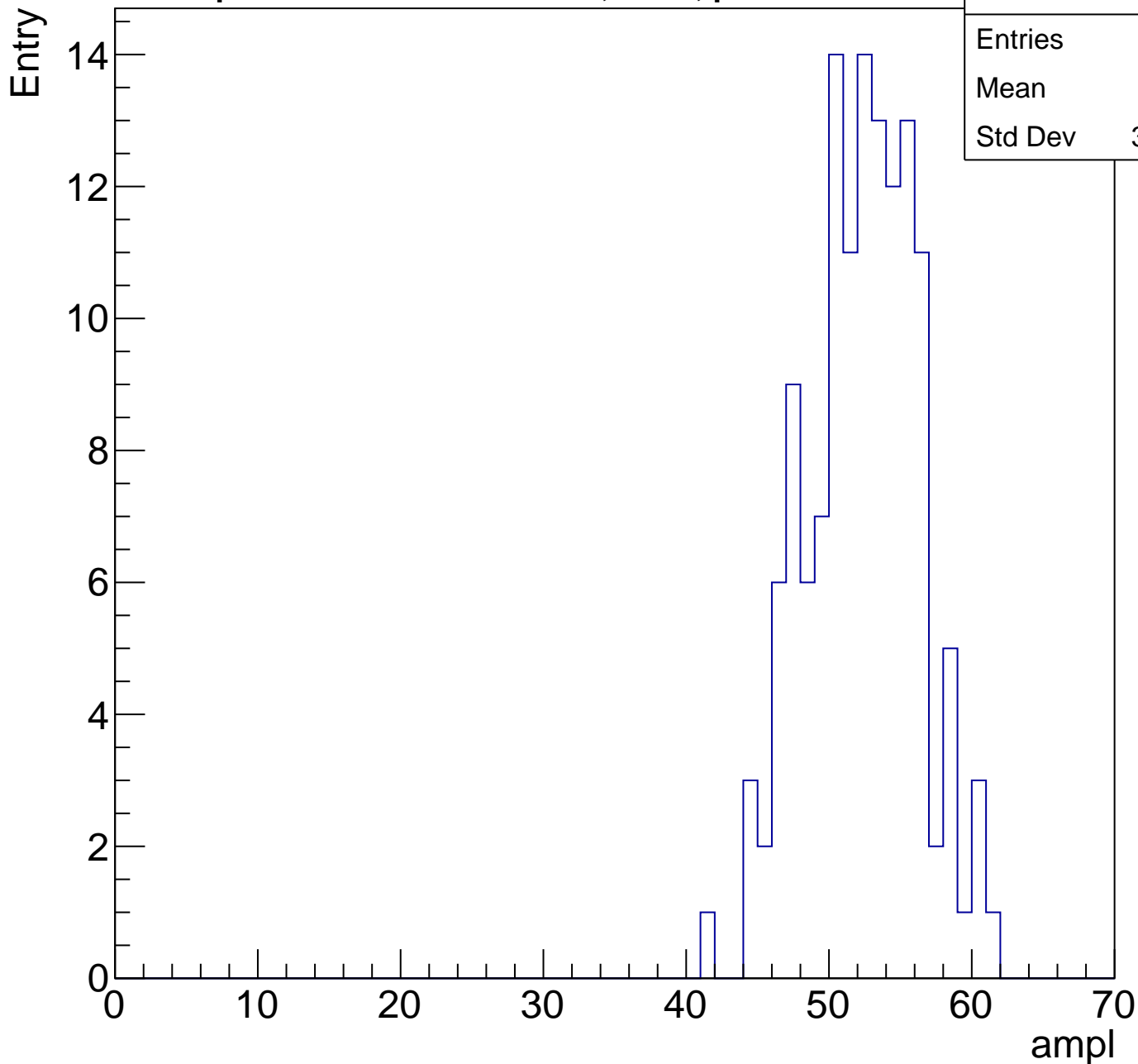
ampl



# B0L101S, U26-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	51.9
Std Dev	3.846



# B0L101S, U26-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

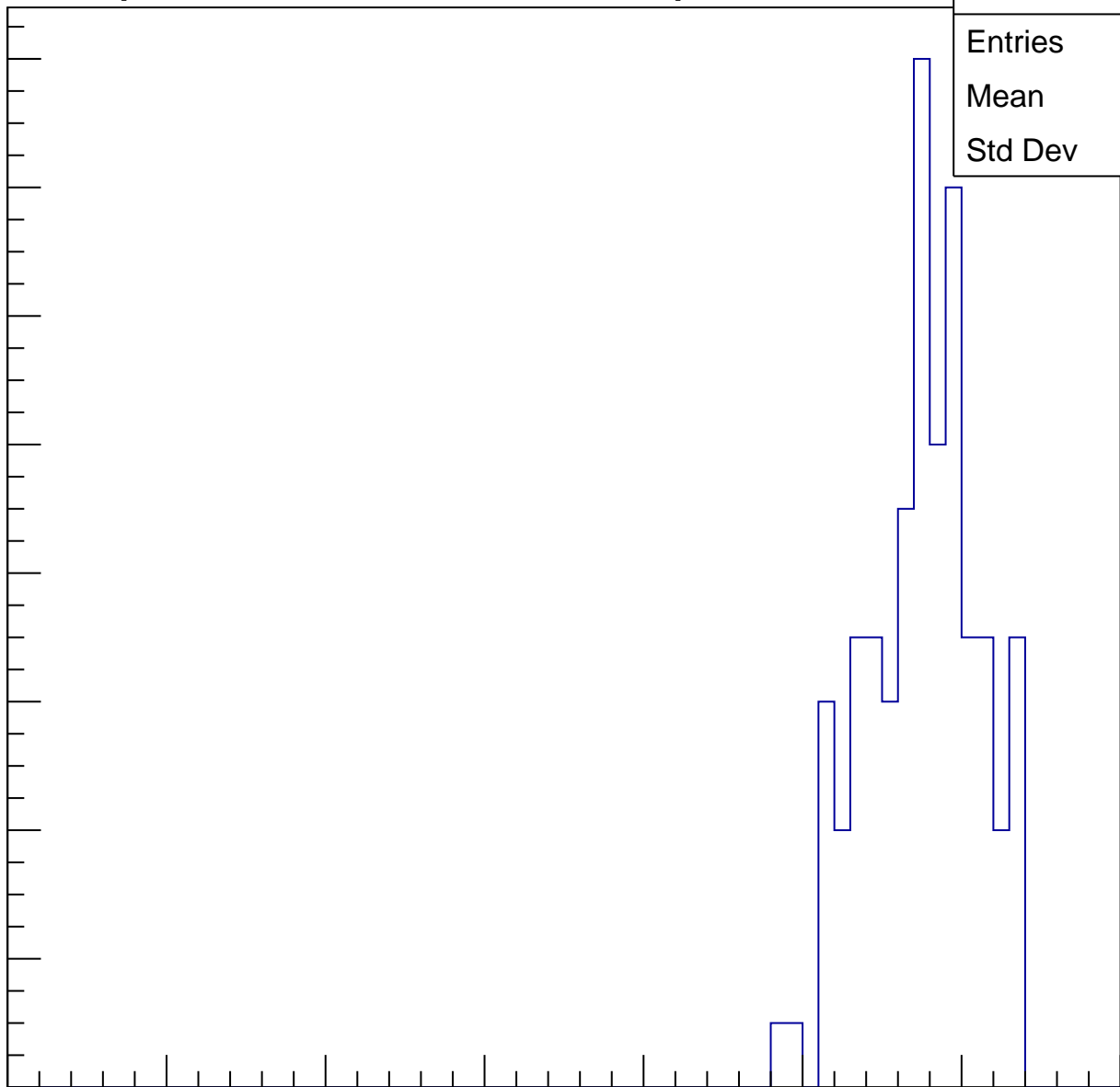
Entries	106
Mean	57.06
Std Dev	3.45

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

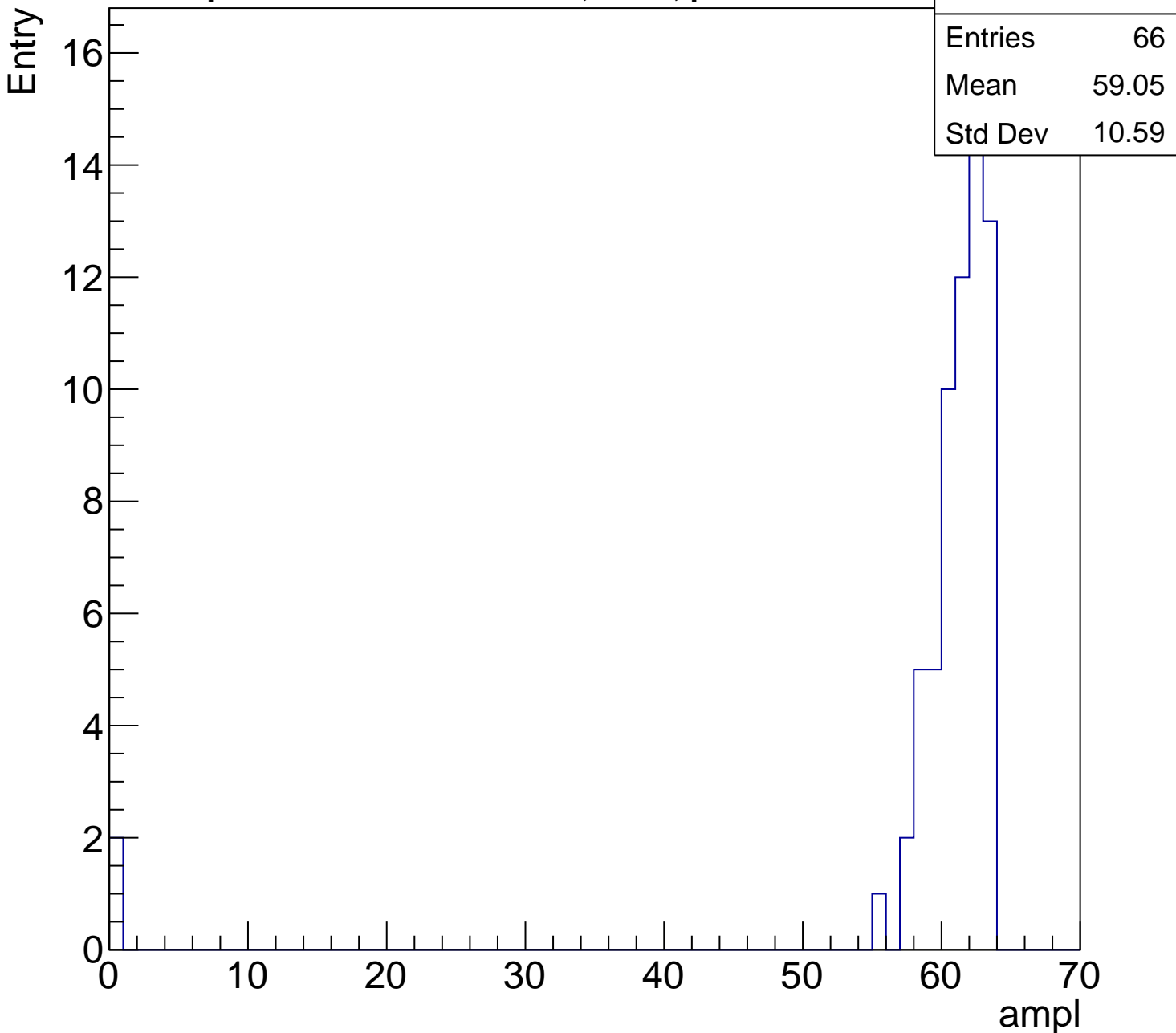
Entries	66
Mean	59.05
Std Dev	10.59

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U26-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L101S, U26-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	29.23
Std Dev	3.906

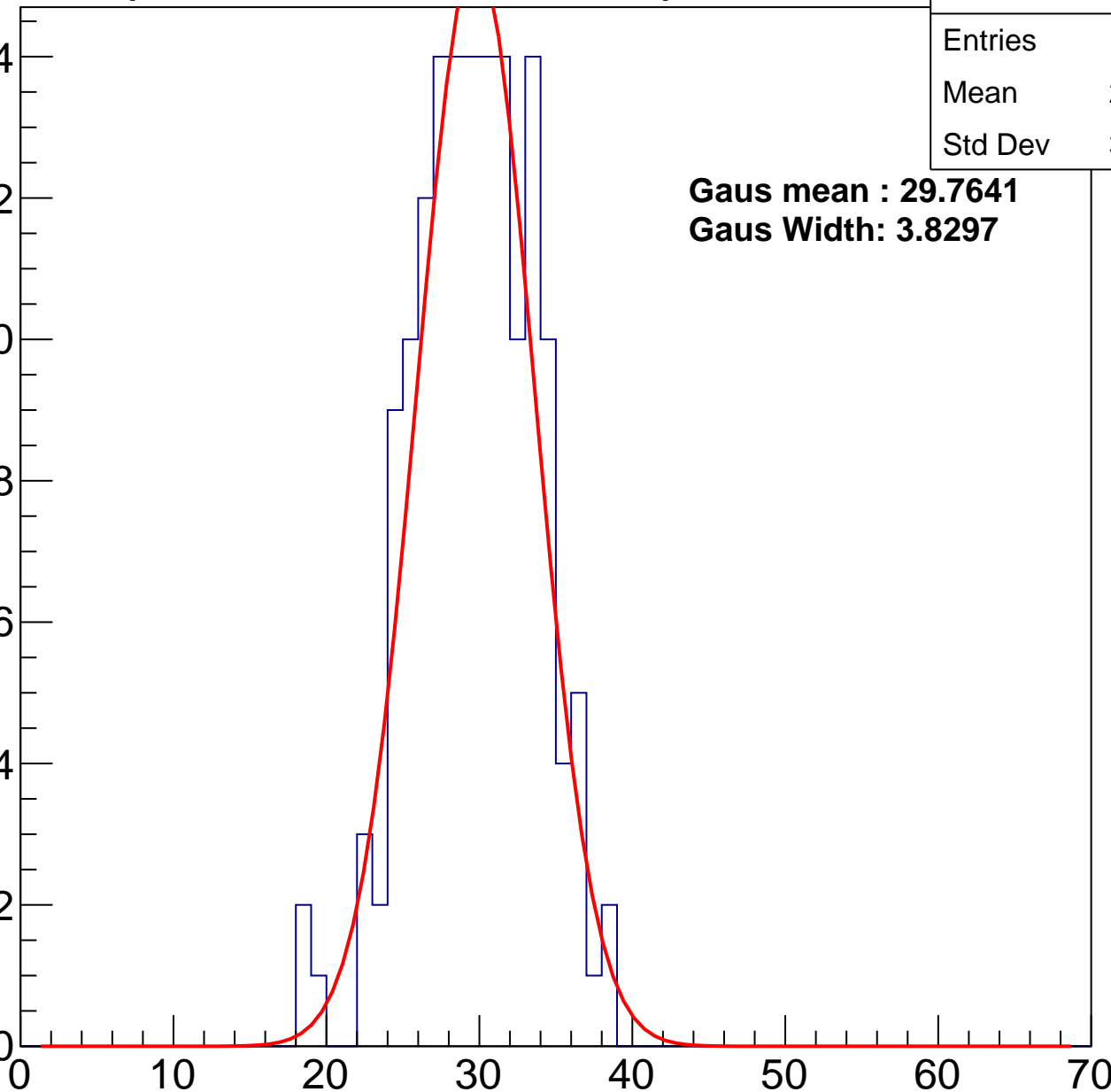
**Gaus mean : 29.7641**

**Gaus Width: 3.8297**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch52, adc1

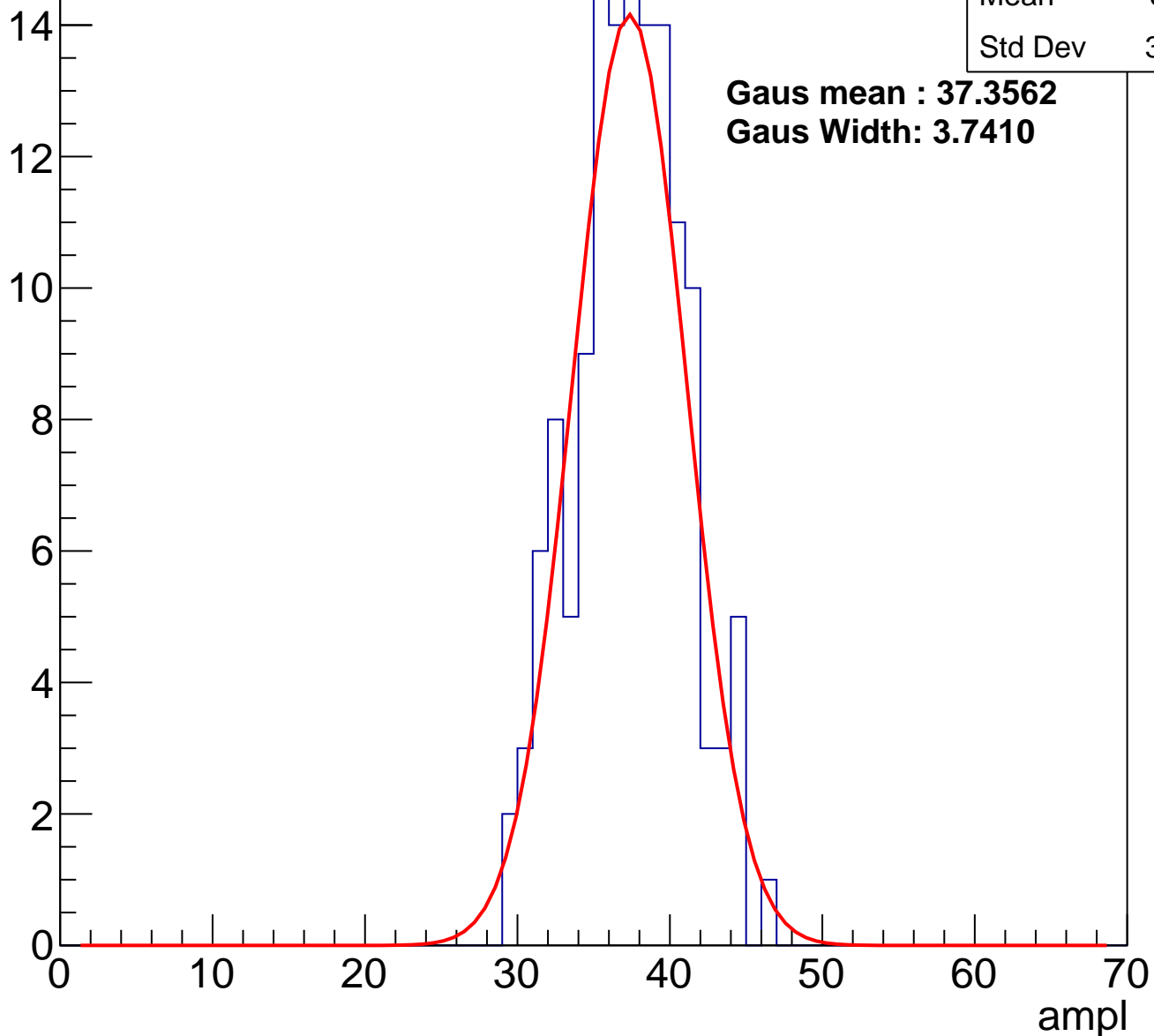
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	36.91
Std Dev	3.574

**Gaus mean : 37.3562**

**Gaus Width: 3.7410**

Entry



# B0L101S, U26-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	43.81
Std Dev	3.723

**Gaus mean : 44.5365**

**Gaus Width: 3.6283**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

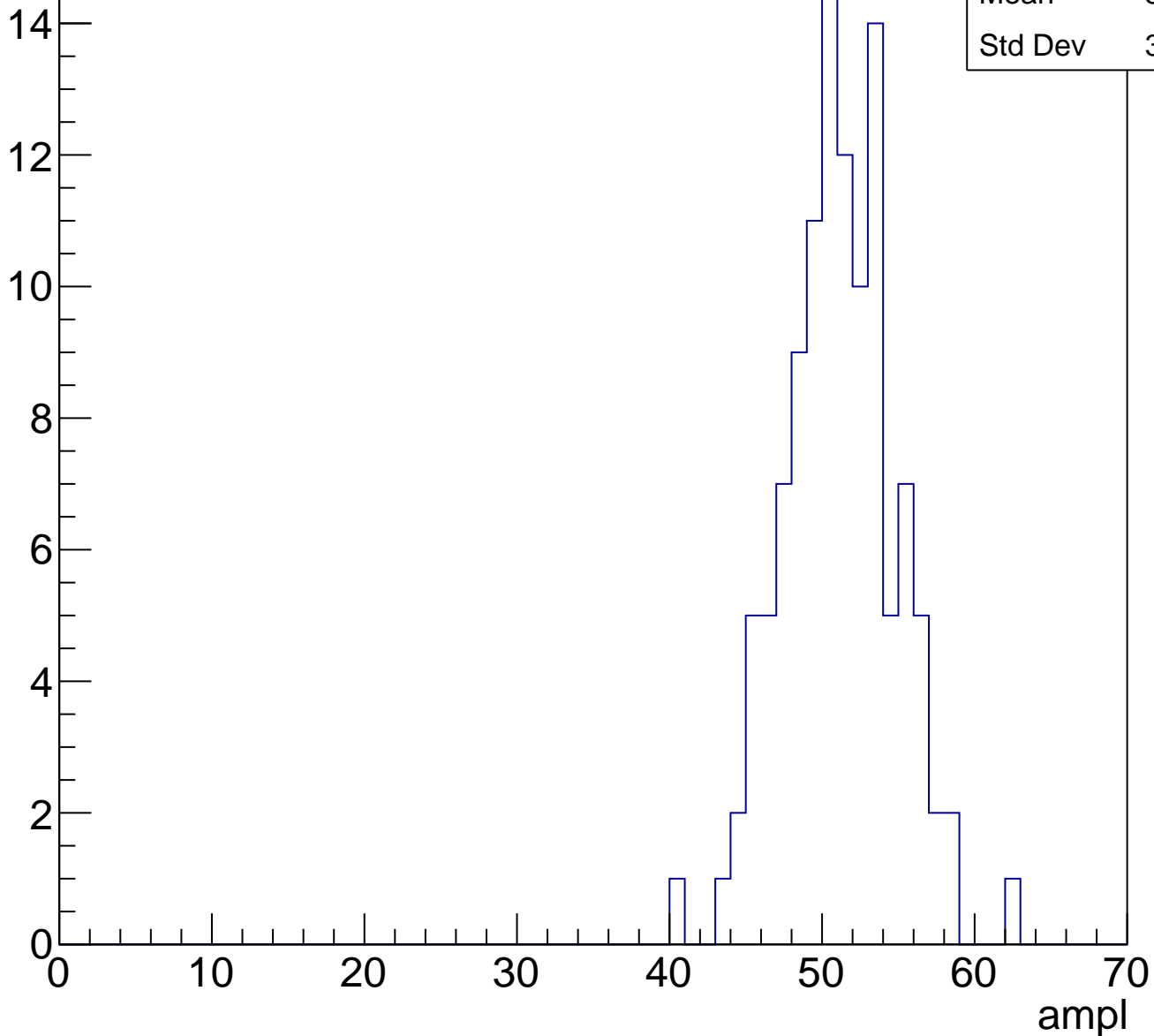
ampl

70

# B0L101S, U26-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch52, adc4

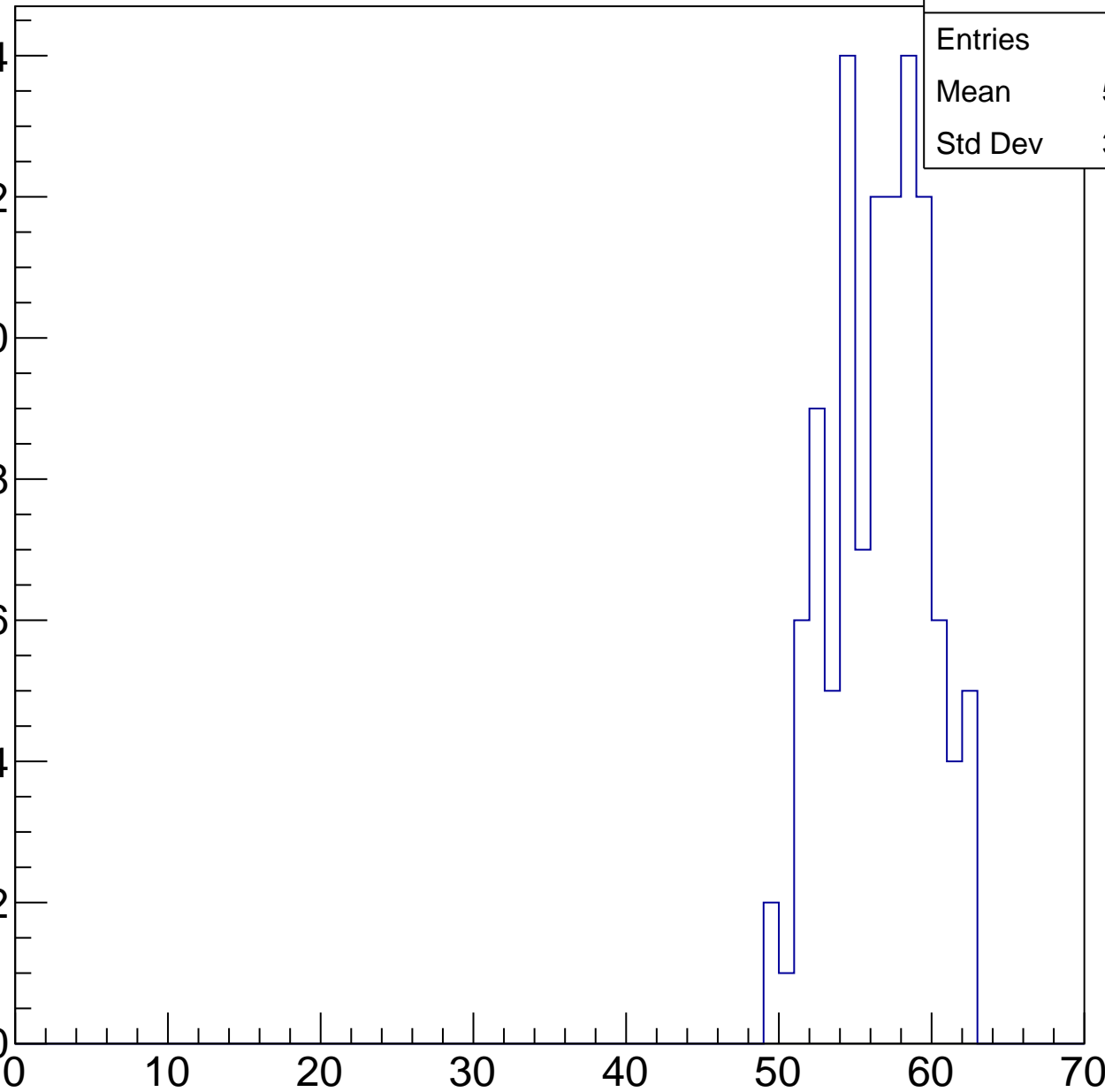
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	56.13
Std Dev	3.157

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch52, adc5

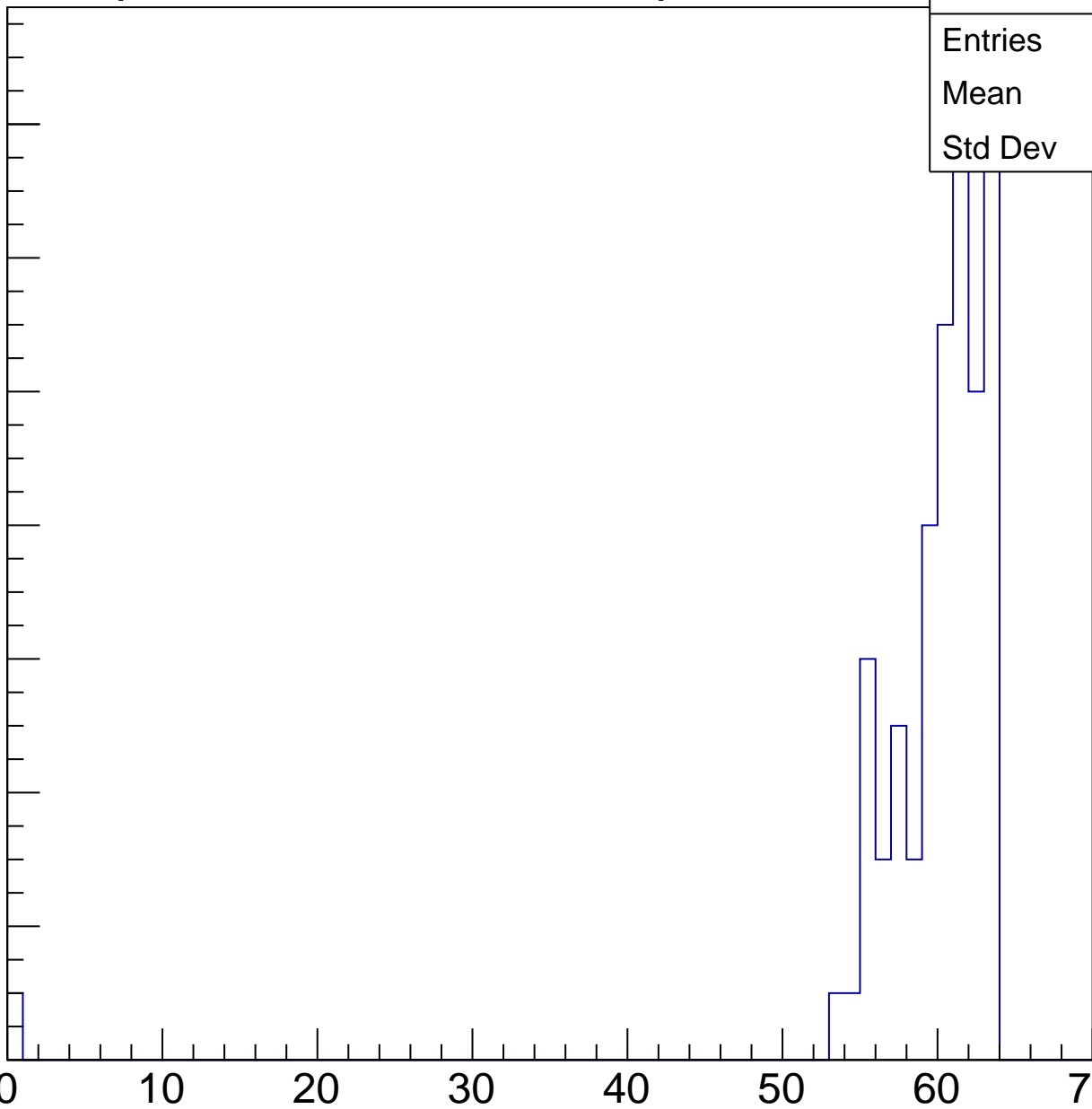
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	79
Mean	59.19
Std Dev	7.192

ampl

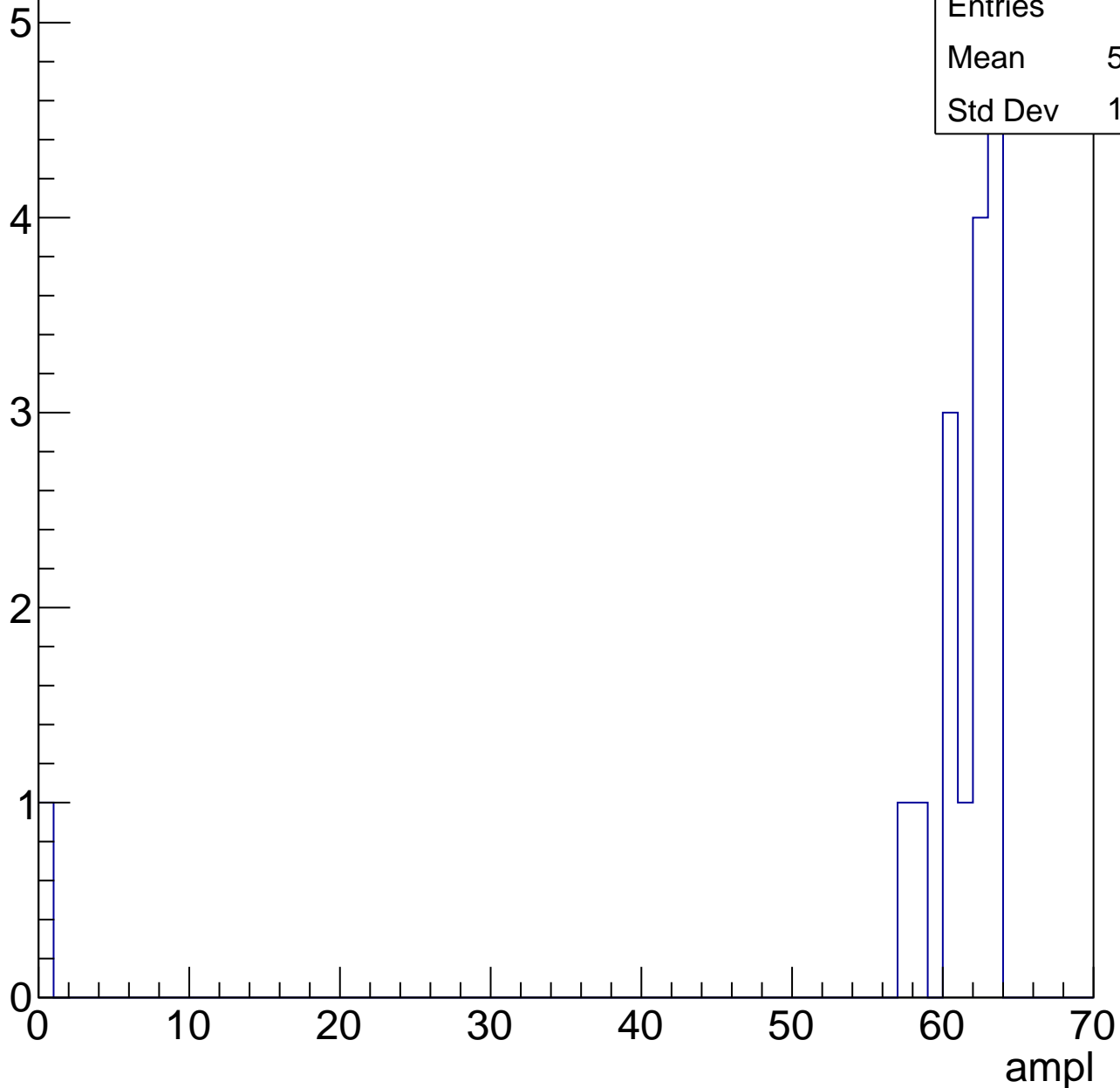


# B0L101S, U26-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	16
Mean	57.44
Std Dev	14.94





# B0L101S, U26-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	29.02
Std Dev	3.544

**Gaus mean : 29.4356**

**Gaus Width: 3.5424**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch53, adc1

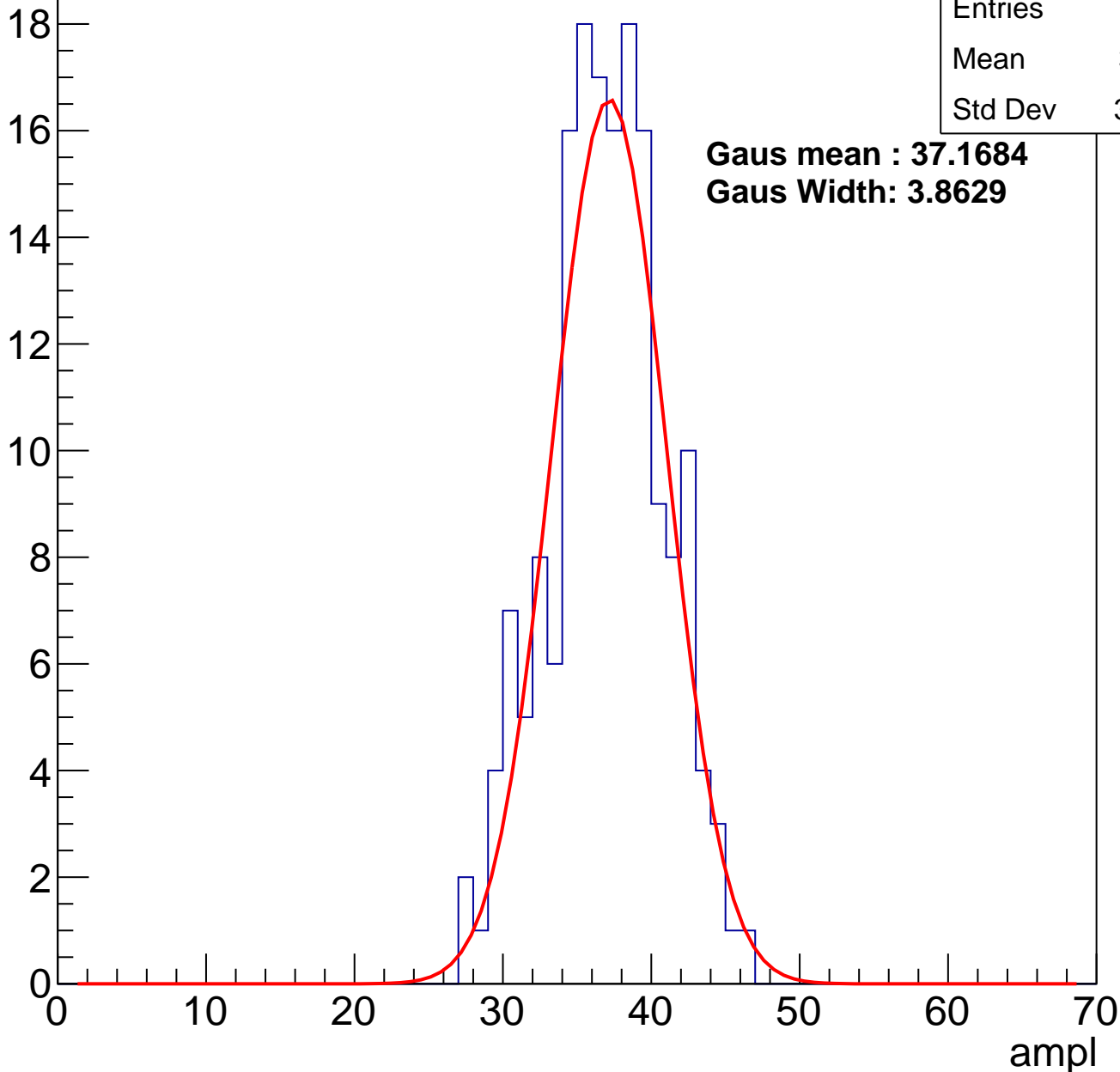
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	170
Mean	36.51
Std Dev	3.854

**Gaus mean : 37.1684**

**Gaus Width: 3.8629**

Entry



# B0L101S, U26-ch53, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

44.3

Std Dev

3.492

**Gaus mean : 44.8988**

**Gaus Width: 3.5347**

0

10

20

30

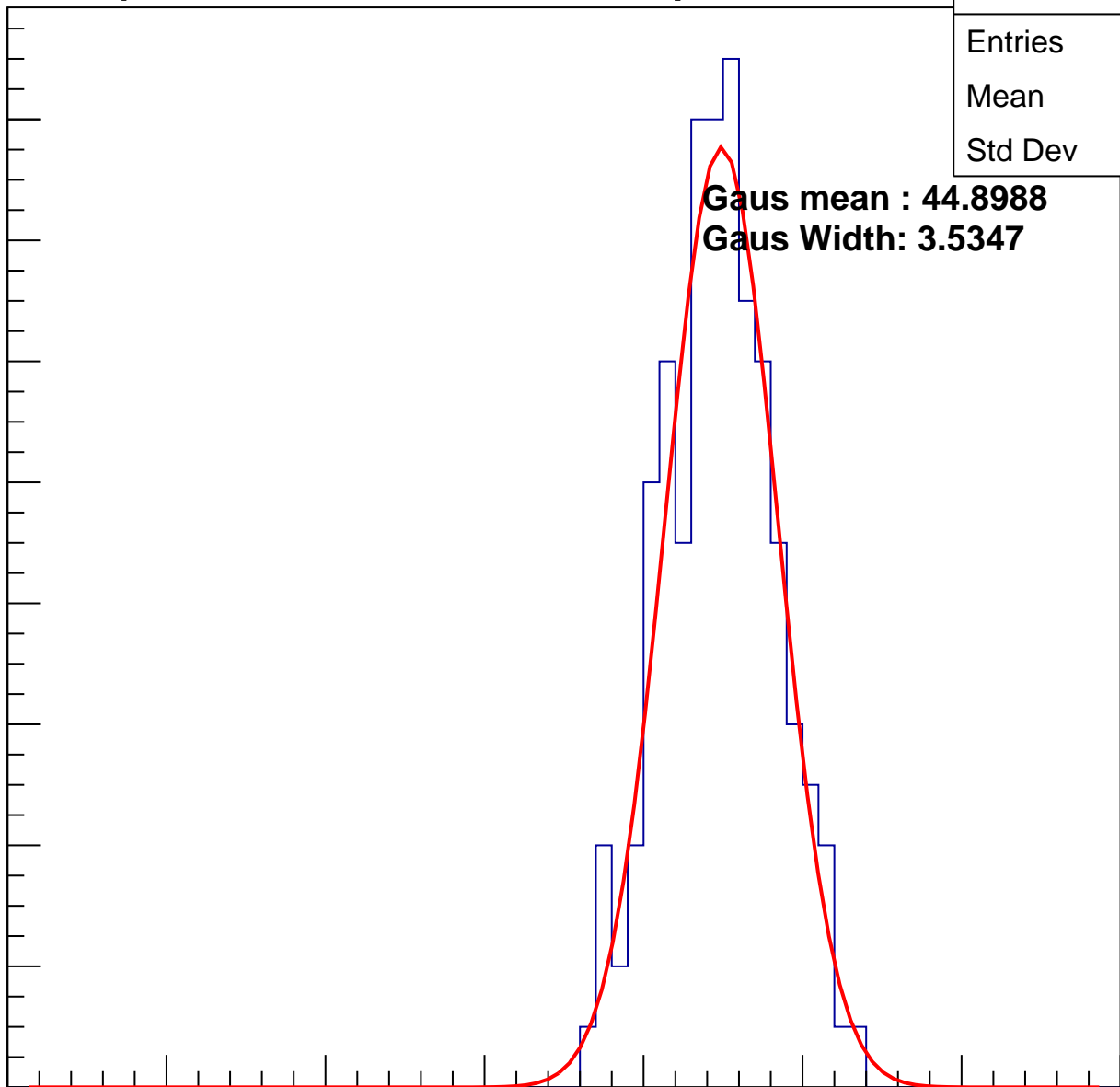
40

50

60

70

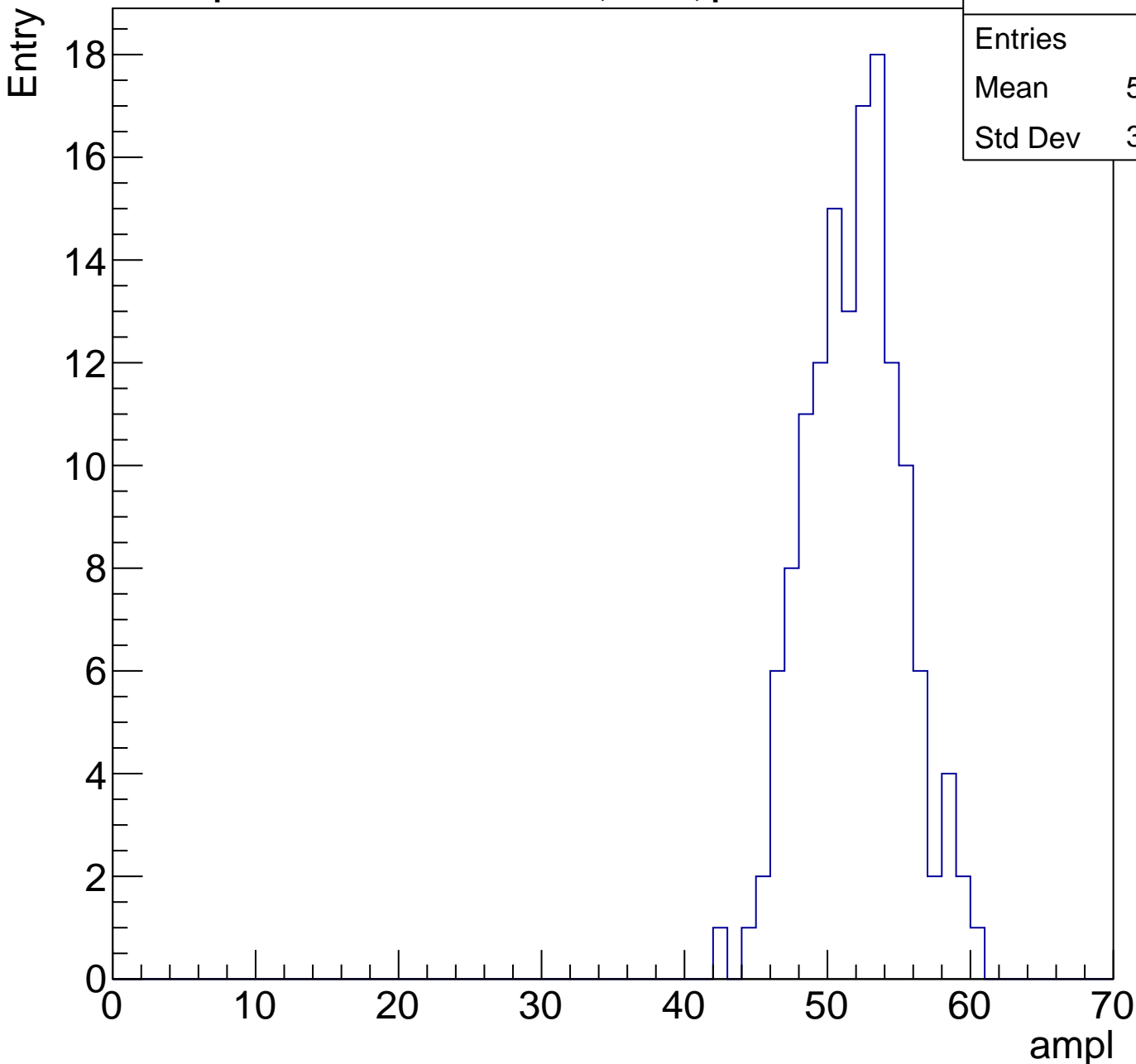
ampl



# B0L101S, U26-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	51.44
Std Dev	3.367



# B0L101S, U26-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

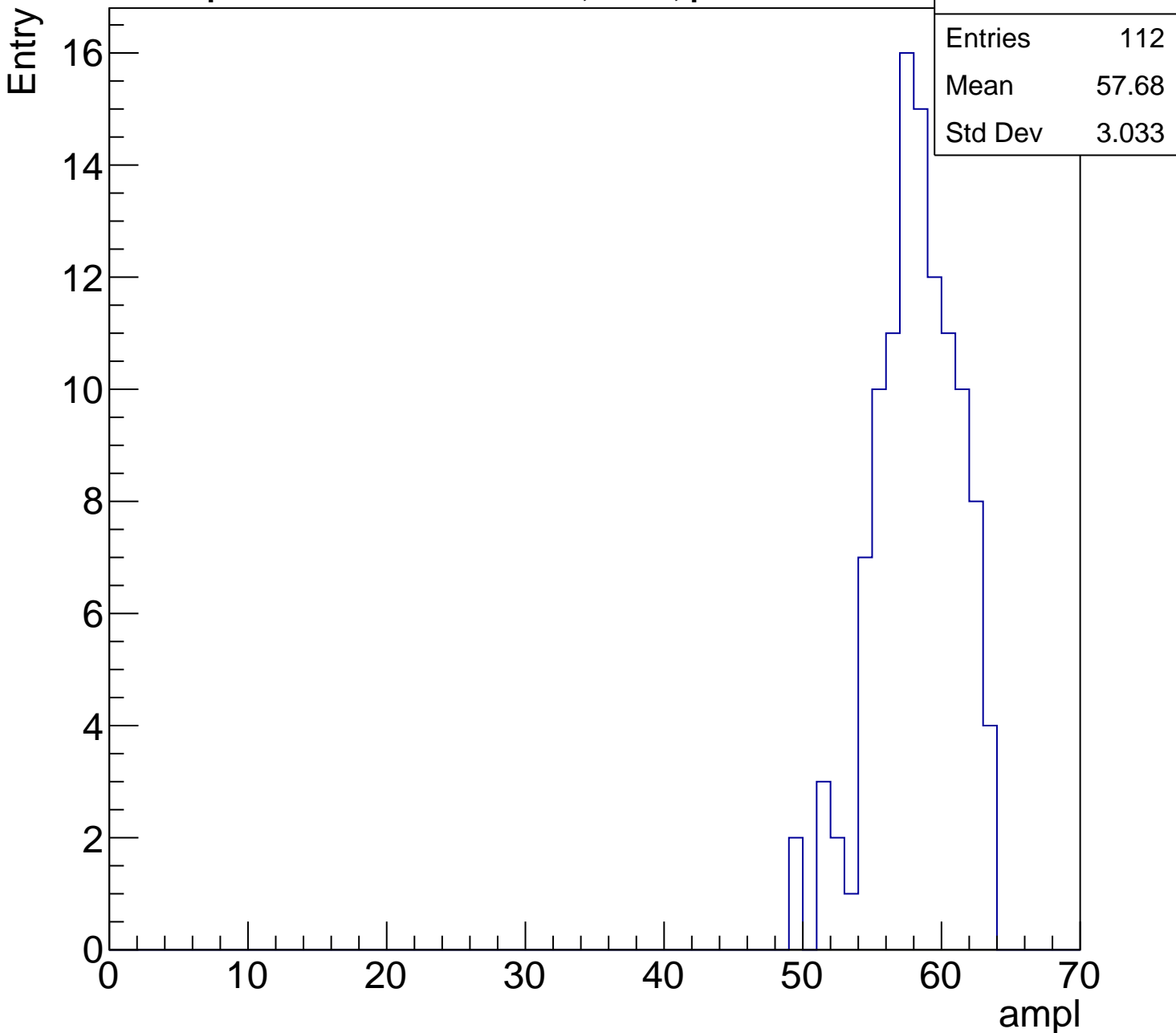
Entries	112
Mean	57.68
Std Dev	3.033

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch53, adc5

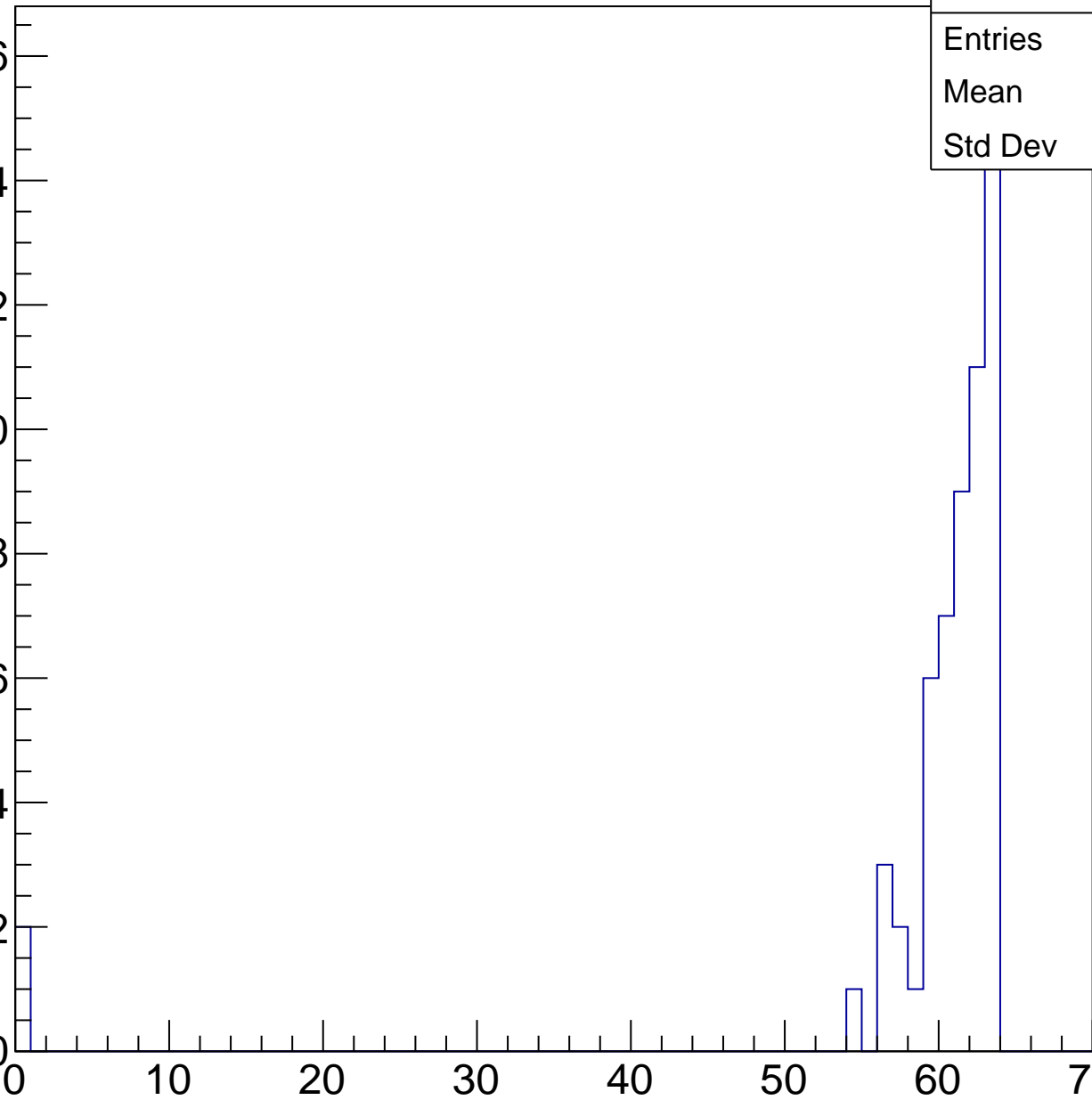
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	58
Mean	58.74
Std Dev	11.31

ampl



# B0L101S, U26-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B0L101S, U26-ch54, adc0

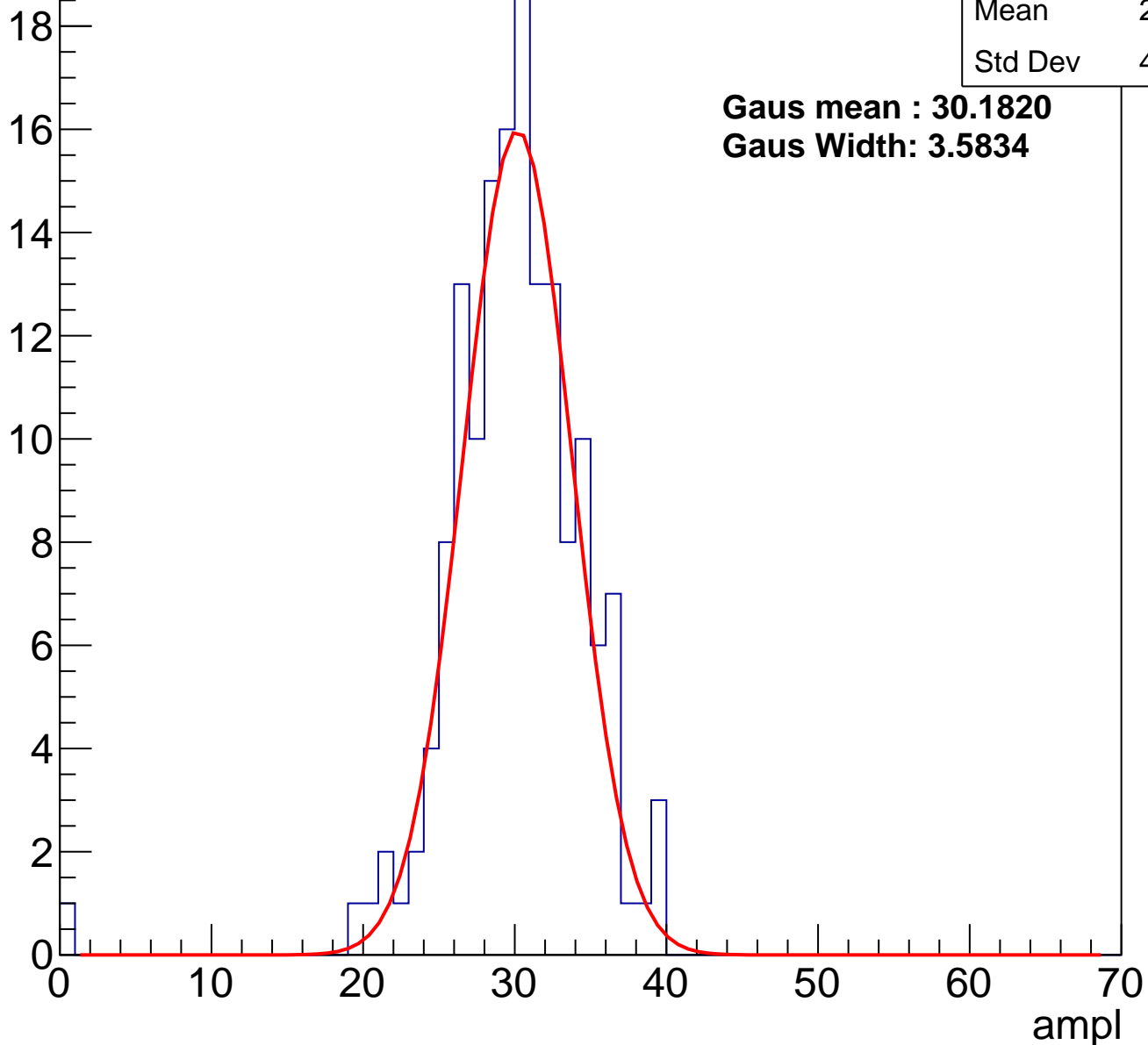
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	29.57
Std Dev	4.537

**Gaus mean : 30.1820**

**Gaus Width: 3.5834**

Entry



# B0L101S, U26-ch54, adc1

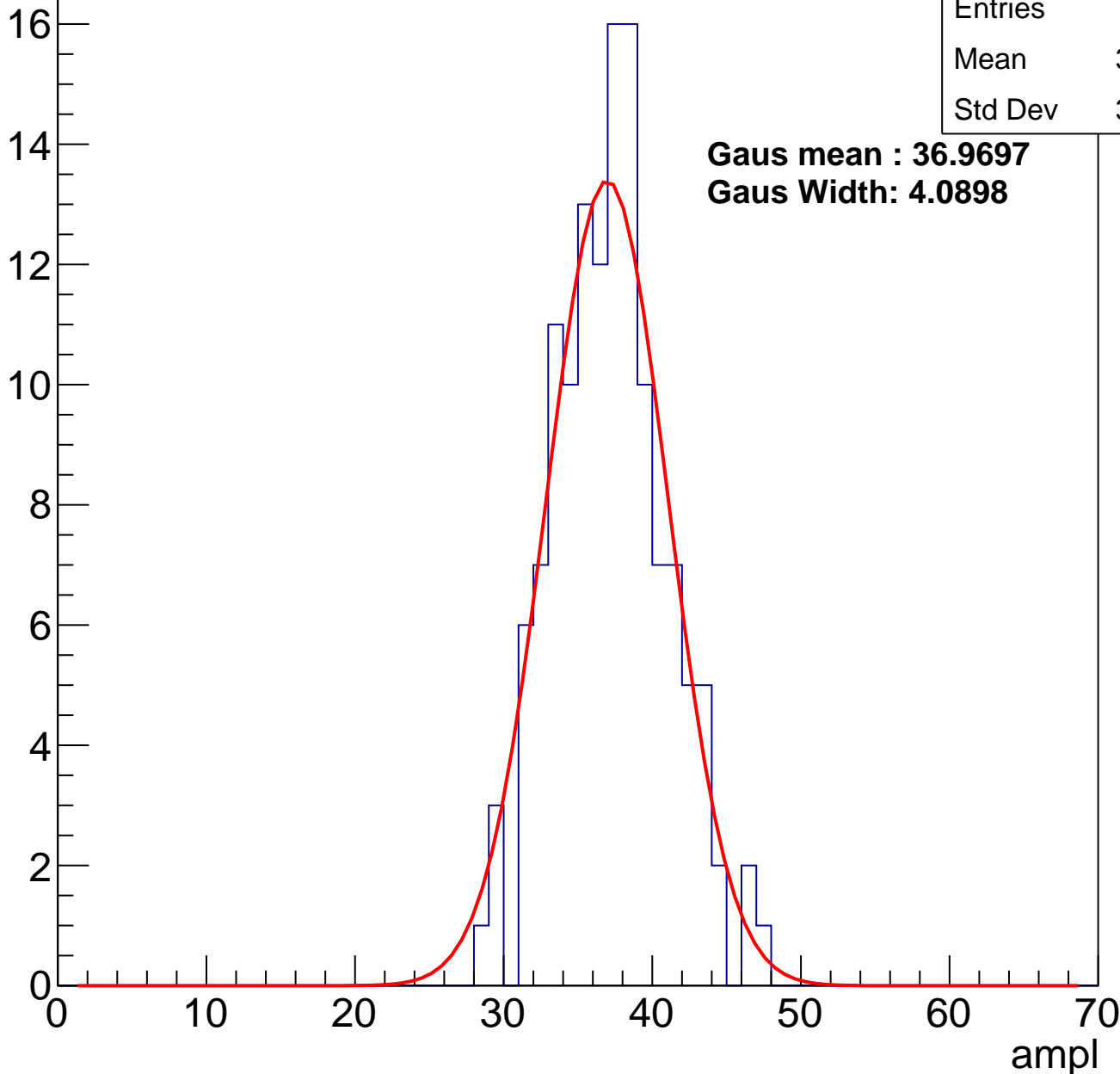
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	36.75
Std Dev	3.747

**Gaus mean : 36.9697**

**Gaus Width: 4.0898**

Entry



# B0L101S, U26-ch54, adc2

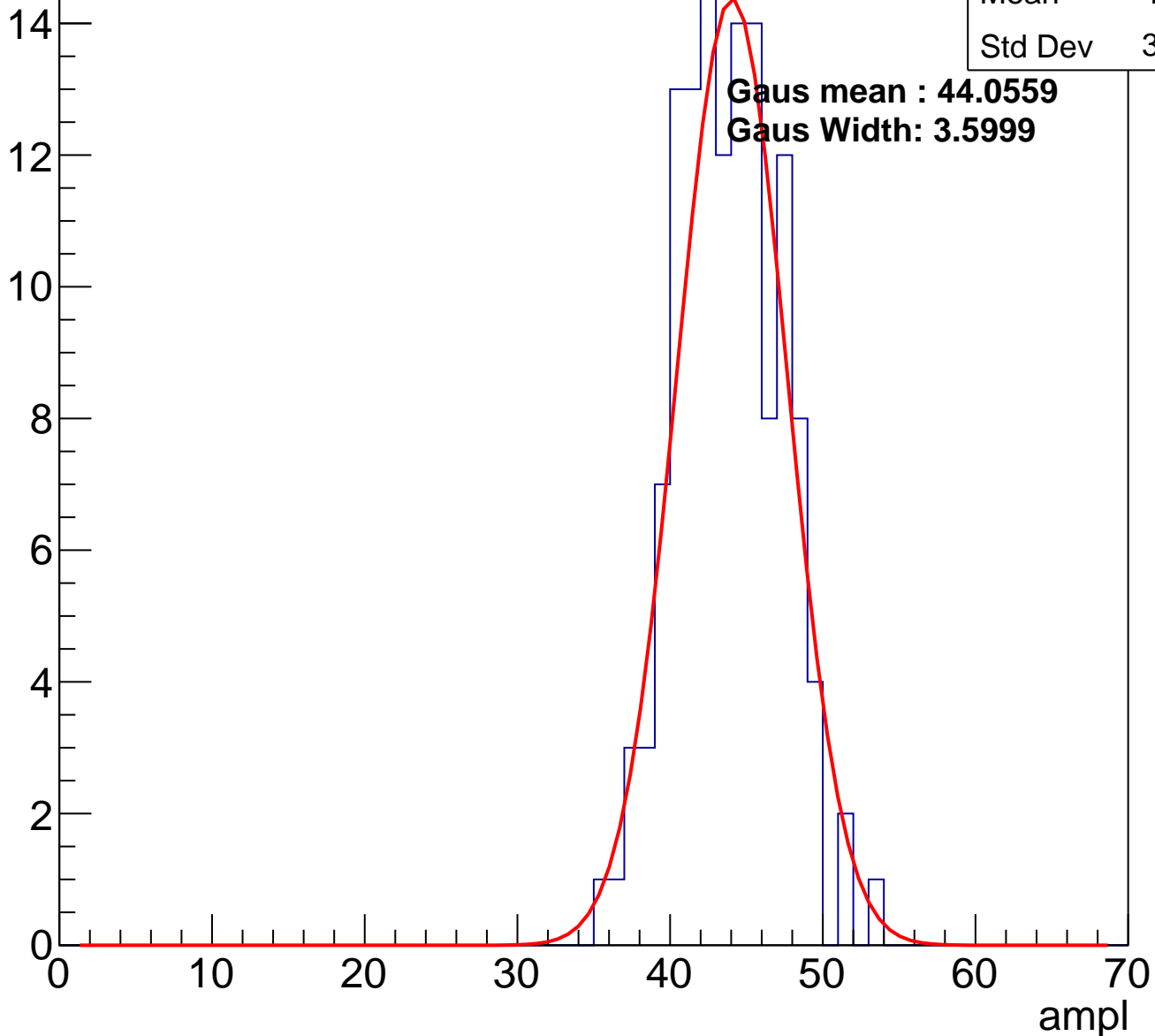
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	43.37
Std Dev	3.362

**Gaus mean : 44.0559**

**Gaus Width: 3.5999**

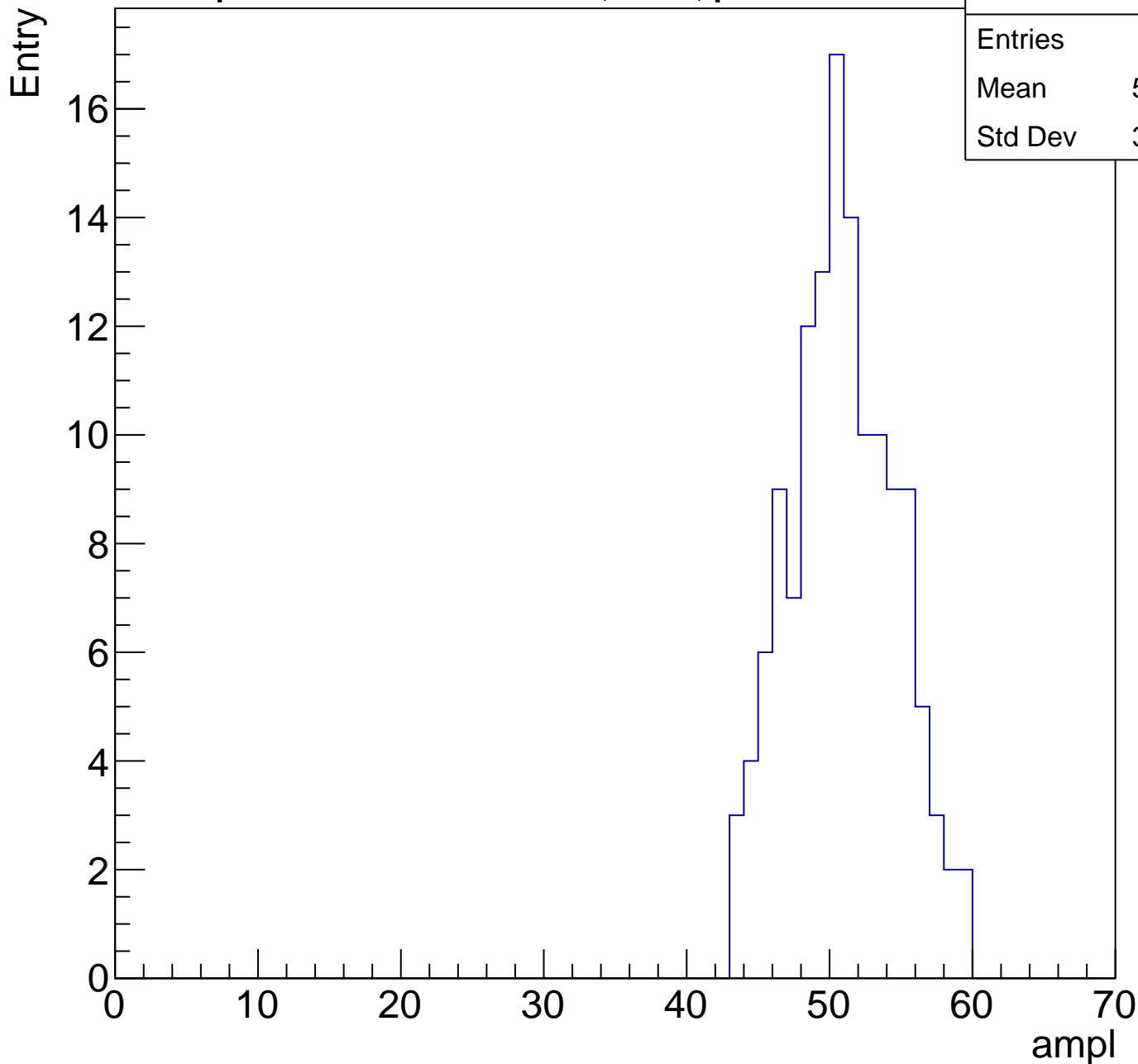
Entry



# B0L101S, U26-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	50.45
Std Dev	3.663

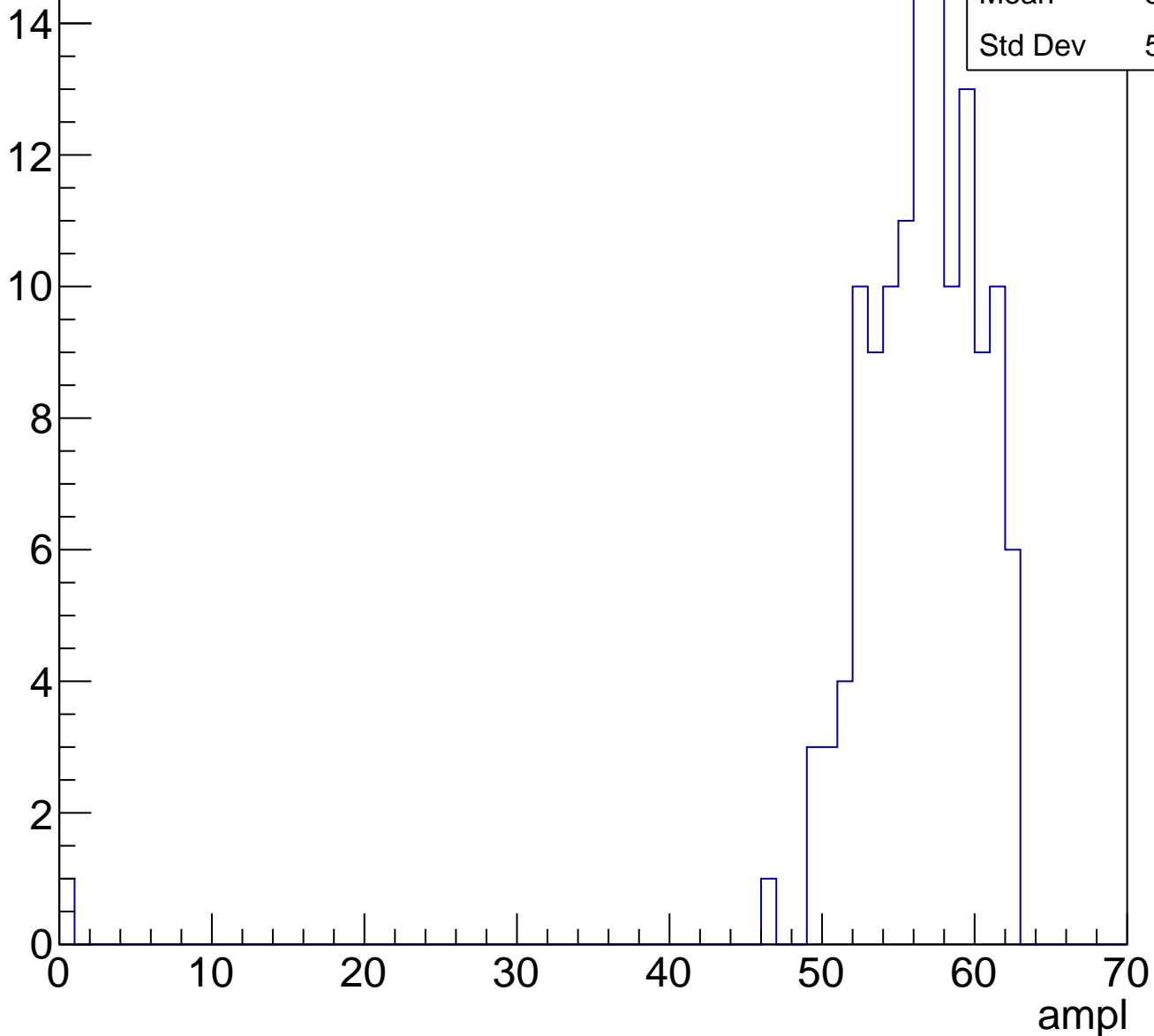


# B0L101S, U26-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	55.79
Std Dev	5.987

Entry



# B0L101S, U26-ch54, adc5

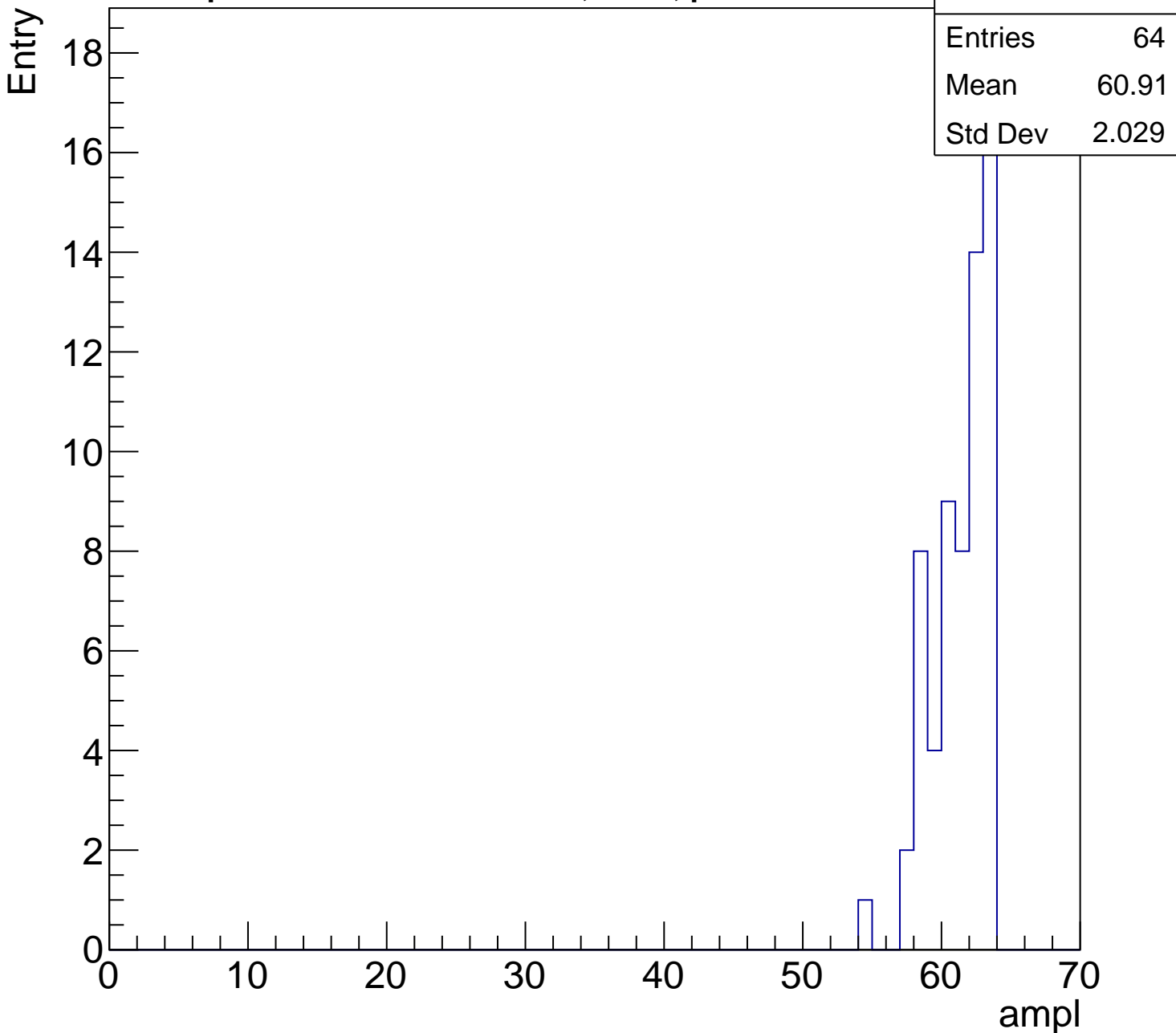
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	64
Mean	60.91
Std Dev	2.029

ampl



# B0L101S, U26-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

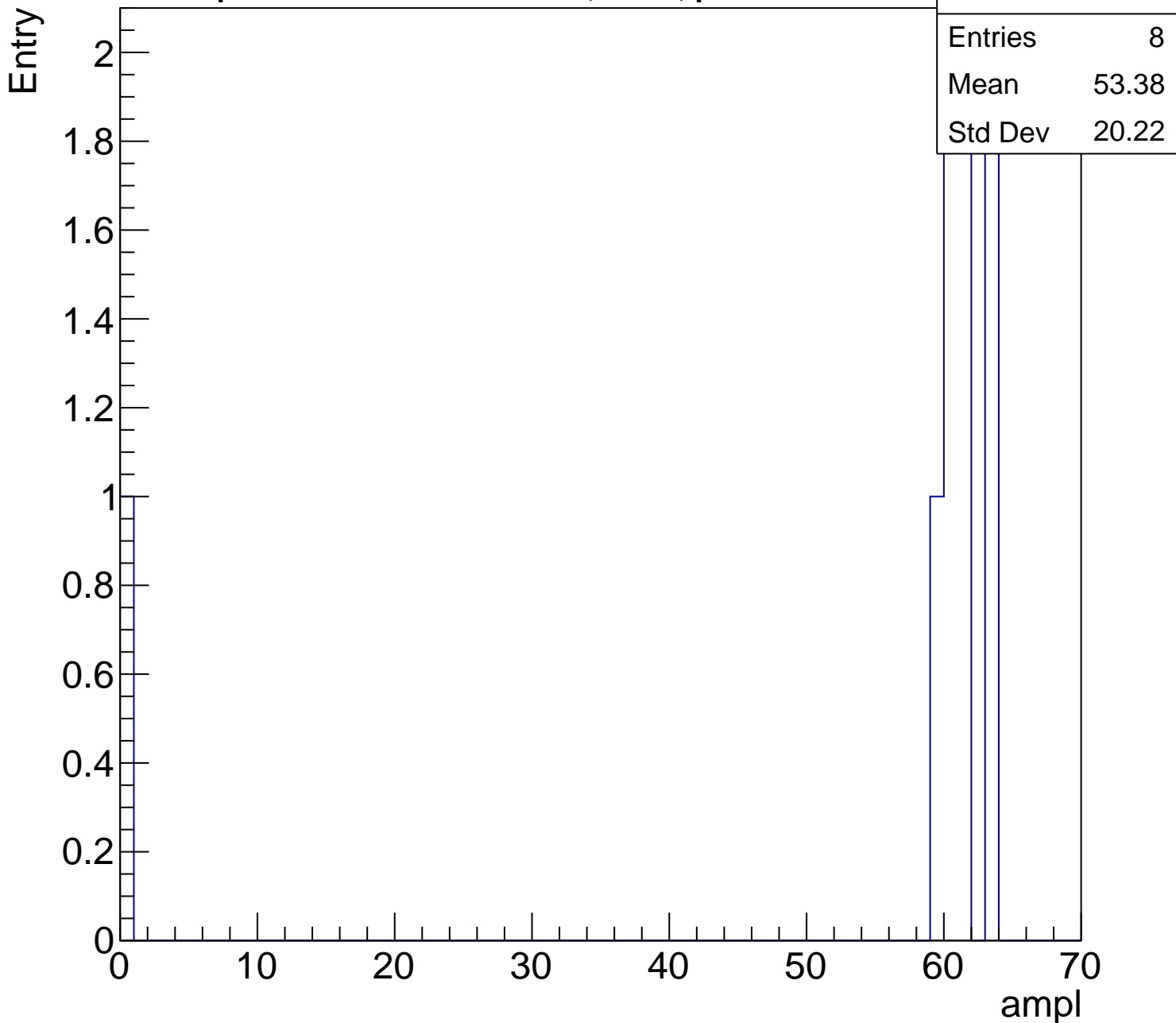
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.38
Std Dev	20.22

0 10 20 30 40 50 60 70

ampl





# B0L101S, U26-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	30.33
Std Dev	4.174

**Gaus mean : 30.8578**

**Gaus Width: 3.3583**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

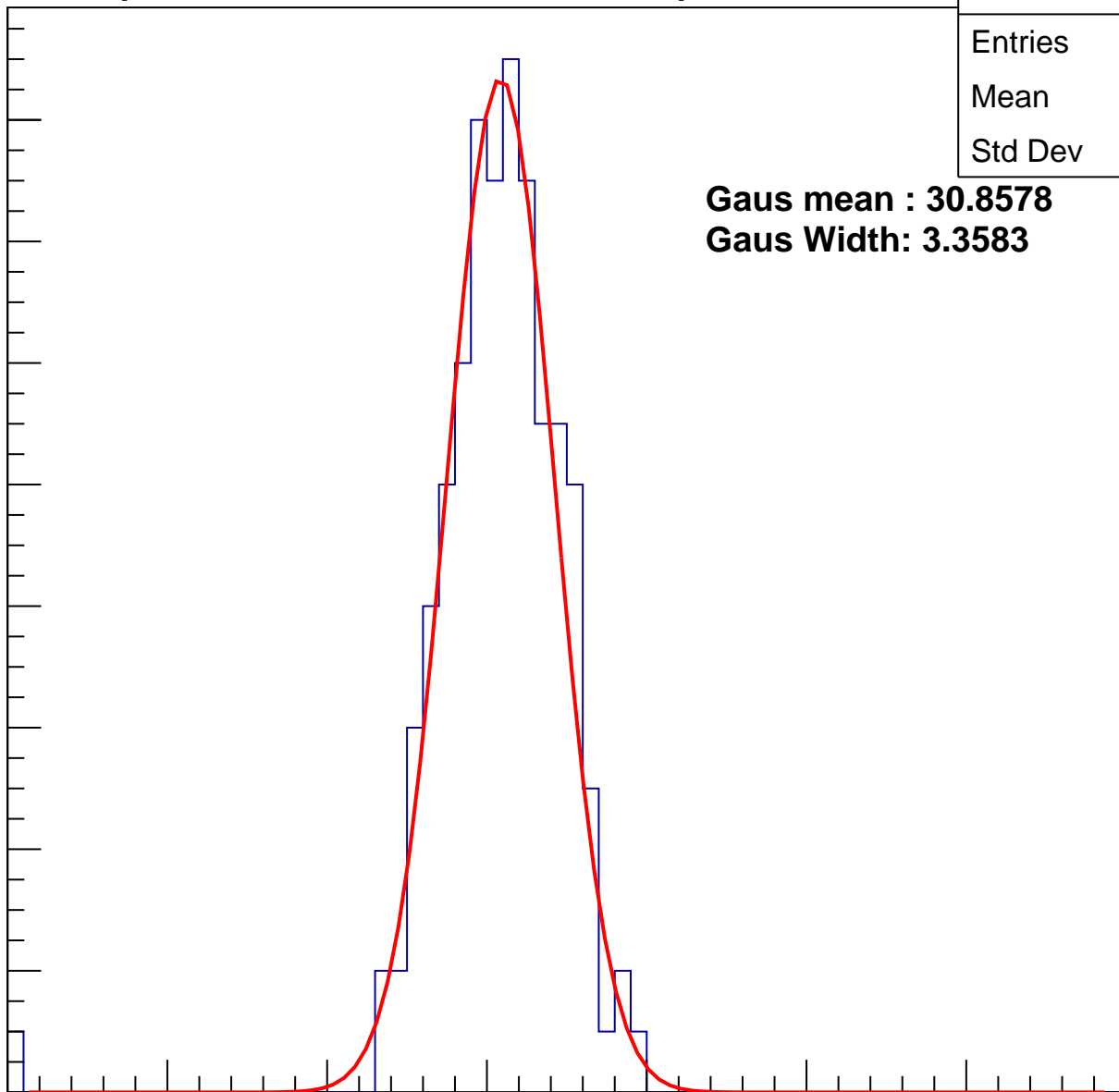
40

50

60

70

ampl



# B0L101S, U26-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	37.22
Std Dev	3.209

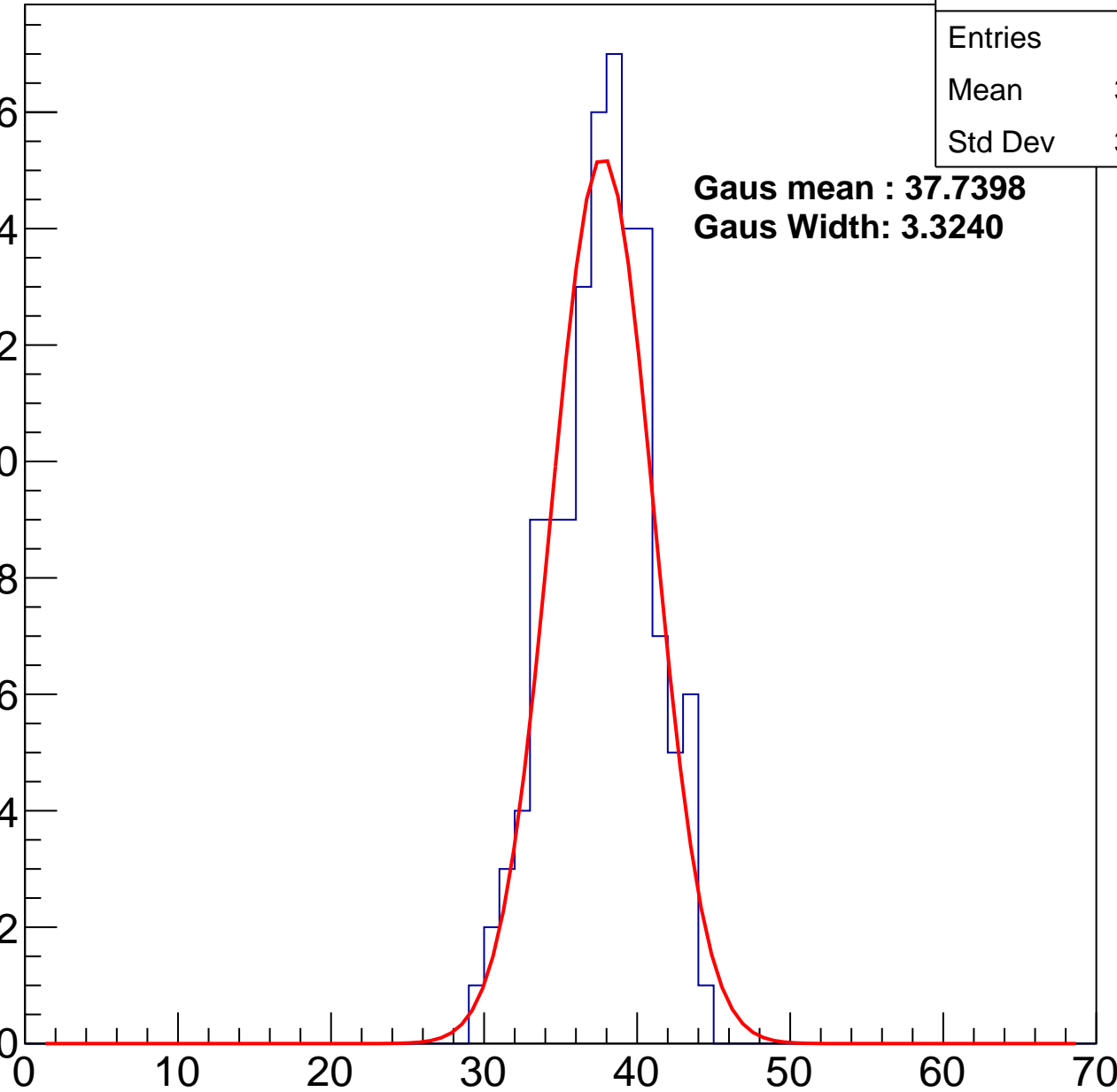
**Gaus mean : 37.7398**

**Gaus Width: 3.3240**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch55, adc2

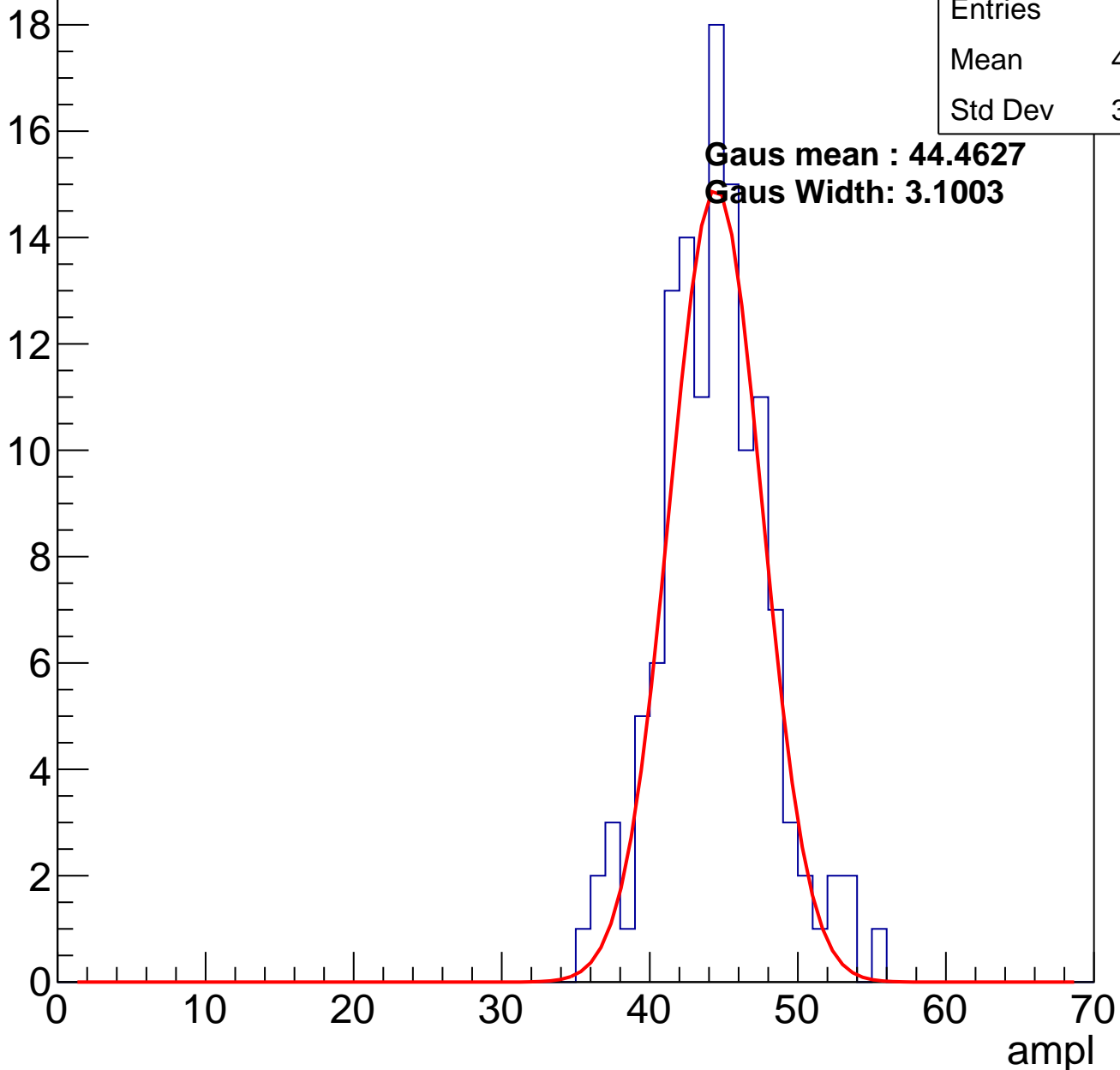
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	43.97
Std Dev	3.612

**Gaus mean : 44.4627**

**Gaus Width: 3.1003**

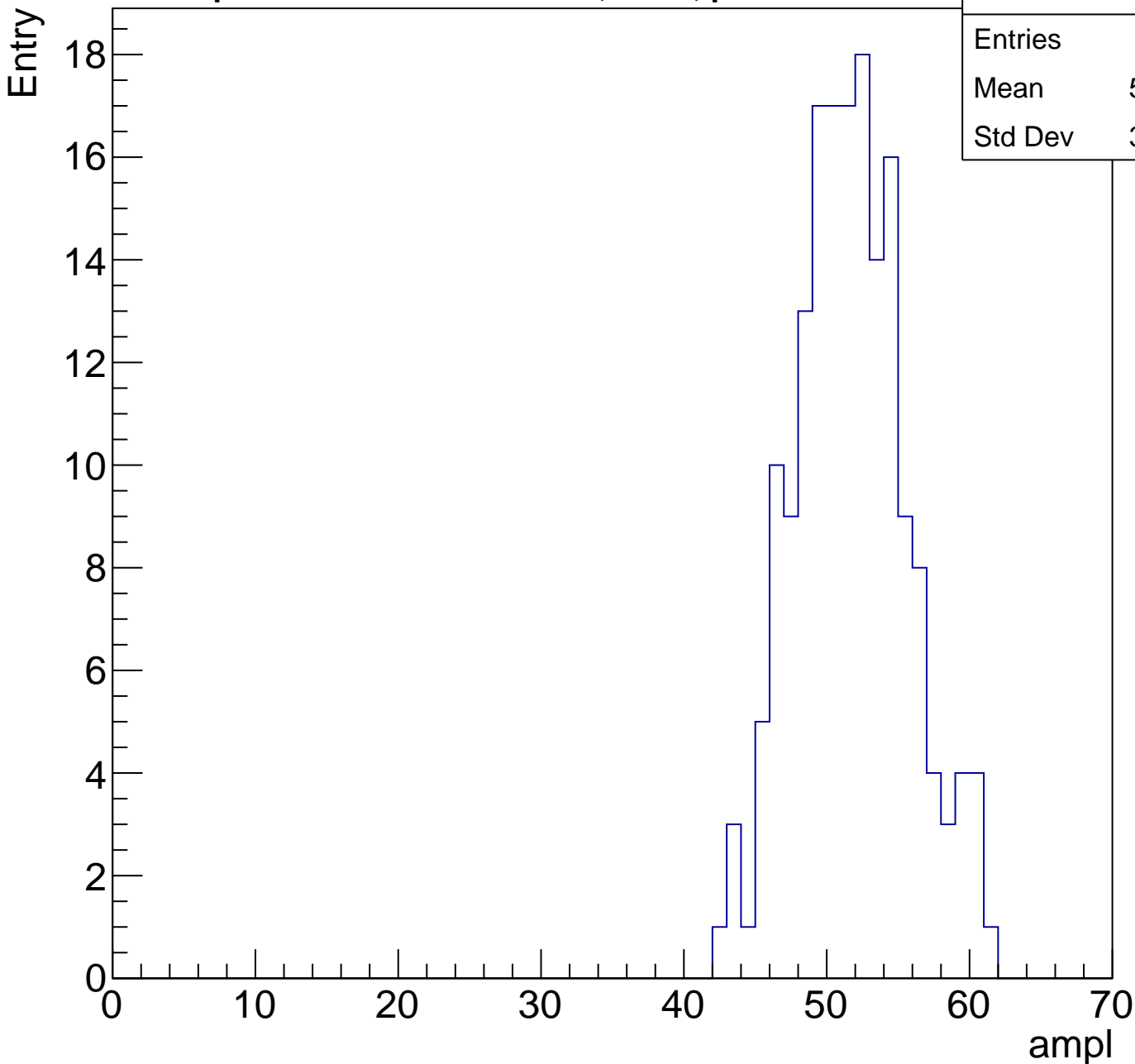
Entry



# B0L101S, U26-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	174
Mean	51.27
Std Dev	3.879



# B0L101S, U26-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

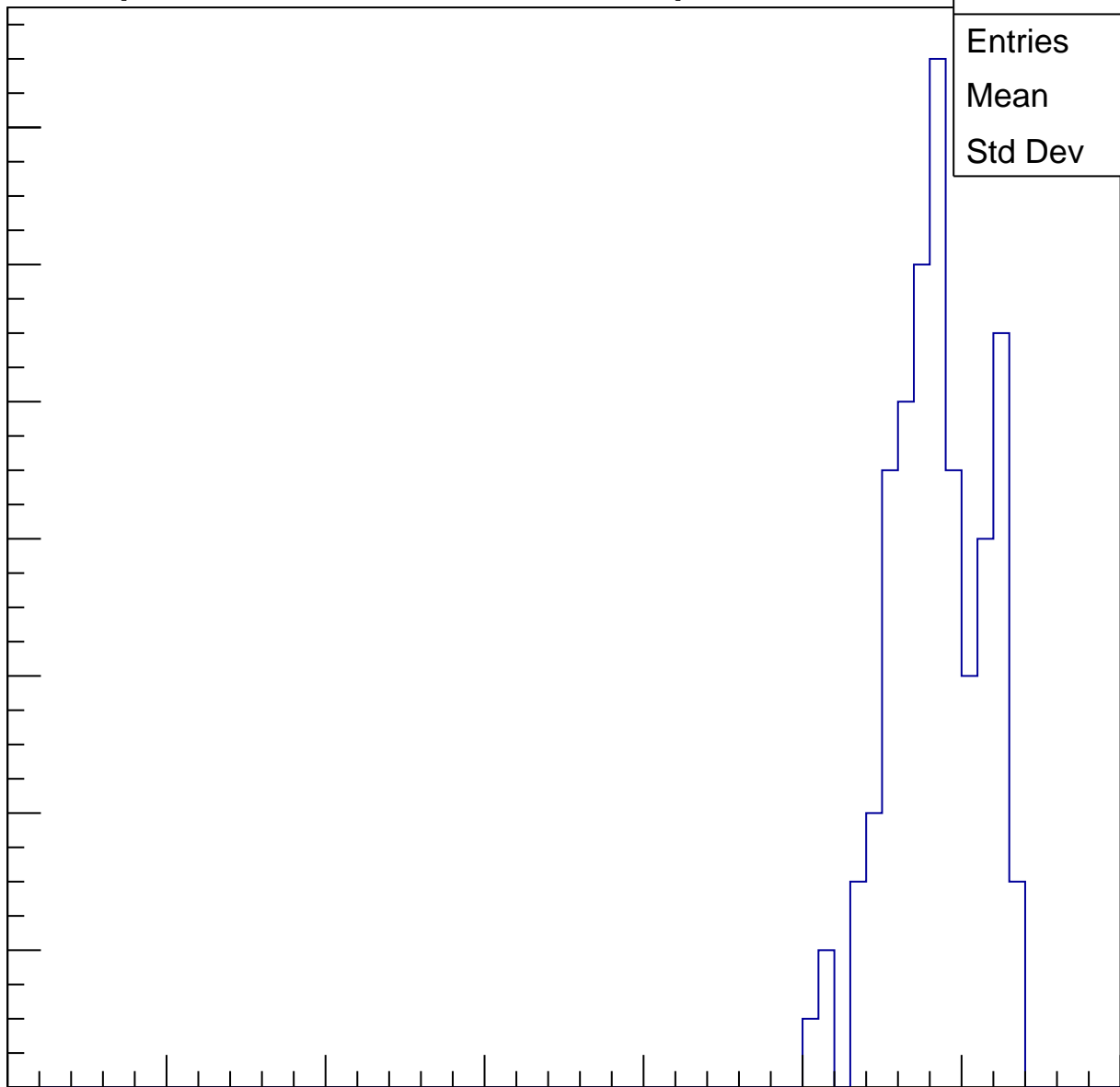
Entries	93
Mean	57.91
Std Dev	2.913

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch55, adc5

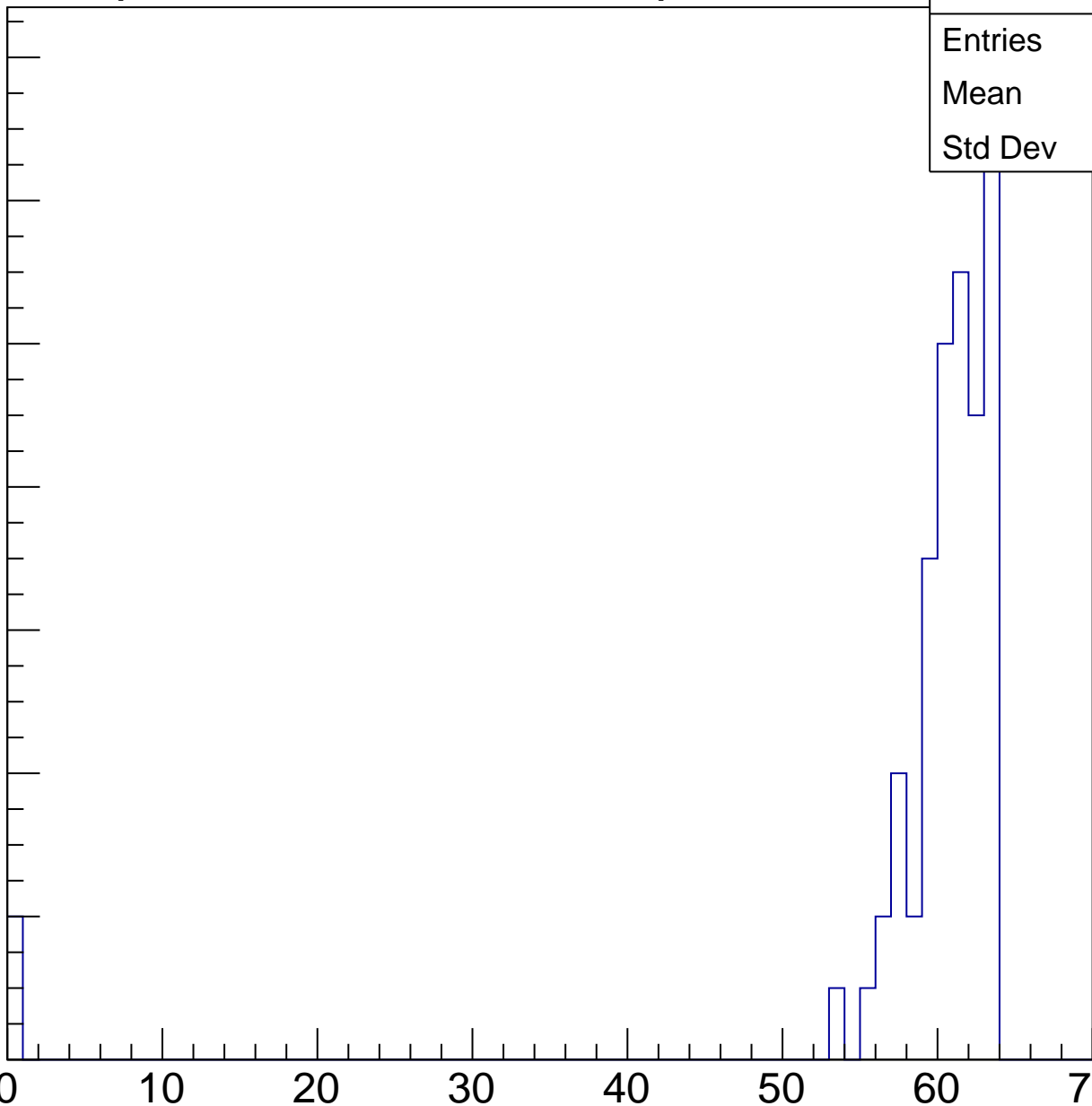
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	63
Mean	58.54
Std Dev	10.83

ampl



# B0L101S, U26-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

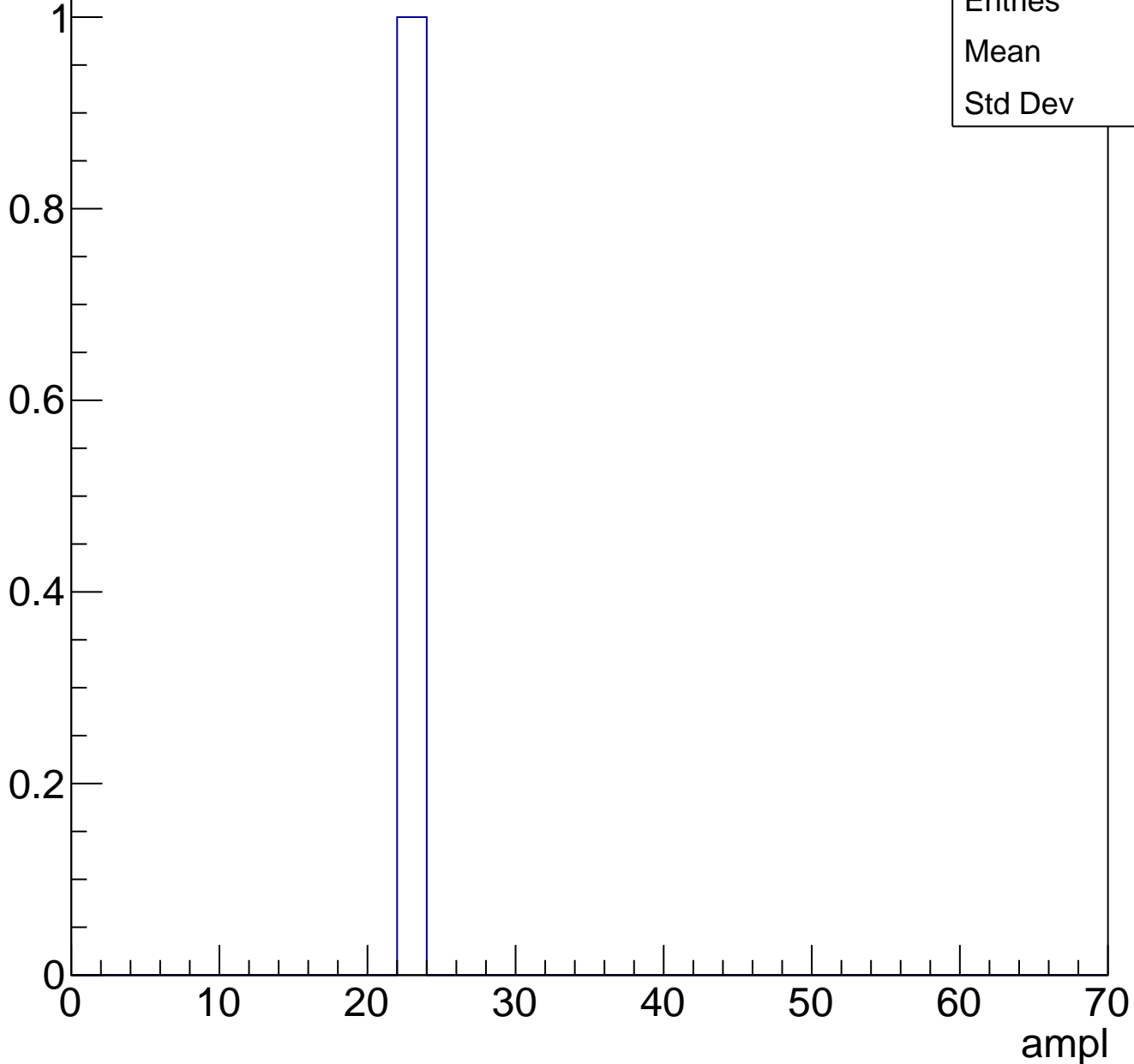




# B0L101S, U26-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch56, adc0

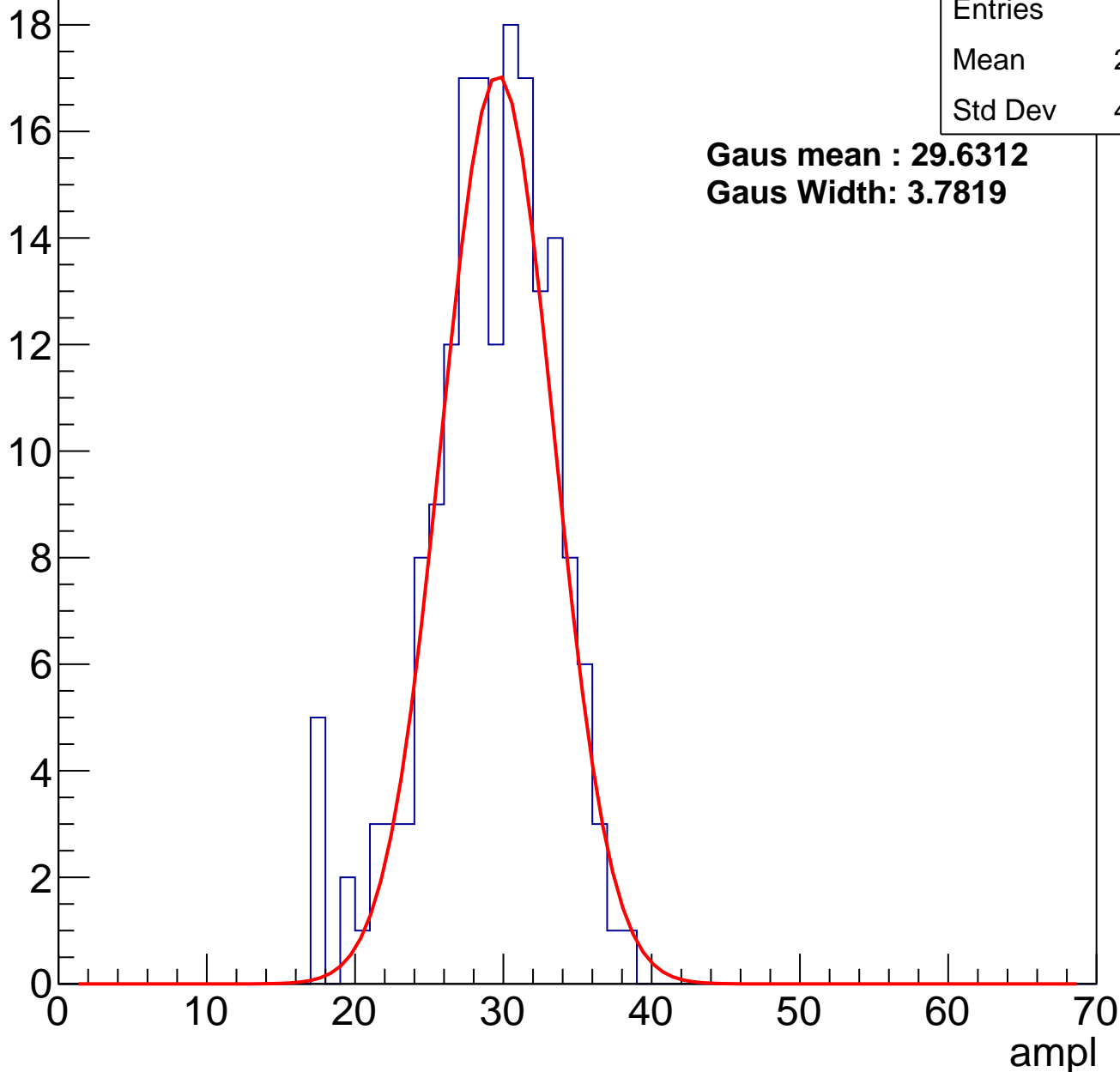
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	173
Mean	28.69
Std Dev	4.235

**Gaus mean : 29.6312**

**Gaus Width: 3.7819**

Entry



# B0L101S, U26-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	36.89
Std Dev	3.58

**Gaus mean : 37.4496**

**Gaus Width: 3.1916**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

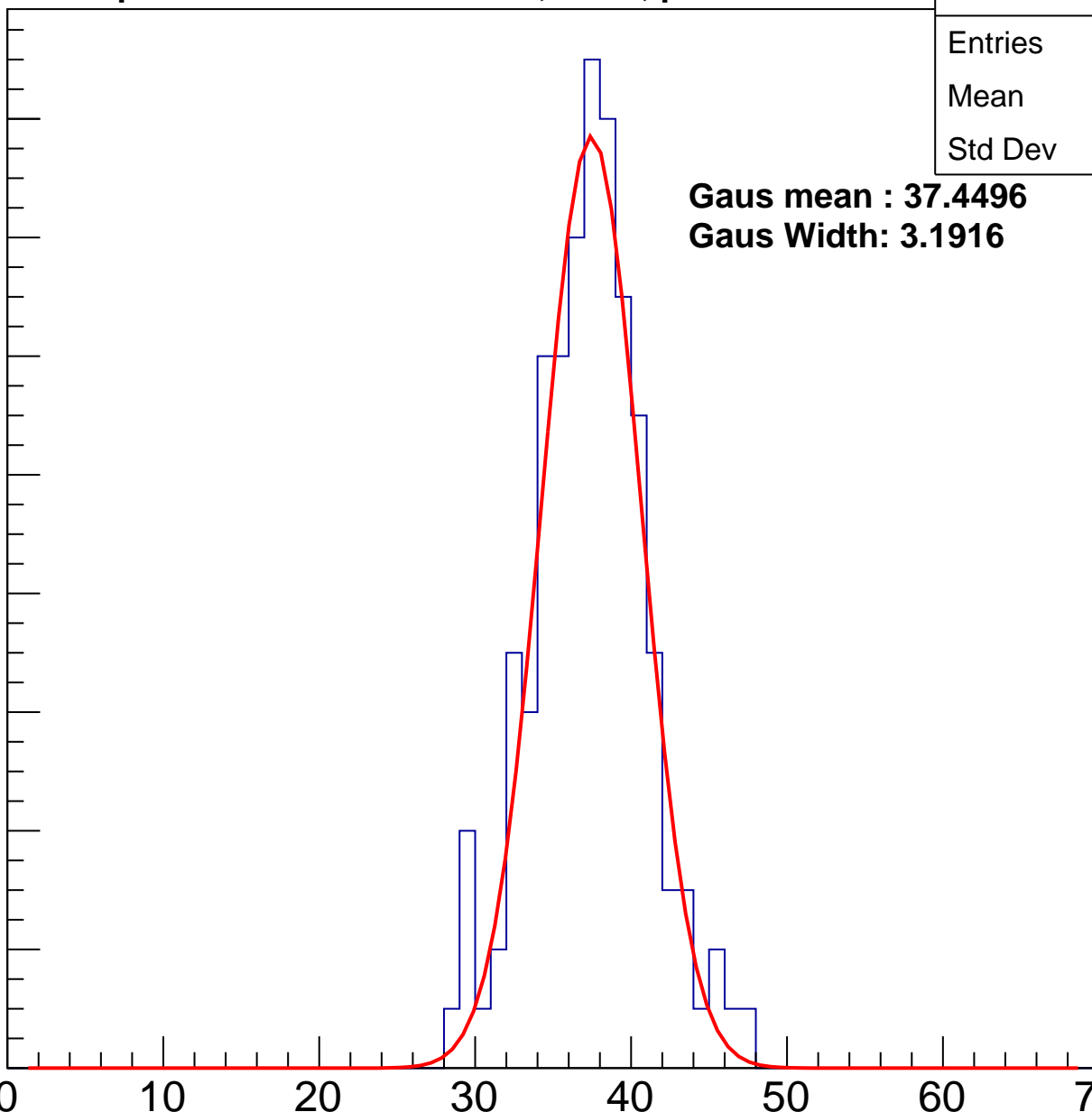
40

50

60

70

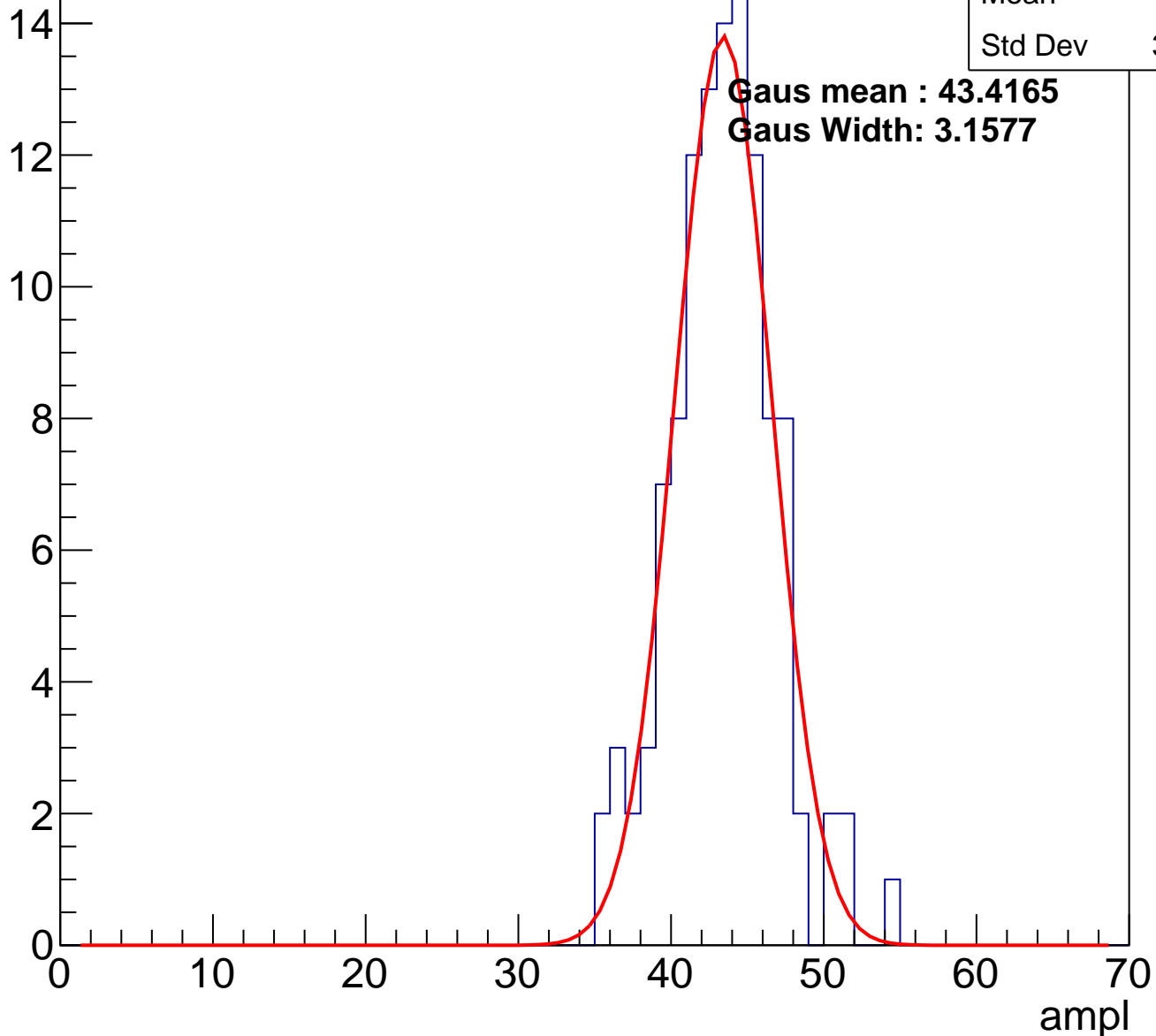
ampl



# B0L101S, U26-ch56, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

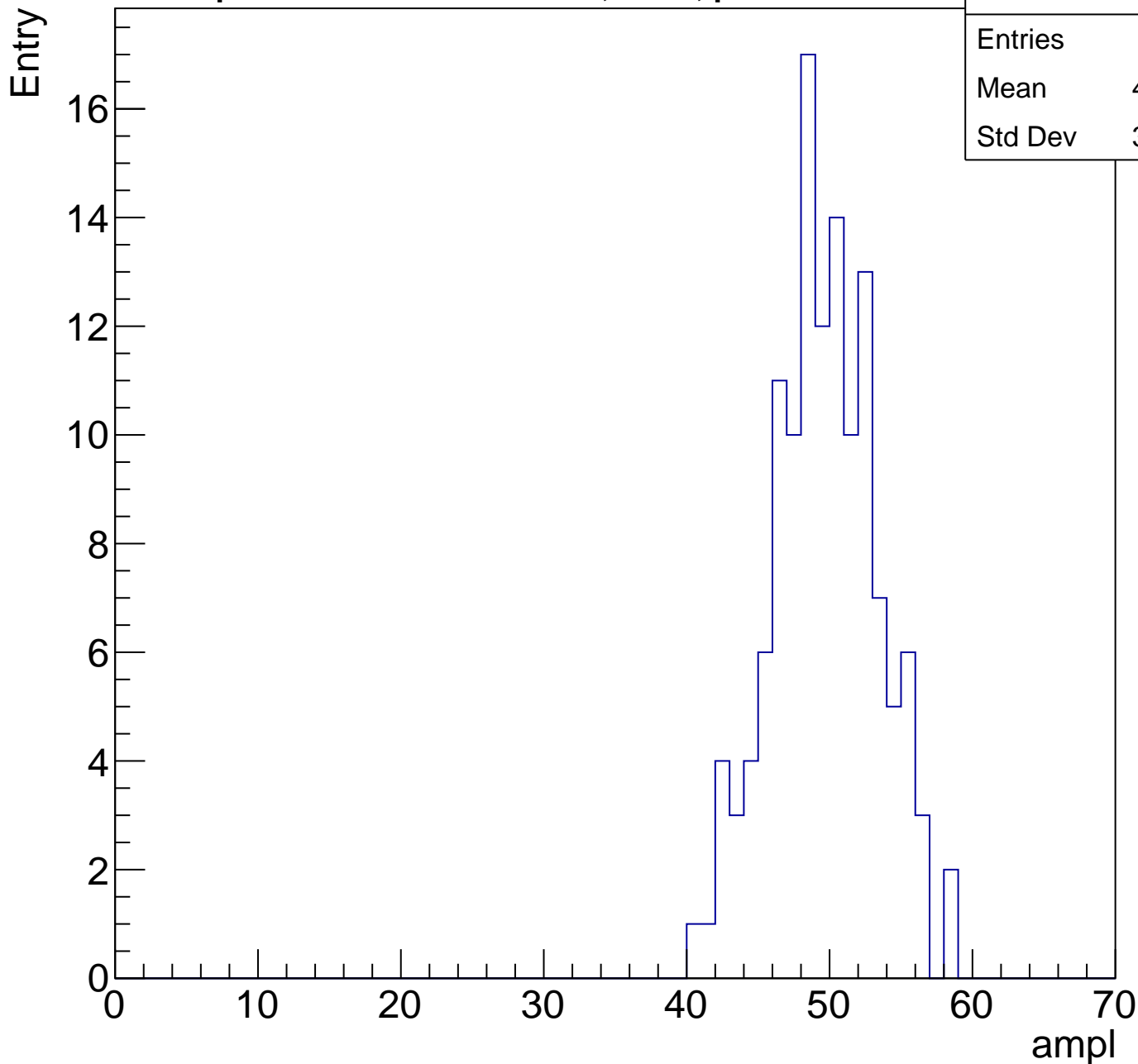


Entries	114
Mean	42.94
Std Dev	3.411

# B0L101S, U26-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	49.19
Std Dev	3.662



# B0L101S, U26-ch56, adc4

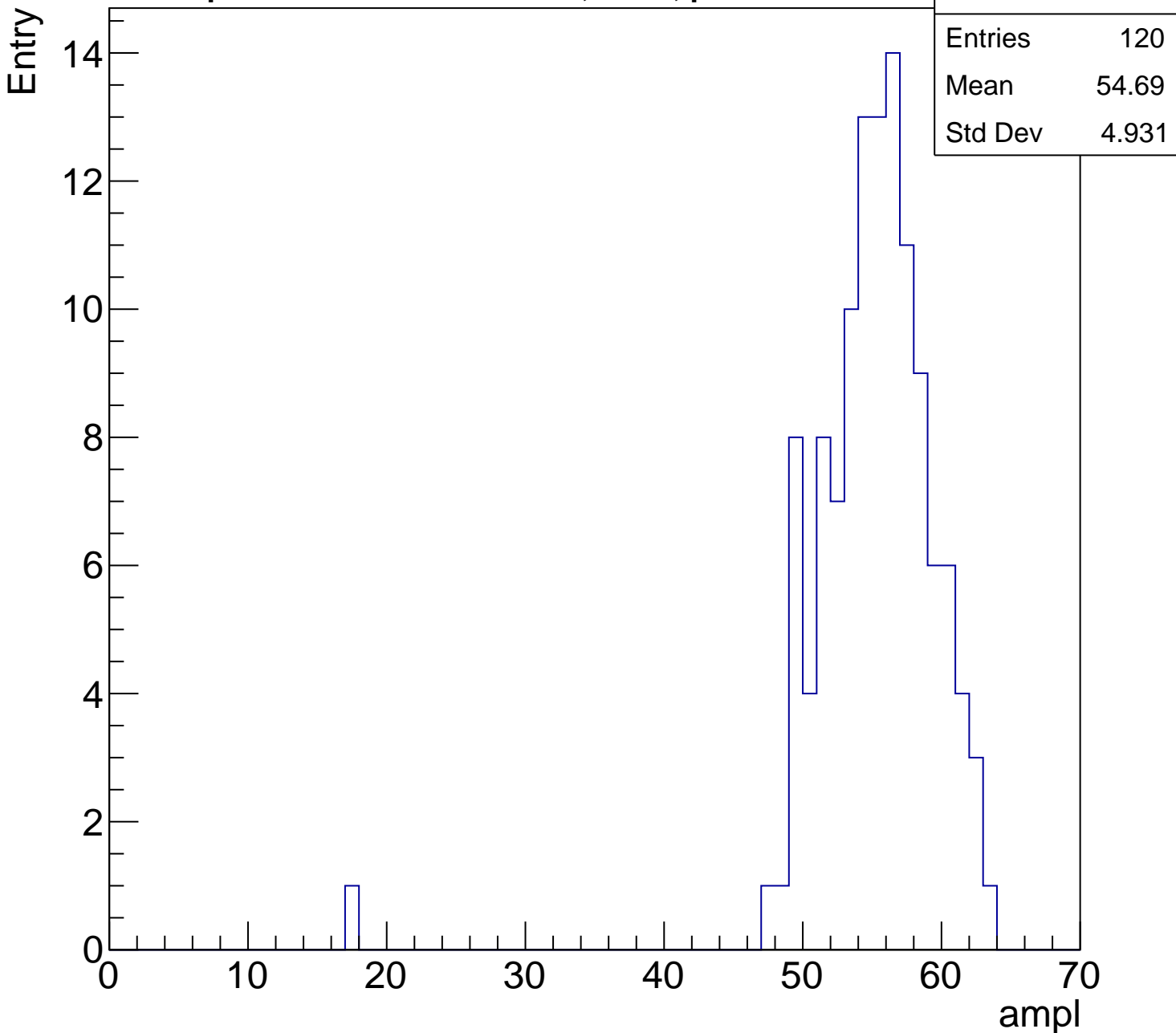
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	54.69
Std Dev	4.931

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch56, adc5

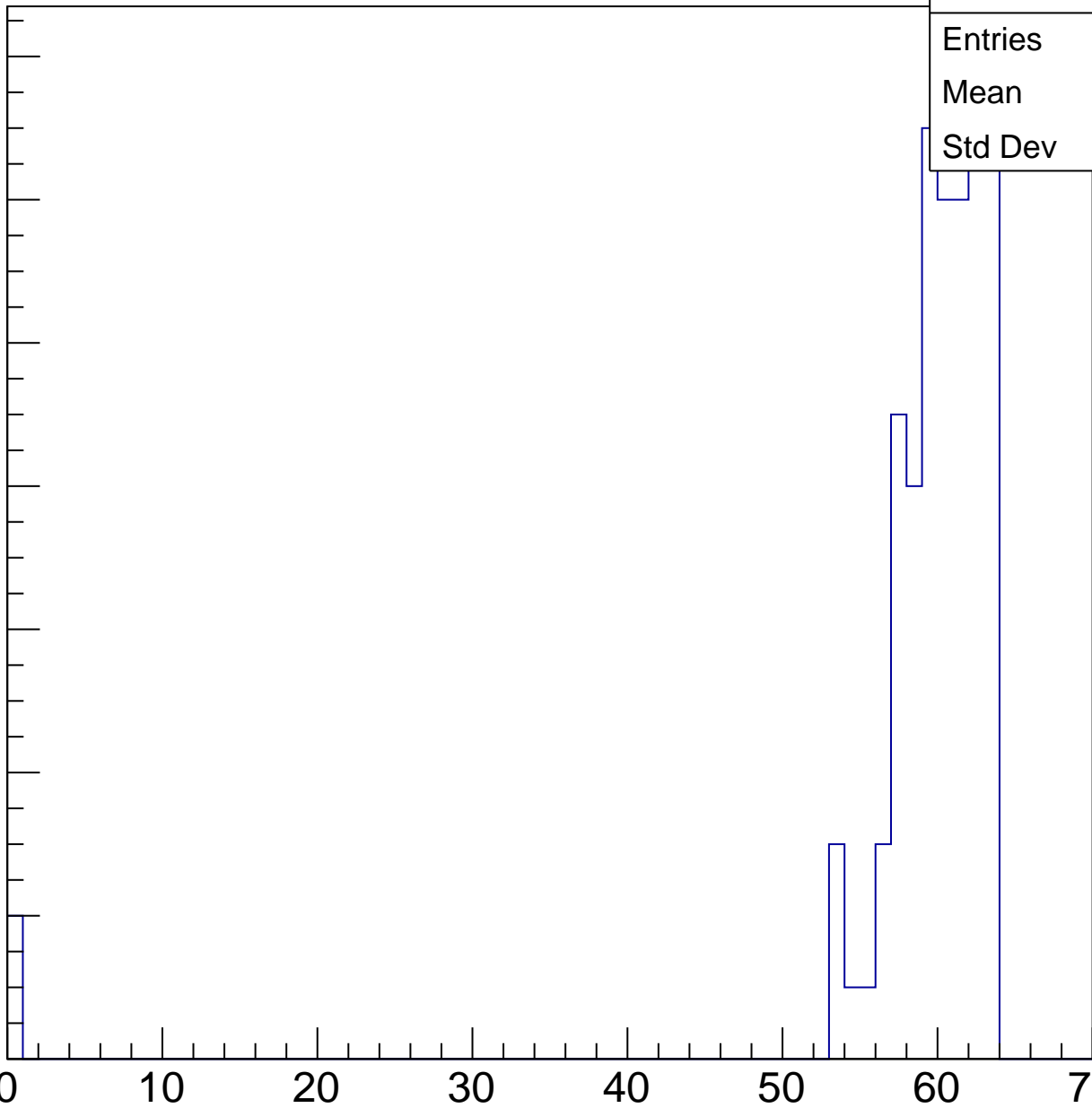
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	91
Mean	58.45
Std Dev	9.108

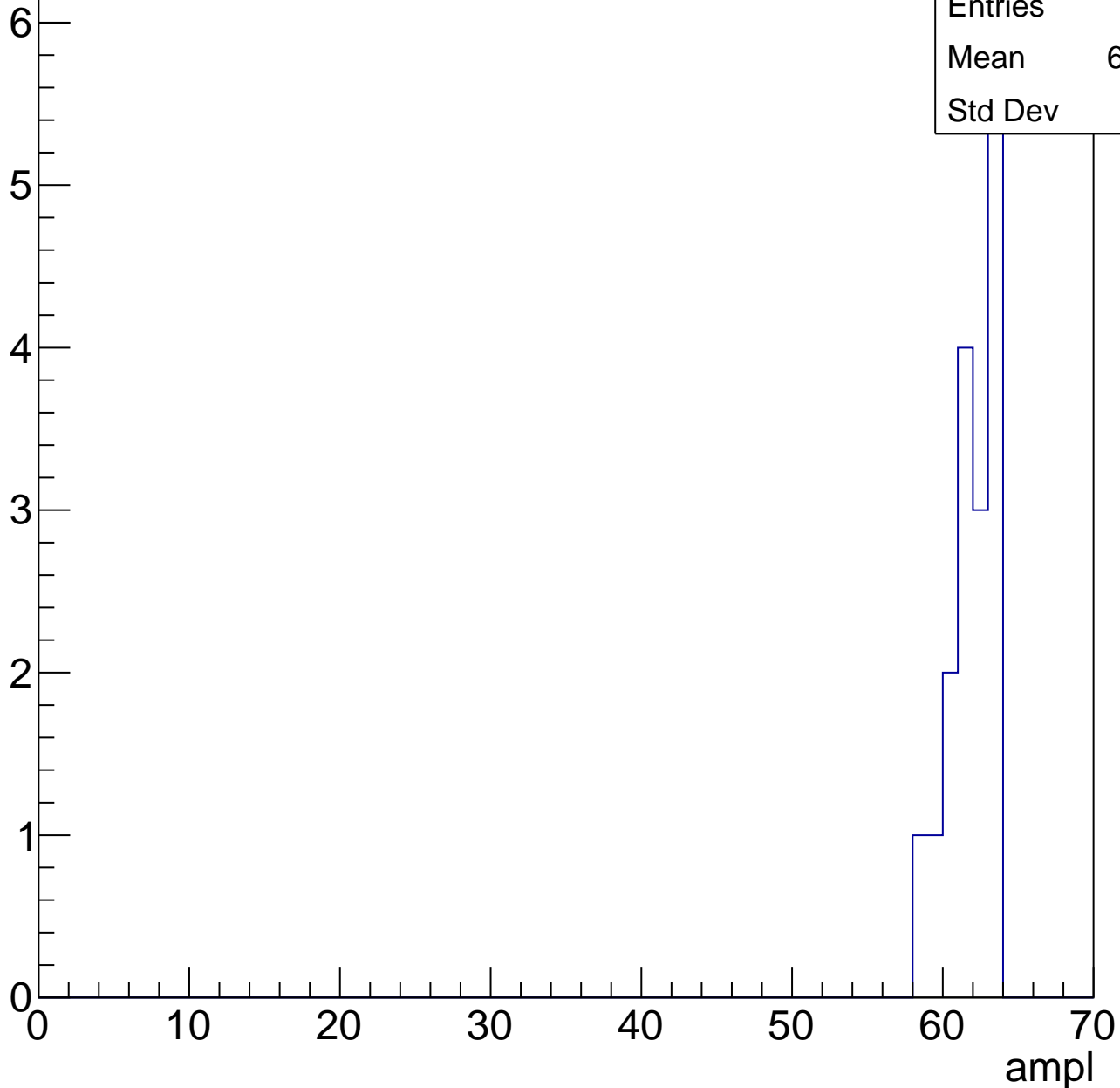
ampl



# B0L101S, U26-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

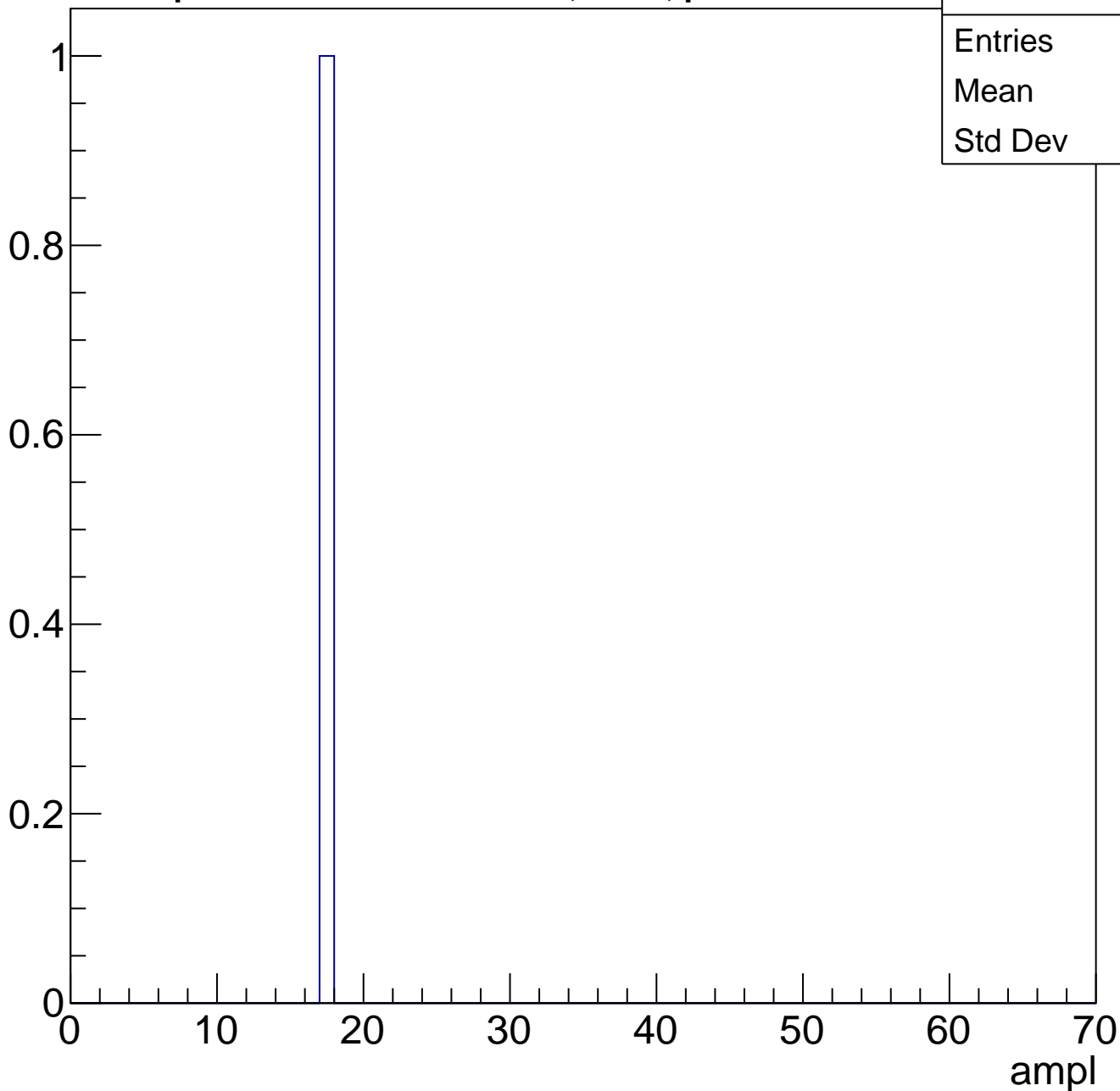




# B0L101S, U26-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



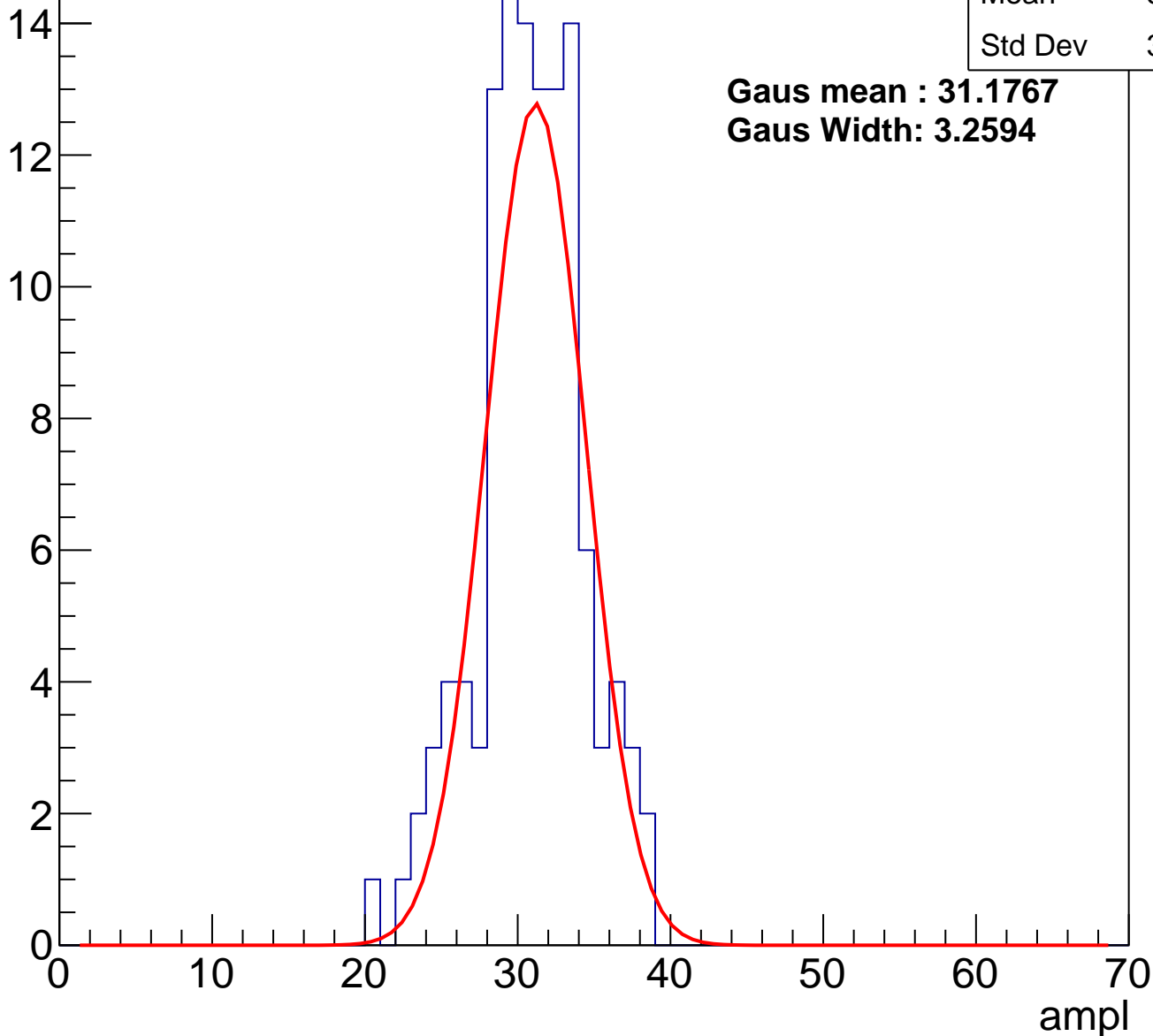
# B0L101S, U26-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	30.38
Std Dev	3.434

**Gaus mean : 31.1767**  
**Gaus Width: 3.2594**

Entry



# B0L101S, U26-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	37.84
Std Dev	3.893

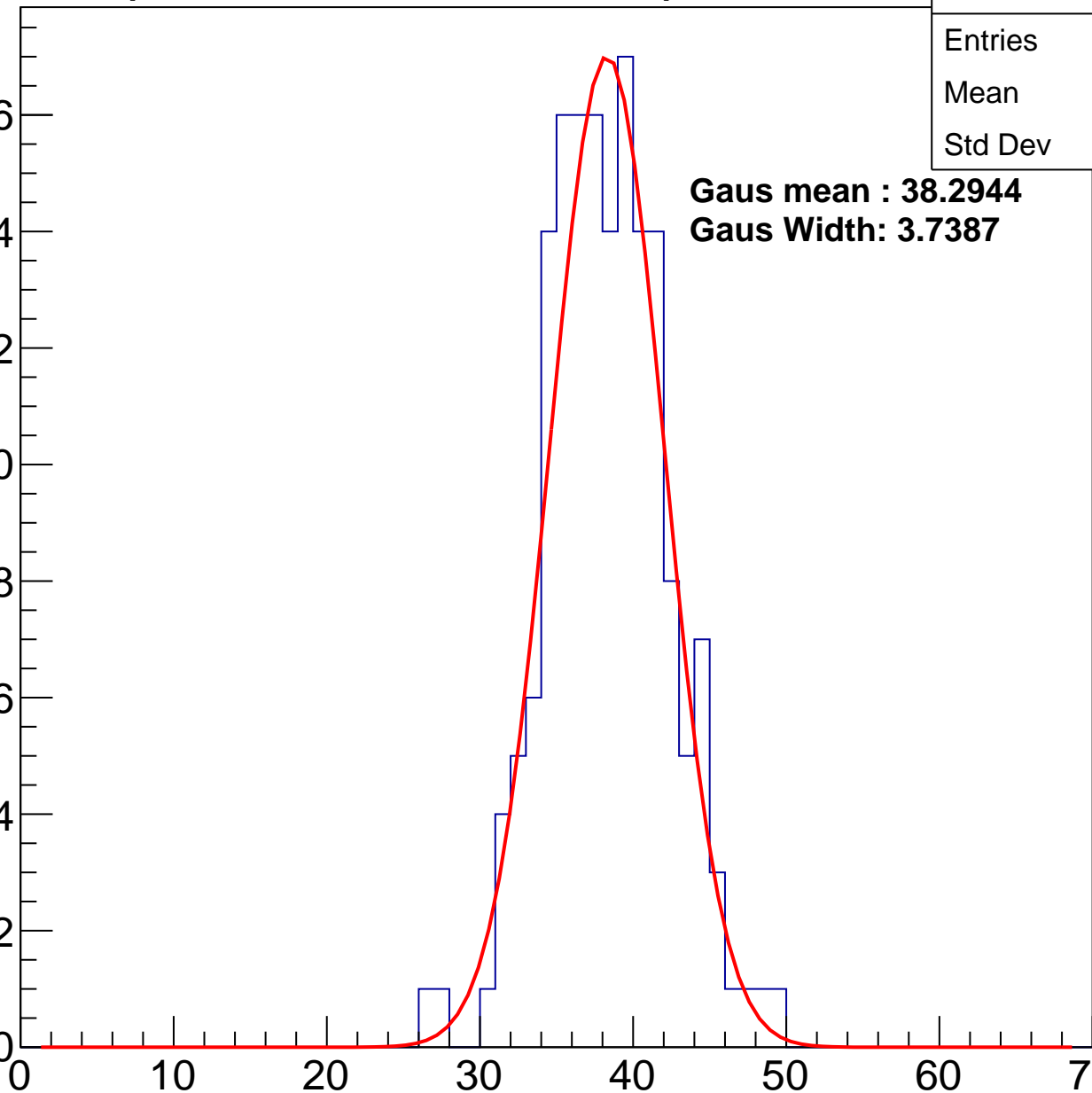
**Gaus mean : 38.2944**

**Gaus Width: 3.7387**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

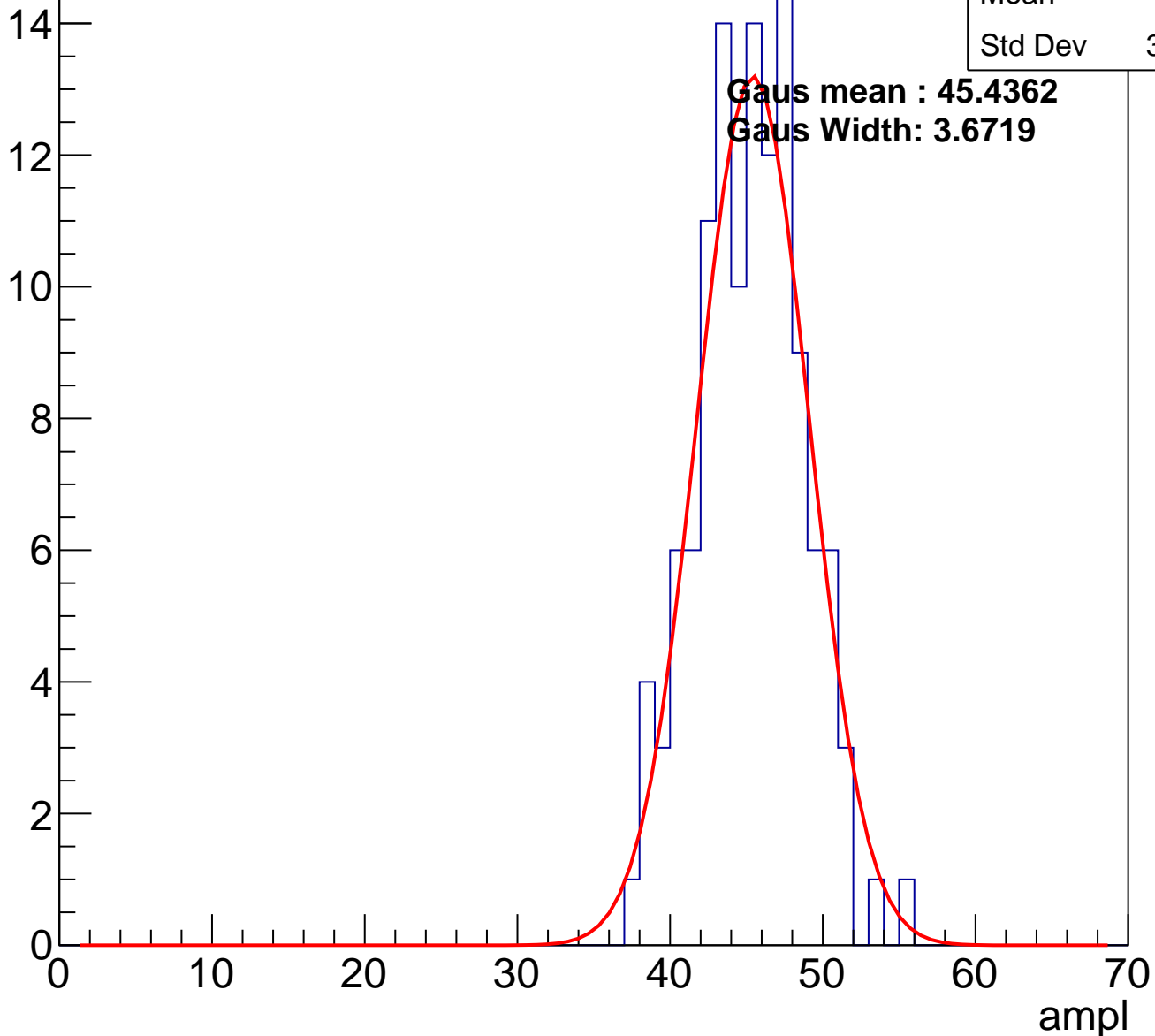
ampl



# B0L101S, U26-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

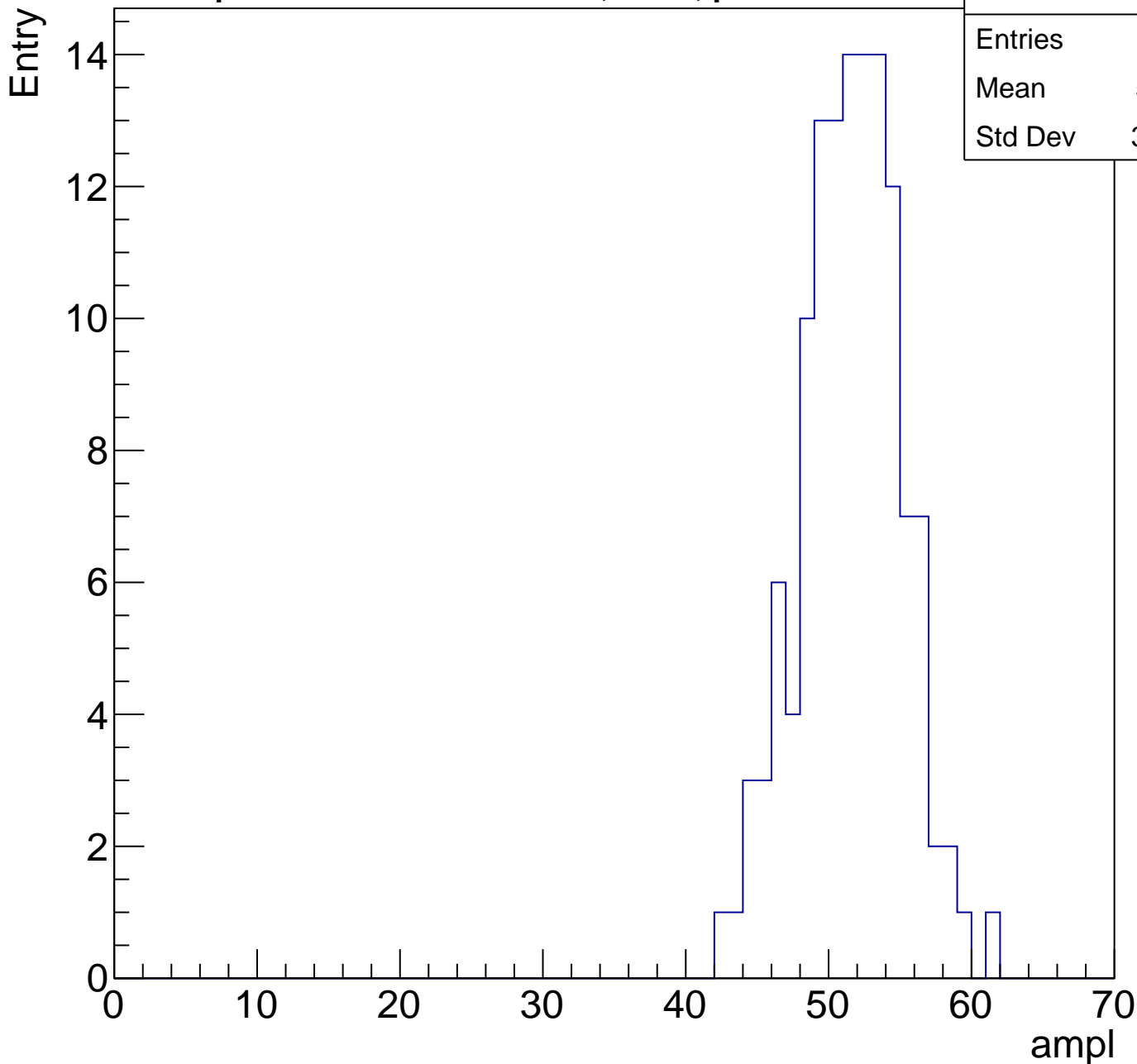
Entry



# B0L101S, U26-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	51.11
Std Dev	3.498



# B0L101S, U26-ch57, adc4

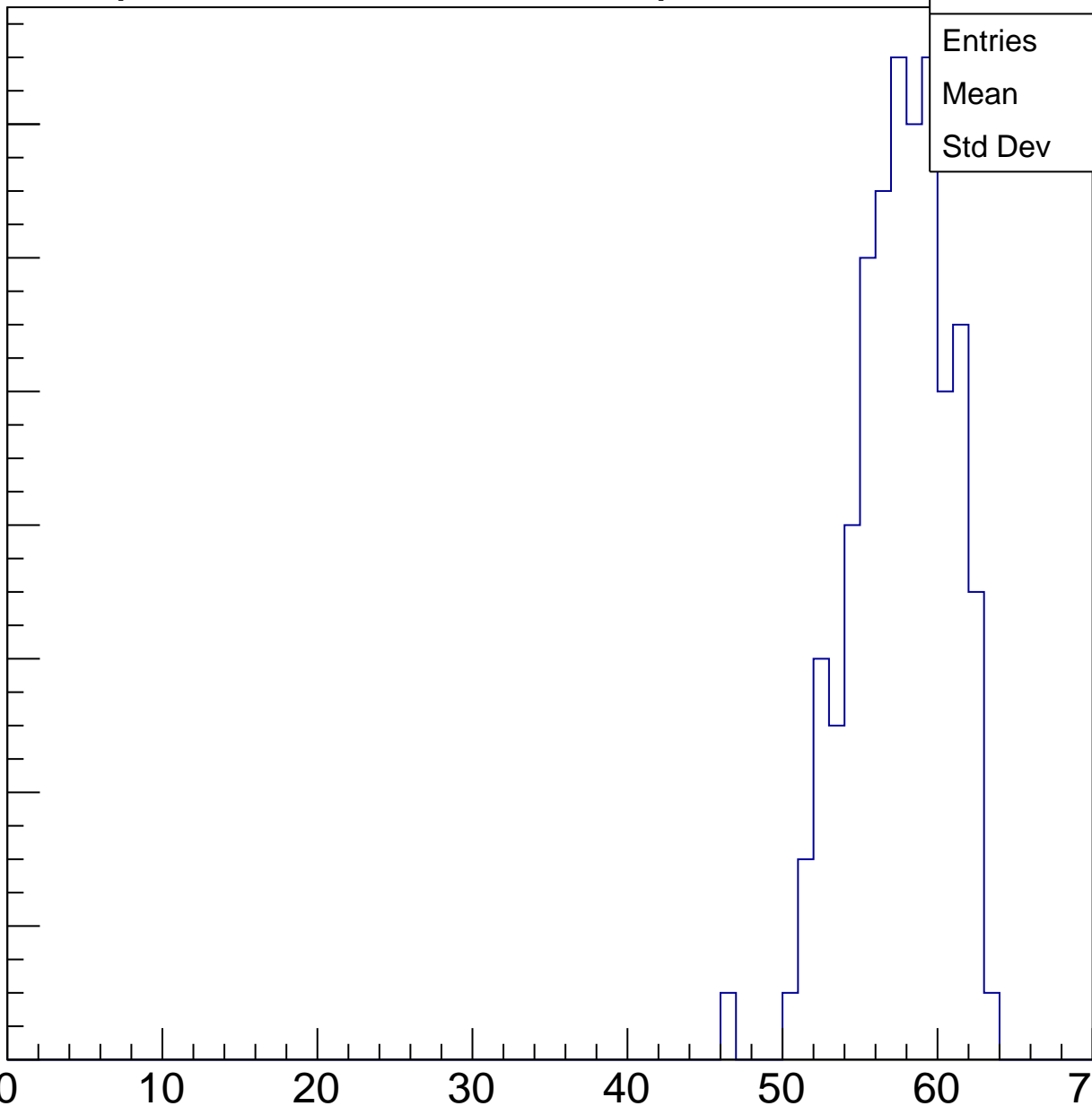
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	57.1
Std Dev	3.124

ampl



# B0L101S, U26-ch57, adc5

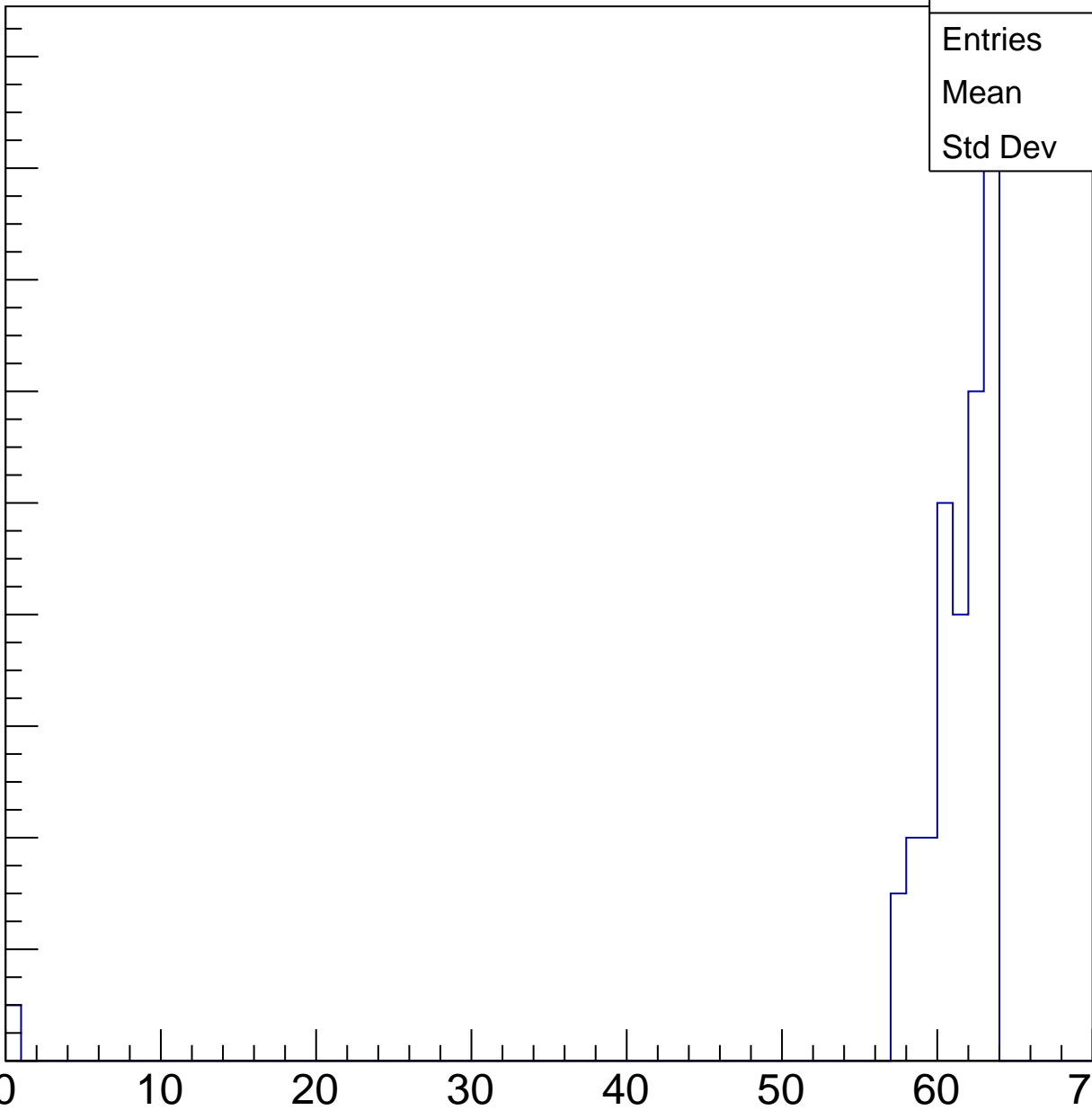
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	60
Mean	60.08
Std Dev	8.026

ampl



# B0L101S, U26-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch58, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	29.5
Std Dev	3.781

**Gaus mean : 29.8404**

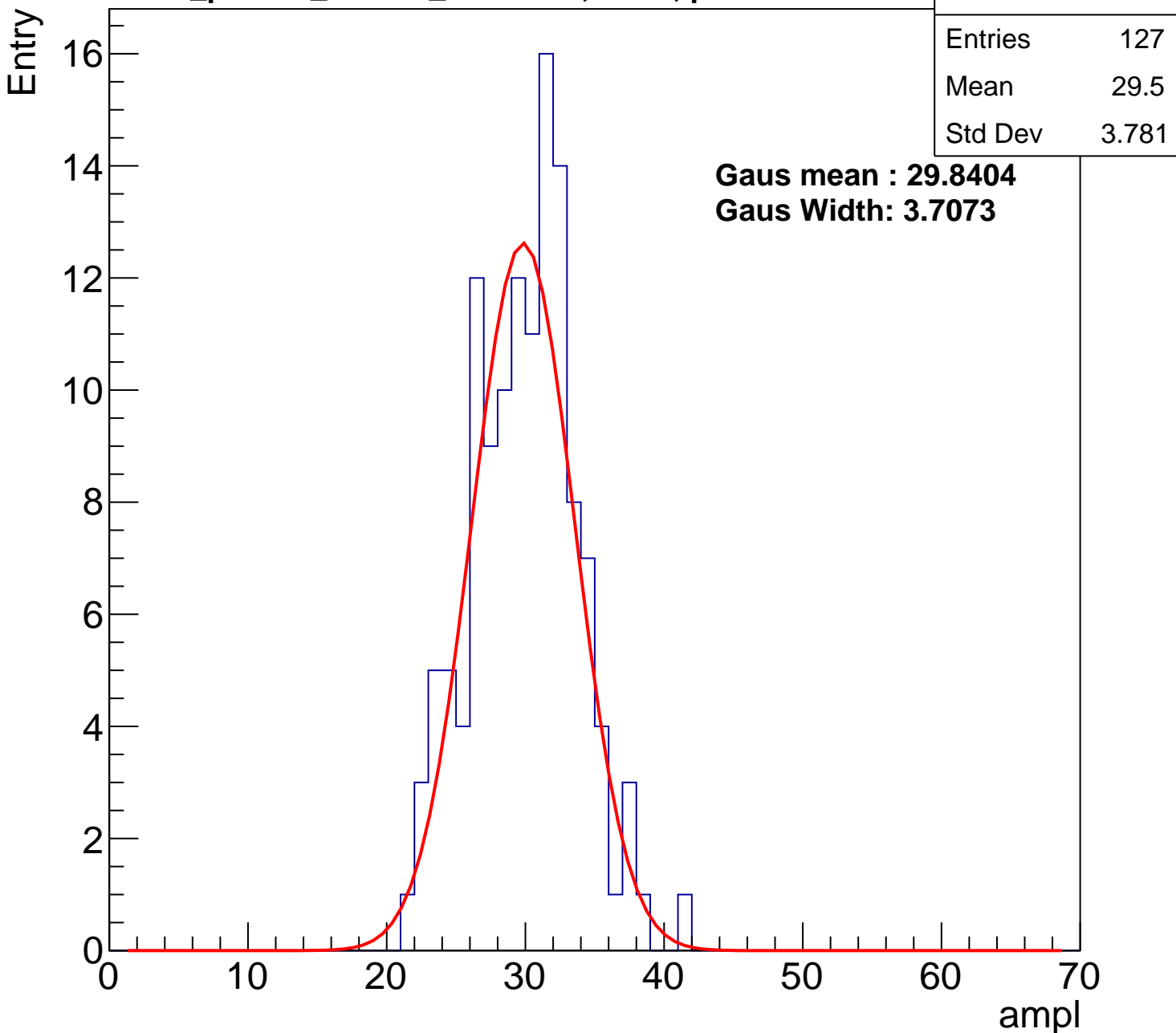
**Gaus Width: 3.7073**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch58, adc1

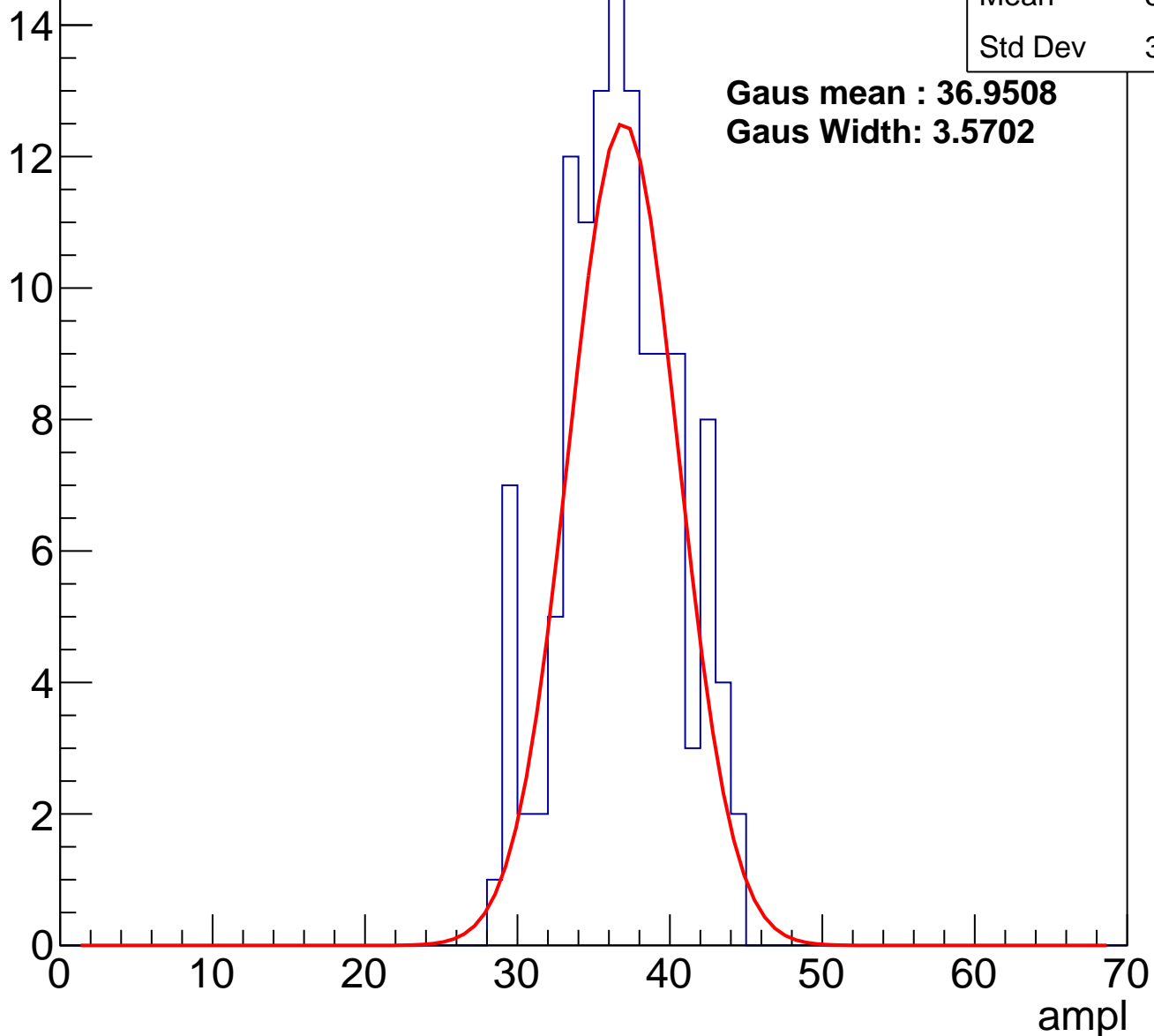
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	36.25
Std Dev	3.717

**Gaus mean : 36.9508**

**Gaus Width: 3.5702**

Entry



# B0L101S, U26-ch58, adc2

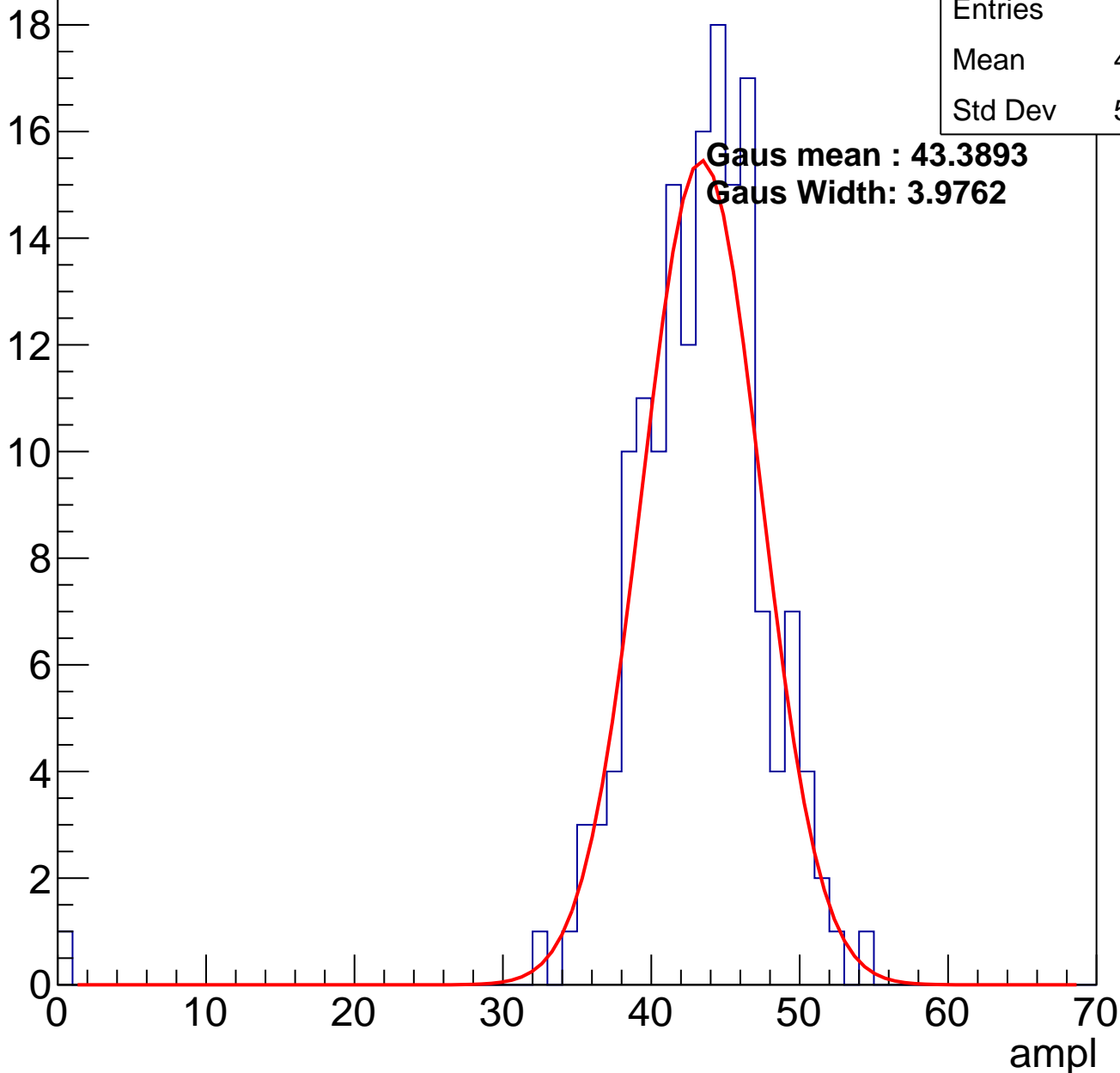
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	42.72
Std Dev	5.154

**Gaus mean : 43.3893**

**Gaus Width: 3.9762**

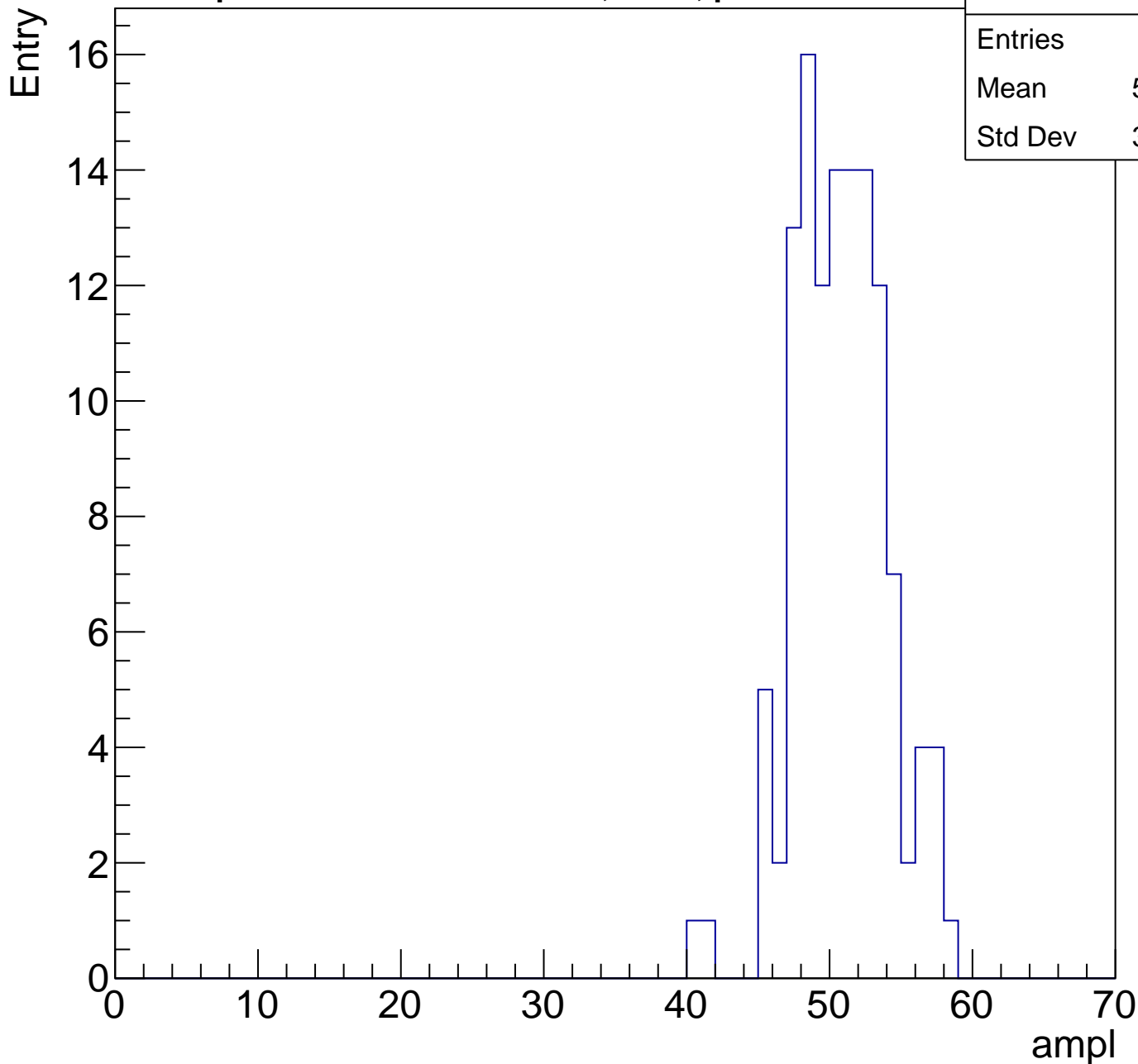
Entry



# B0L101S, U26-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

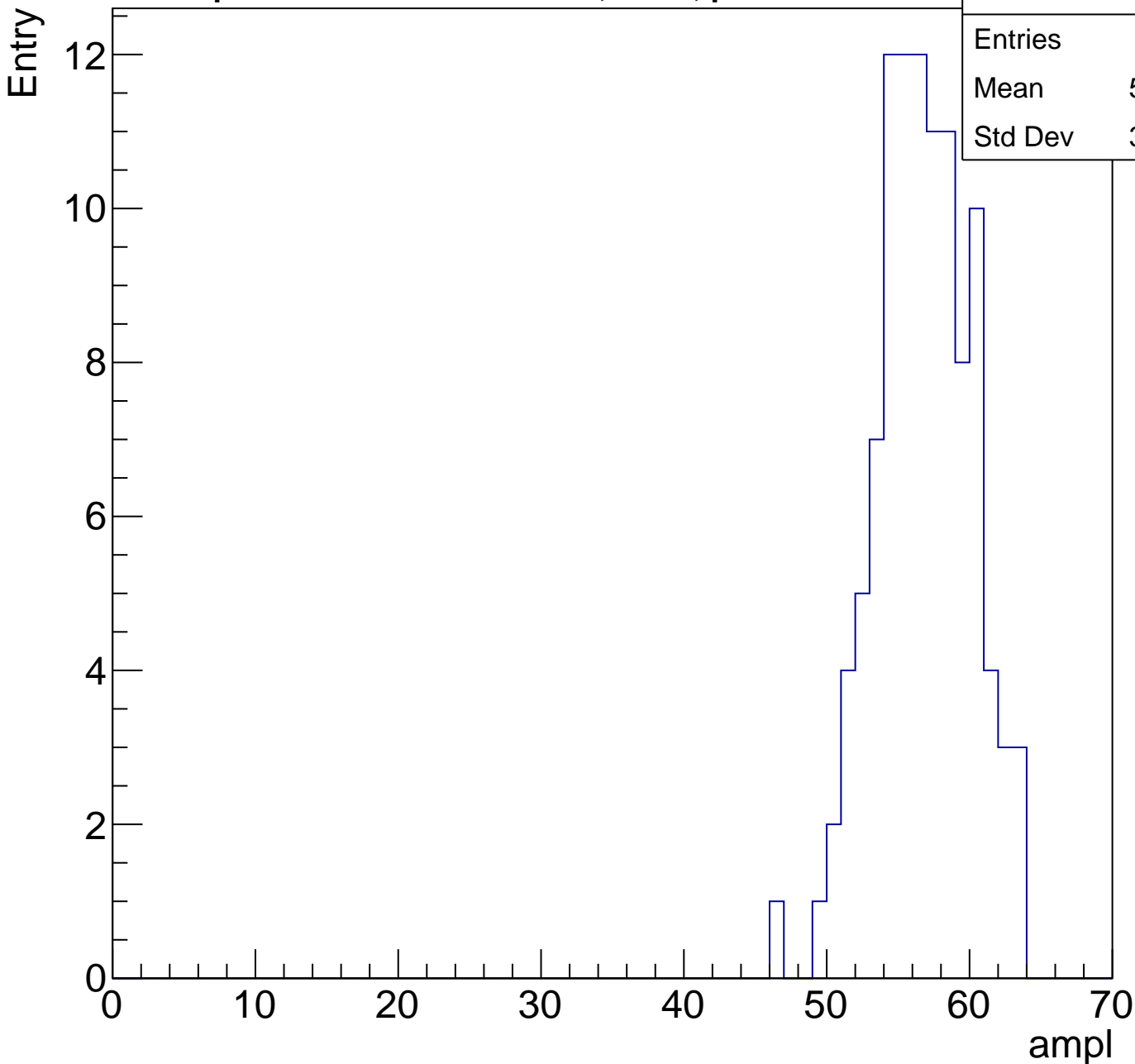
Entries	122
Mean	50.34
Std Dev	3.215



# B0L101S, U26-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	56.28
Std Dev	3.305



# B0L101S, U26-ch58, adc5

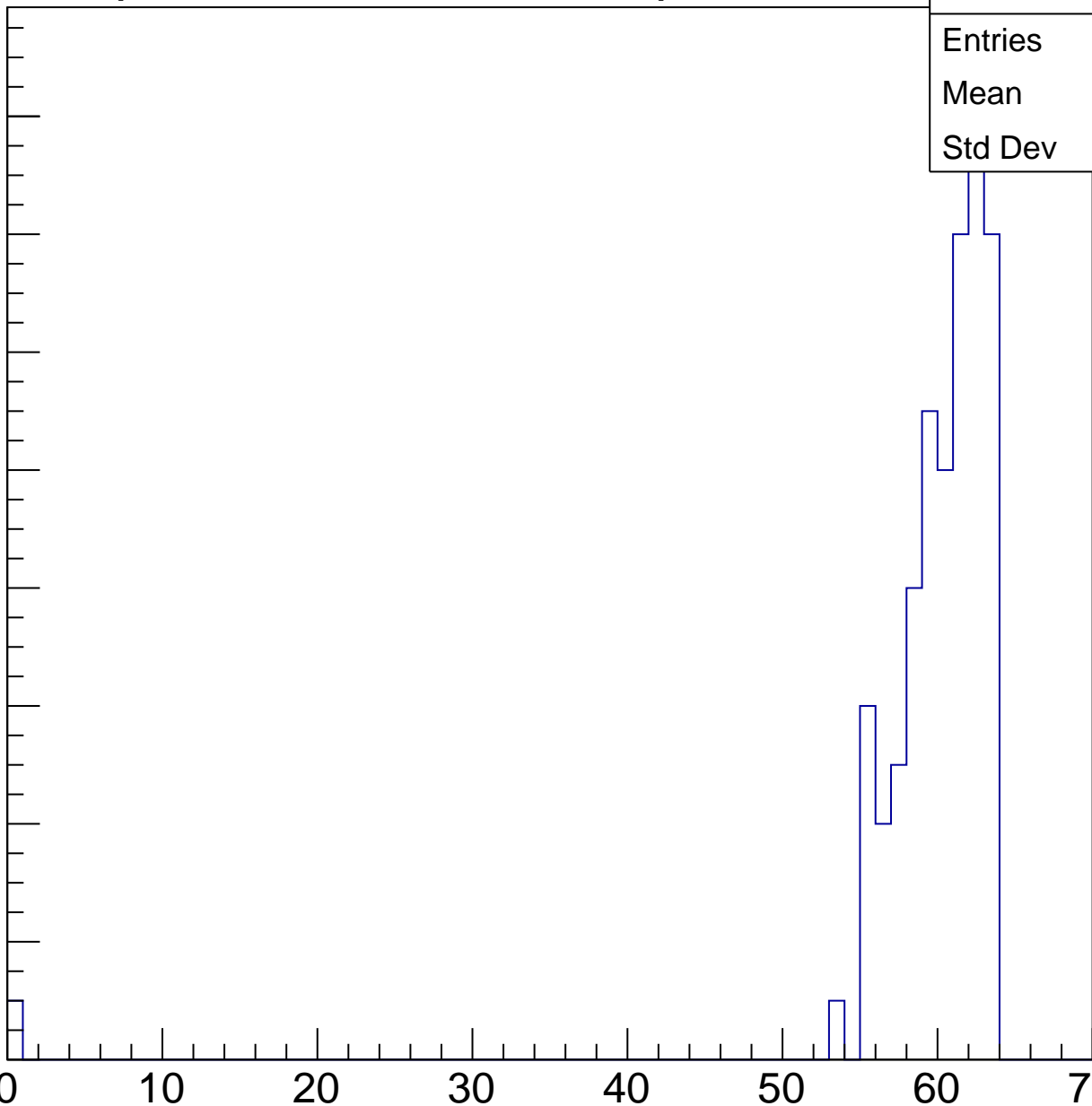
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	91
Mean	59.29
Std Dev	6.723

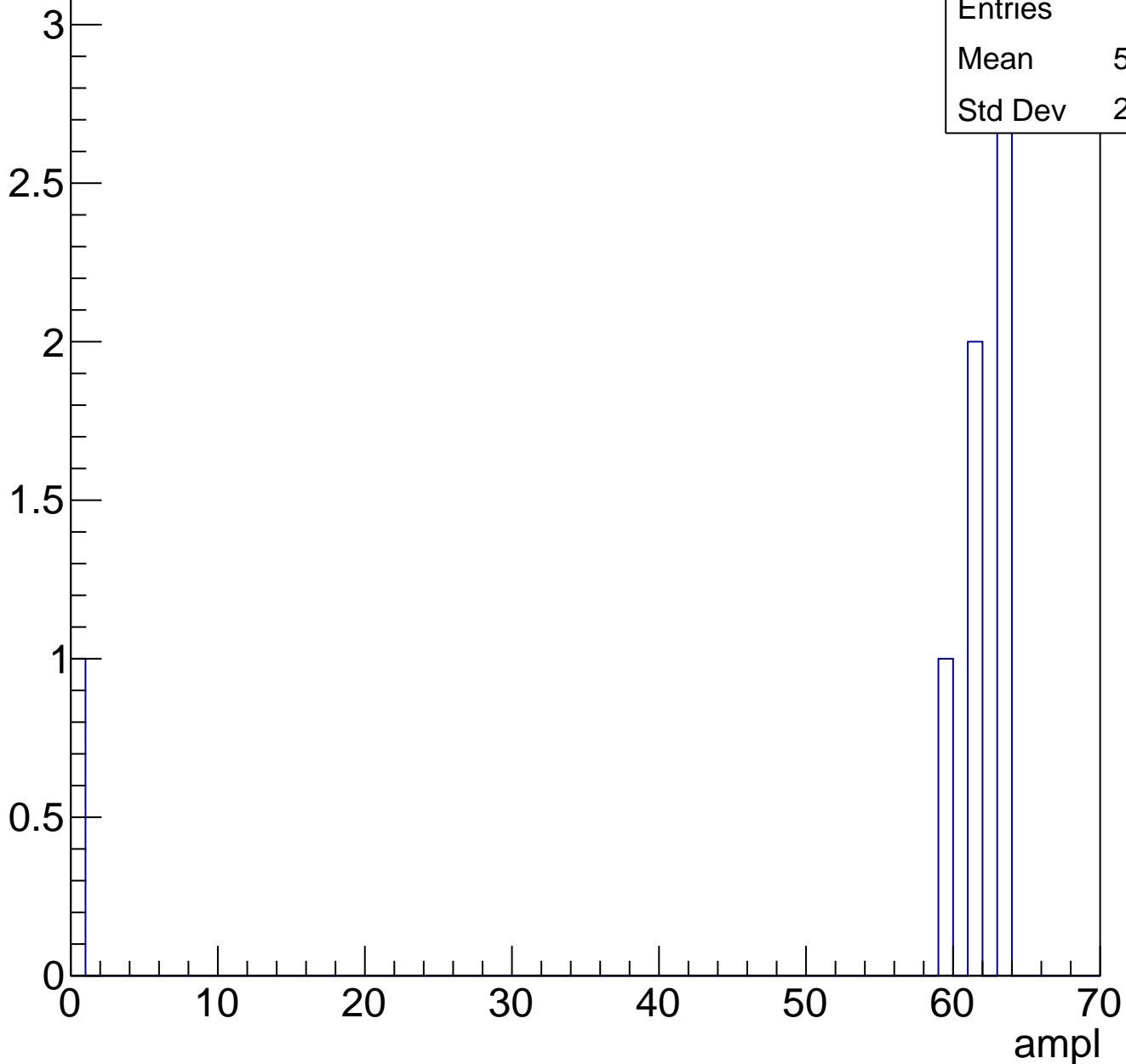
ampl



# B0L101S, U26-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

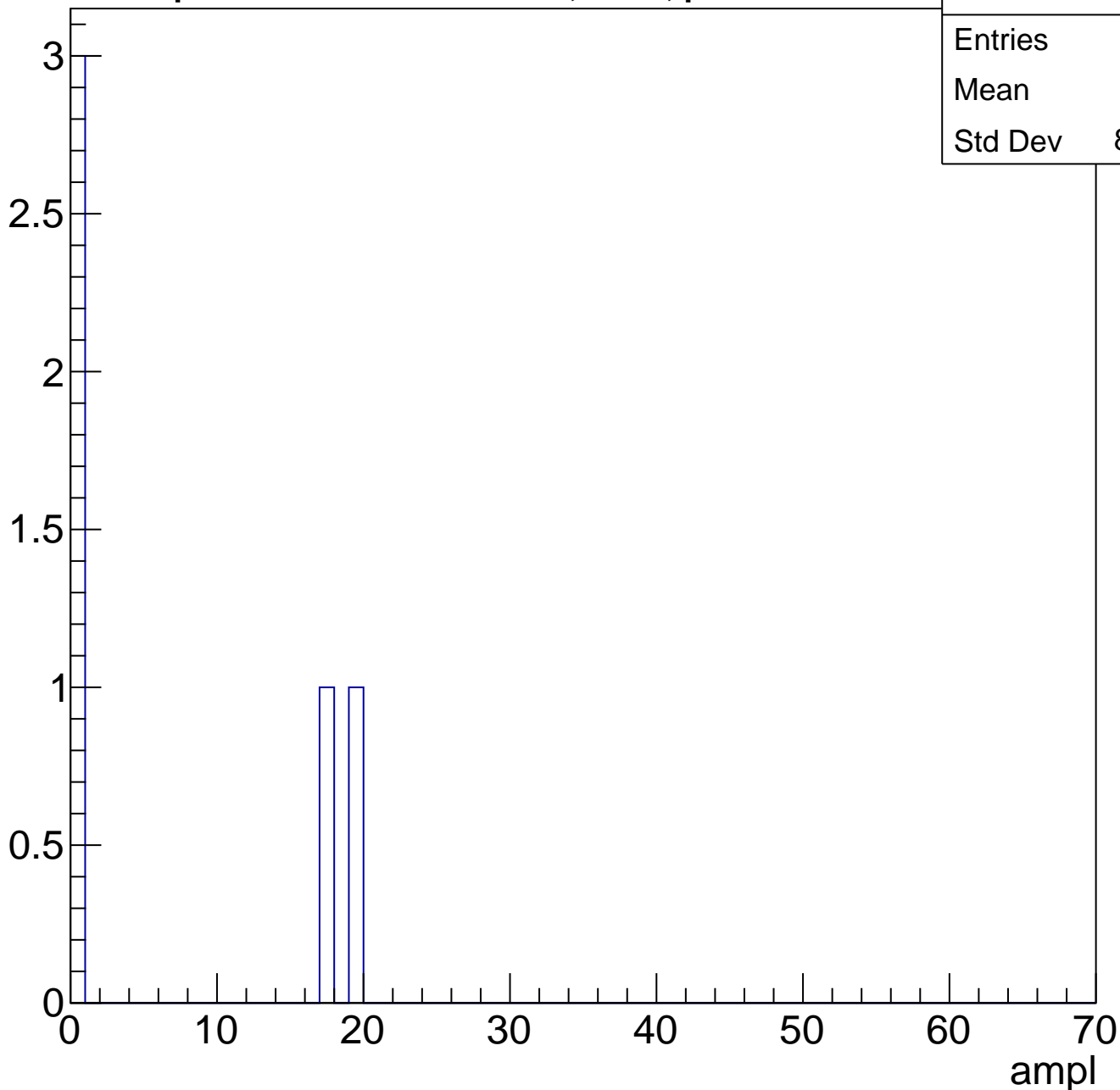




# B0L101S, U26-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	5
Mean	7.2
Std Dev	8.841

# B0L101S, U26-ch59, adc0

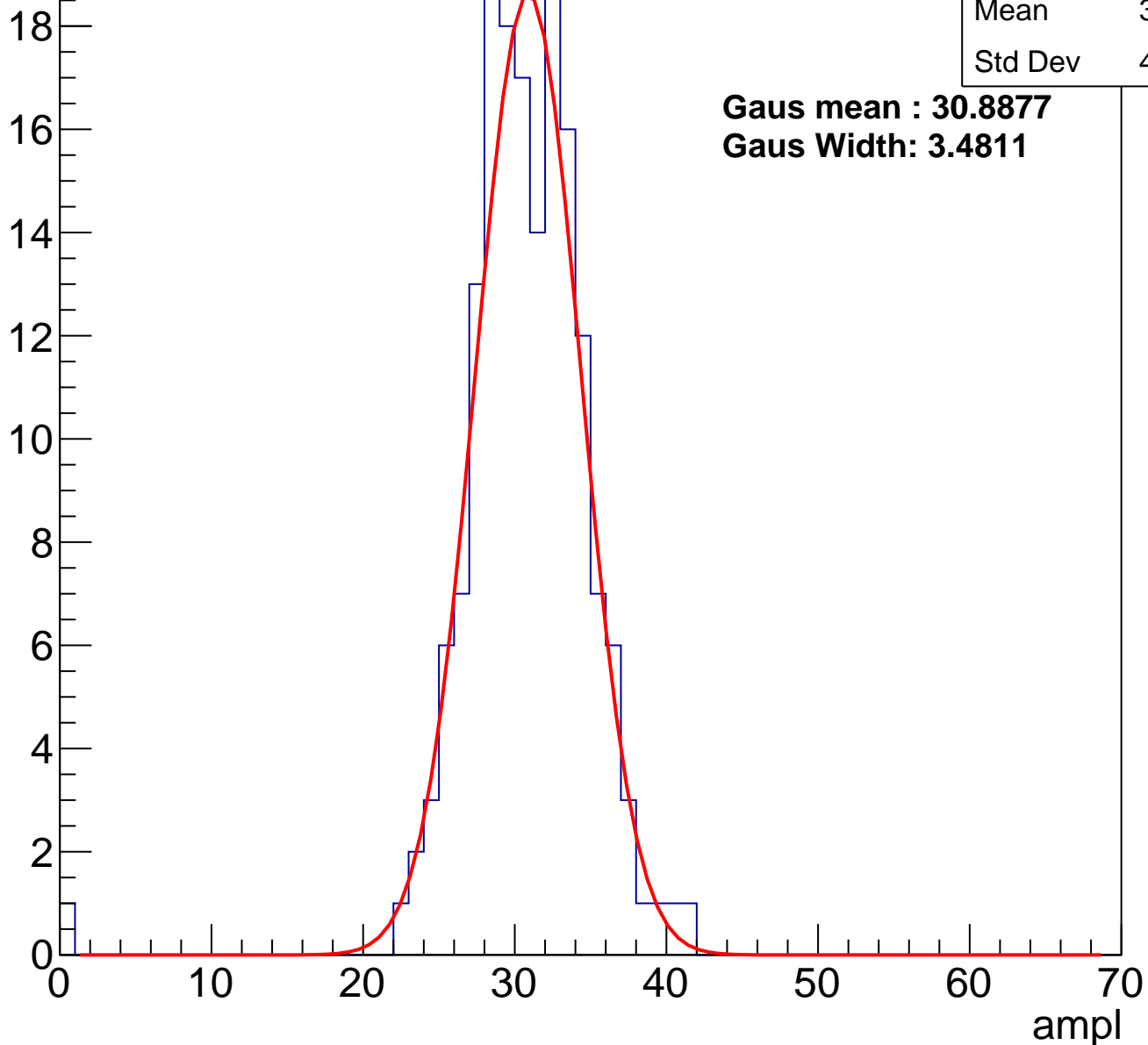
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	168
Mean	30.33
Std Dev	4.194

**Gaus mean : 30.8877**

**Gaus Width: 3.4811**

Entry



# B0L101S, U26-ch59, adc1

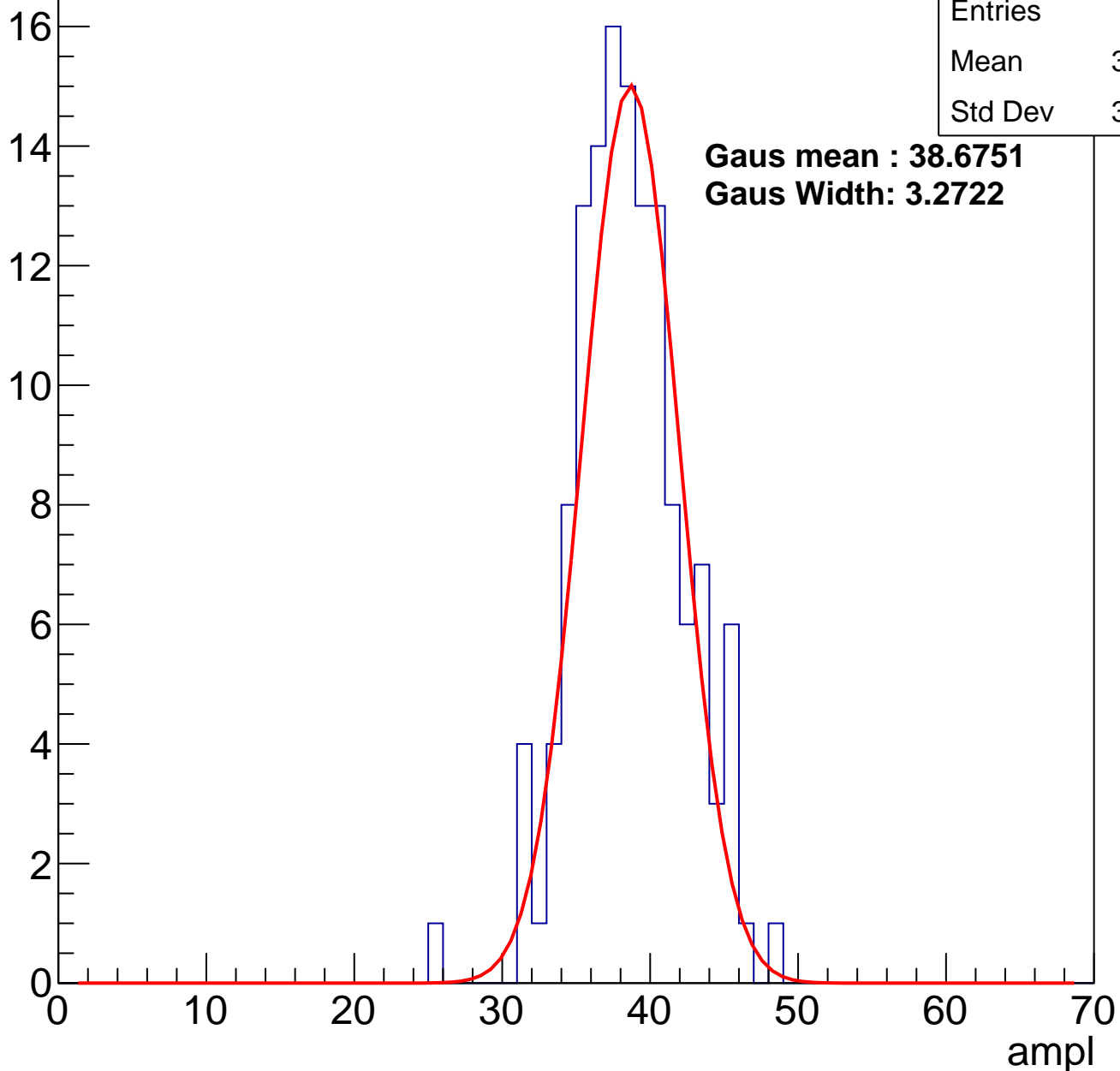
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	38.13
Std Dev	3.654

**Gaus mean : 38.6751**

**Gaus Width: 3.2722**

Entry



# B0L101S, U26-ch59, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

44.4

Std Dev

3.331

**Gaus mean : 44.7321**

**Gaus Width: 3.4080**

0

10

20

30

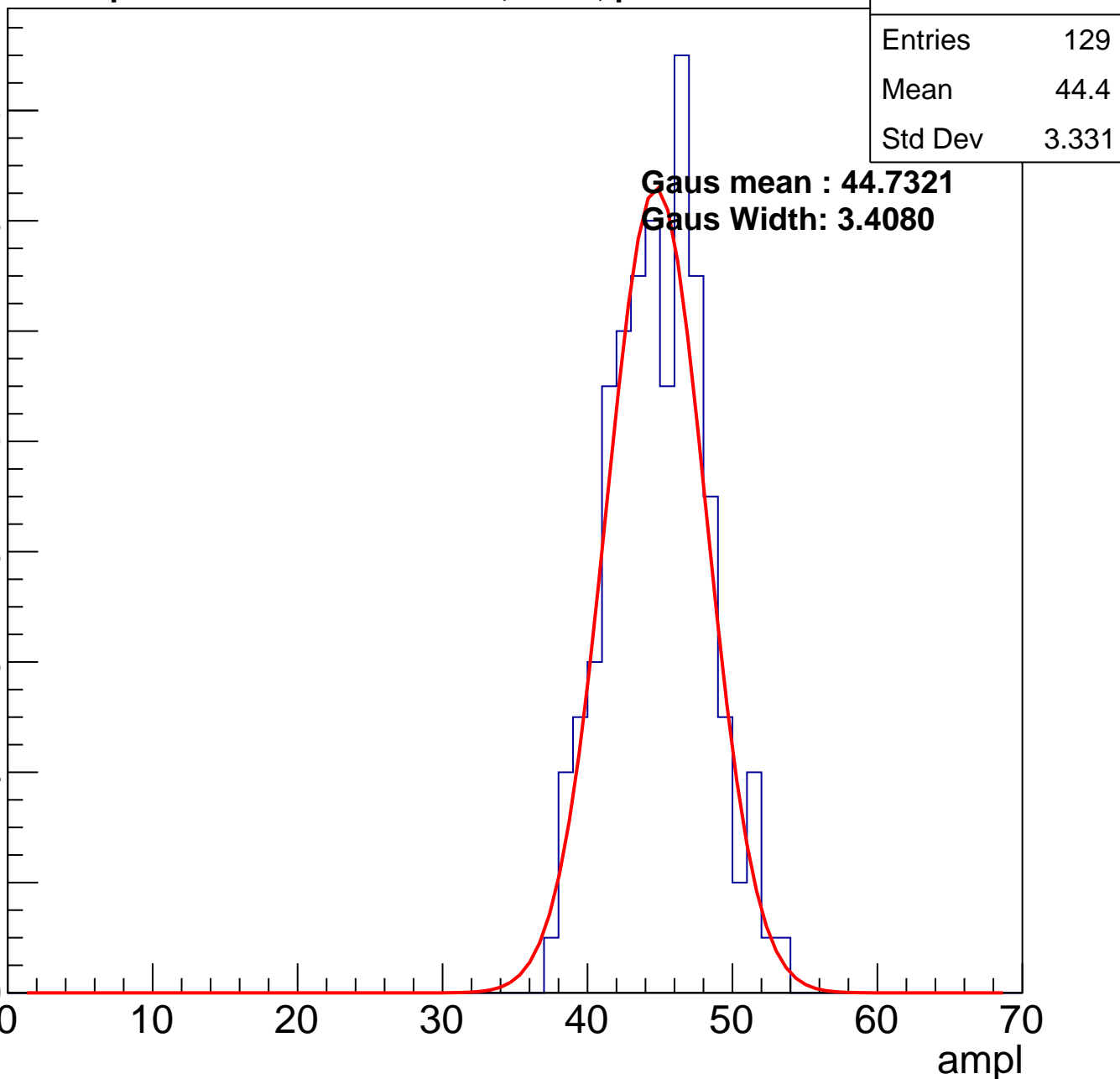
40

50

60

70

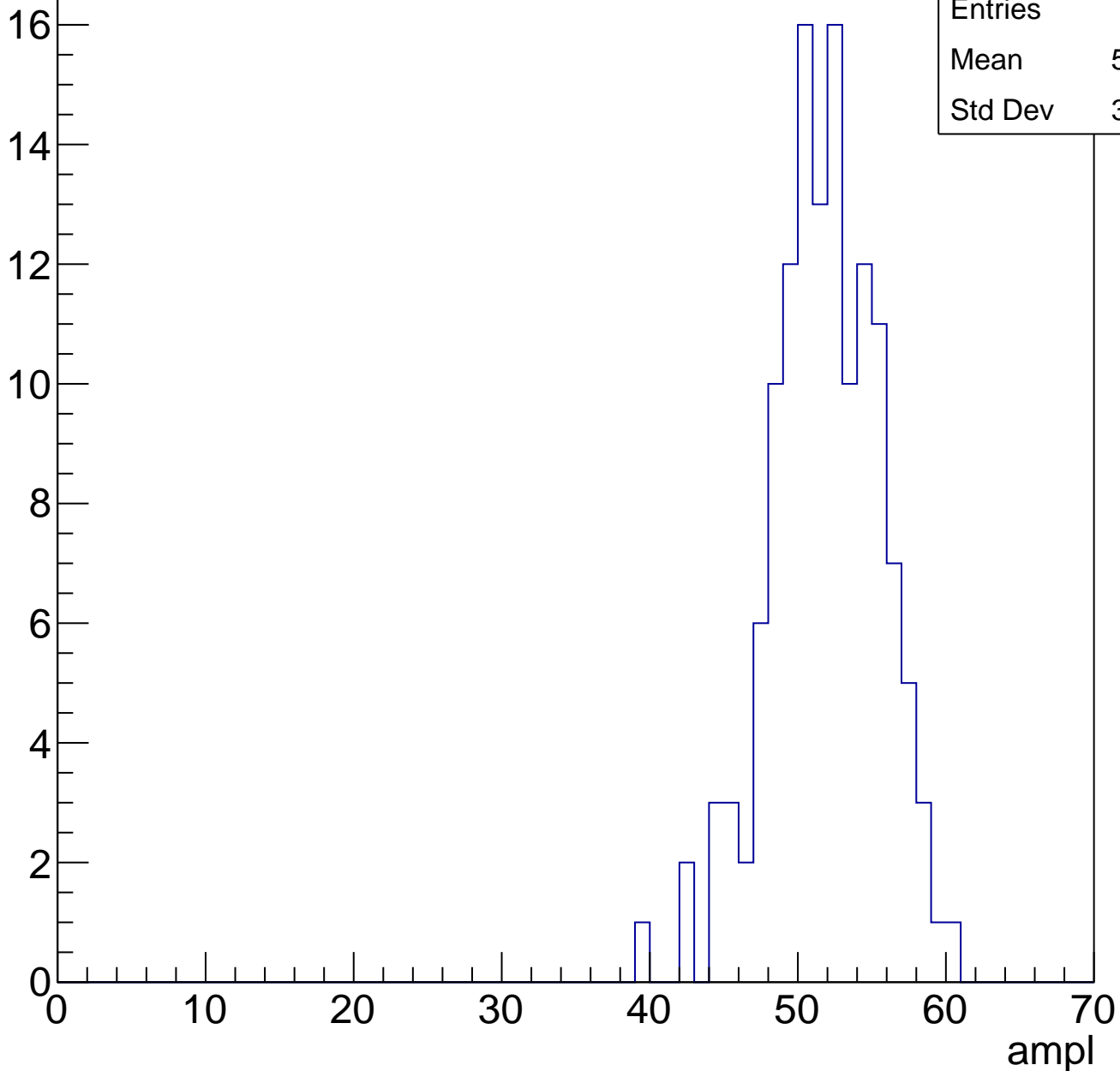
ampl



# B0L101S, U26-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

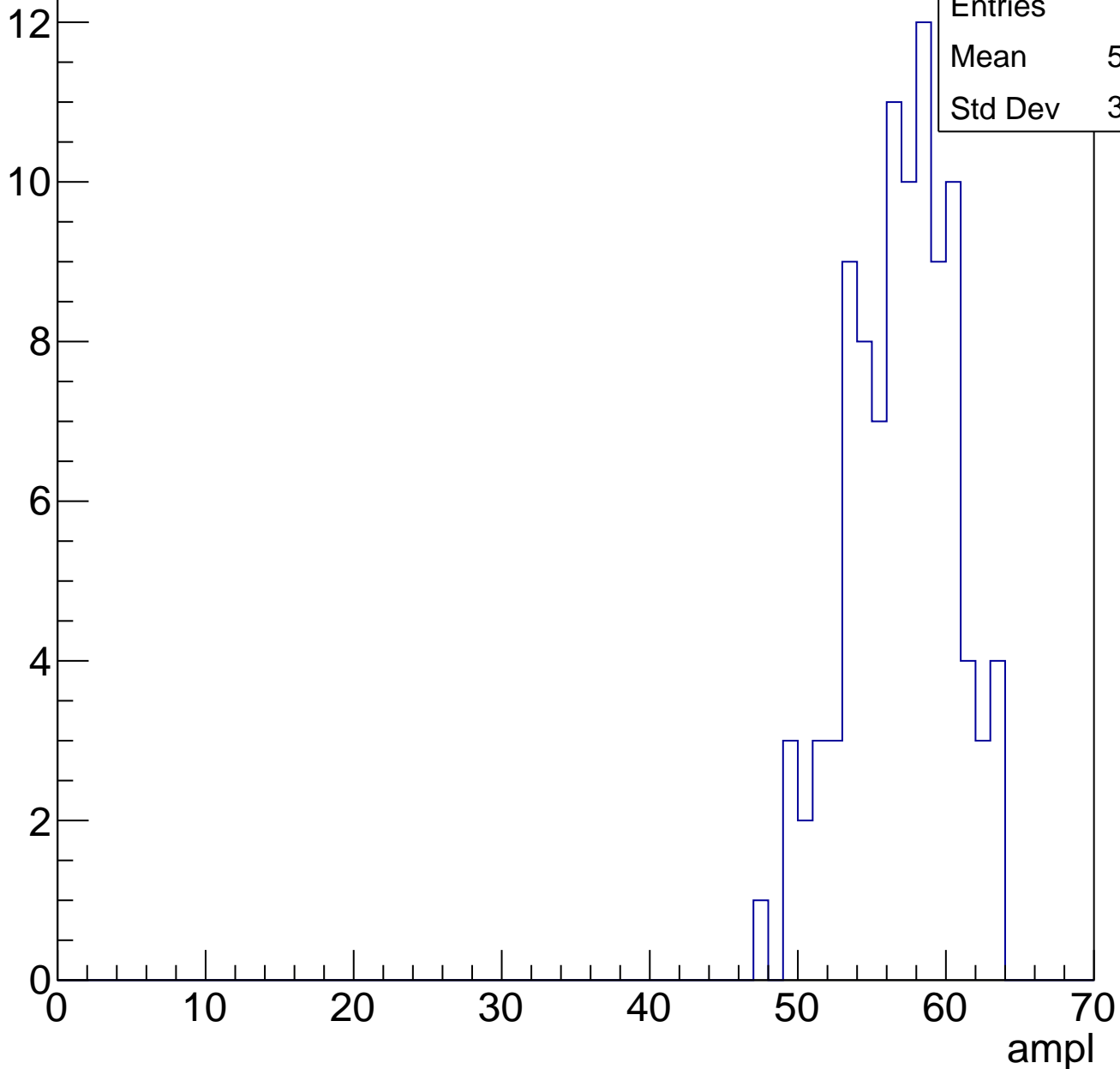


# B0L101S, U26-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	99
Mean	56.48
Std Dev	3.514

Entry



# B0L101S, U26-ch59, adc5

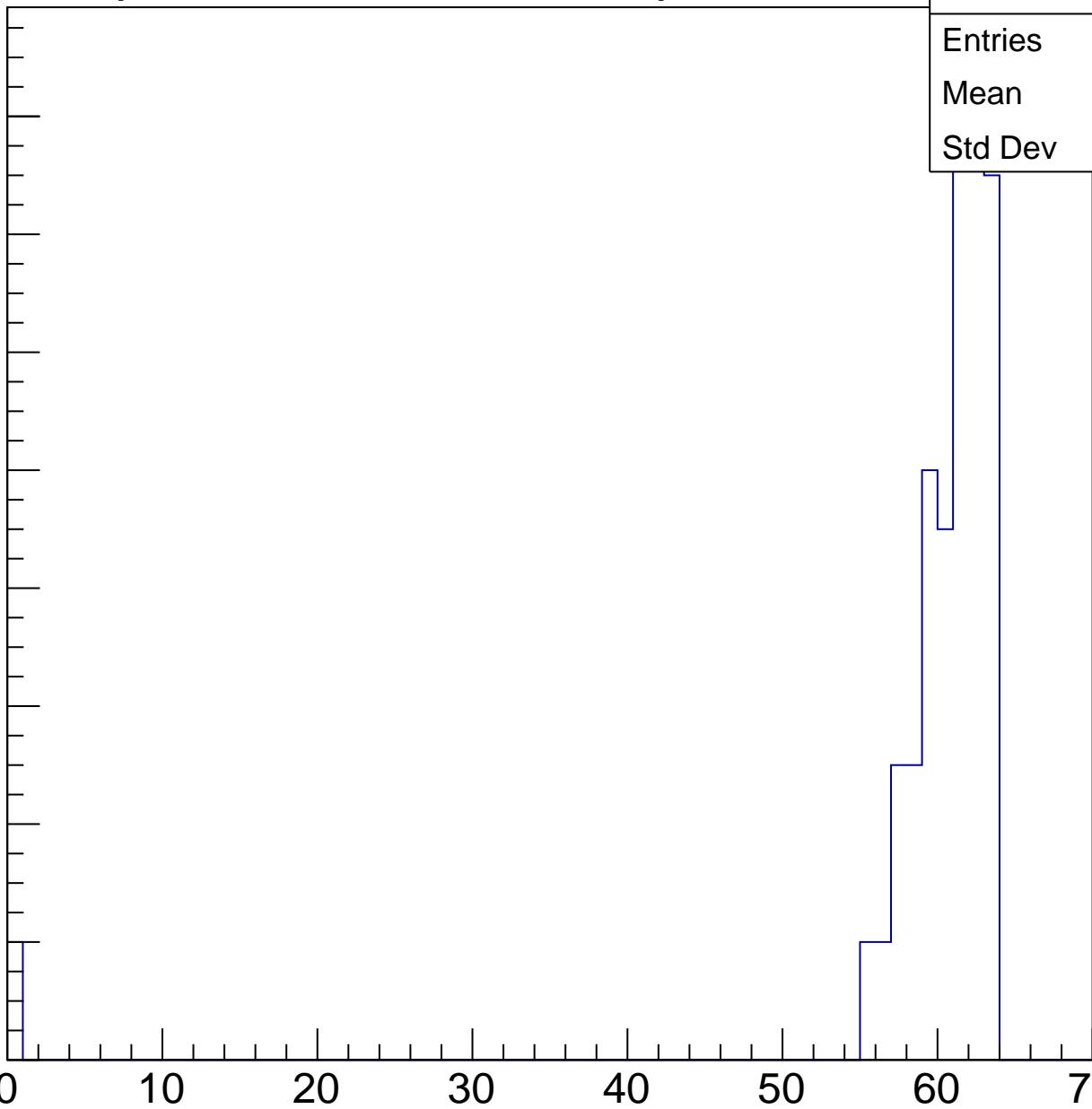
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	59.06
Std Dev	9.508

ampl



# B0L101S, U26-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch60, adc0

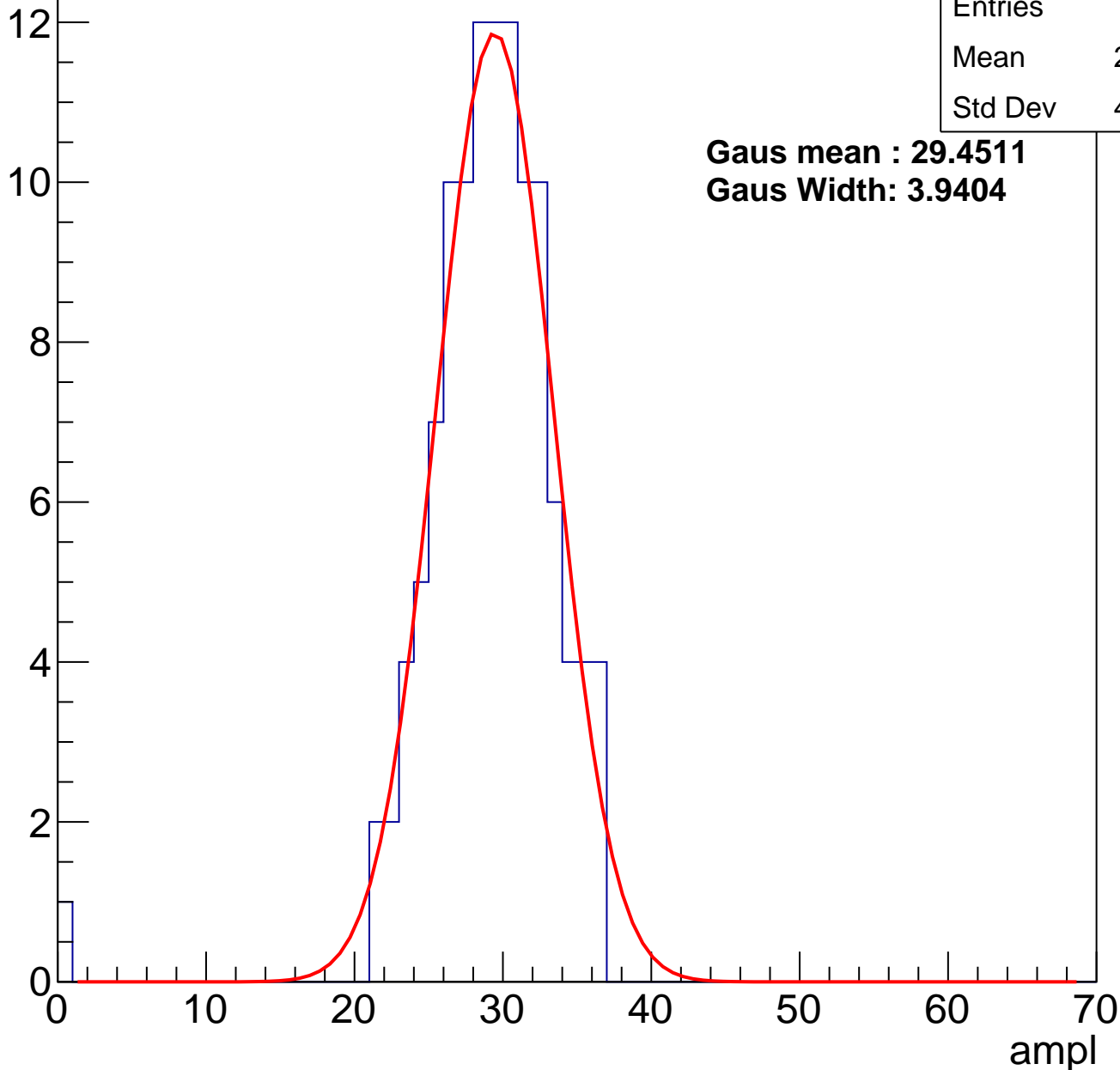
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	28.65
Std Dev	4.427

**Gaus mean : 29.4511**

**Gaus Width: 3.9404**

Entry

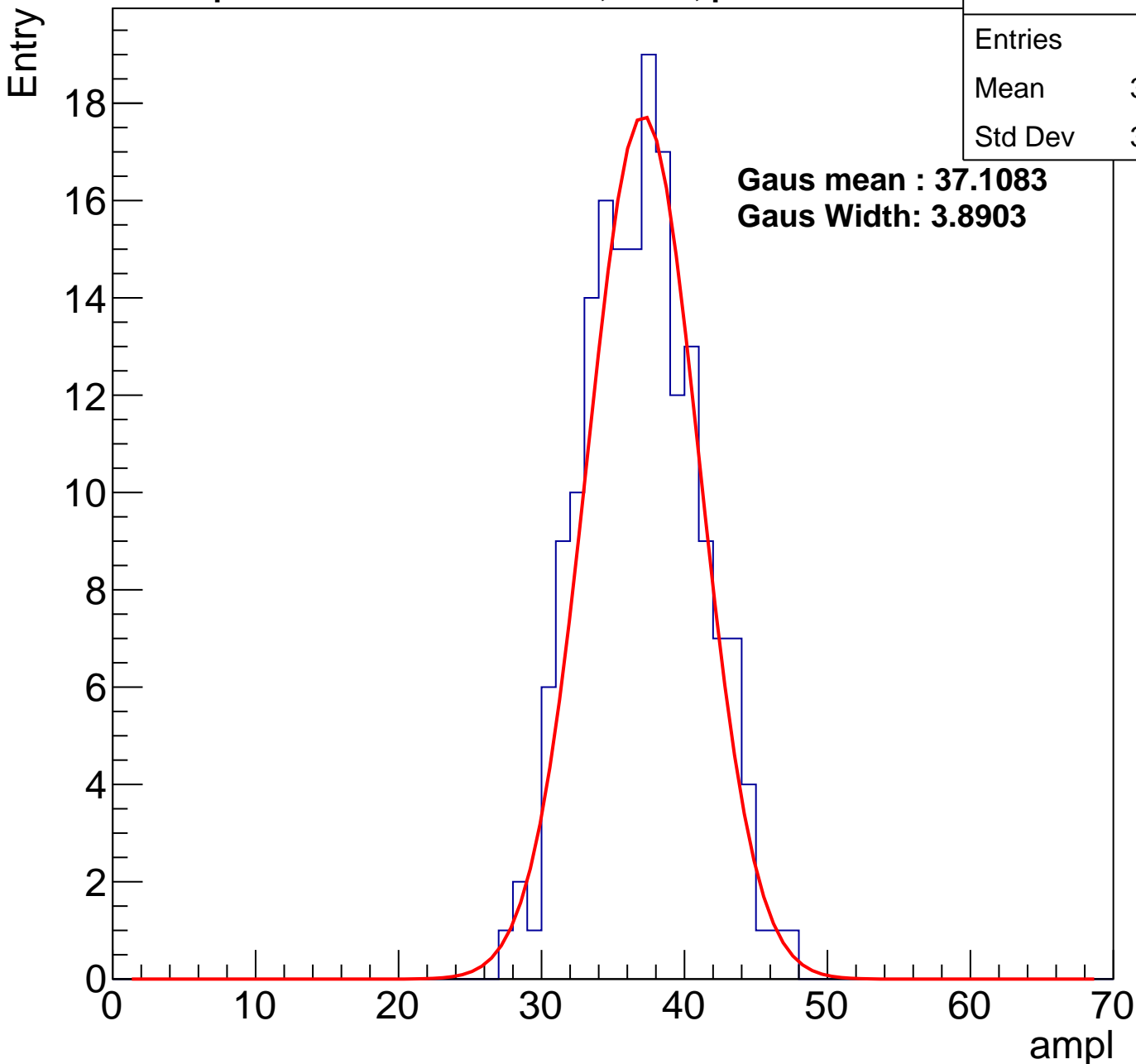


# B0L101S, U26-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	180
Mean	36.54
Std Dev	3.939

**Gaus mean : 37.1083**  
**Gaus Width: 3.8903**



# B0L101S, U26-ch60, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

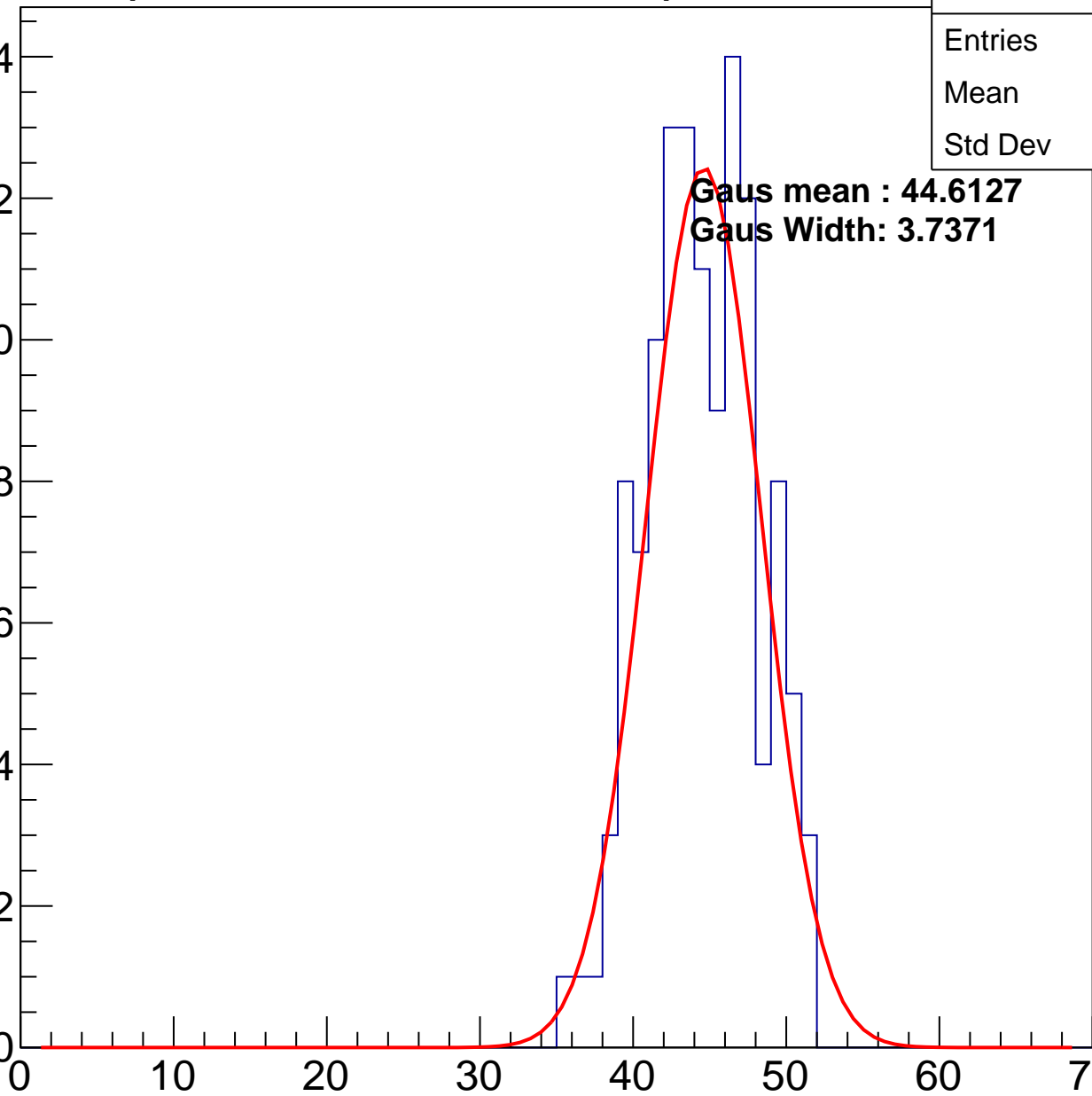
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	44.01
Std Dev	3.53

**Gaus mean : 44.6127**  
**Gaus Width: 3.7371**

ampl



# B0L101S, U26-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

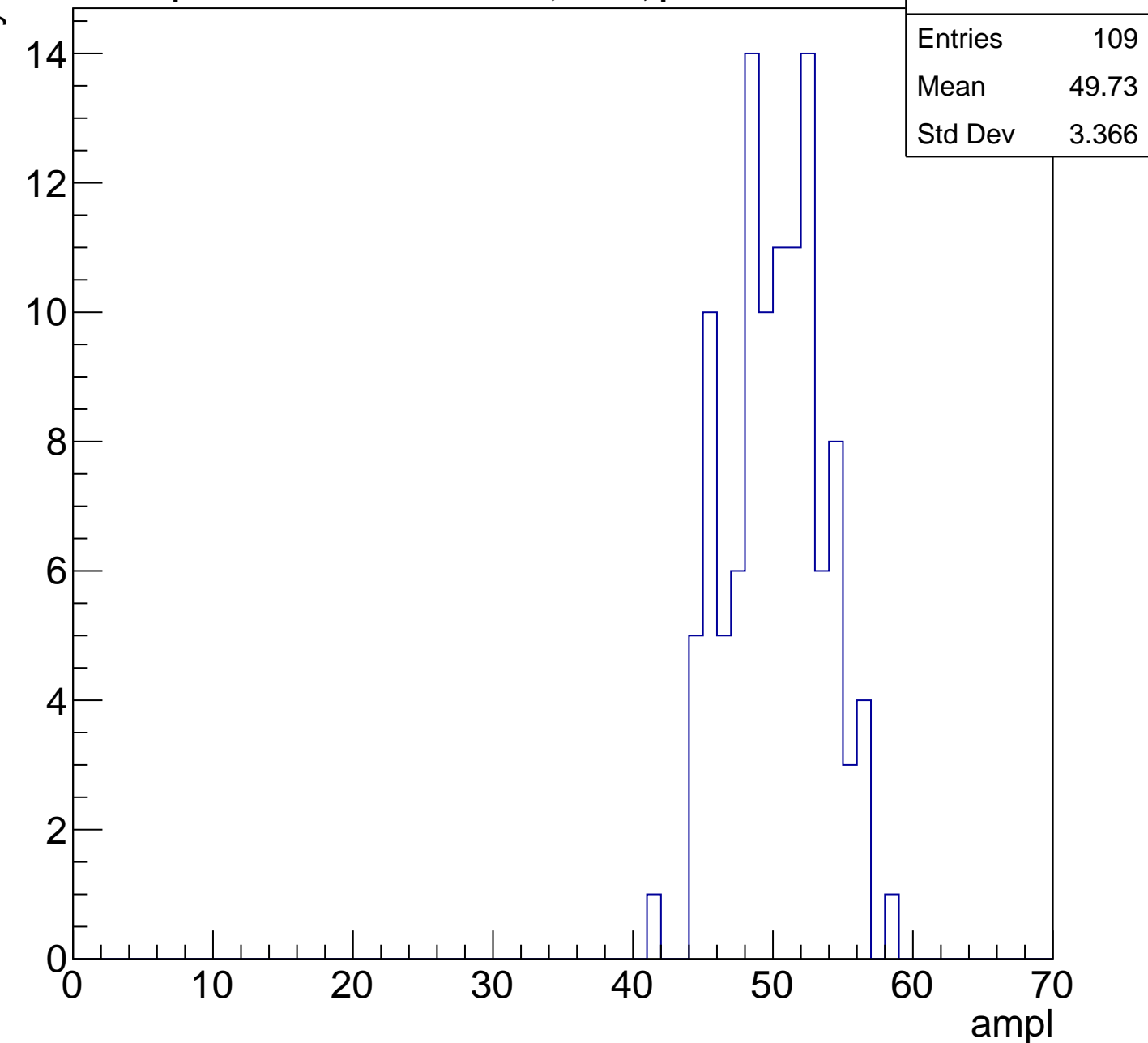
Entries	109
Mean	49.73
Std Dev	3.366

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch60, adc4

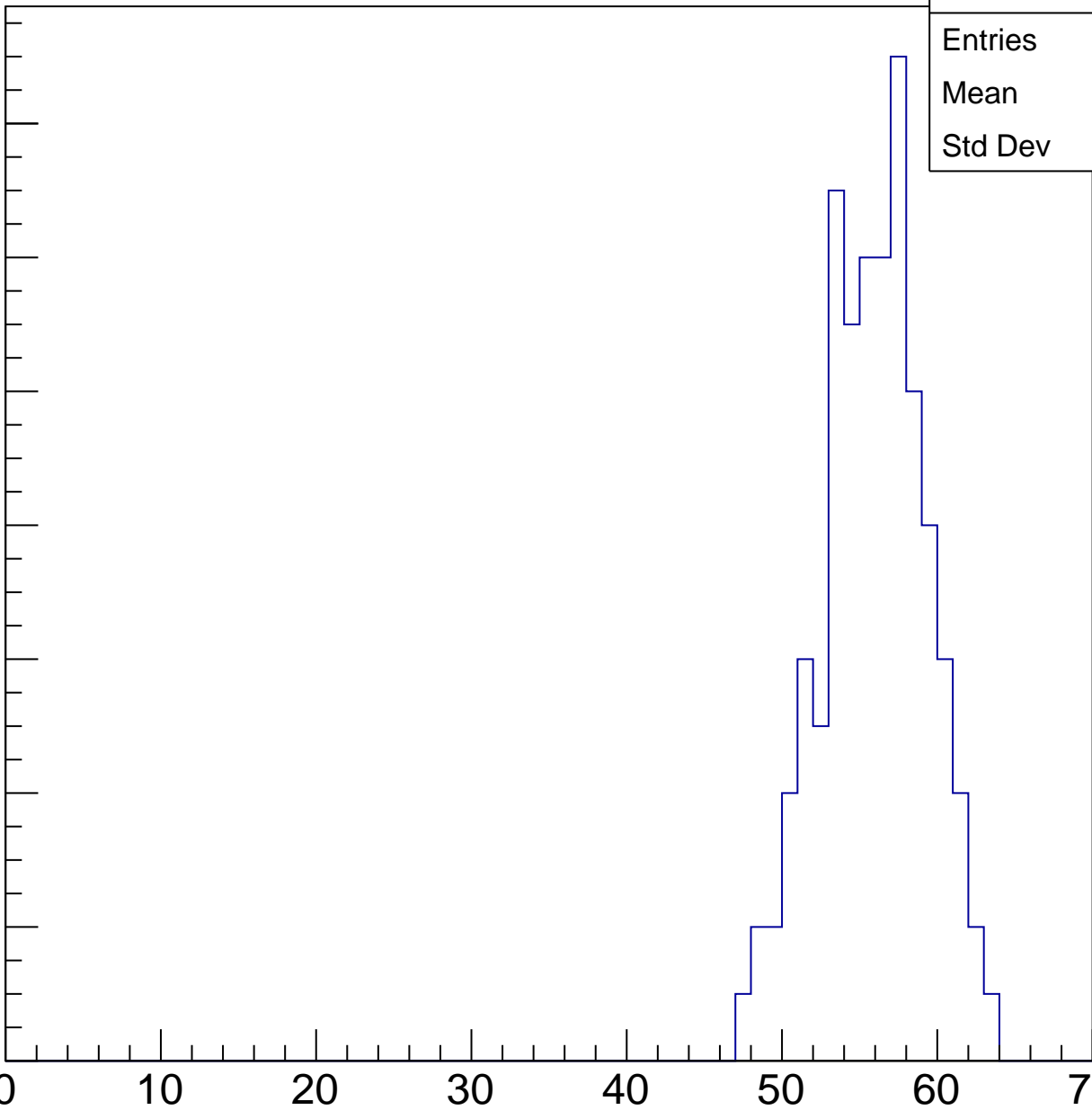
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	114
Mean	55.44
Std Dev	3.327

ampl



# B0L101S, U26-ch60, adc5

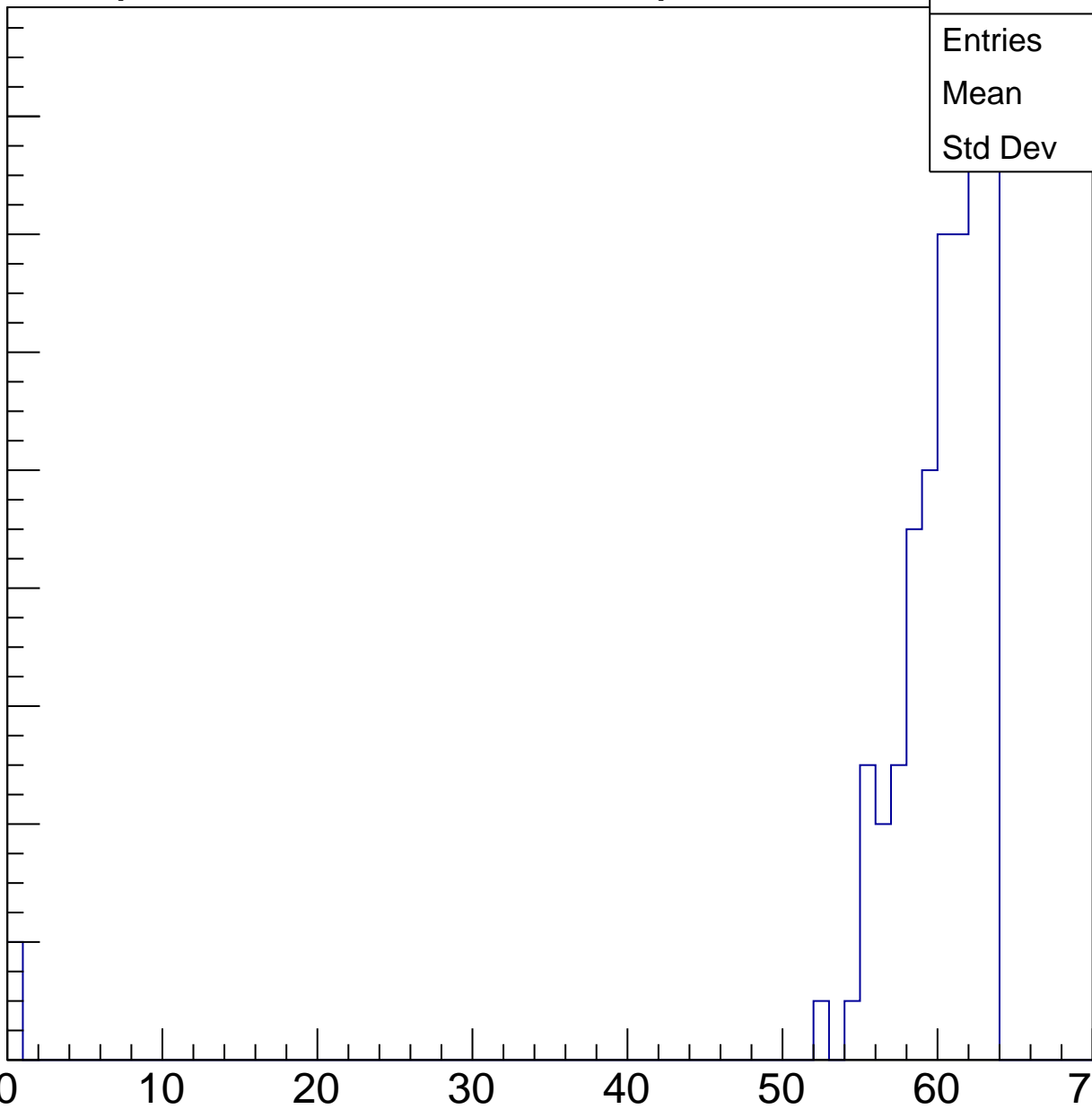
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	98
Mean	58.77
Std Dev	8.842

ampl



# B0L101S, U26-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

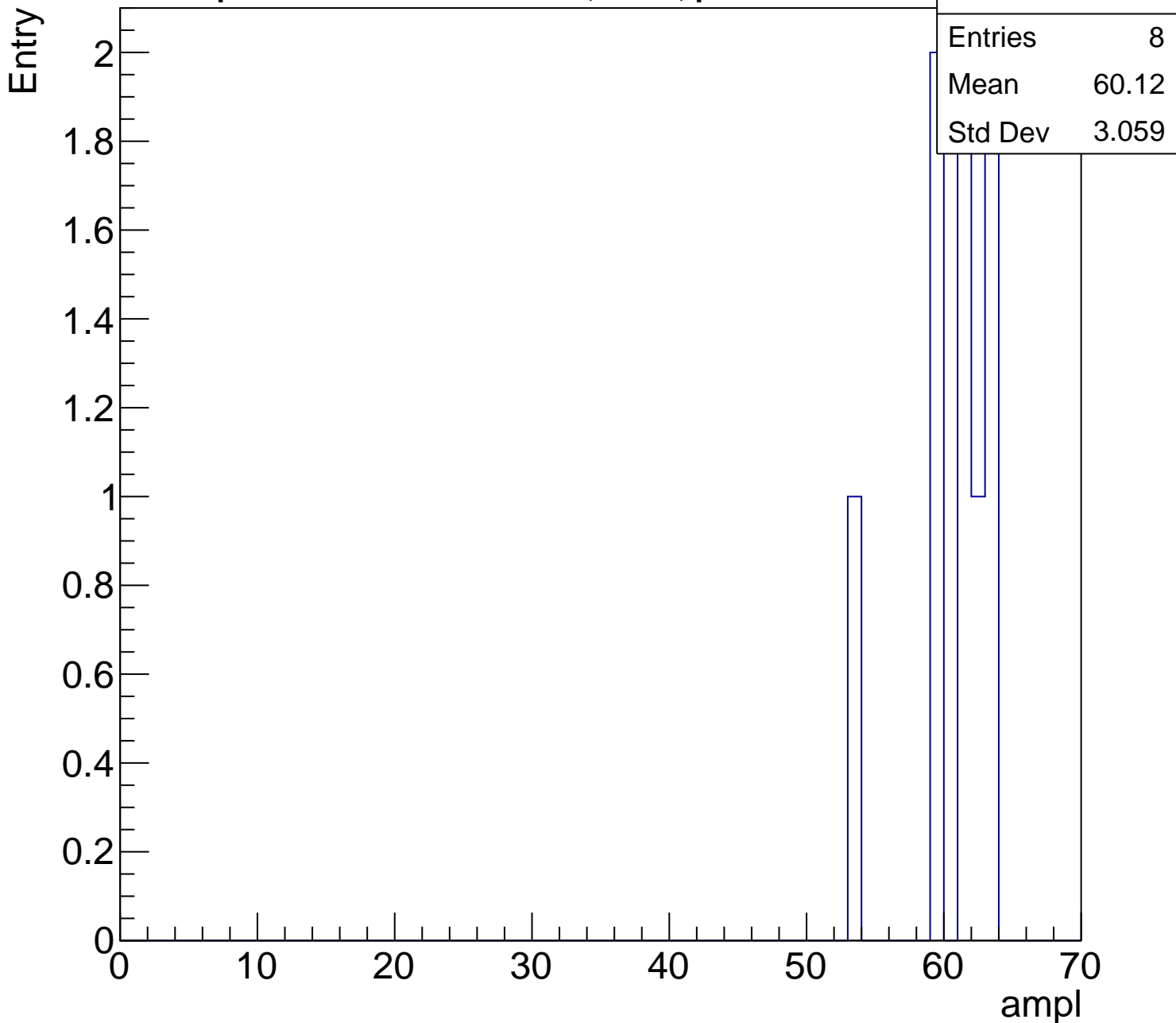
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	60.12
Std Dev	3.059

0 10 20 30 40 50 60 70

ampl

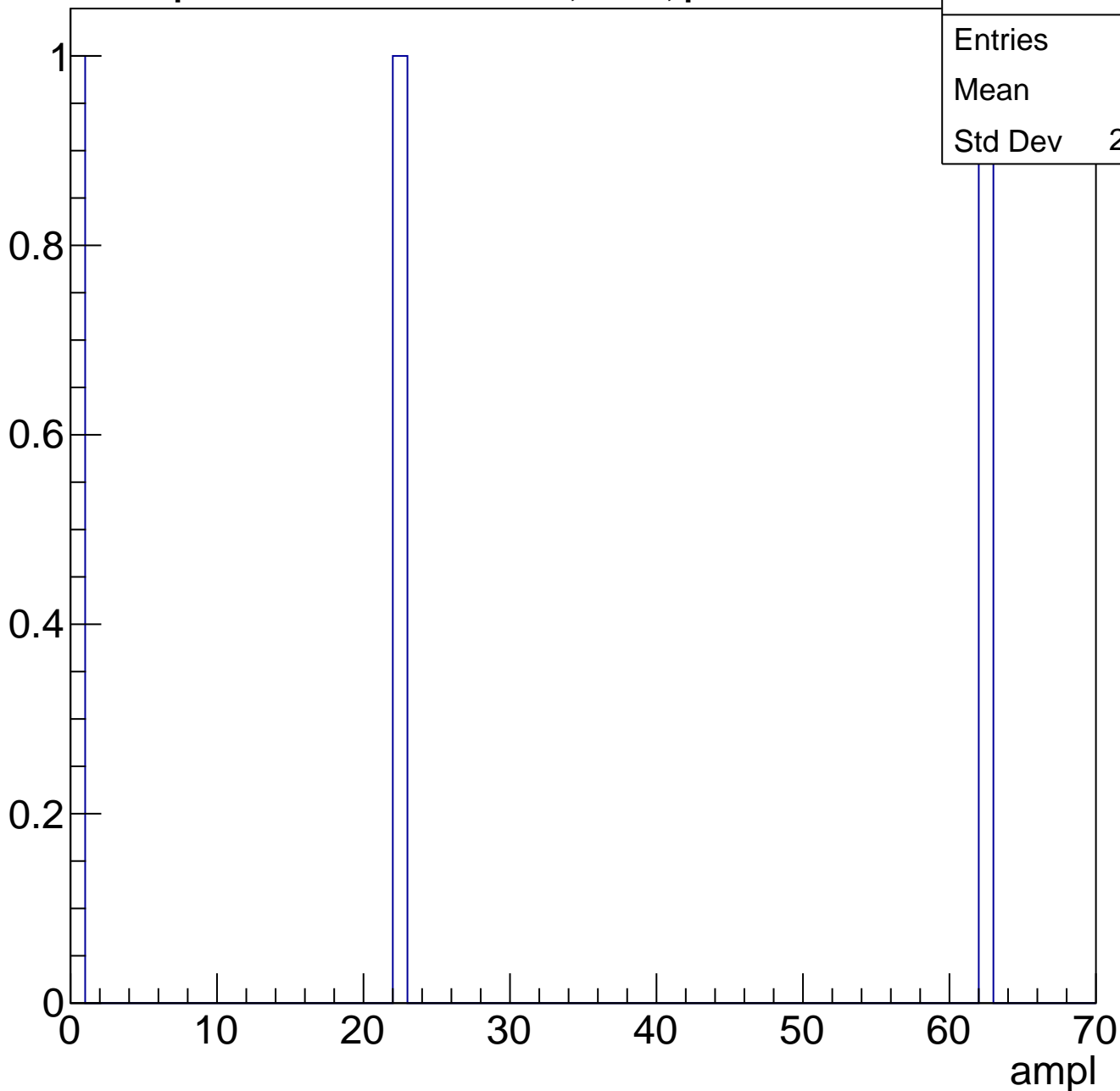




# B0L101S, U26-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	28
Std Dev	25.66

# B0L101S, U26-ch61, adc0

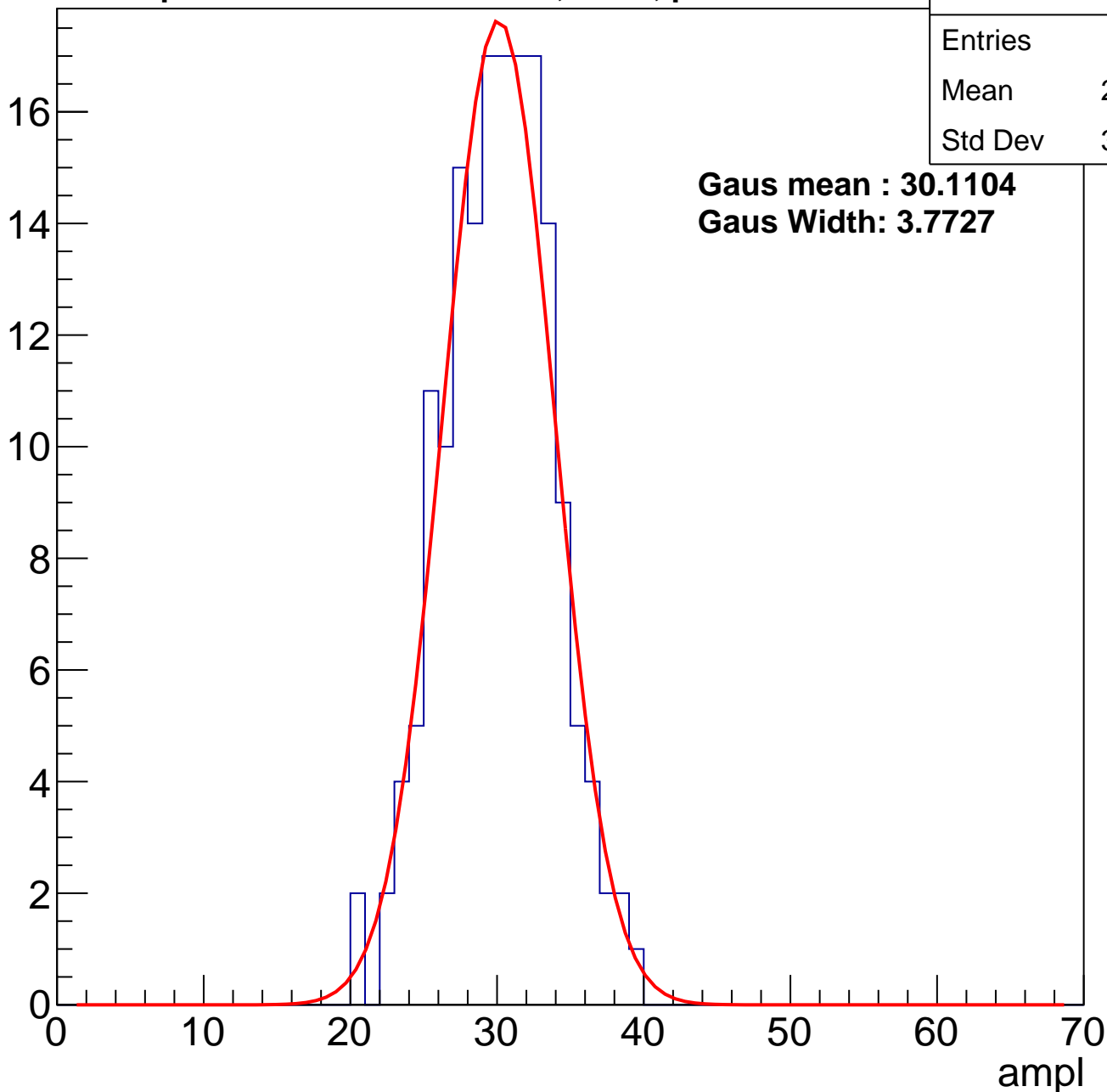
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	168
Mean	29.63
Std Dev	3.664

**Gaus mean : 30.1104**

**Gaus Width: 3.7727**

Entry

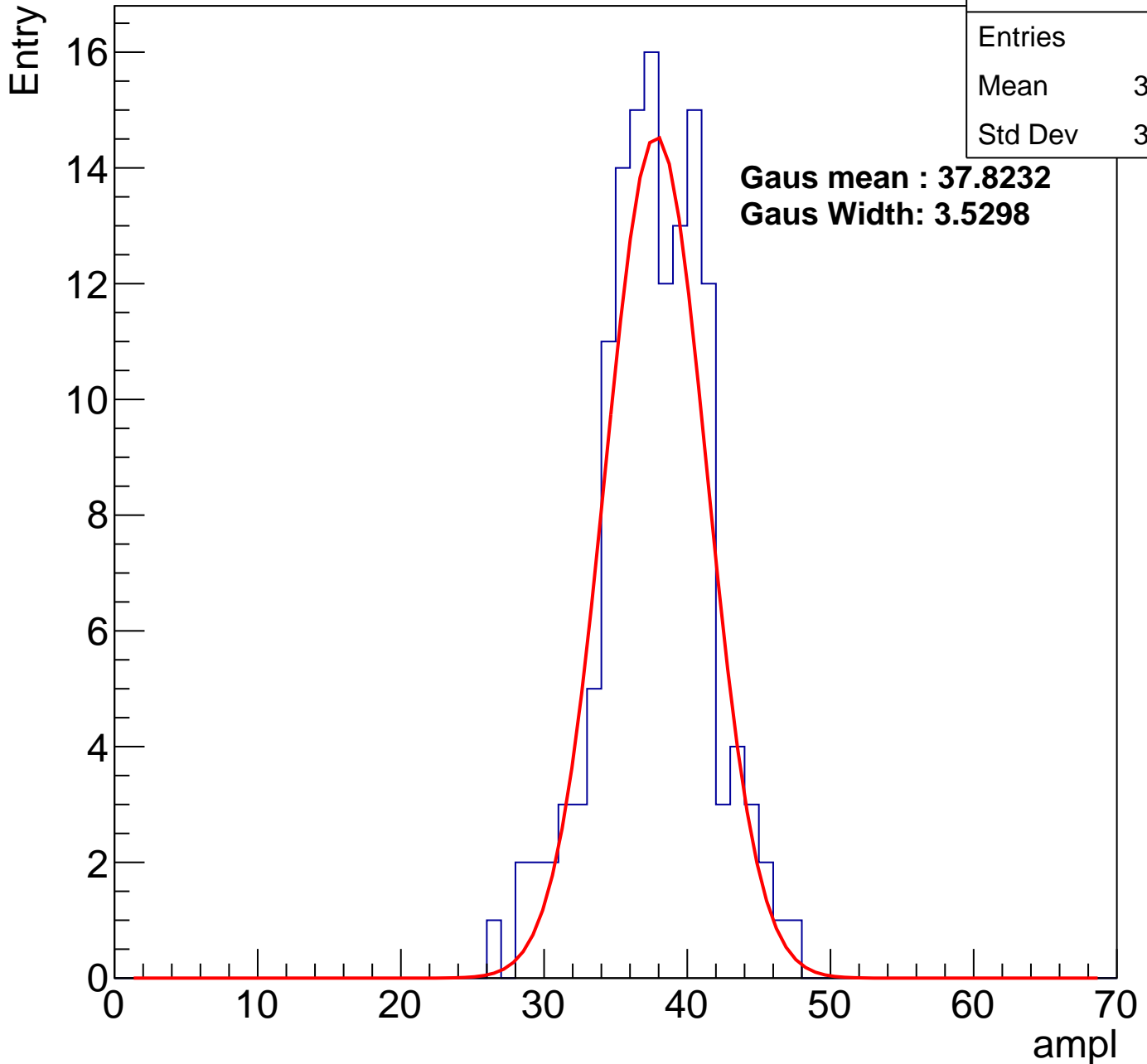


# B0L101S, U26-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	37.27
Std Dev	3.777

**Gaus mean : 37.8232**  
**Gaus Width: 3.5298**



# B0L101S, U26-ch61, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	44.29
Std Dev	3.626

**Gaus mean : 44.8420**

**Gaus Width: 3.5583**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

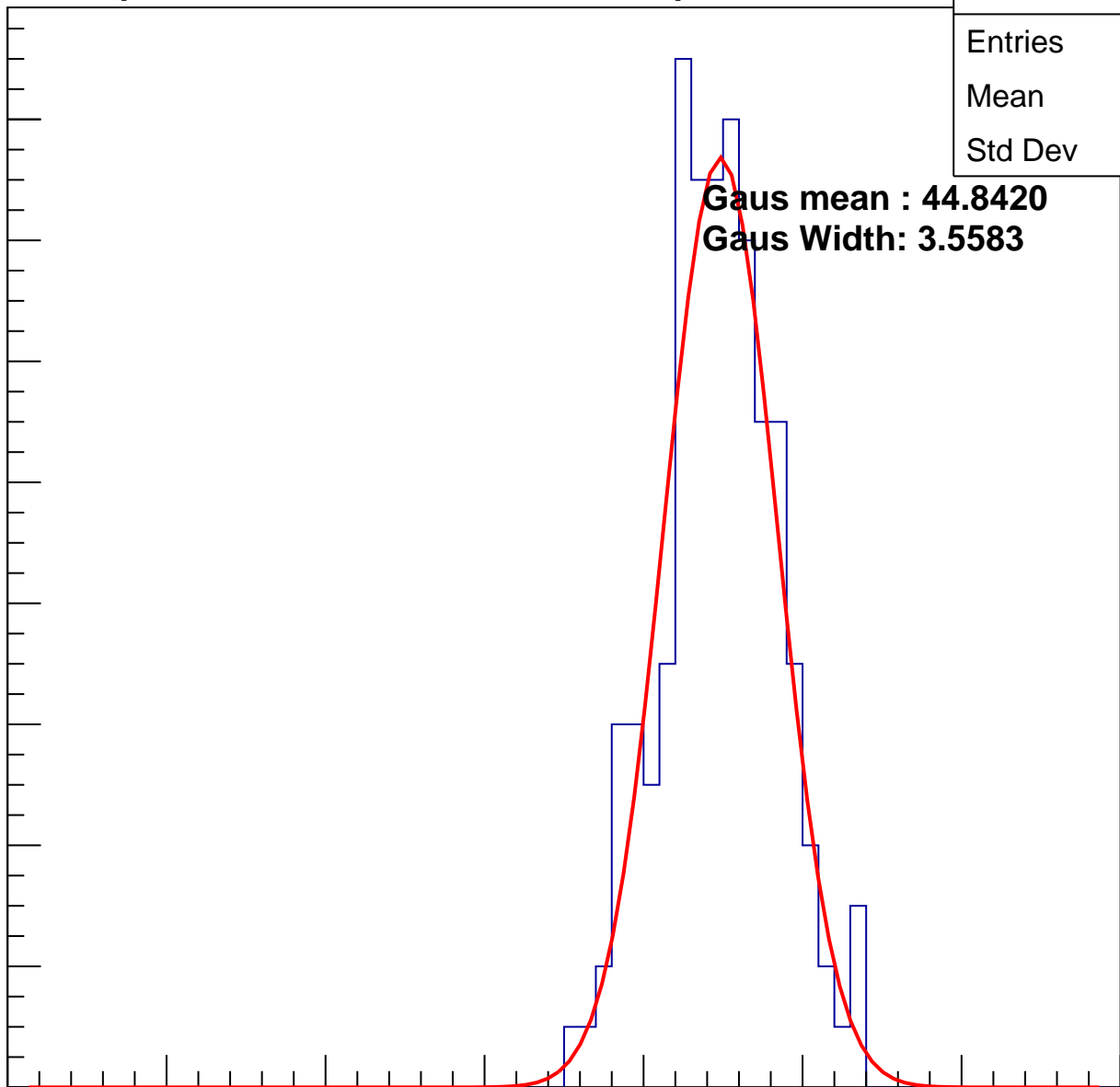
40

50

60

70

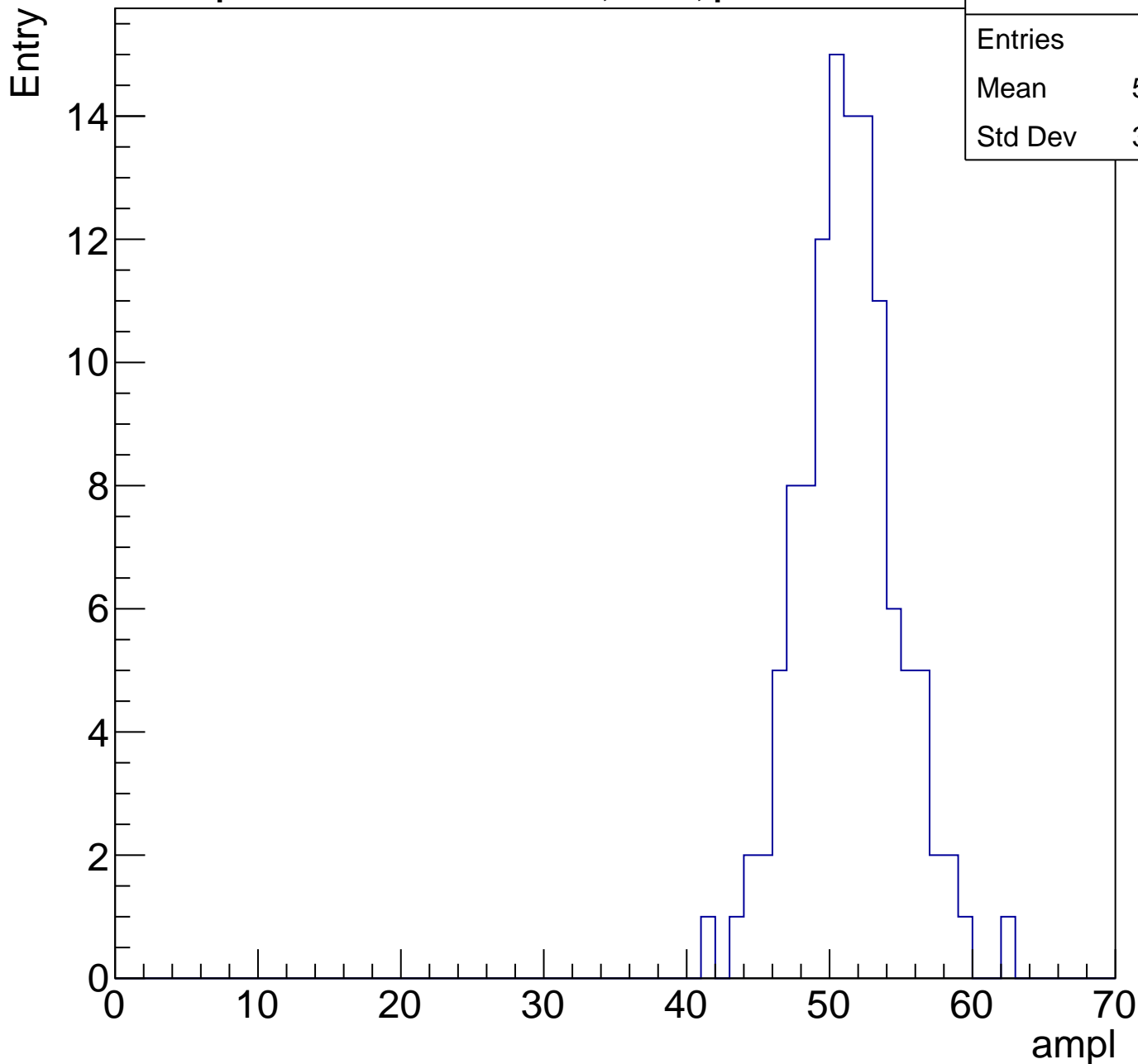
ampl



# B0L101S, U26-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	50.83
Std Dev	3.477



# B0L101S, U26-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

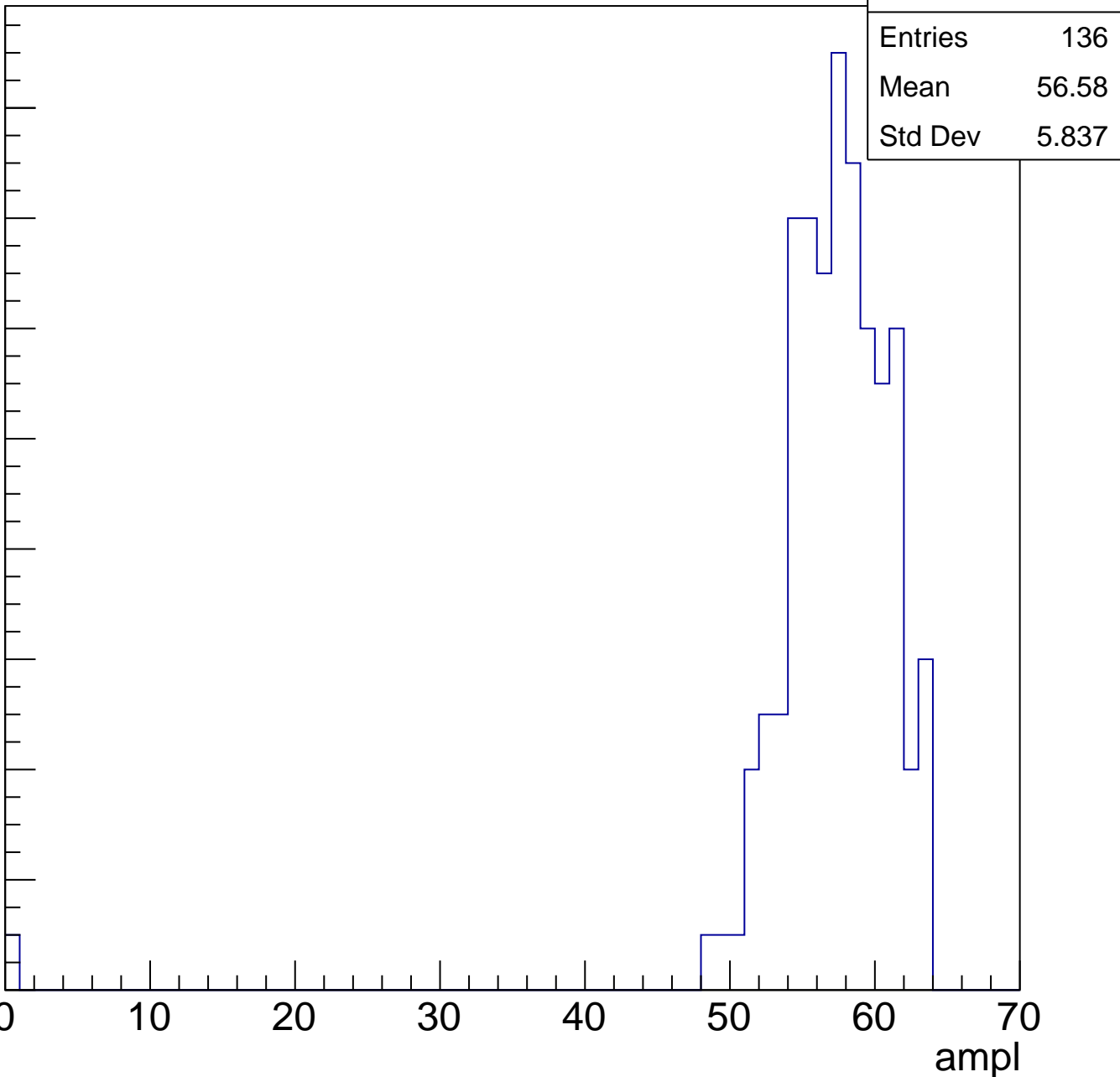
4

2

0

Entries	136
Mean	56.58
Std Dev	5.837

ampl



# B0L101S, U26-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

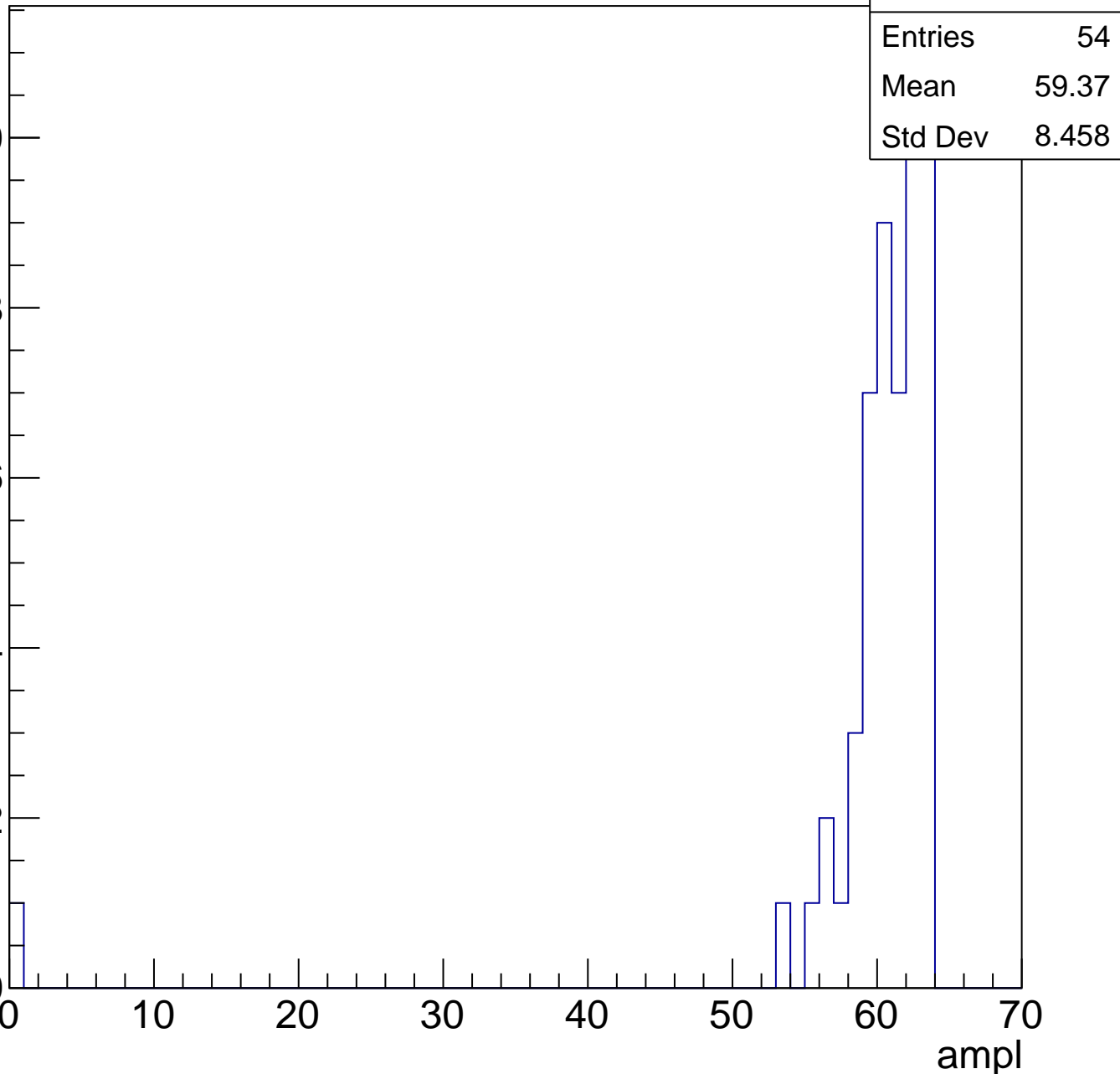
30

40

50

ampl

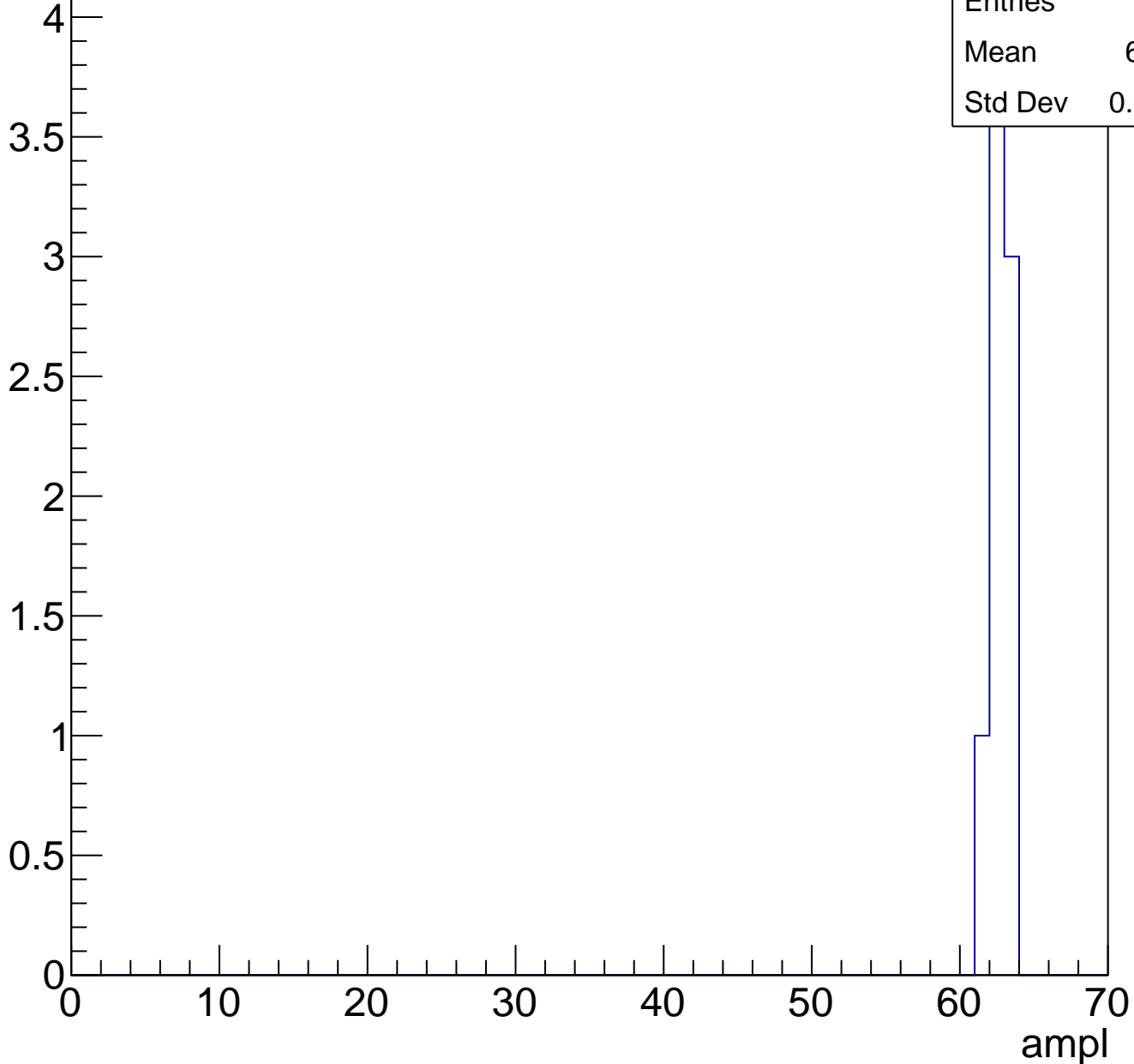
Entries	54
Mean	59.37
Std Dev	8.458



# B0L101S, U26-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch62, adc0

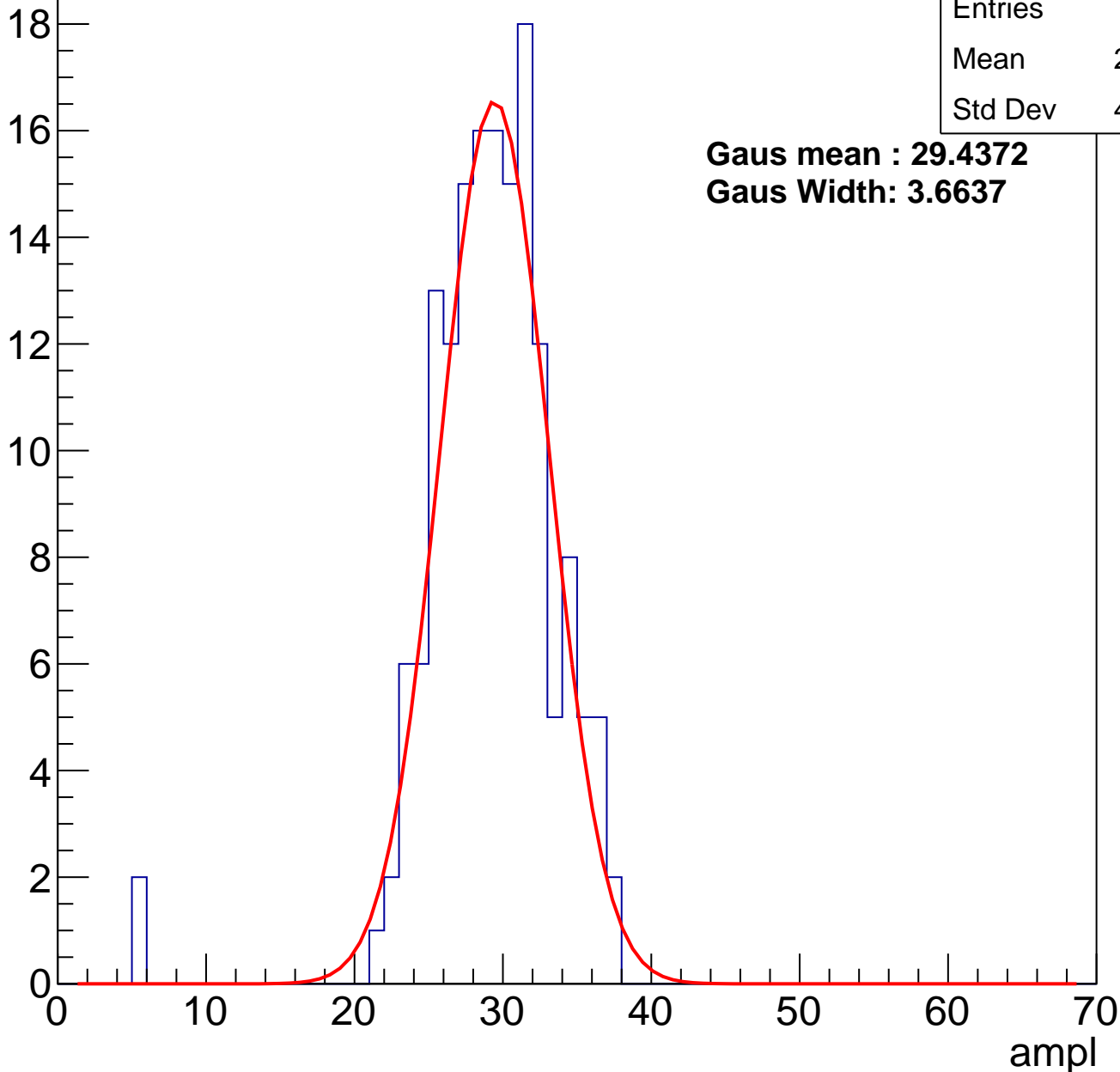
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	28.74
Std Dev	4.408

**Gaus mean : 29.4372**

**Gaus Width: 3.6637**

Entry



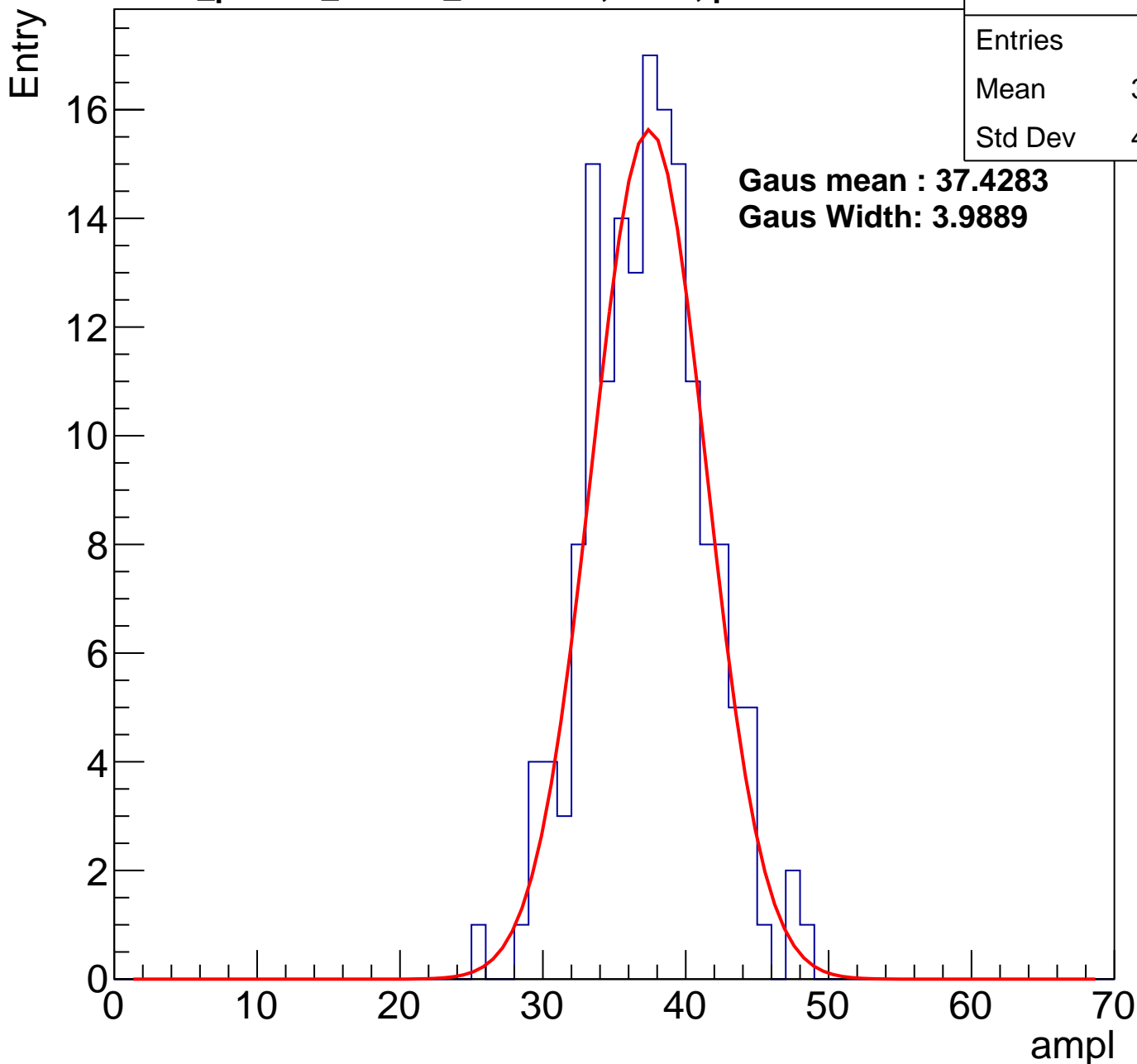
# B0L101S, U26-ch62, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	36.89
Std Dev	4.073

**Gaus mean : 37.4283**

**Gaus Width: 3.9889**



# B0L101S, U26-ch62, adc2

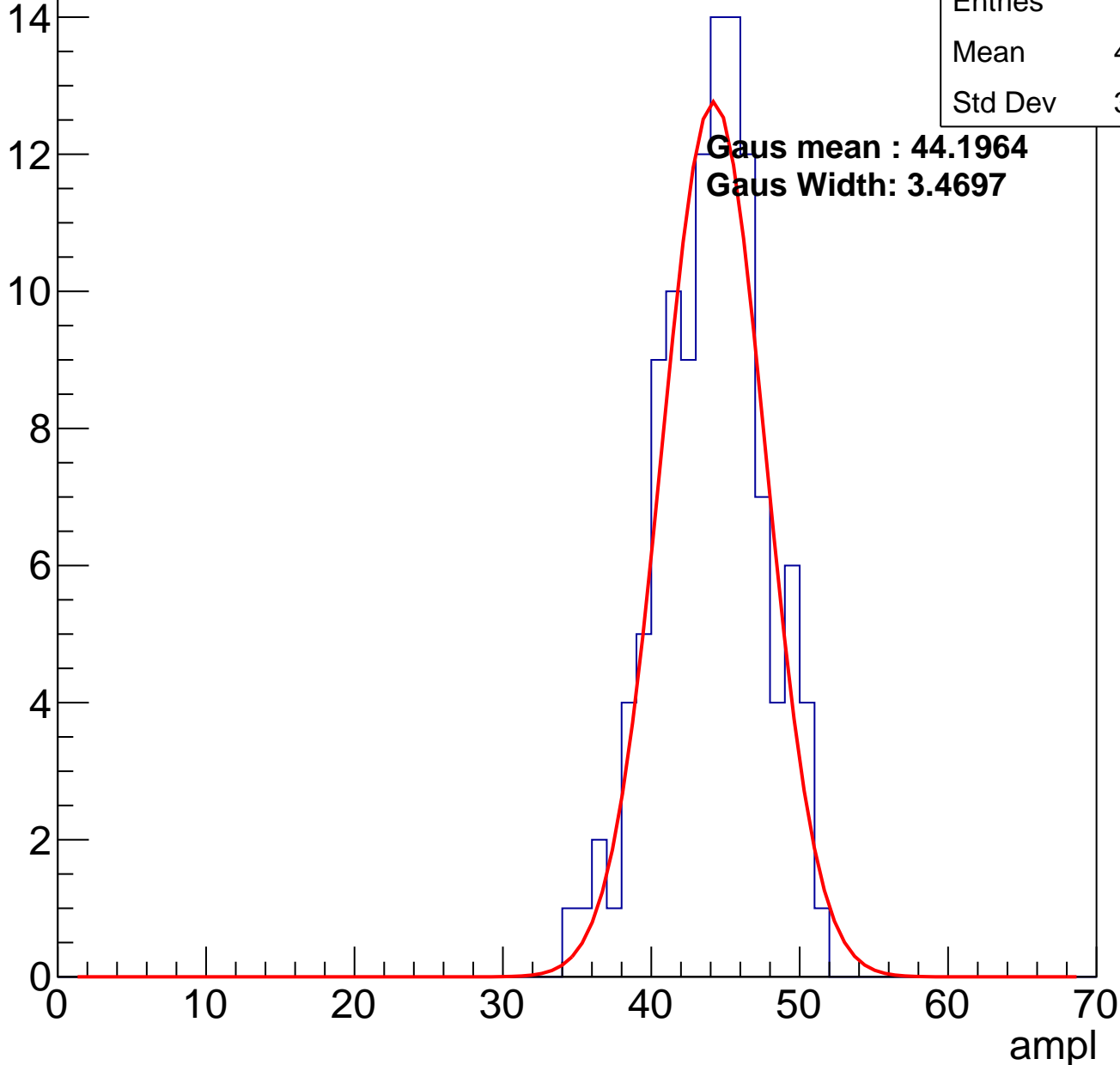
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	43.56
Std Dev	3.497

**Gaus mean : 44.1964**

**Gaus Width: 3.4697**

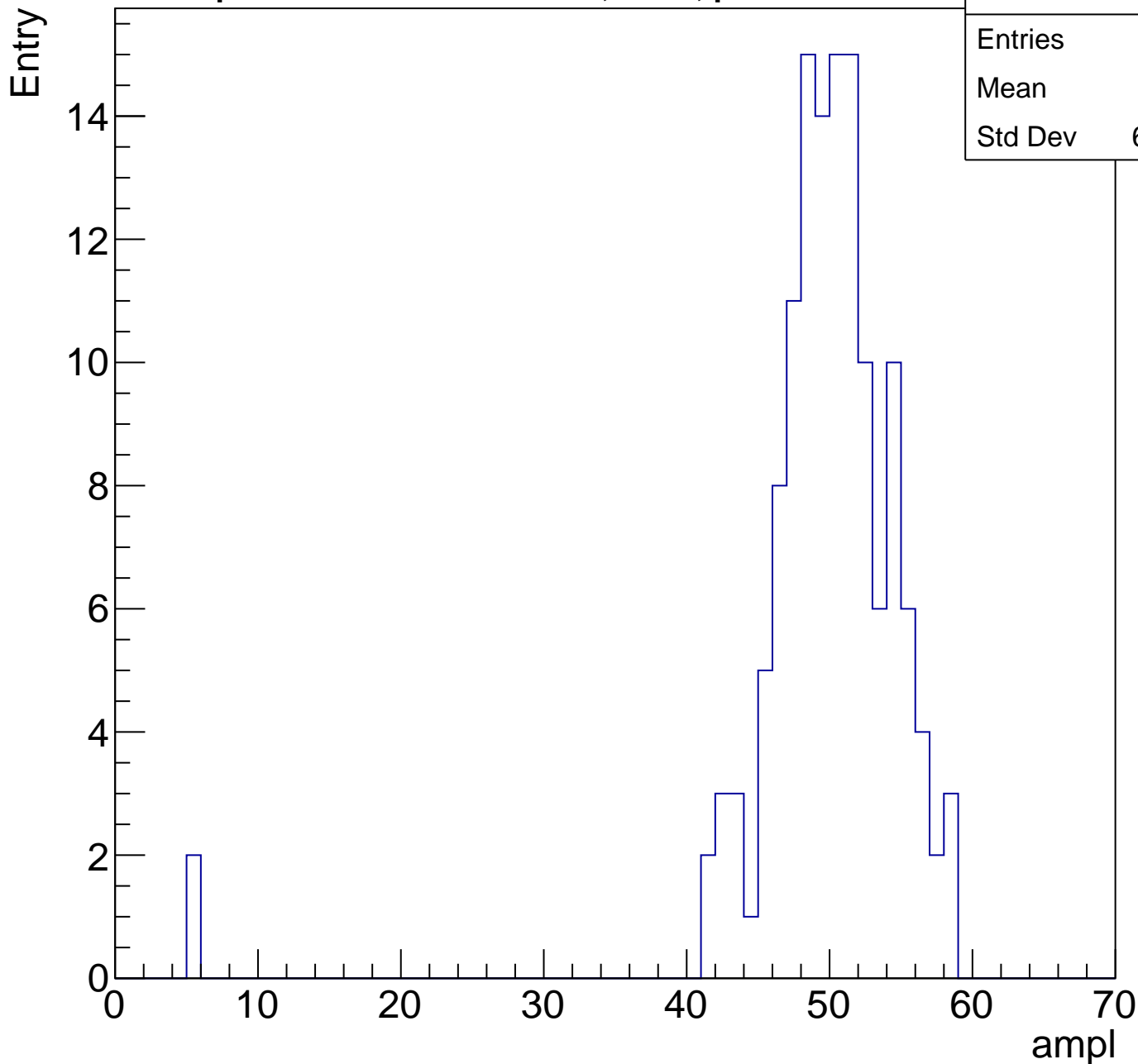
Entry



# B0L101S, U26-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

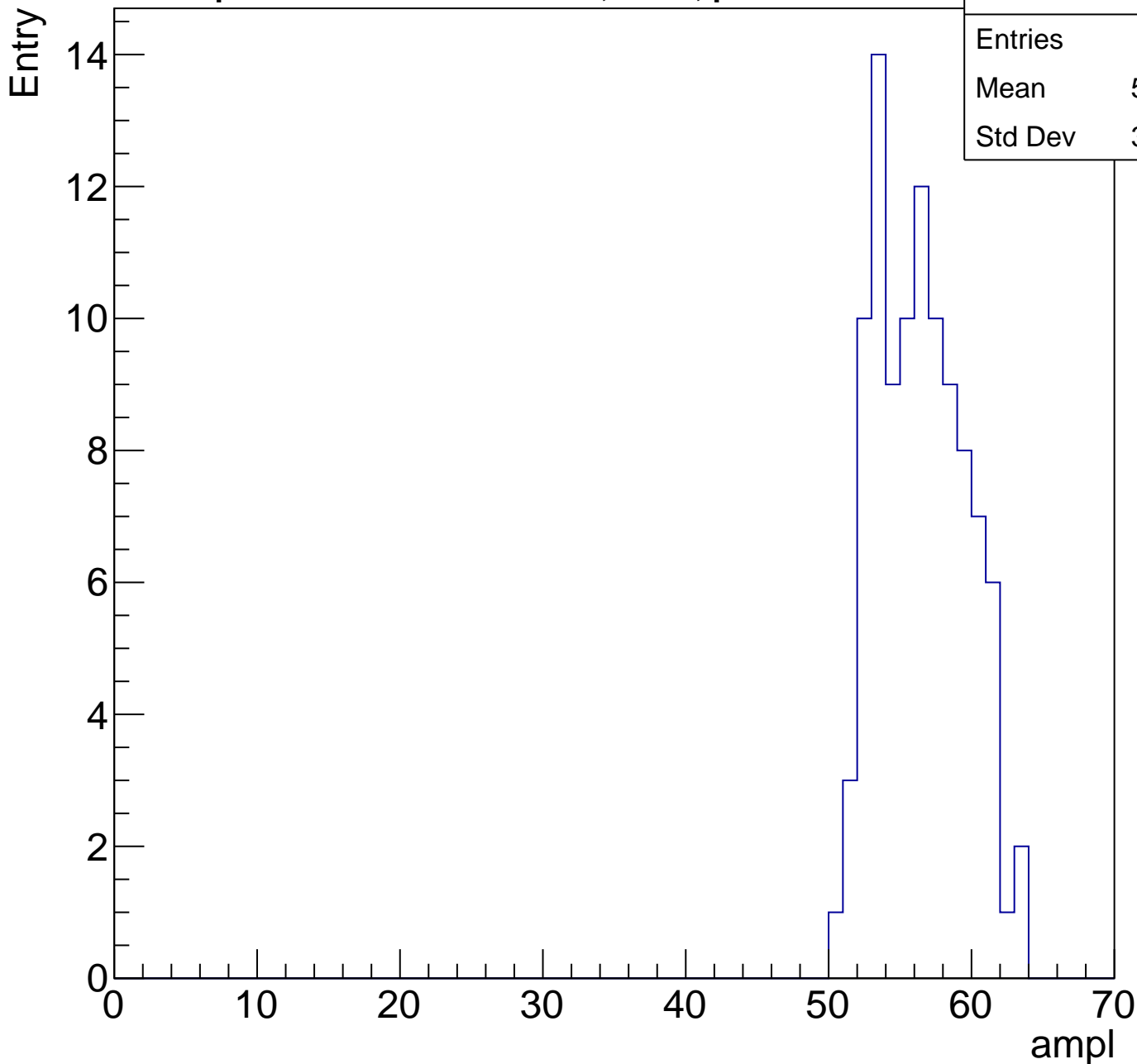
Entries	135
Mean	49.2
Std Dev	6.543



# B0L101S, U26-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	102
Mean	55.99
Std Dev	3.063



# B0L101S, U26-ch62, adc5

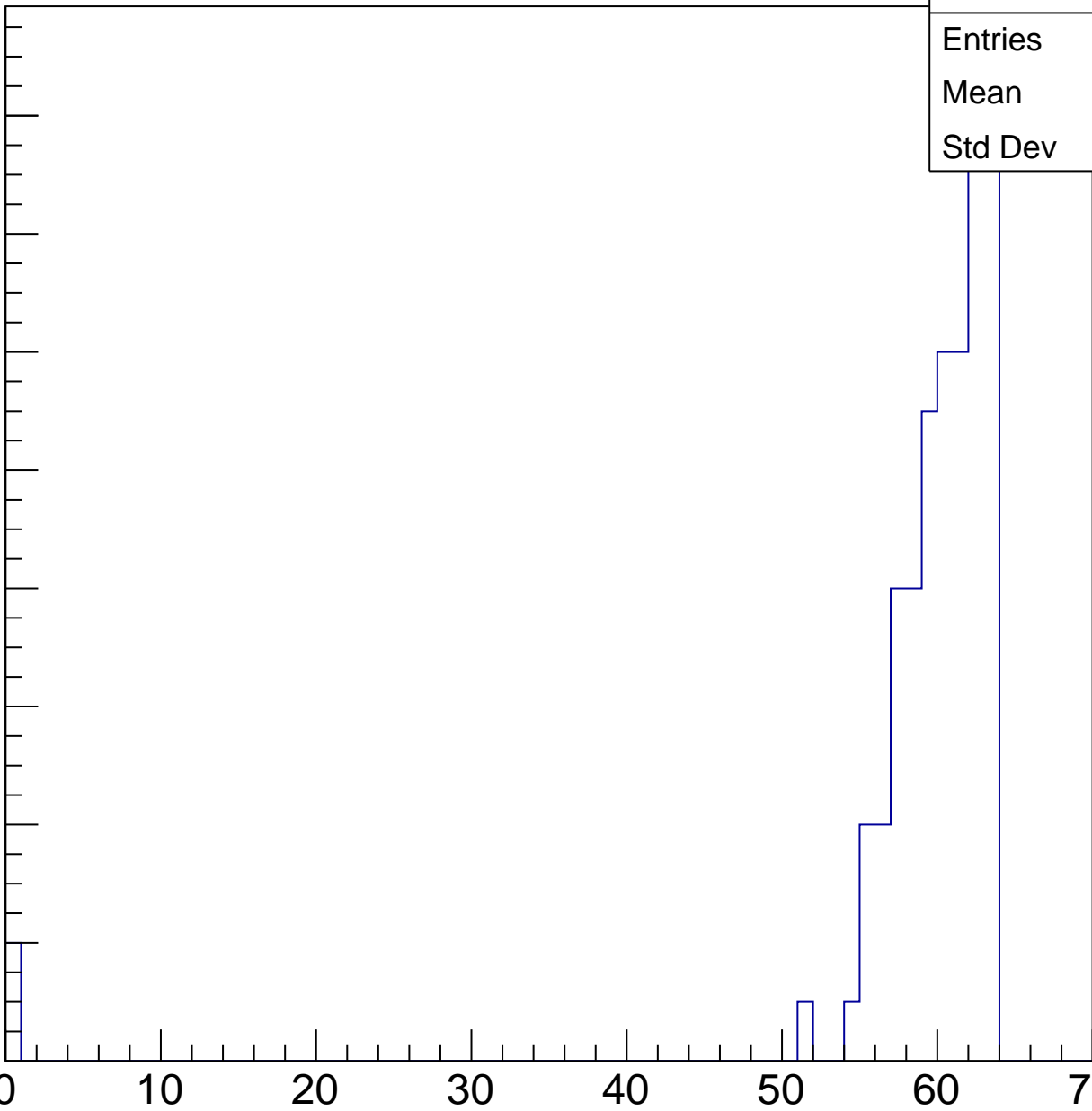
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	96
Mean	58.67
Std Dev	8.926

ampl



# B0L101S, U26-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	61.38
Std Dev	1.317

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

154

Mean

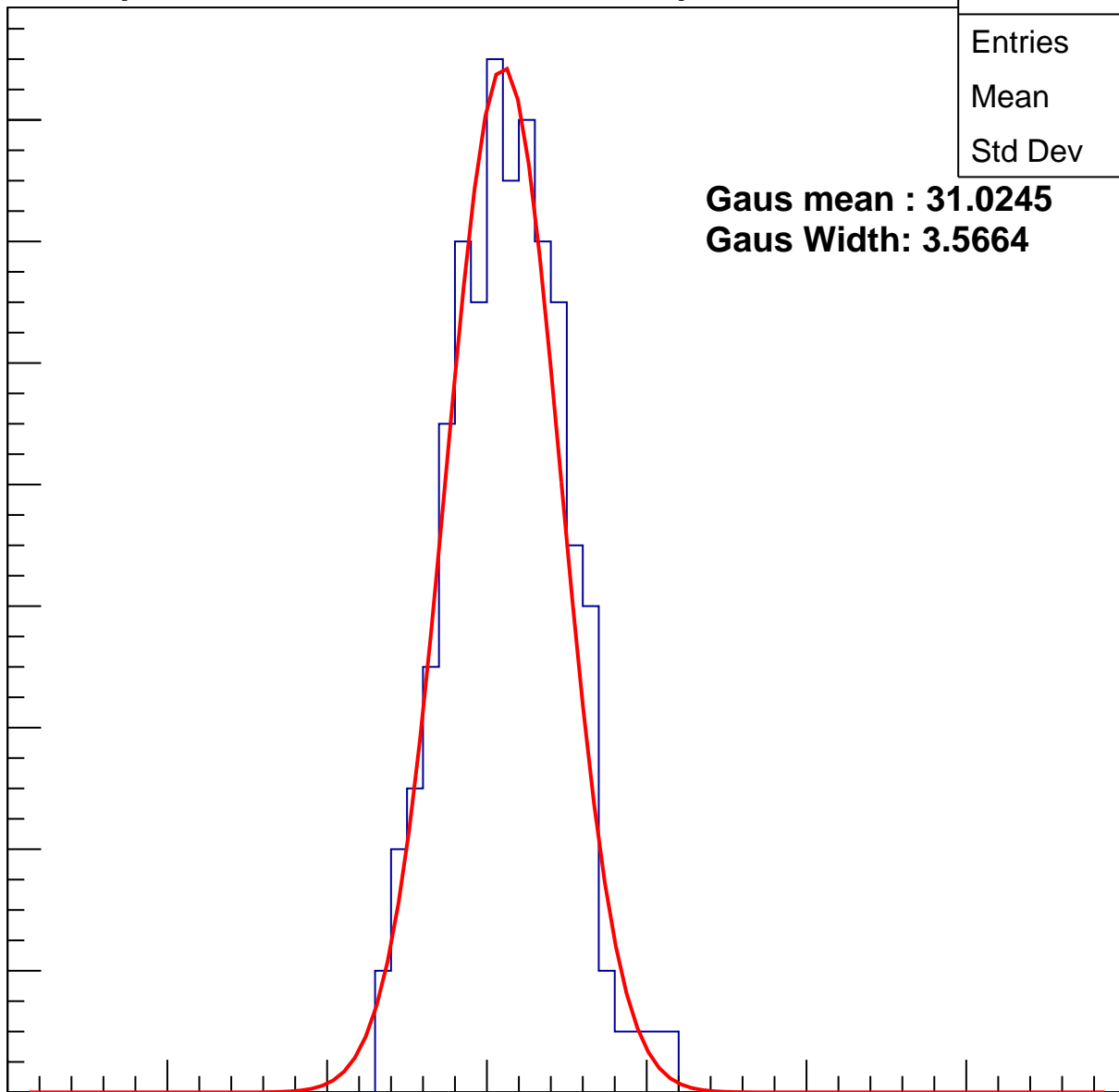
30.79

Std Dev

3.551

**Gaus mean : 31.0245**

**Gaus Width: 3.5664**



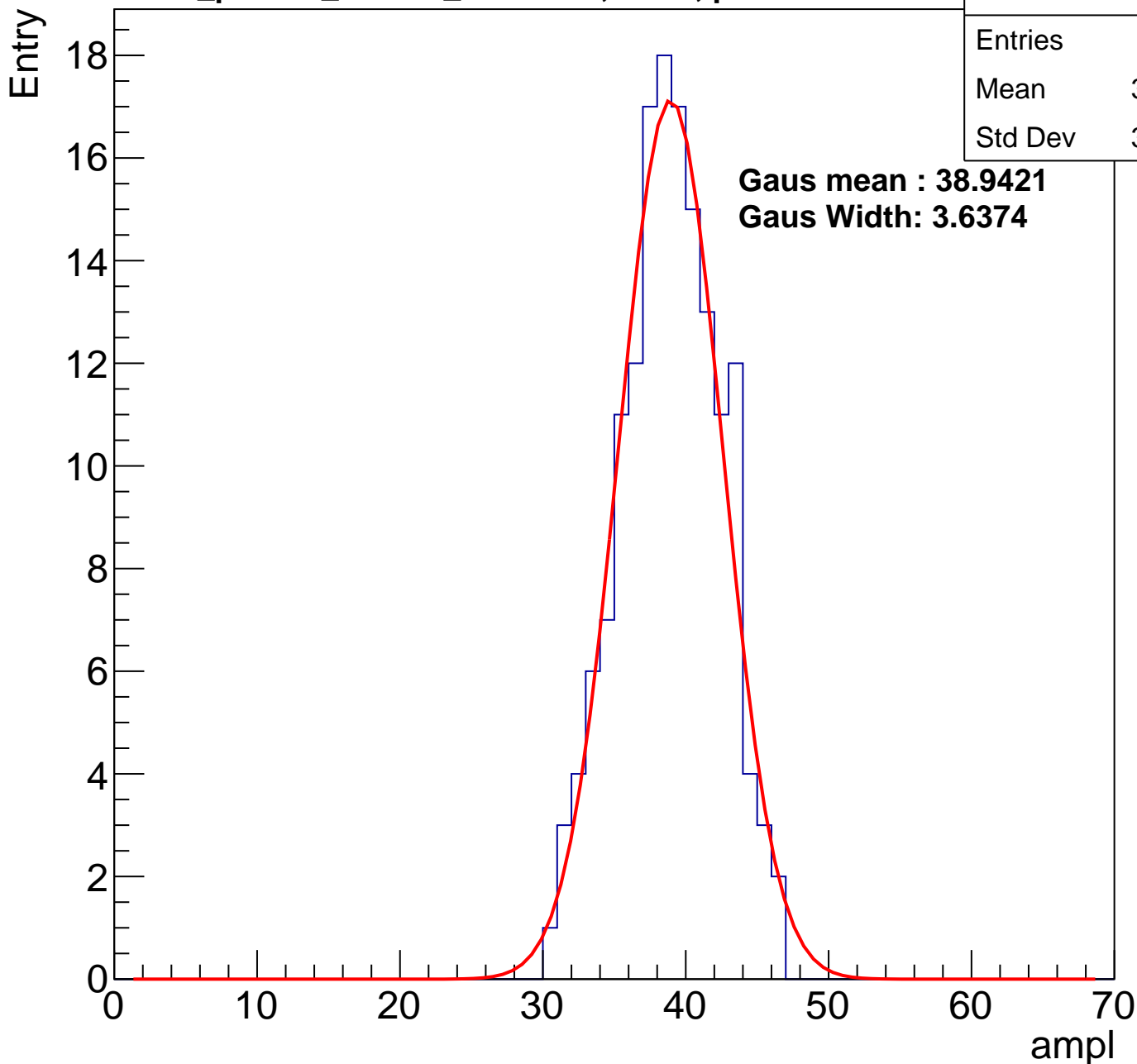
# B0L101S, U26-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	38.42
Std Dev	3.436

**Gaus mean : 38.9421**

**Gaus Width: 3.6374**



# B0L101S, U26-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

45.68

Std Dev

3.322

**Gaus mean : 46.0657**

**Gaus Width: 3.2601**

0

10

20

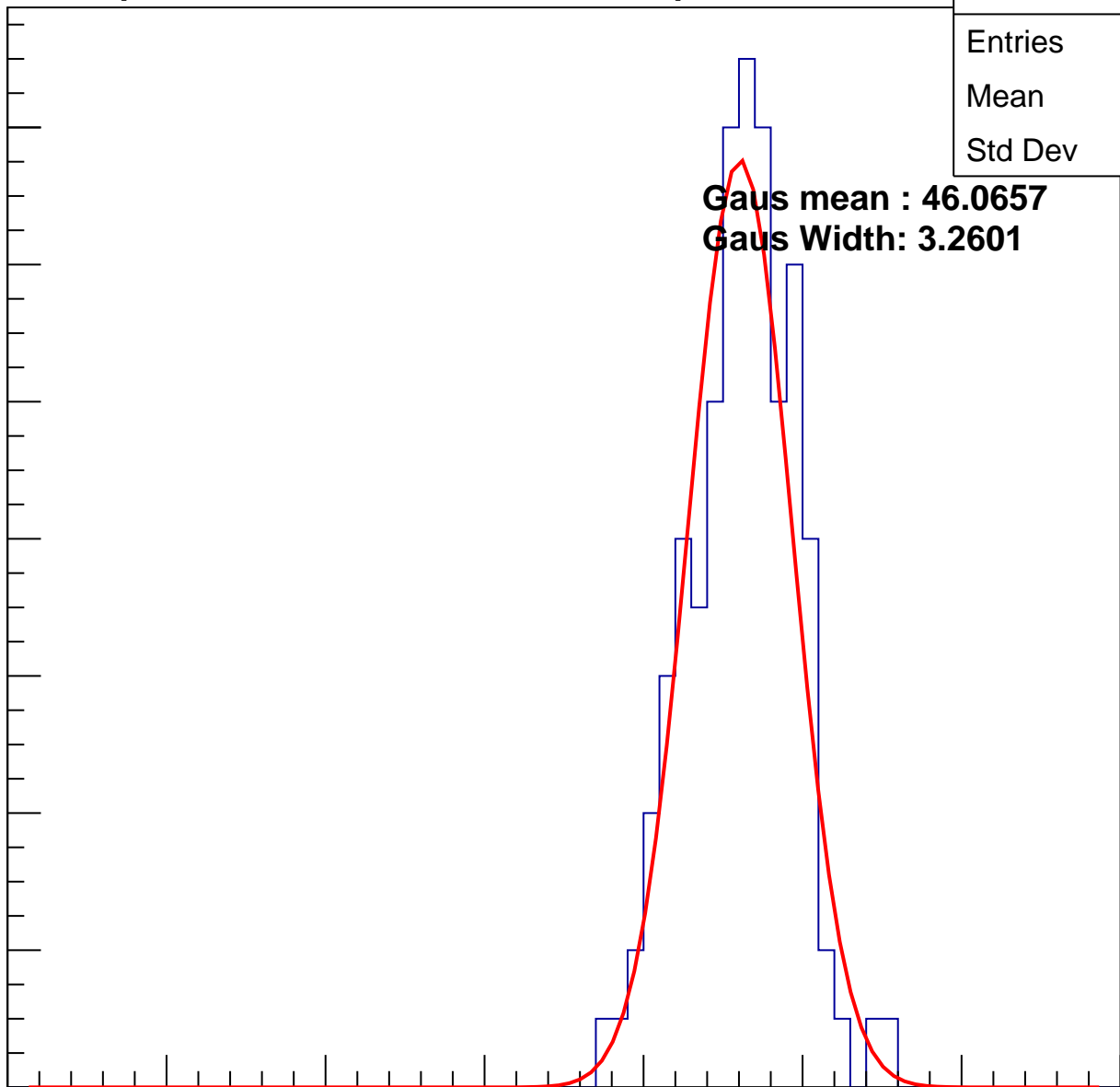
30

40

50

60

ampl



# B0L101S, U26-ch63, adc3

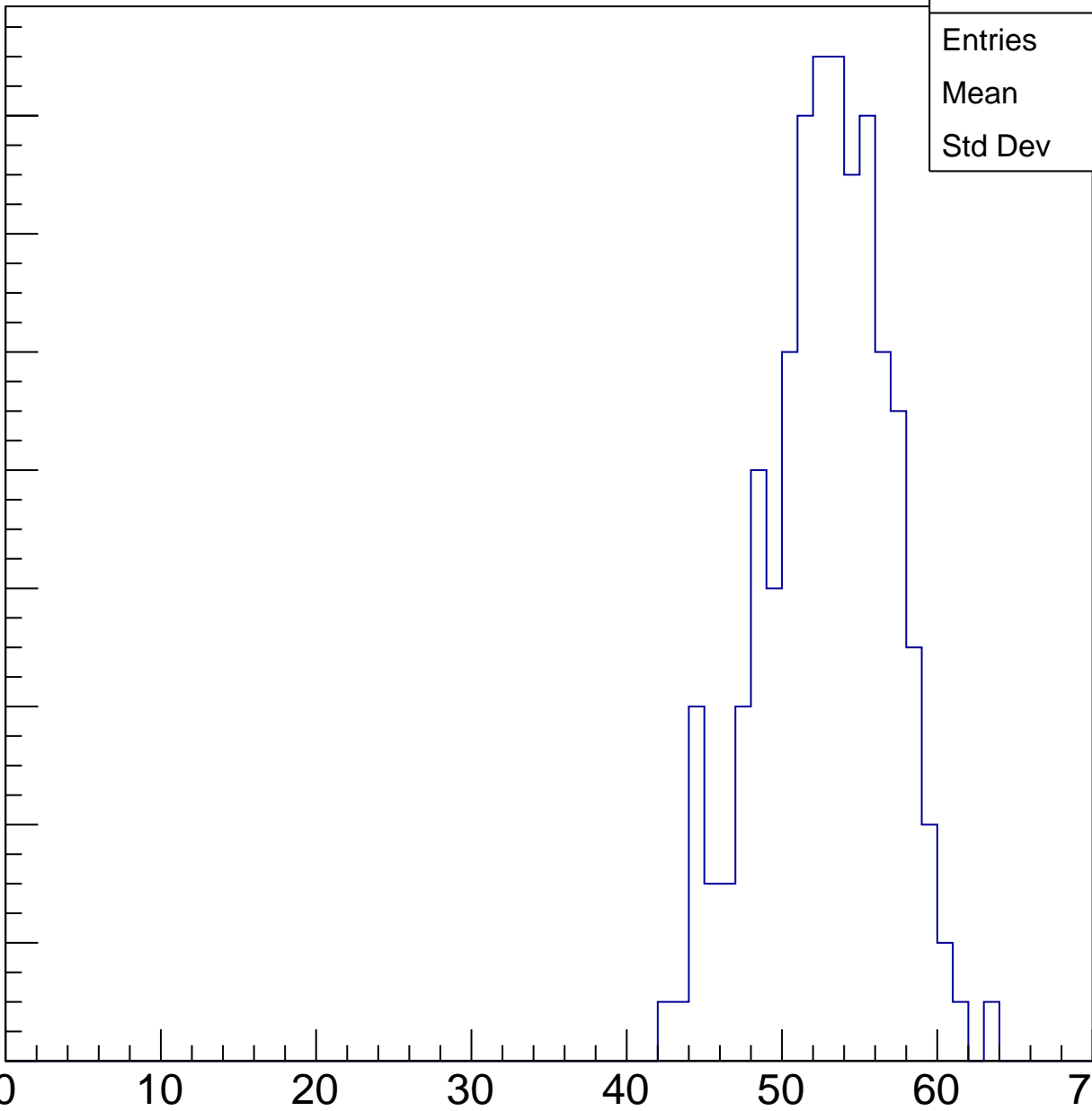
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	169
Mean	52.38
Std Dev	4.003

ampl

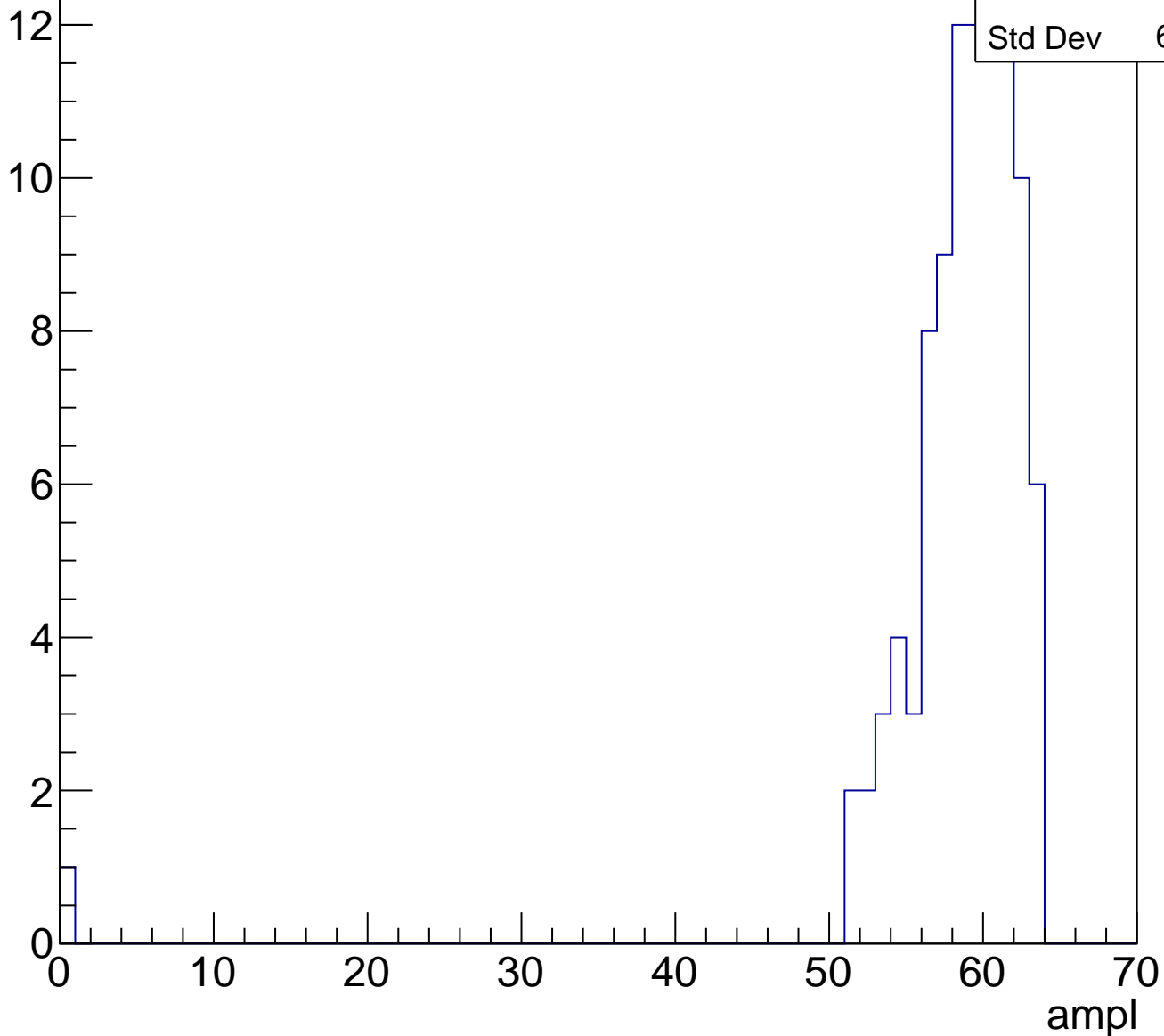


# B0L101S, U26-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	98
Mean	57.98
Std Dev	6.571

Entry



# B0L101S, U26-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	39
Mean	59.9
Std Dev	9.821

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

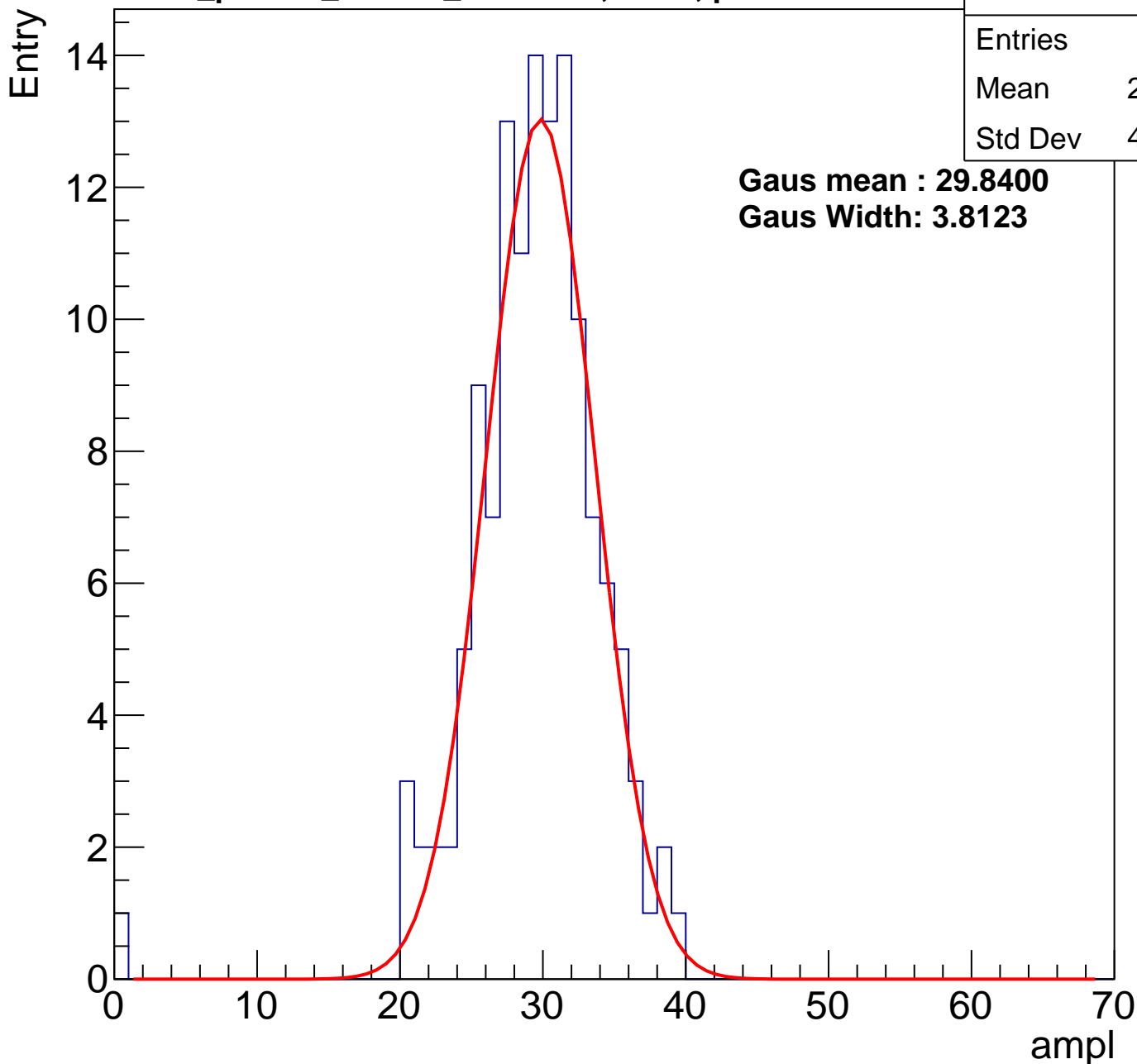
# B0L101S, U26-ch64, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	28.99
Std Dev	4.665

**Gaus mean : 29.8400**

**Gaus Width: 3.8123**



# B0L101S, U26-ch64, adc1

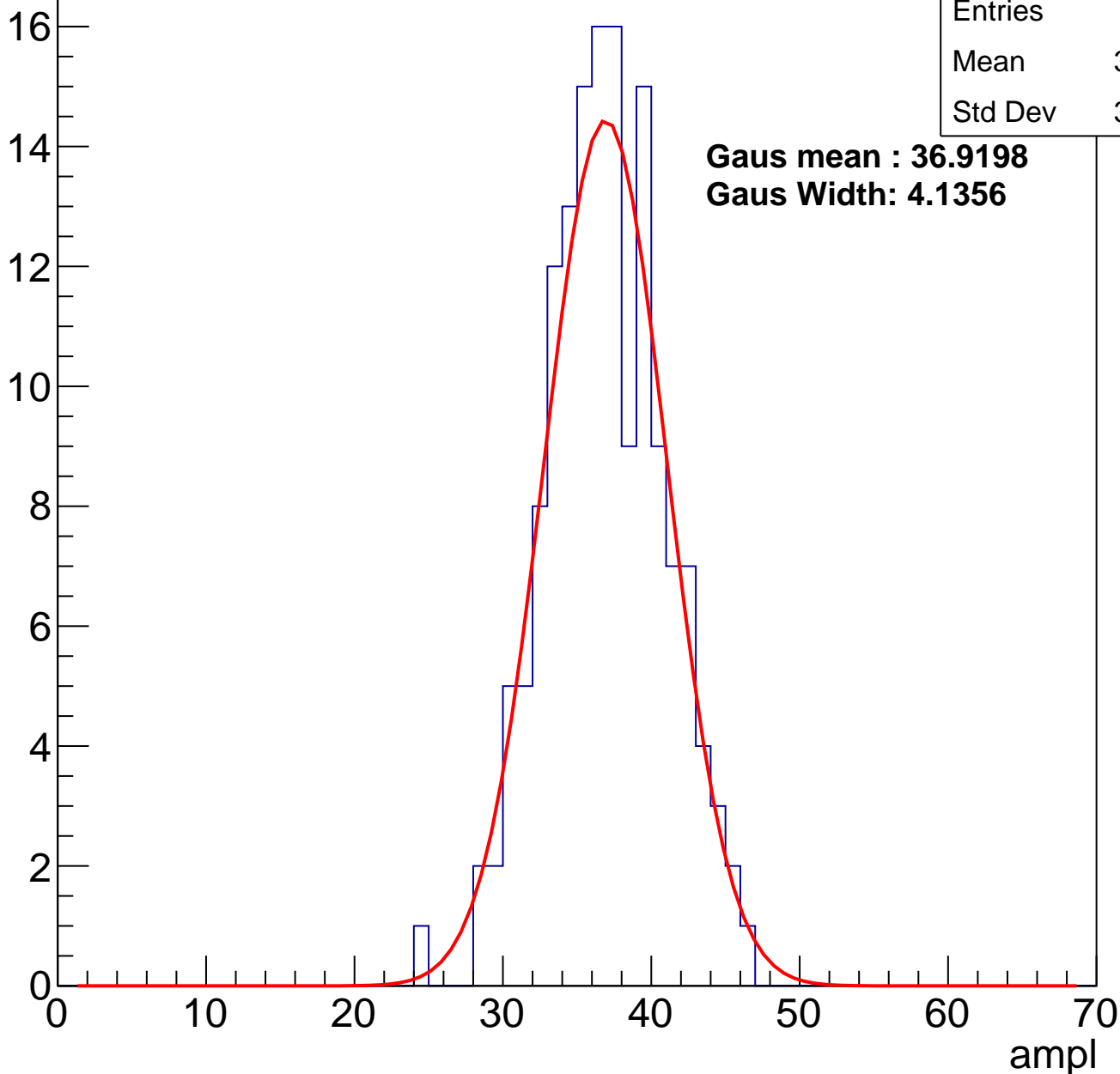
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	36.43
Std Dev	3.938

**Gaus mean : 36.9198**

**Gaus Width: 4.1356**

Entry



# B0L101S, U26-ch64, adc2

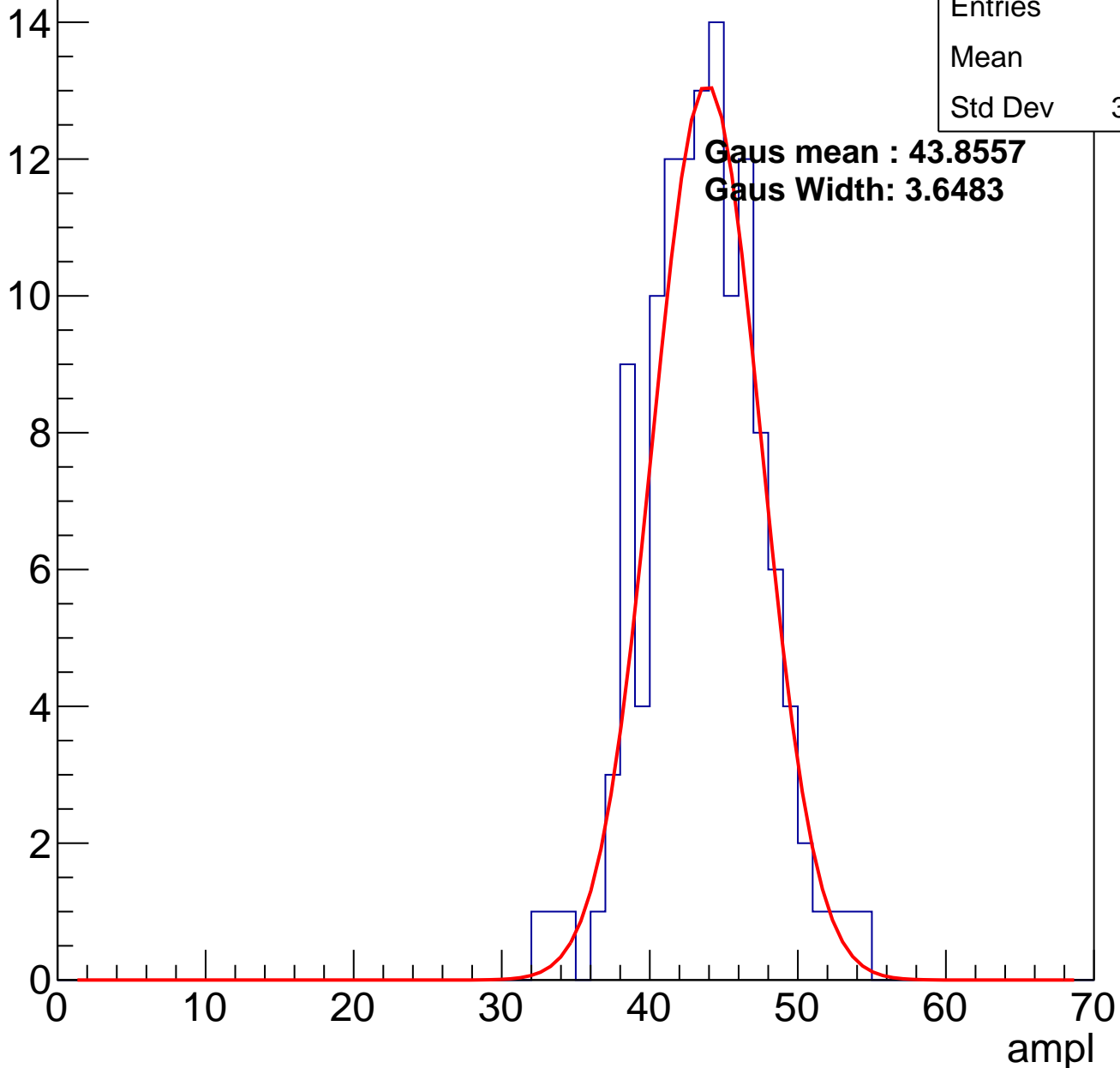
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	43.2
Std Dev	3.909

**Gaus mean : 43.8557**

**Gaus Width: 3.6483**

Entry



# B0L101S, U26-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

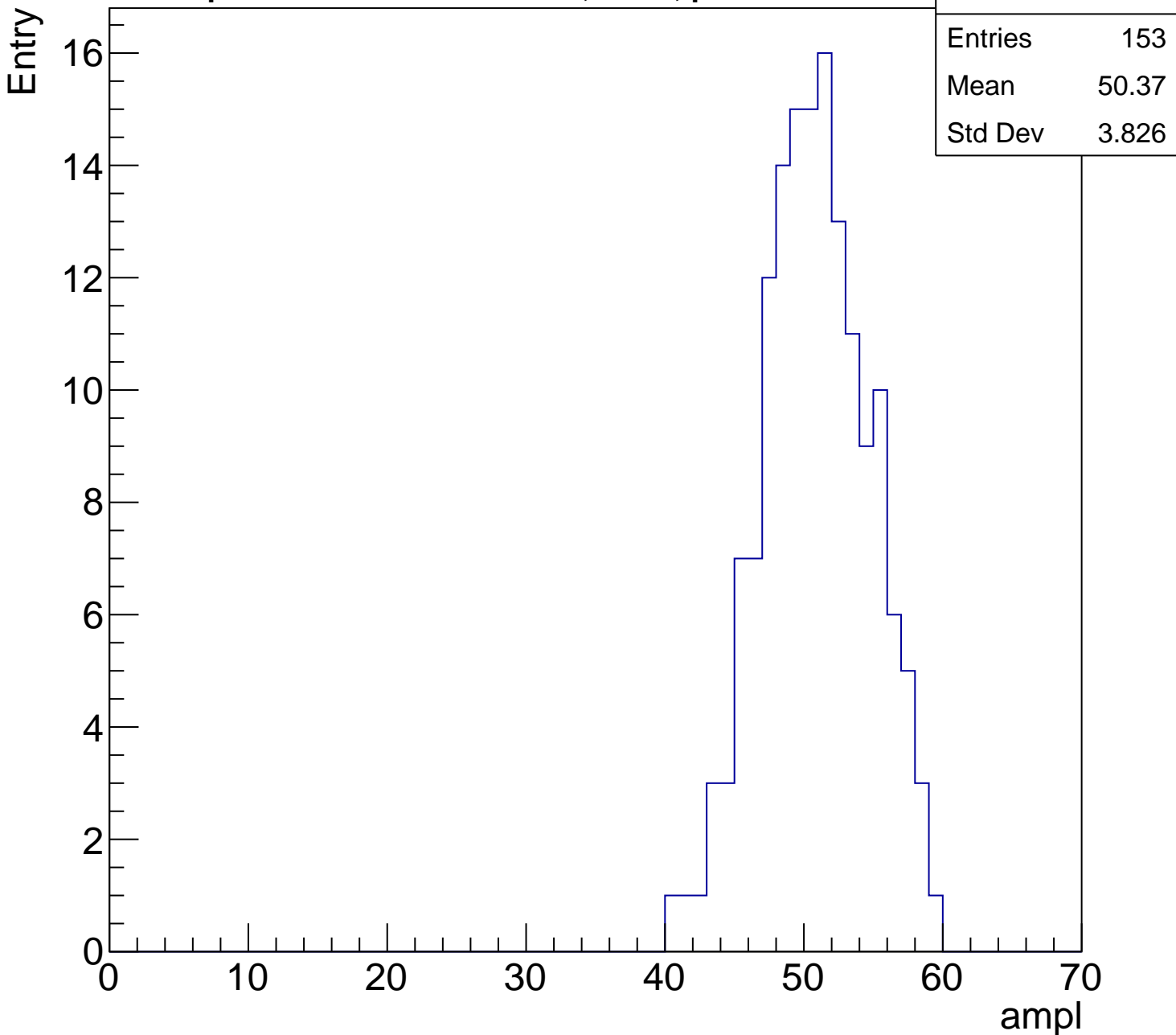
Entries	153
Mean	50.37
Std Dev	3.826

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

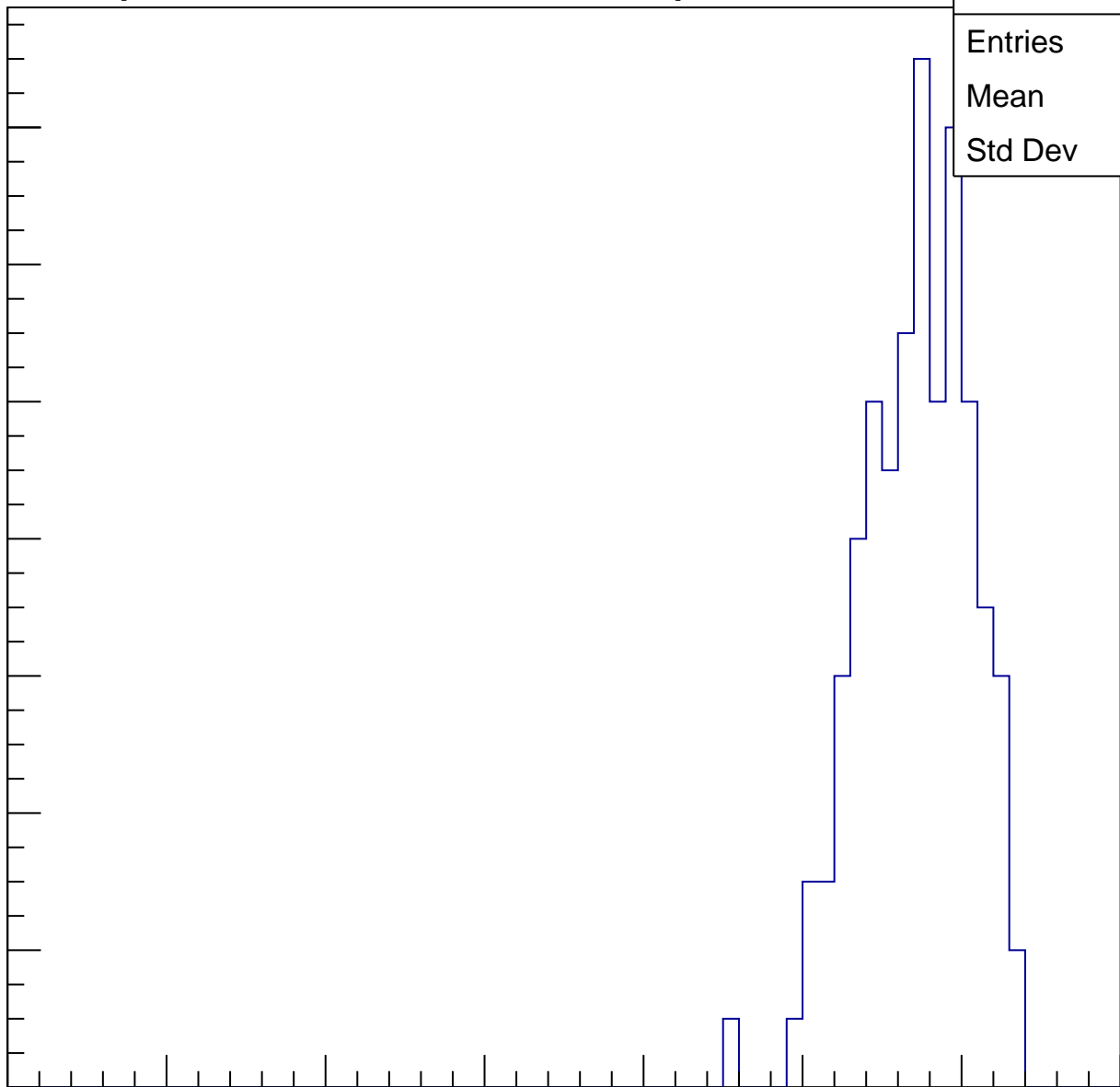
116

Mean

56.64

Std Dev

3.415



# B0L101S, U26-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	63
Mean	59.73
Std Dev	7.846

ampl

0

10

20

30

40

50

60

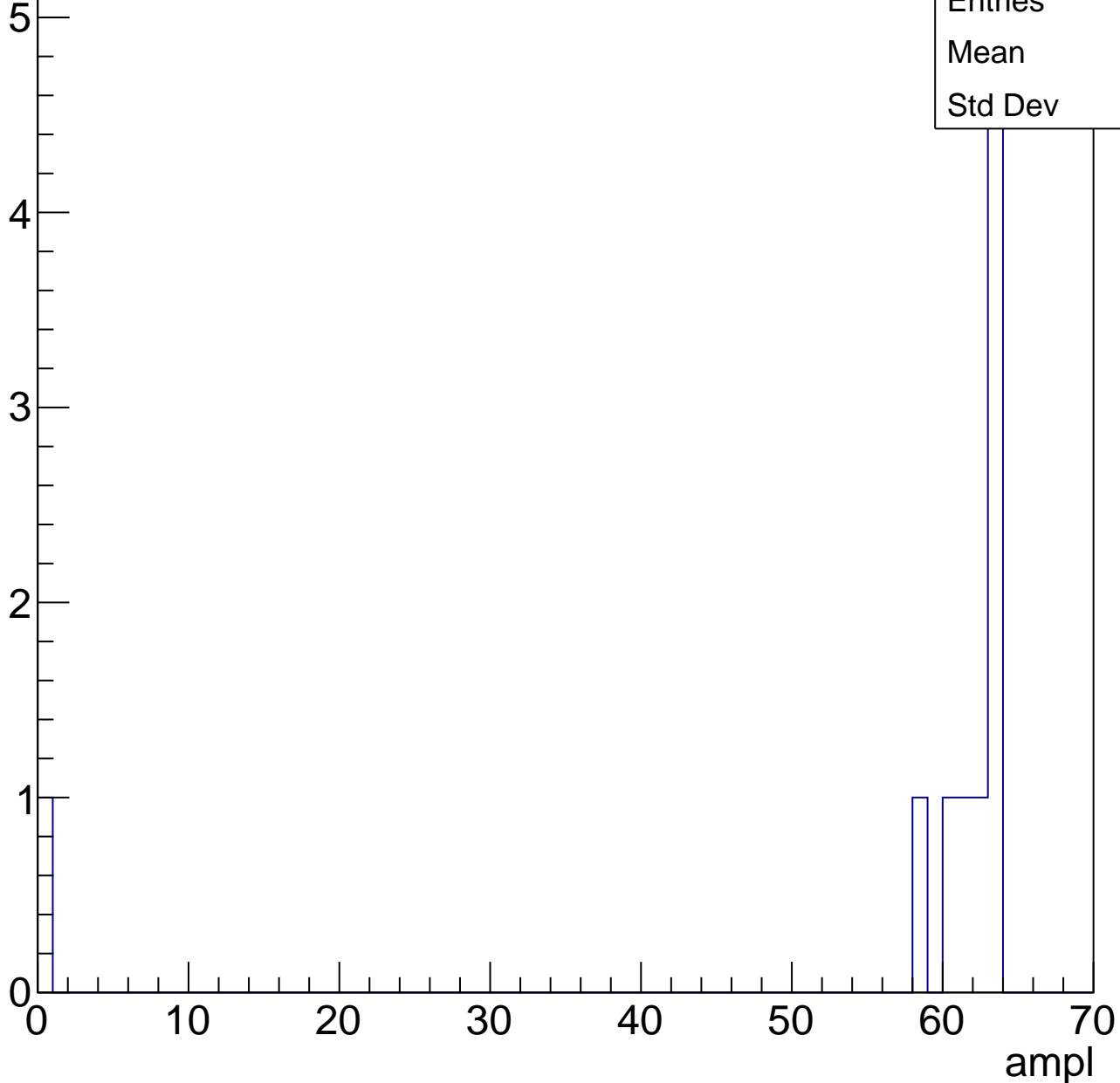
70

# B0L101S, U26-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	10
Mean	55.6
Std Dev	18.6





# B0L101S, U26-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch65, adc0

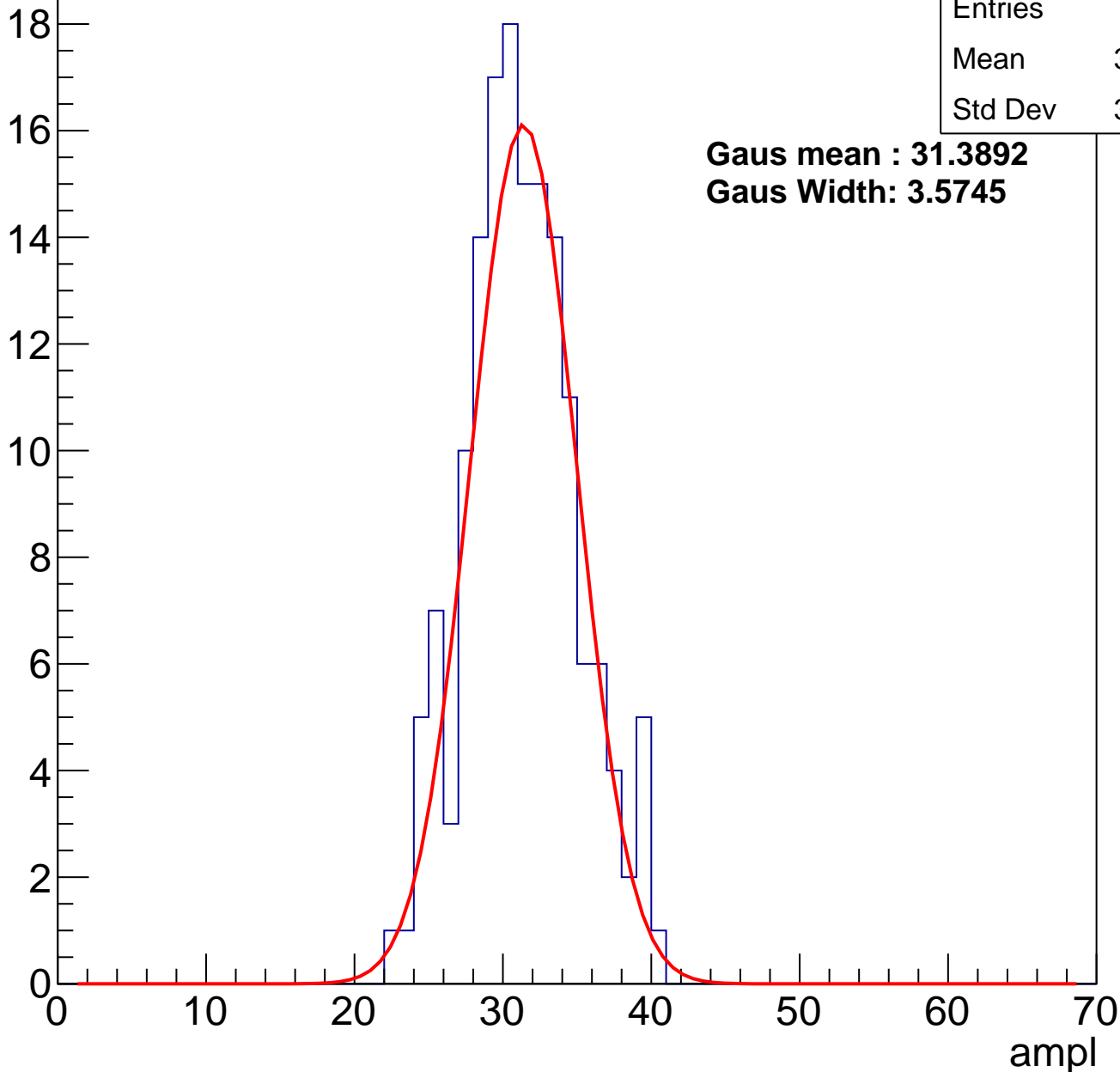
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	30.83
Std Dev	3.719

**Gaus mean : 31.3892**

**Gaus Width: 3.5745**

Entry



# B0L101S, U26-ch65, adc1

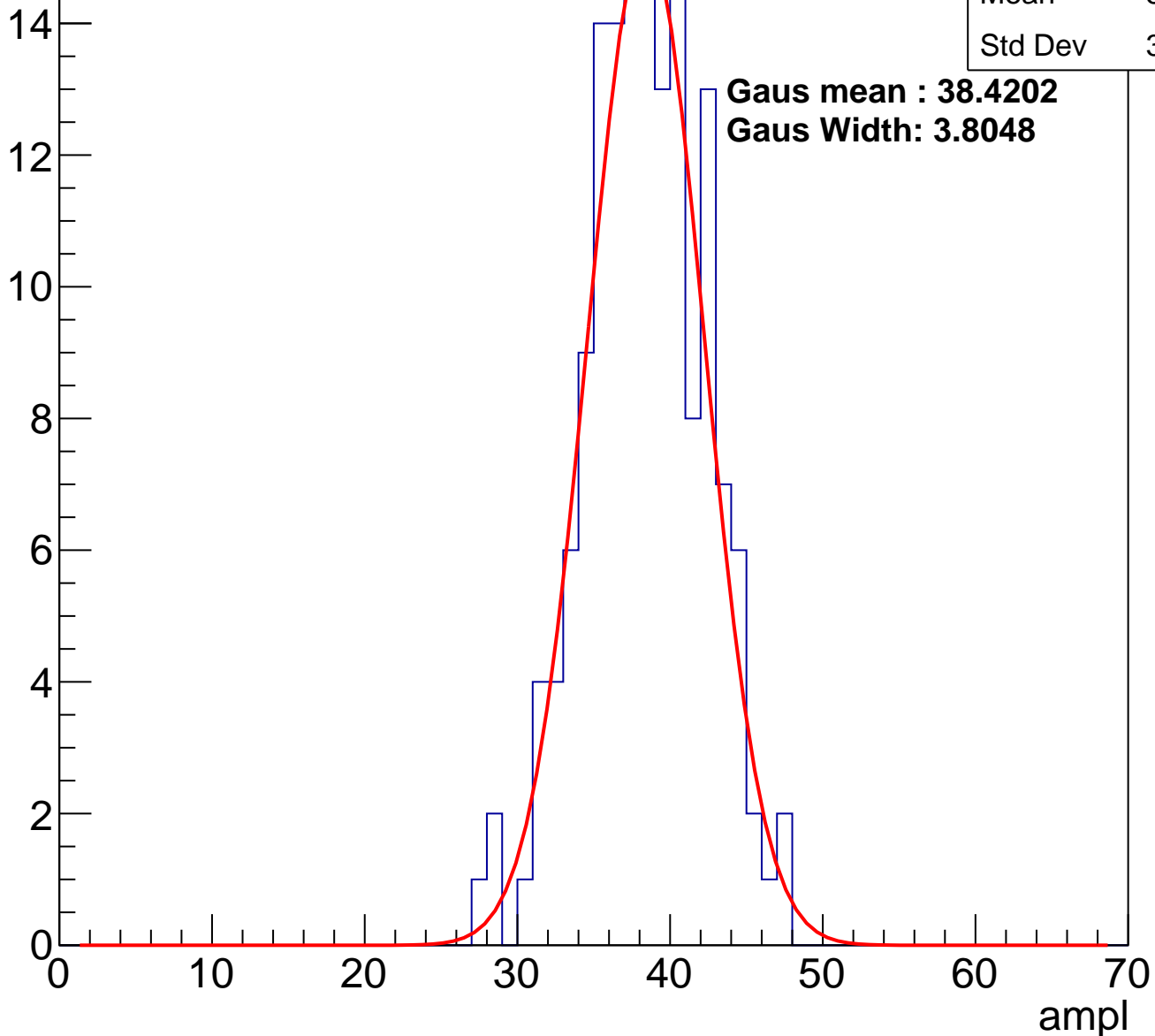
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	37.92
Std Dev	3.876

**Gaus mean : 38.4202**

**Gaus Width: 3.8048**

Entry



# B0L101S, U26-ch65, adc2

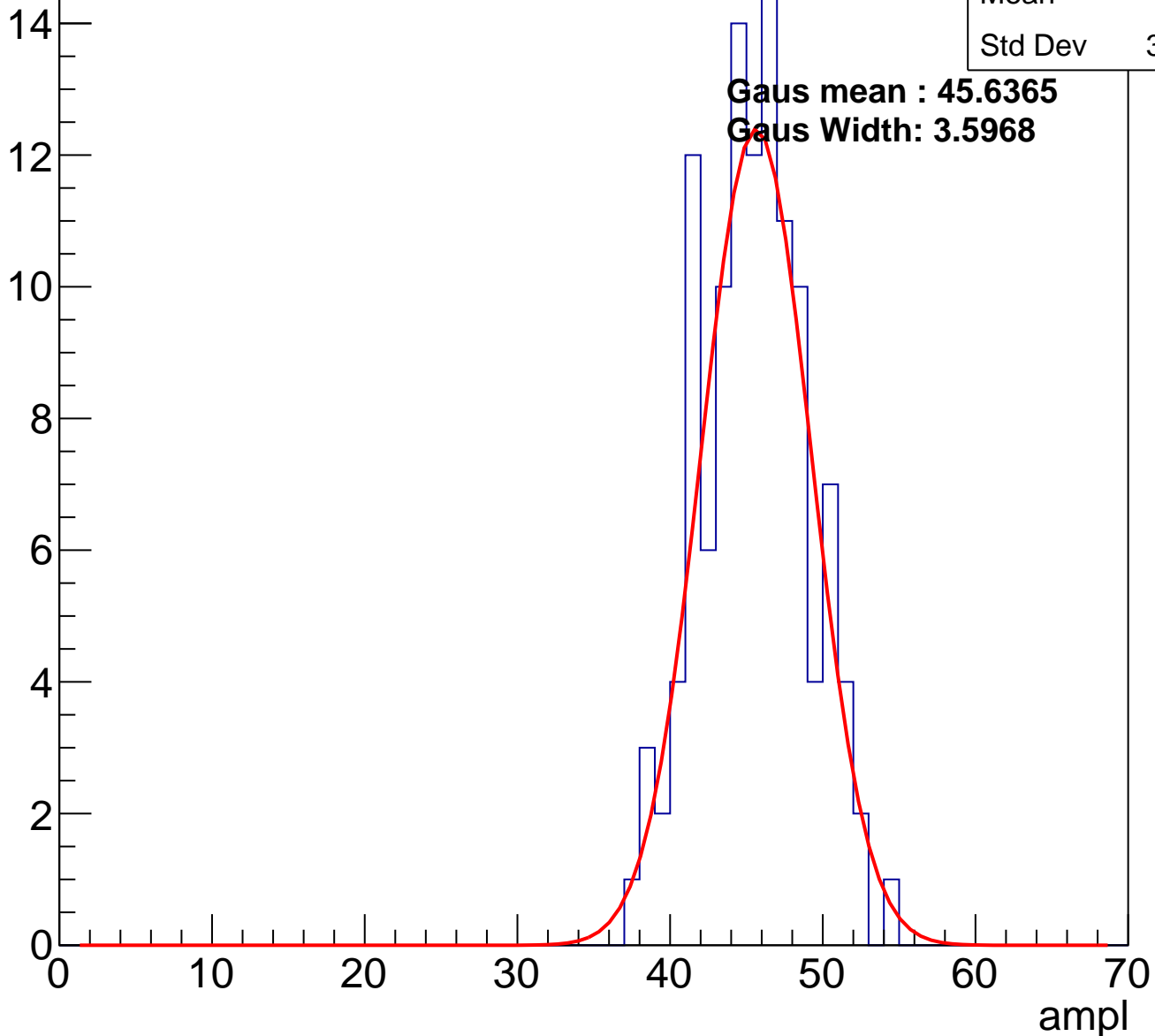
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	45.03
Std Dev	3.437

**Gaus mean : 45.6365**

**Gaus Width: 3.5968**

Entry



# B0L101S, U26-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

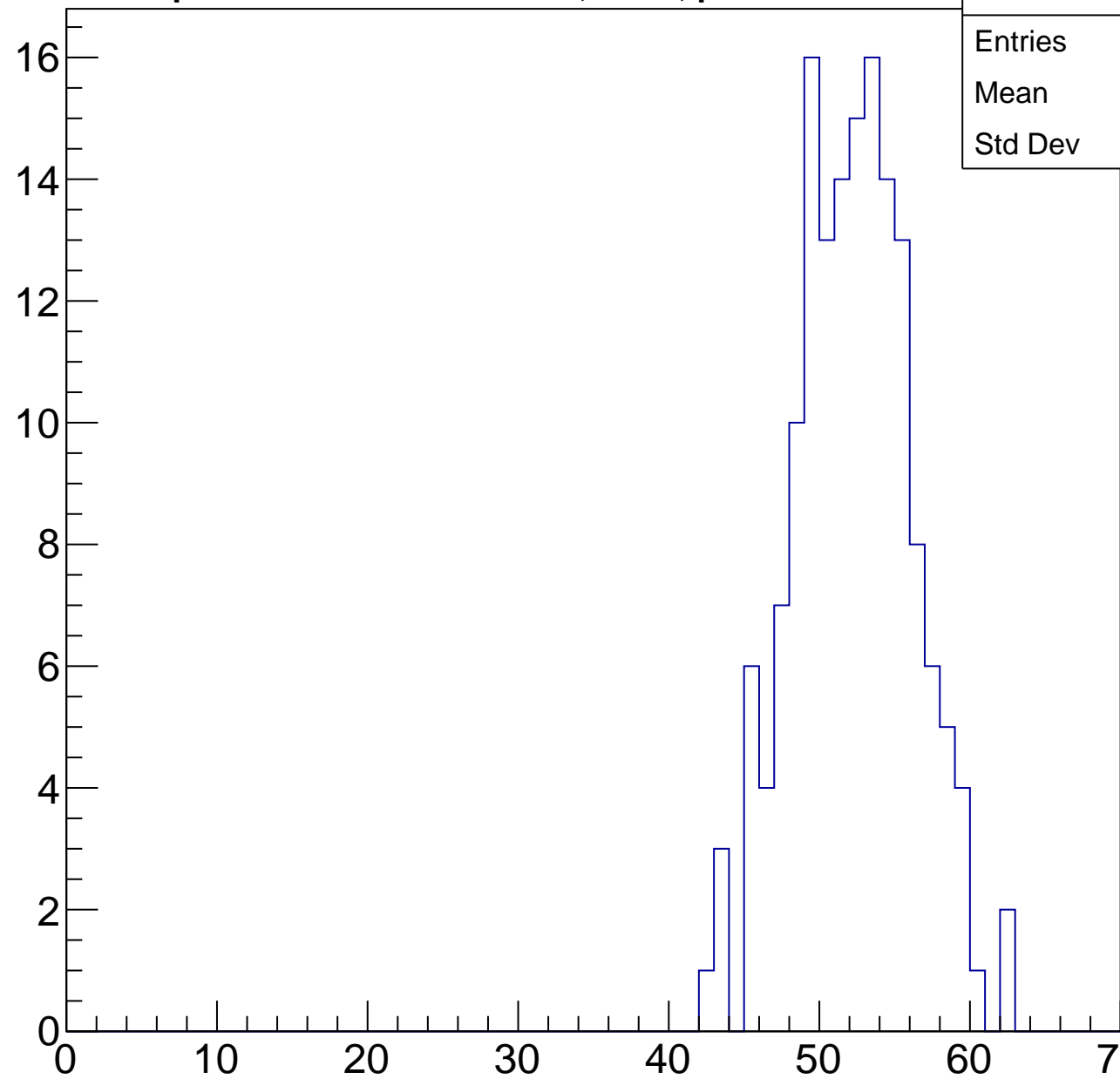
Entries	158
Mean	51.78
Std Dev	3.918

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

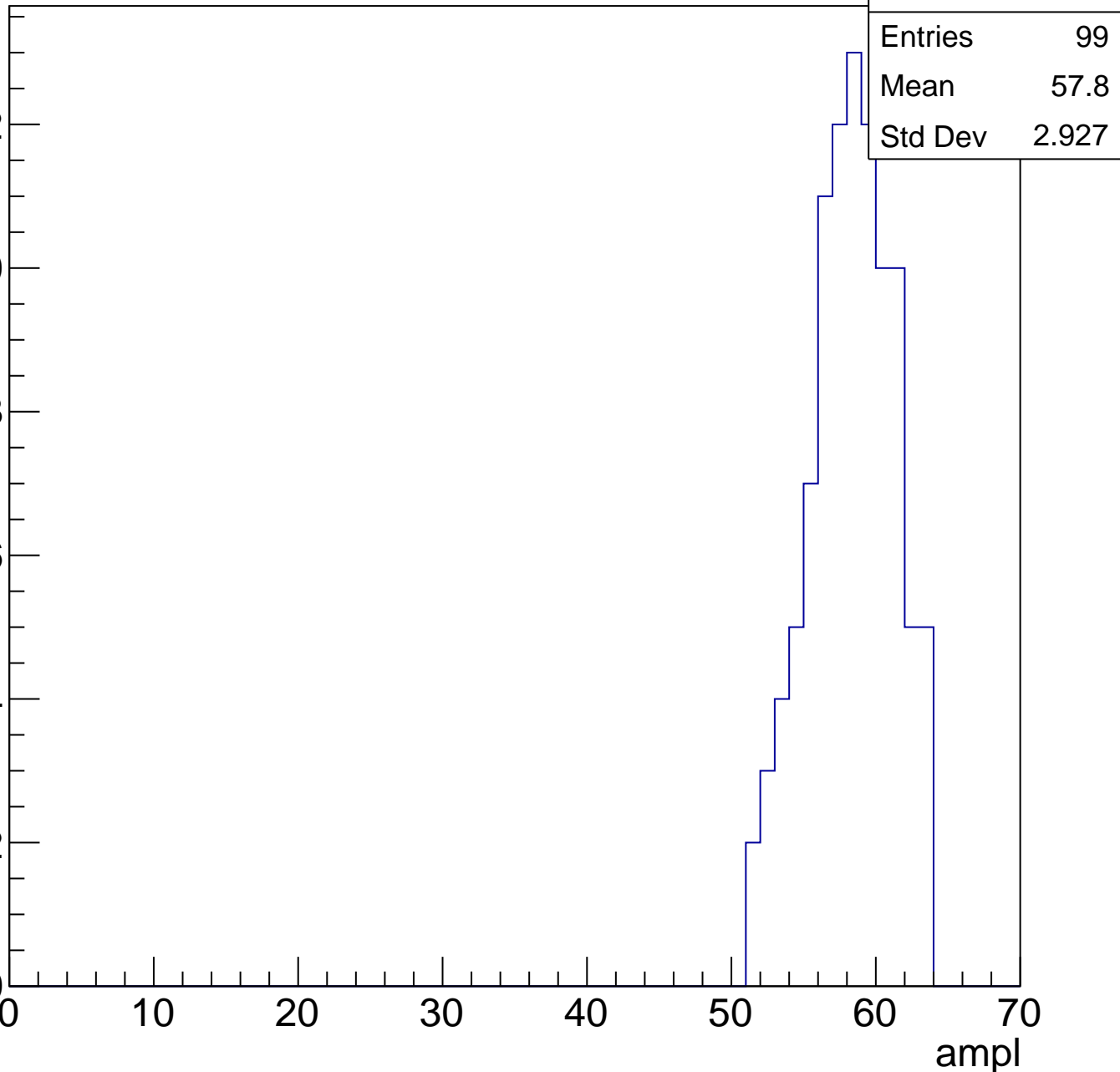
40

50

60

ampl

Entries	99
Mean	57.8
Std Dev	2.927



# B0L101S, U26-ch65, adc5

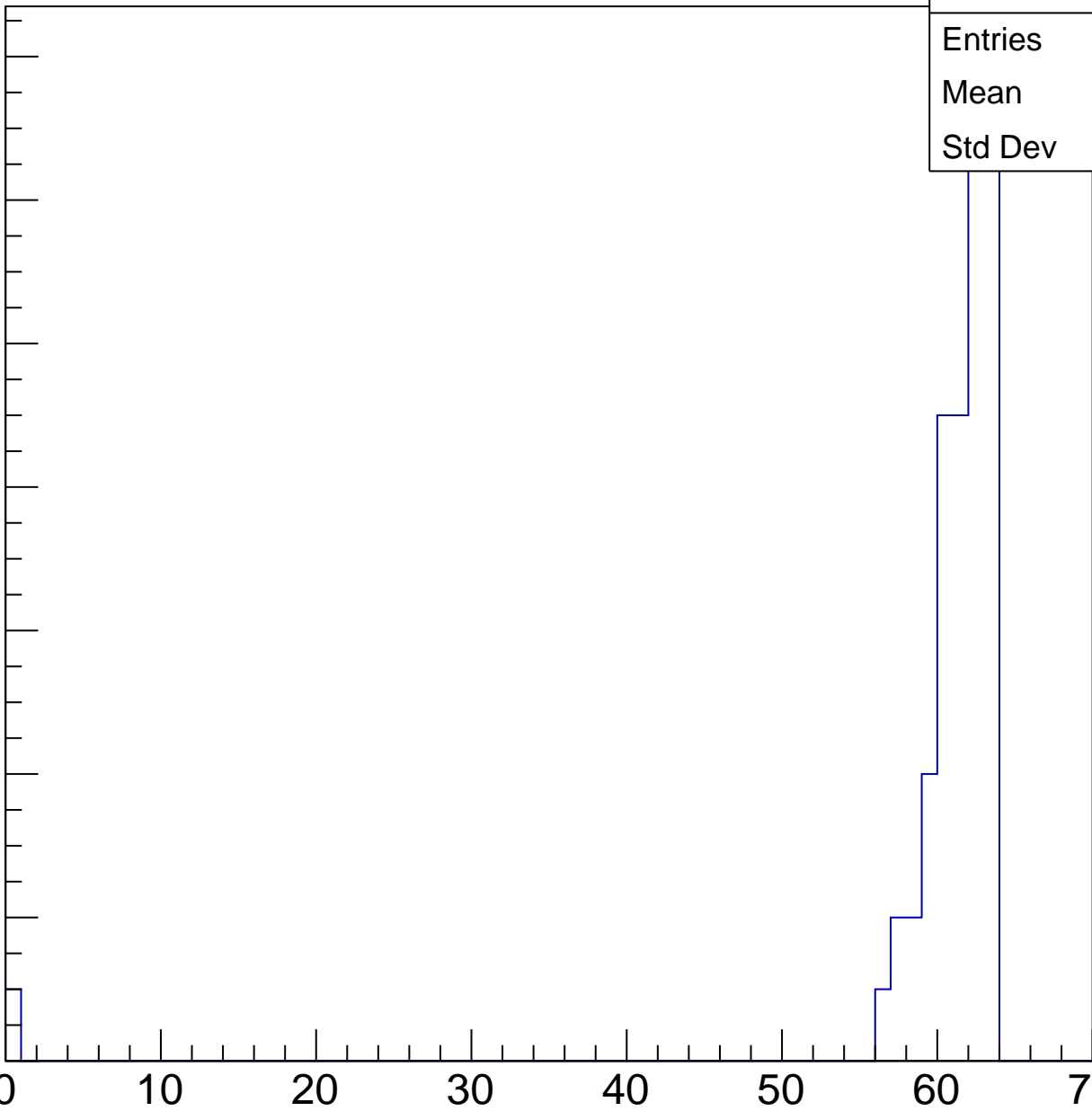
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	55
Mean	59.98
Std Dev	8.348

ampl



# B0L101S, U26-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch66, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	30.03
Std Dev	3.635

**Gaus mean : 30.6382**

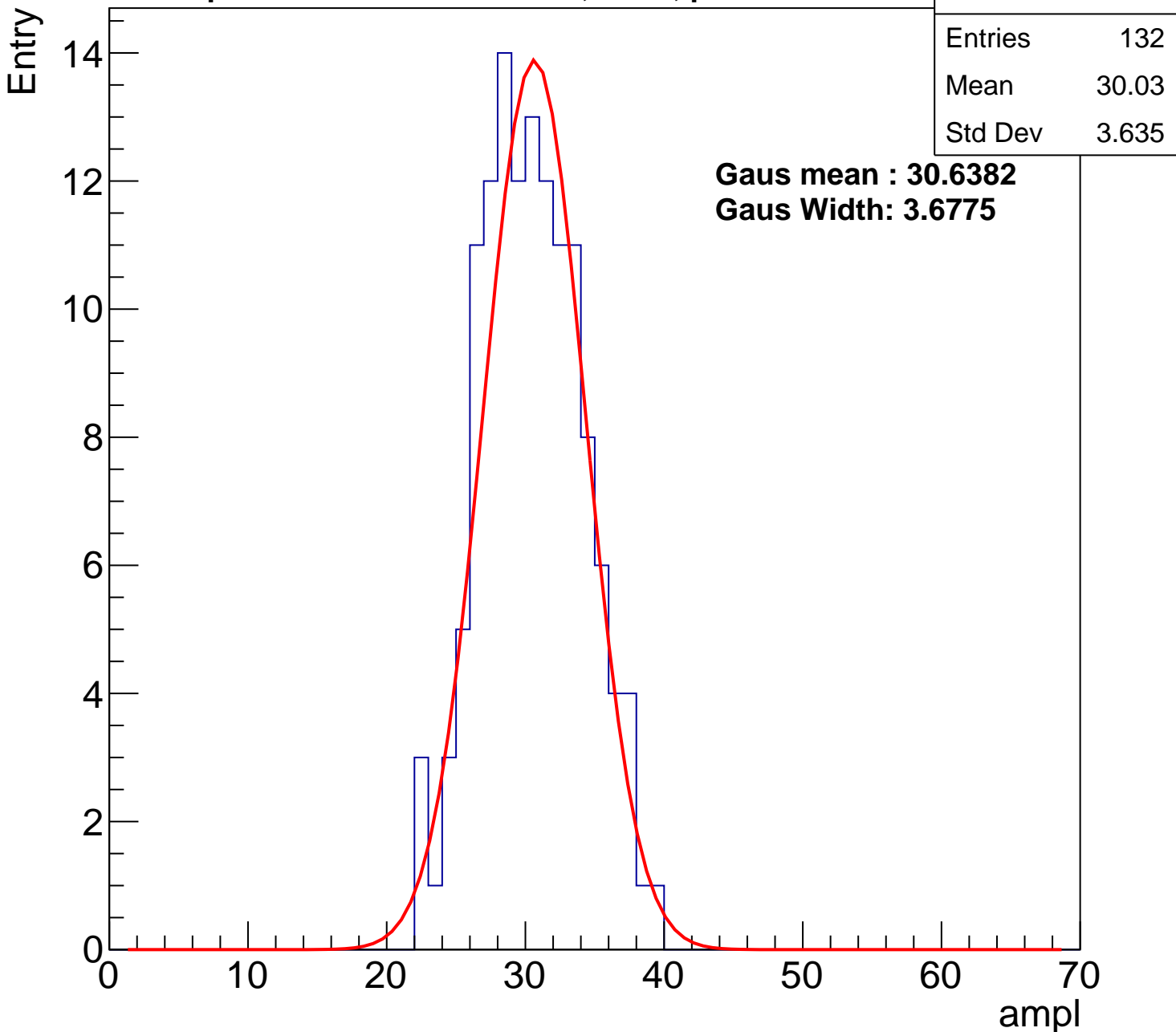
**Gaus Width: 3.6775**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch66, adc1

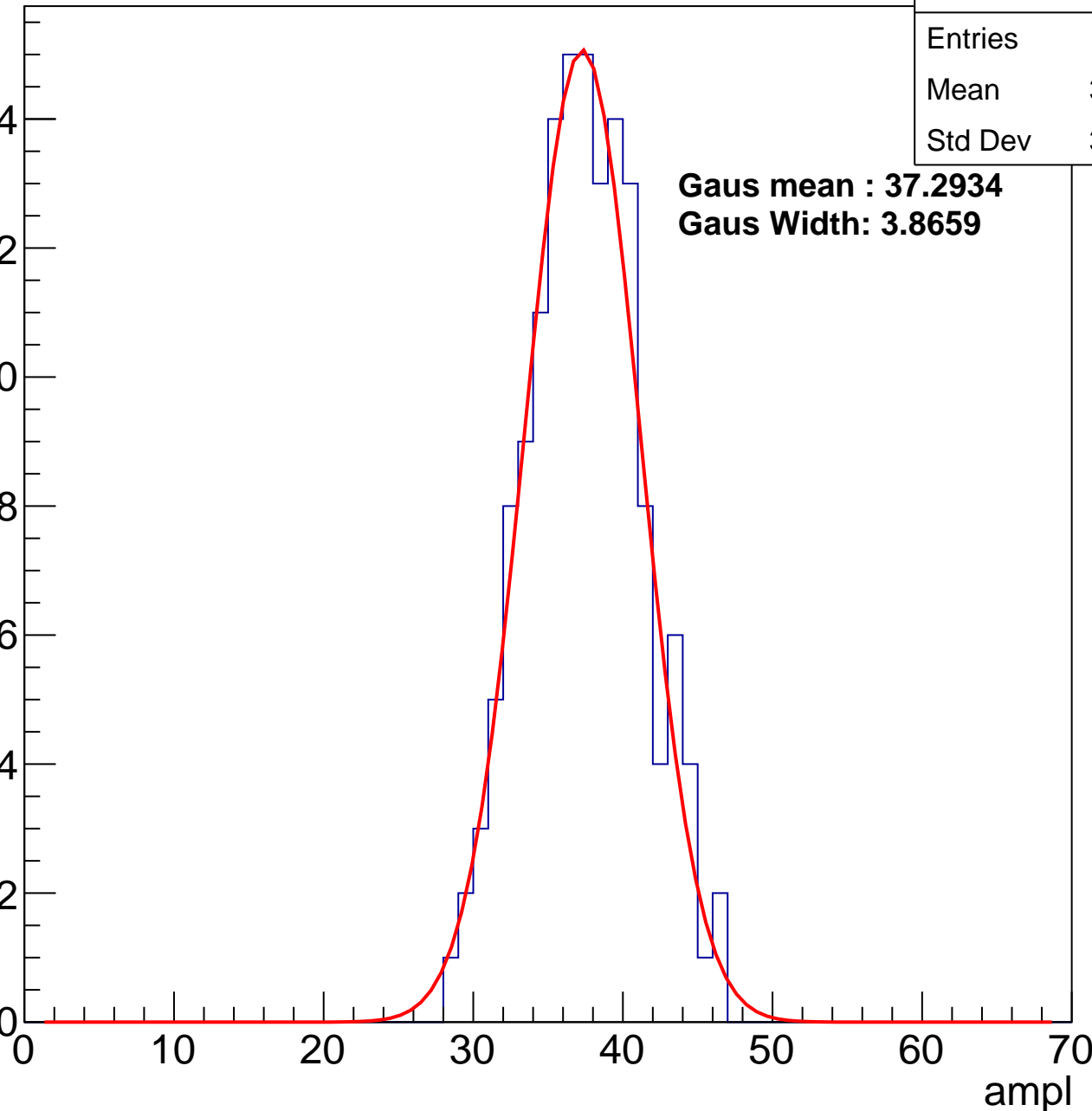
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	148
Mean	36.96
Std Dev	3.768

**Gaus mean : 37.2934**  
**Gaus Width: 3.8659**



# B0L101S, U26-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

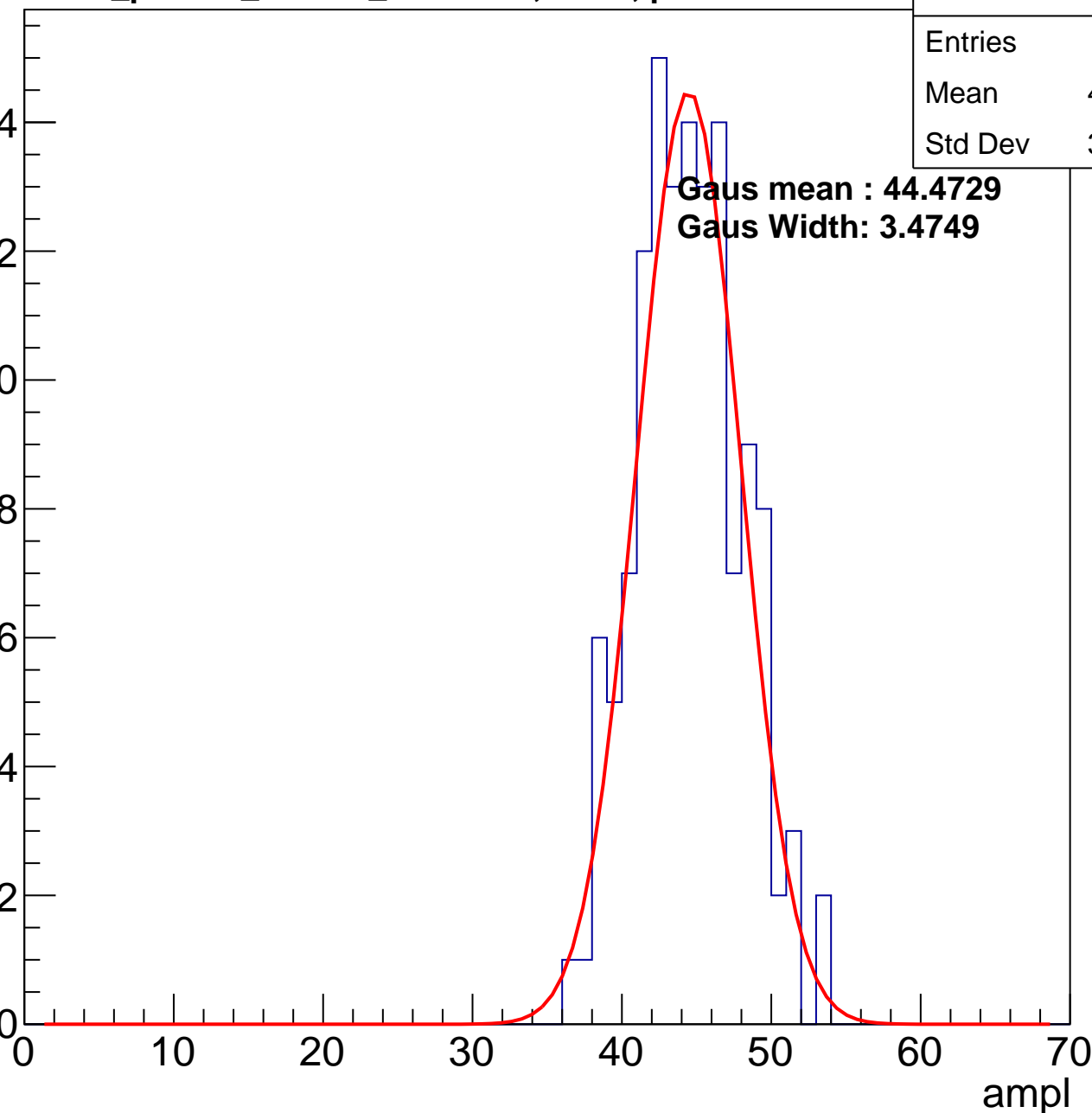
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	132
Mean	44.05
Std Dev	3.486

**Gaus mean : 44.4729**

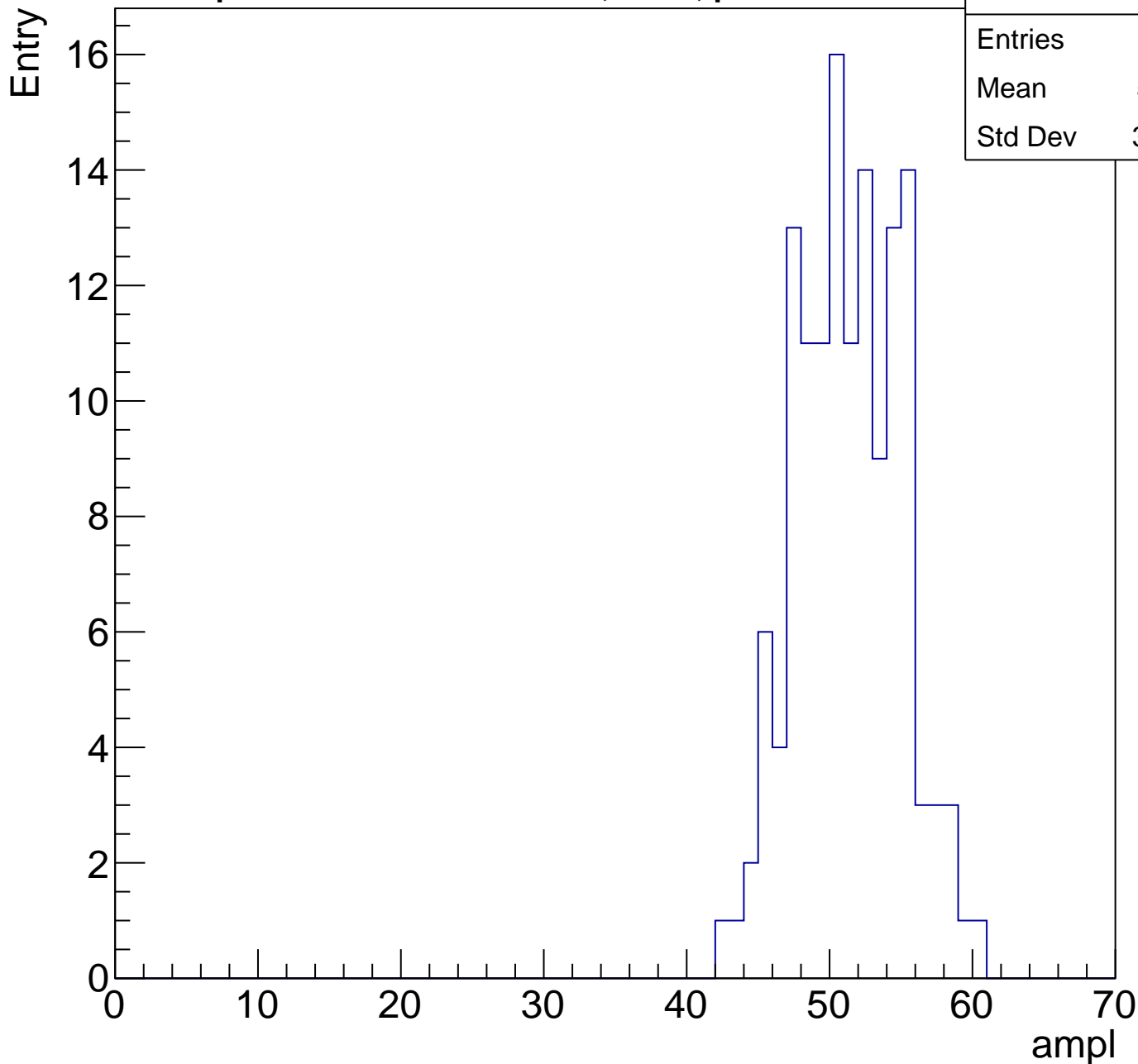
**Gaus Width: 3.4749**



# B0L101S, U26-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

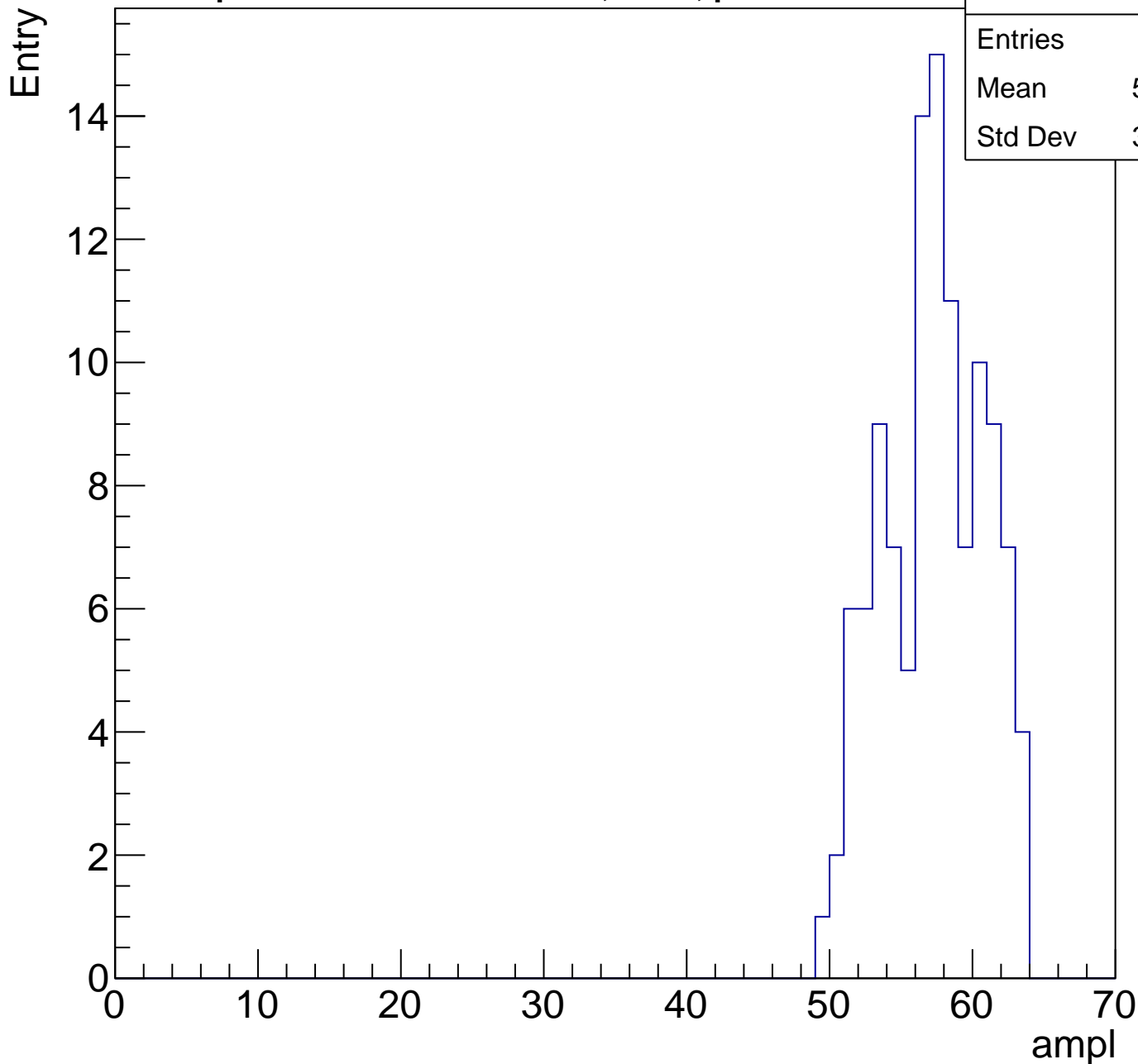
Entries	137
Mean	50.91
Std Dev	3.616



# B0L101S, U26-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	56.83
Std Dev	3.466



# B0L101S, U26-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

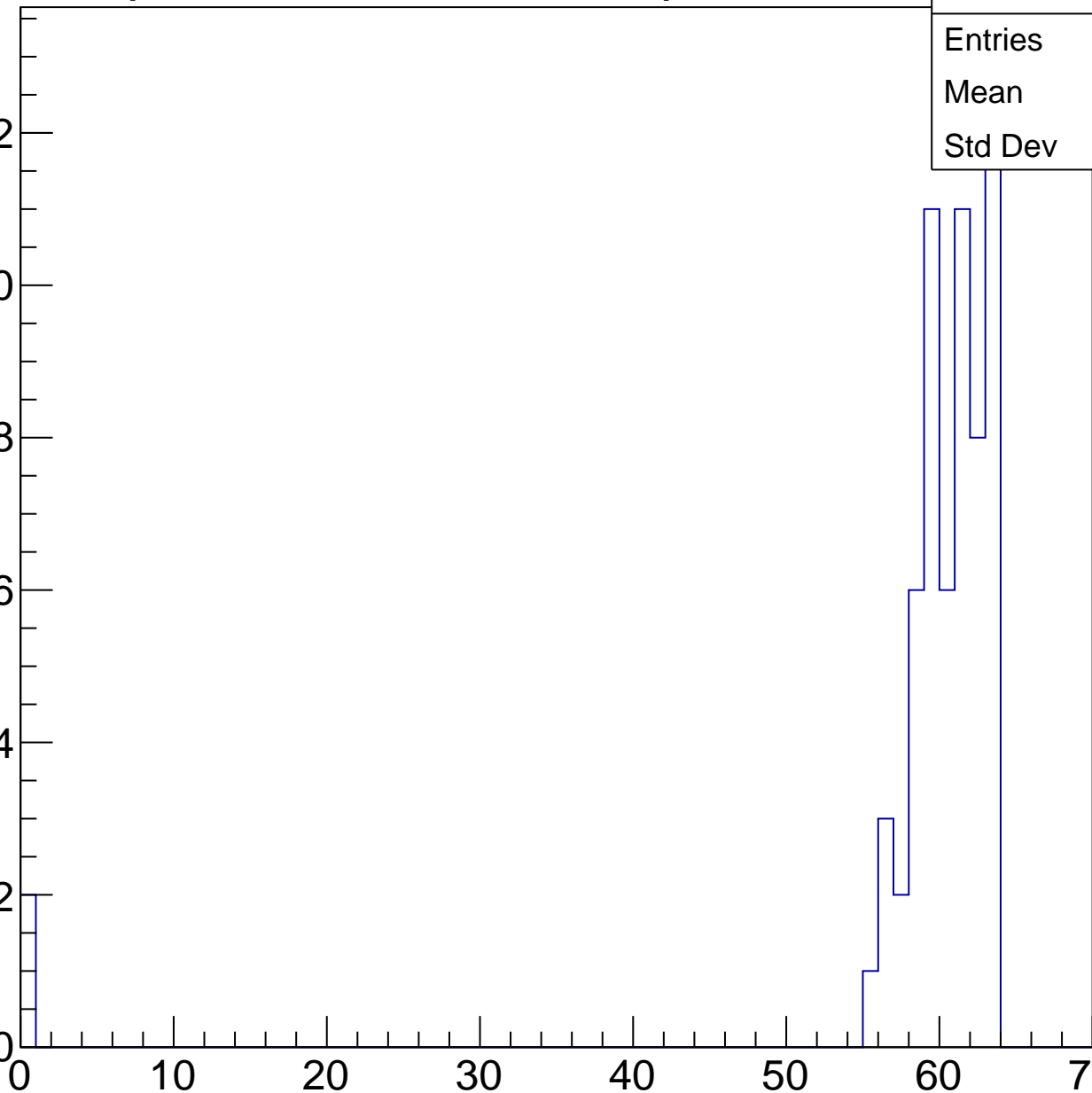
4

2

0

Entries	63
Mean	58.41
Std Dev	10.78

ampl

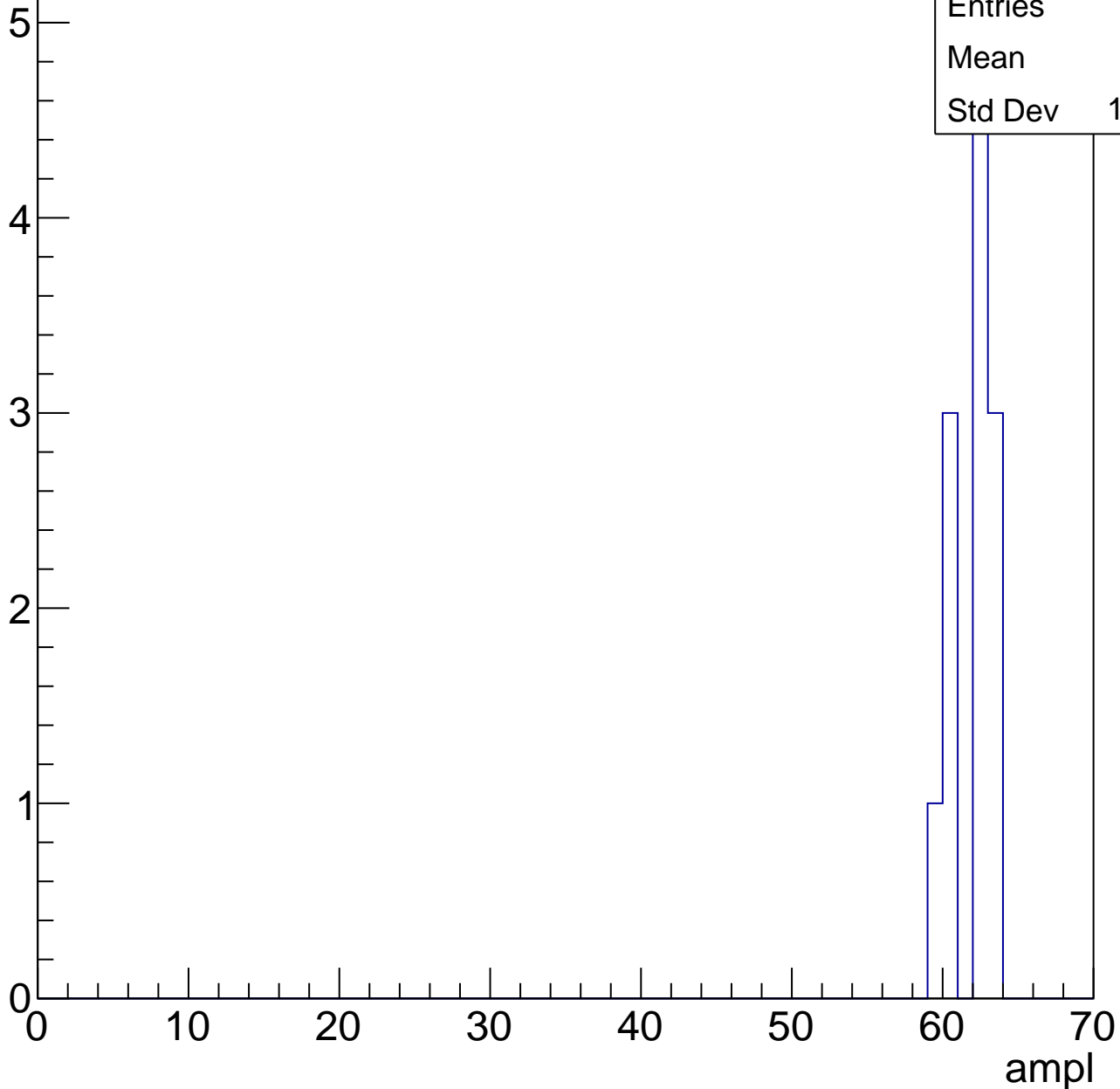


# B0L101S, U26-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.5
Std Dev	1.323

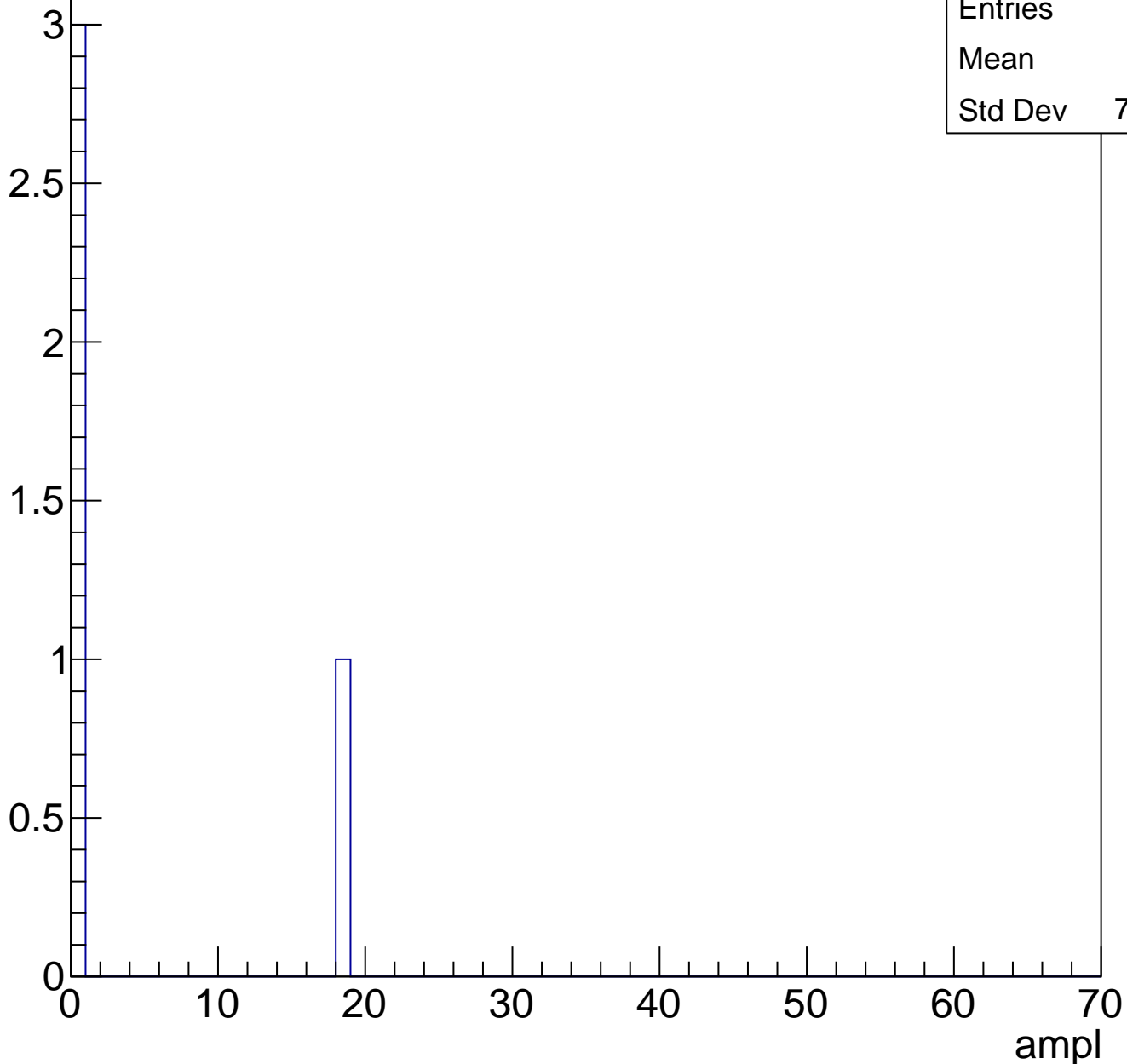




# B0L101S, U26-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	4.5
Std Dev	7.794

# B0L101S, U26-ch67, adc0

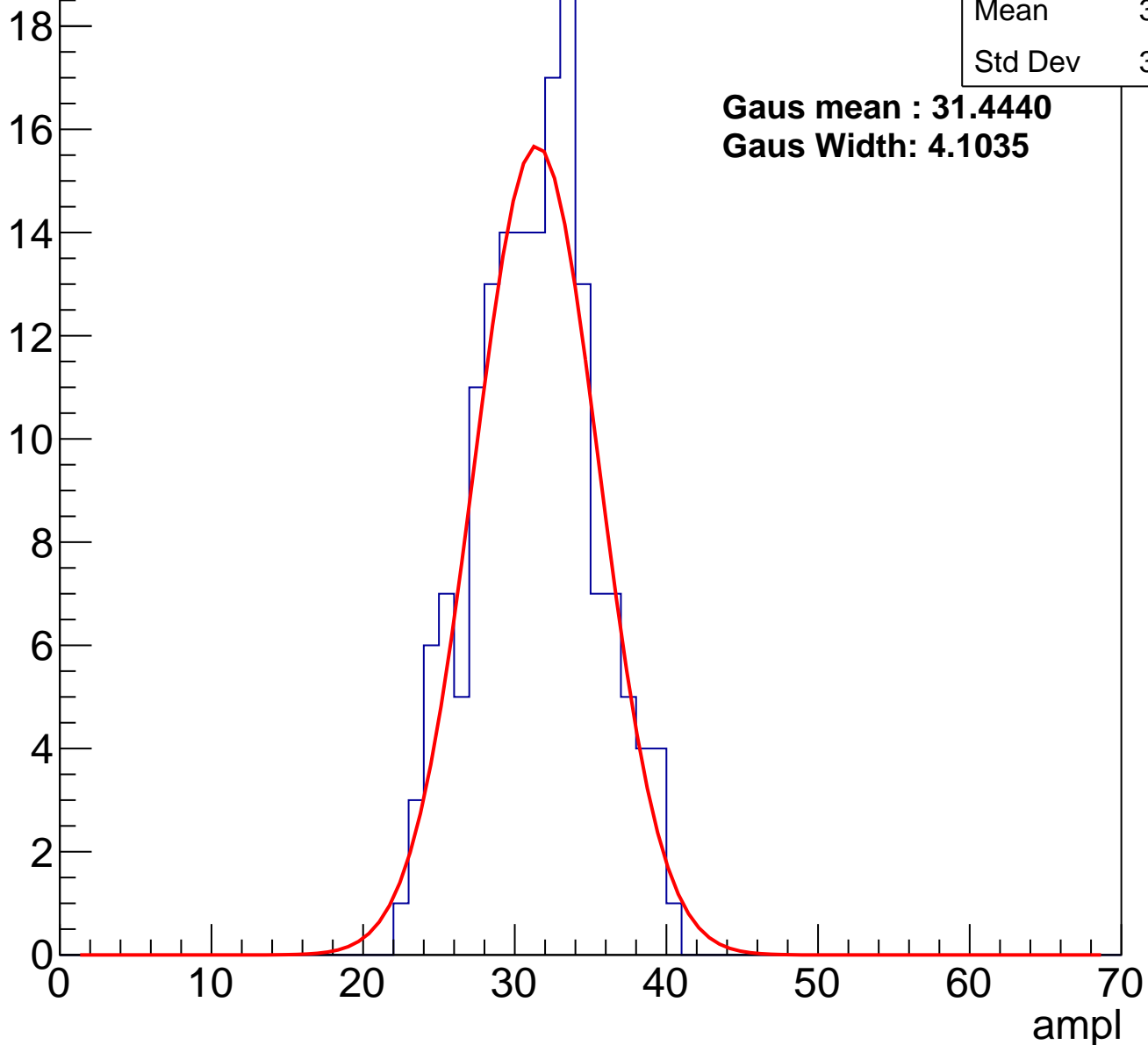
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	30.93
Std Dev	3.905

**Gaus mean : 31.4440**

**Gaus Width: 4.1035**

Entry



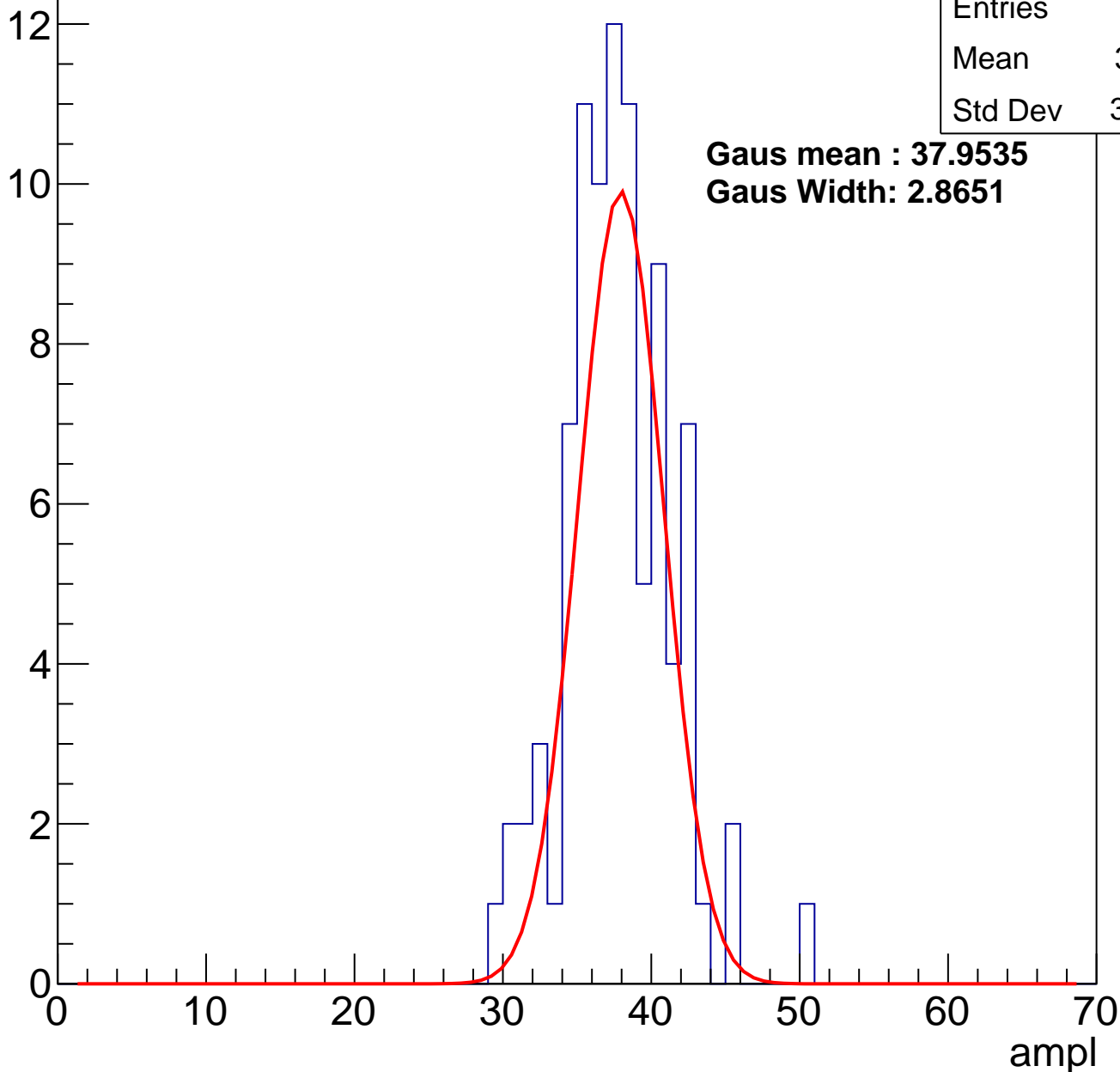
# B0L101S, U26-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	89
Mean	37.31
Std Dev	3.543

**Gaus mean : 37.9535**  
**Gaus Width: 2.8651**

Entry



# B0L101S, U26-ch67, adc2

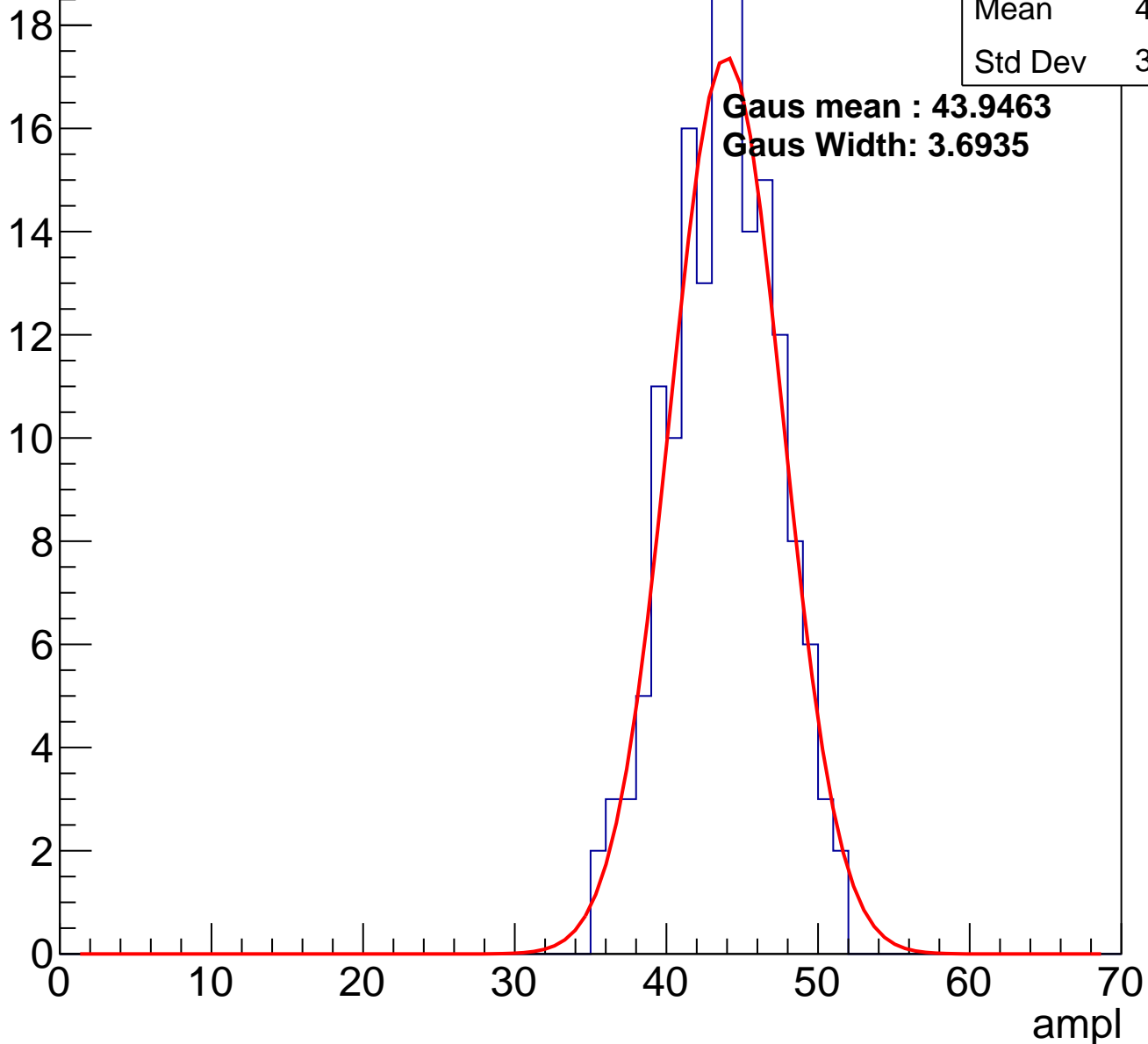
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	43.34
Std Dev	3.459

**Gaus mean : 43.9463**

**Gaus Width: 3.6935**

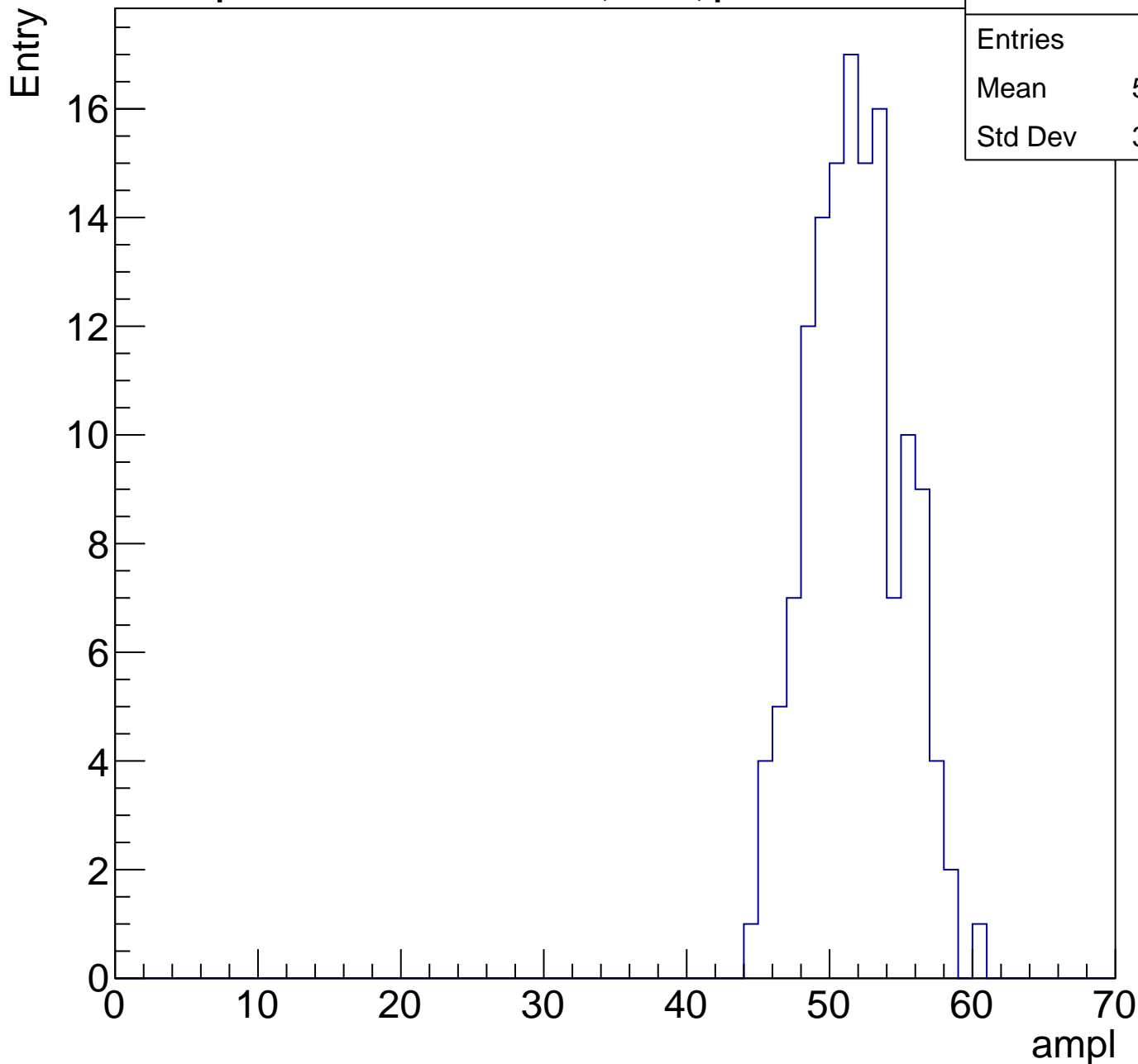
Entry



# B0L101S, U26-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	51.27
Std Dev	3.229



# B0L101S, U26-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

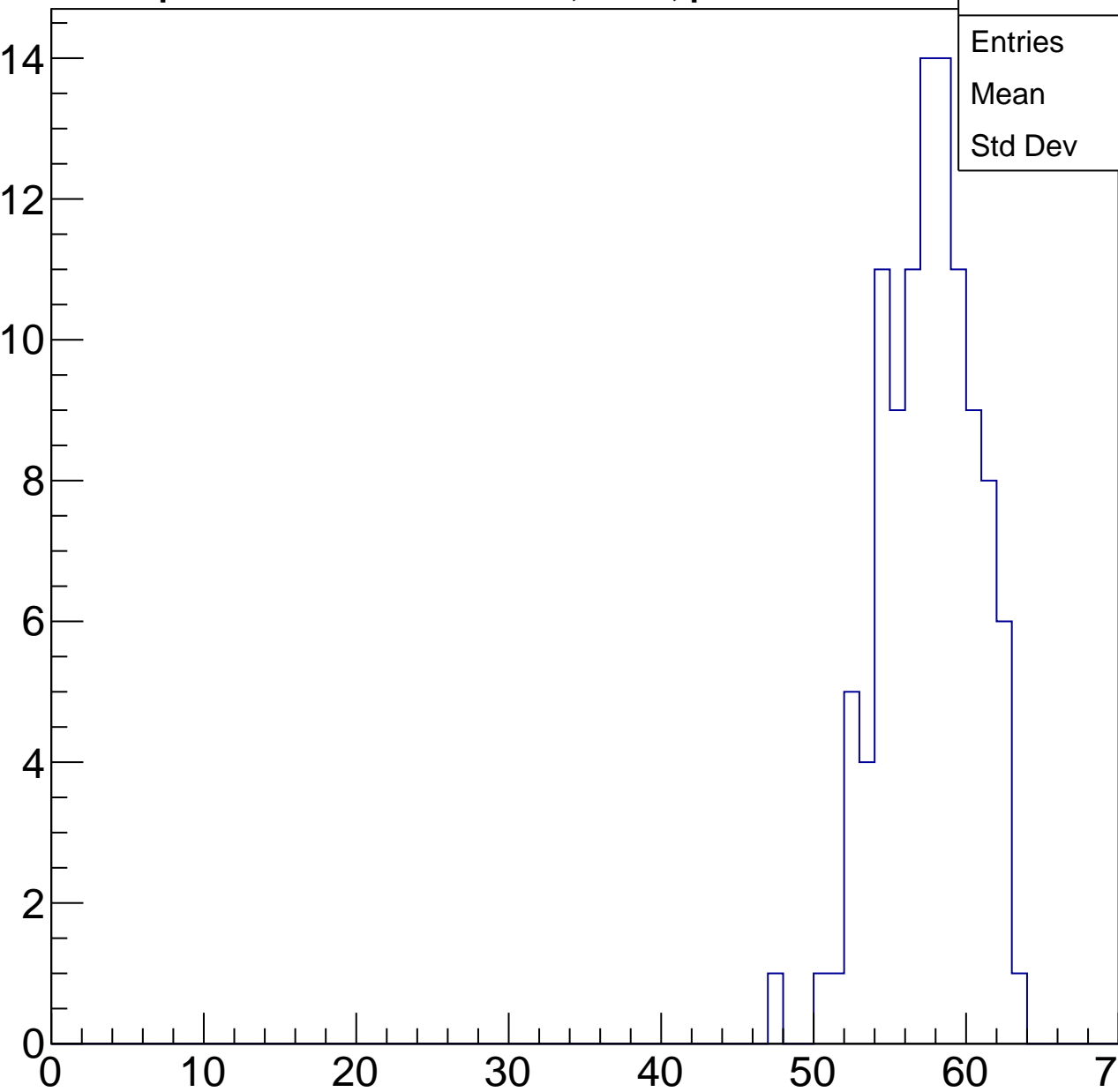
Entries	106
Mean	57.05
Std Dev	3.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	65
Mean	59.71
Std Dev	7.751

ampl

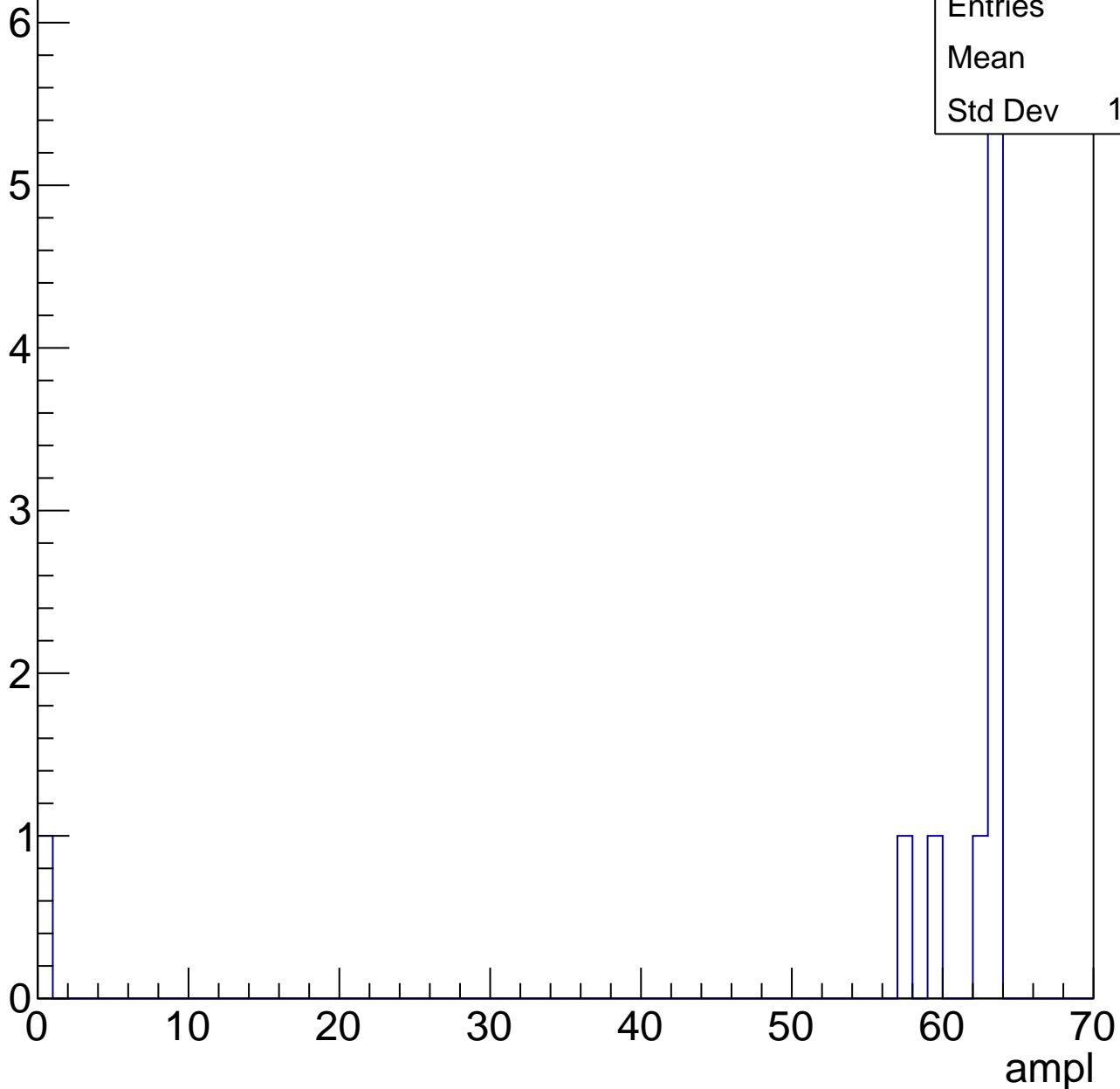
0 10 20 30 40 50 60 70

# B0L101S, U26-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	10
Mean	55.6
Std Dev	18.64





# B0L101S, U26-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

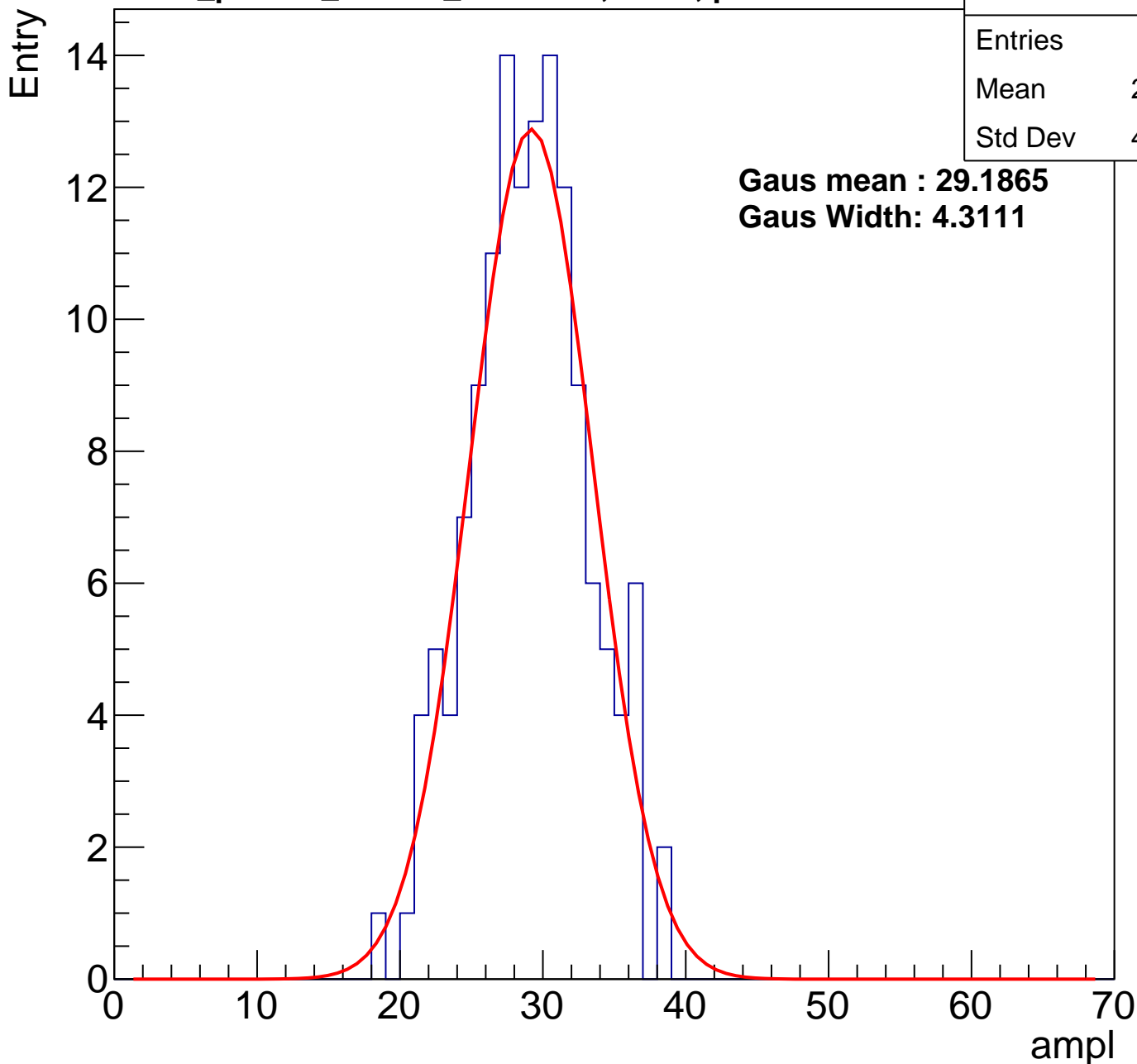
# B0L101S, U26-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	28.59
Std Dev	4.044

**Gaus mean : 29.1865**

**Gaus Width: 4.3111**



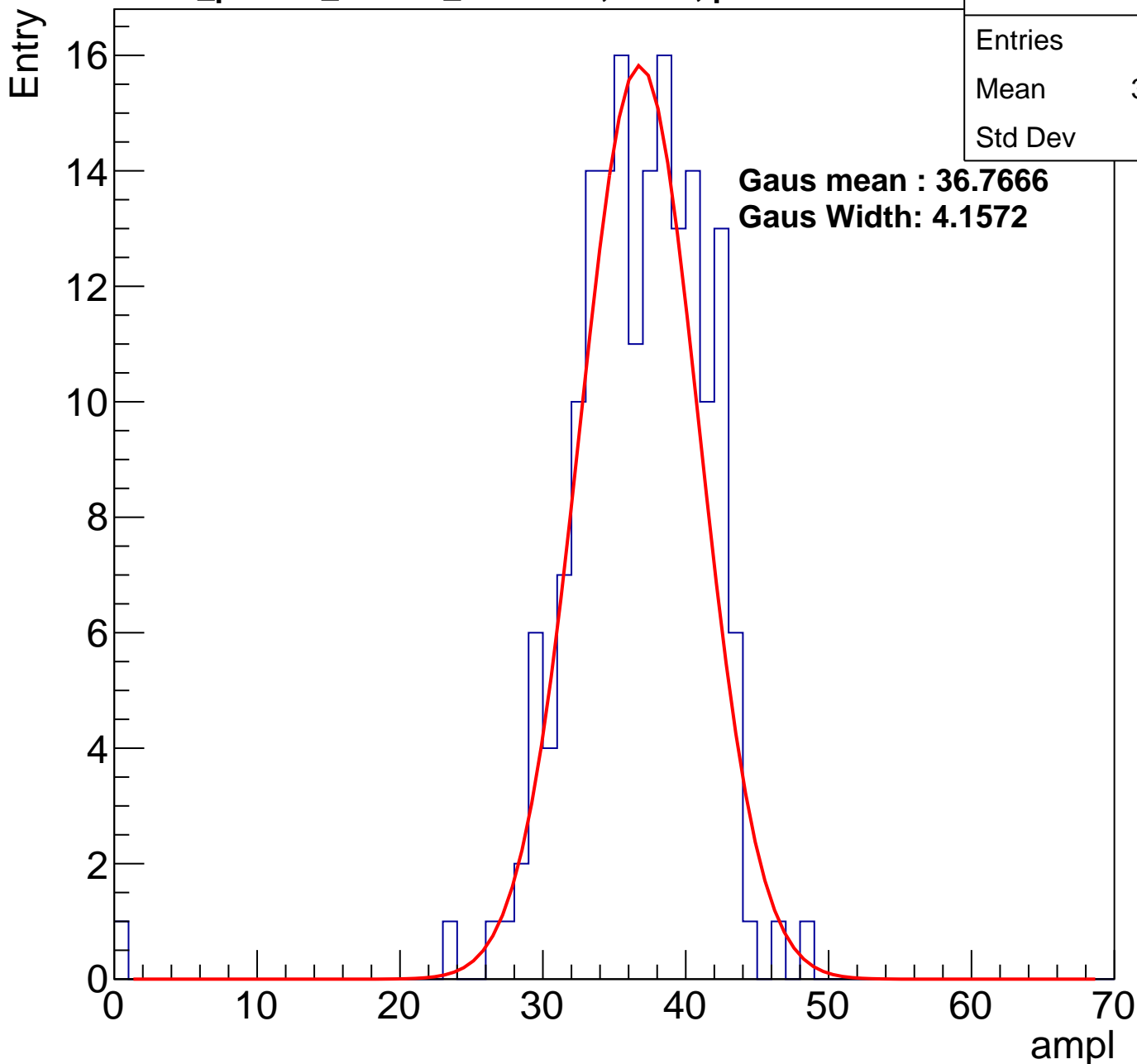
# B0L101S, U26-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	177
Mean	36.17
Std Dev	5.03

**Gaus mean : 36.7666**

**Gaus Width: 4.1572**



# B0L101S, U26-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

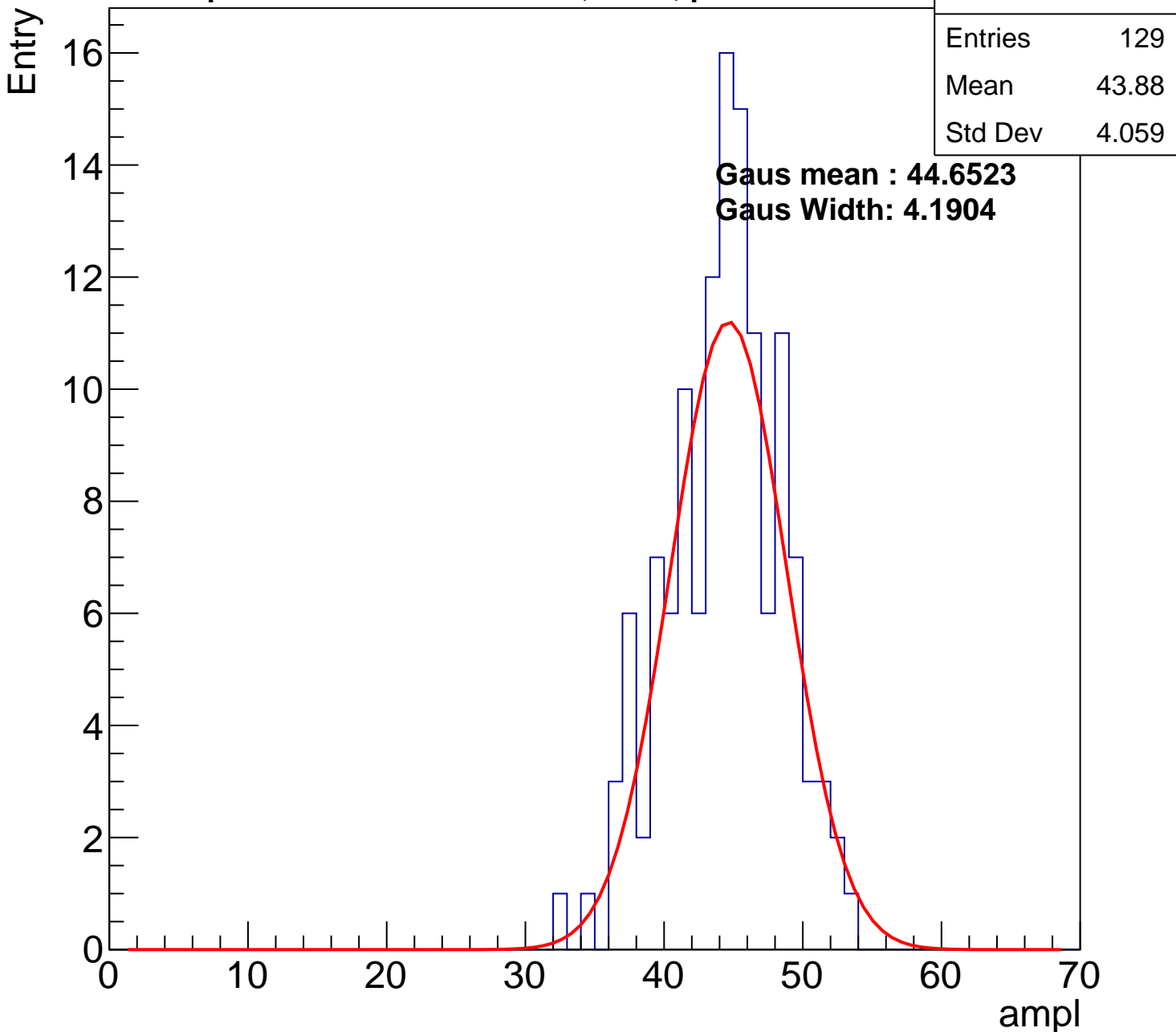
Entries	129
Mean	43.88
Std Dev	4.059

**Gaus mean : 44.6523**

**Gaus Width: 4.1904**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	50.07
Std Dev	3.412

Entry

12

10

8

6

4

2

0

0

10

20

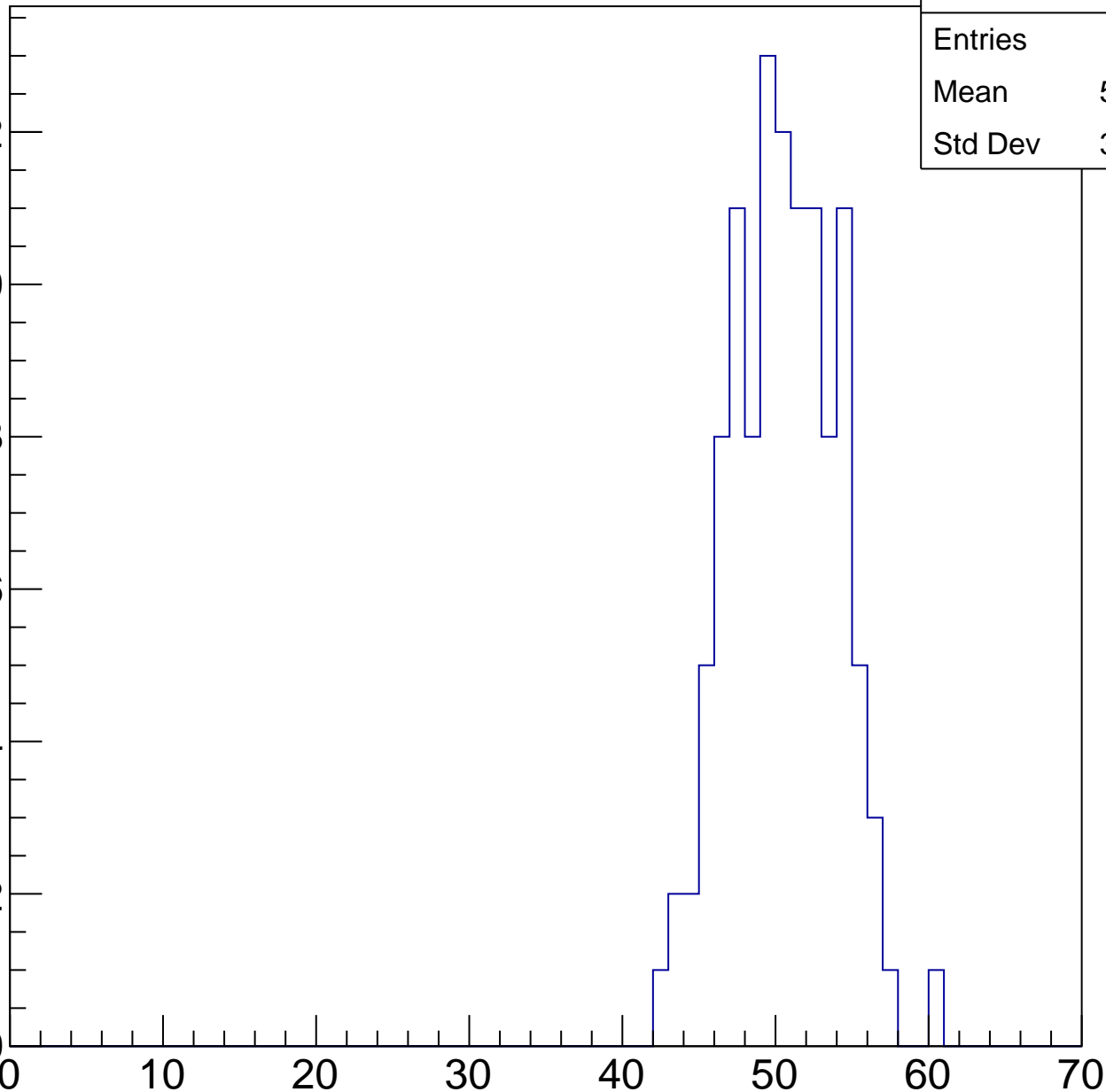
30

40

50

60

ampl



# B0L101S, U26-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

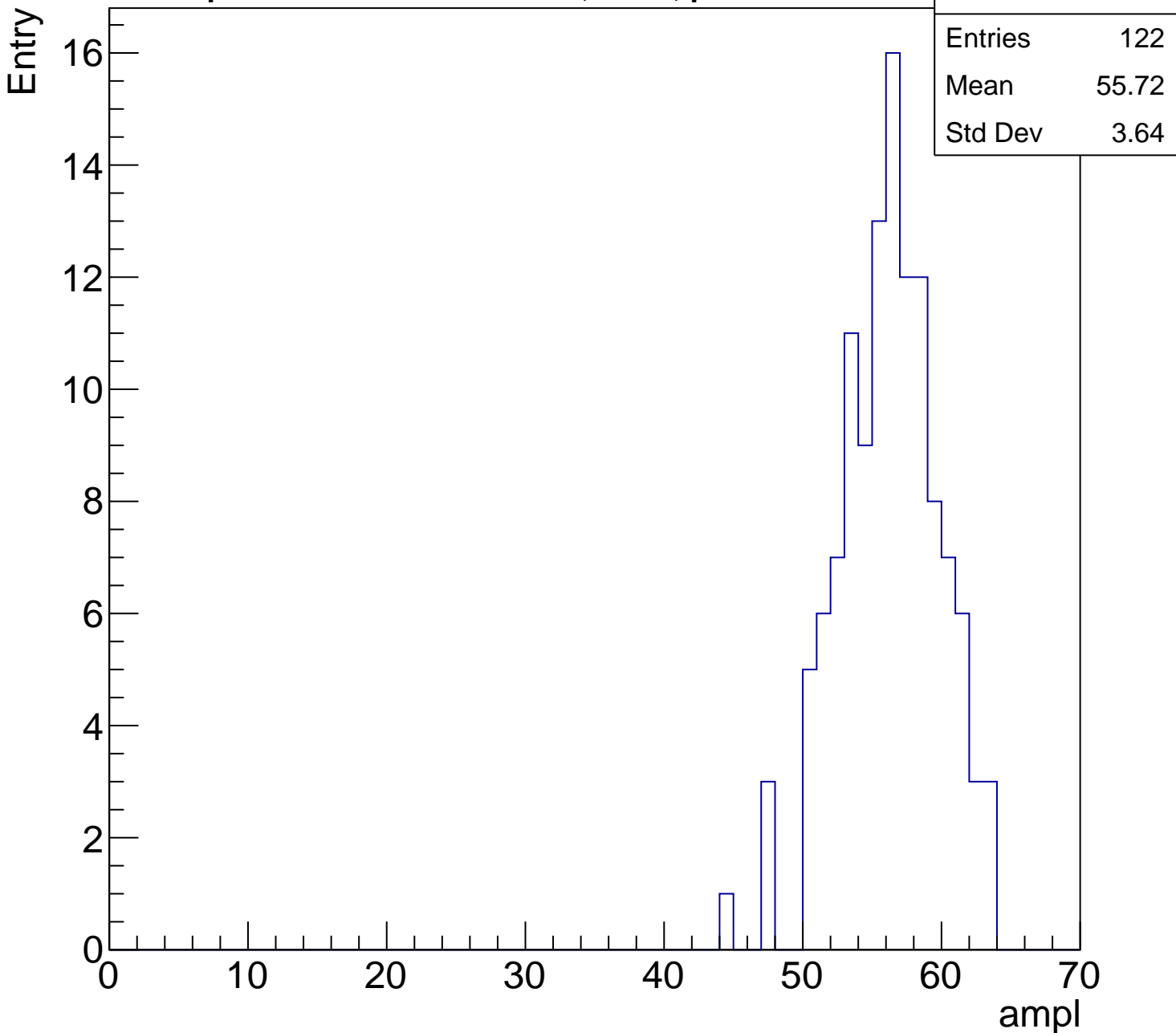
Entries	122
Mean	55.72
Std Dev	3.64

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch68, adc5

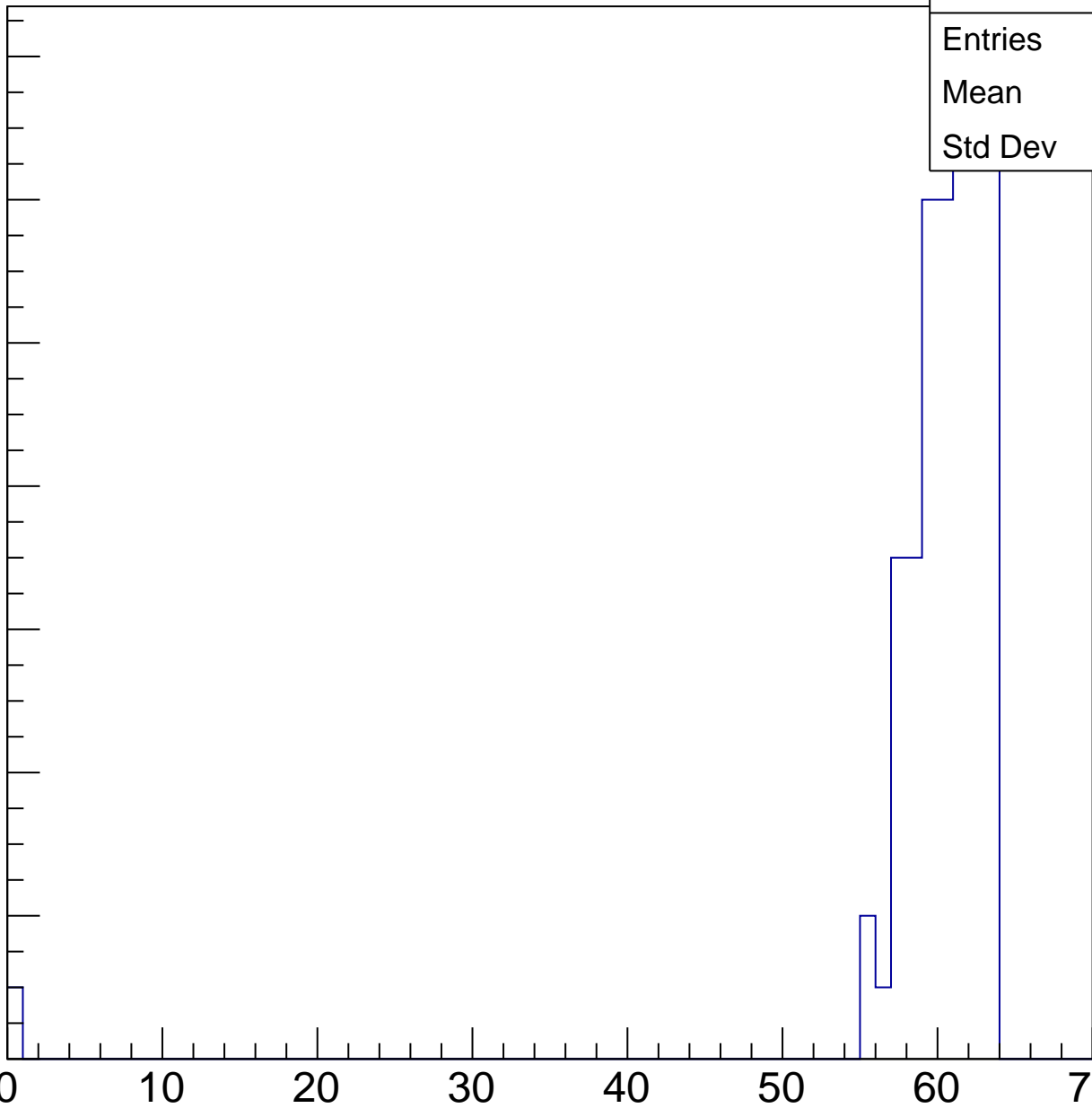
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	59.53
Std Dev	6.892

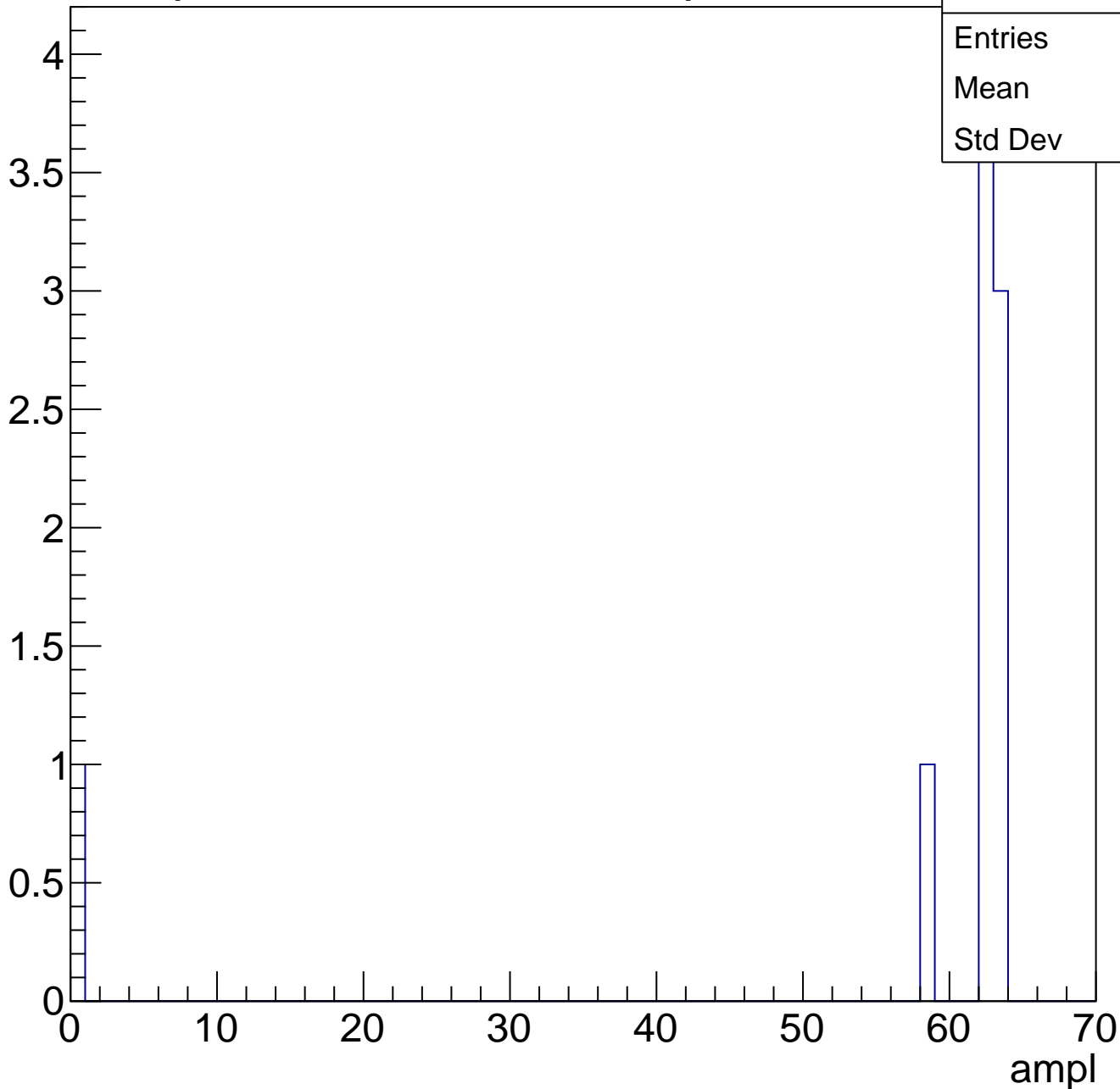
ampl



# B0L101S, U26-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9
Std Dev	9

# B0L101S, U26-ch69, adc0

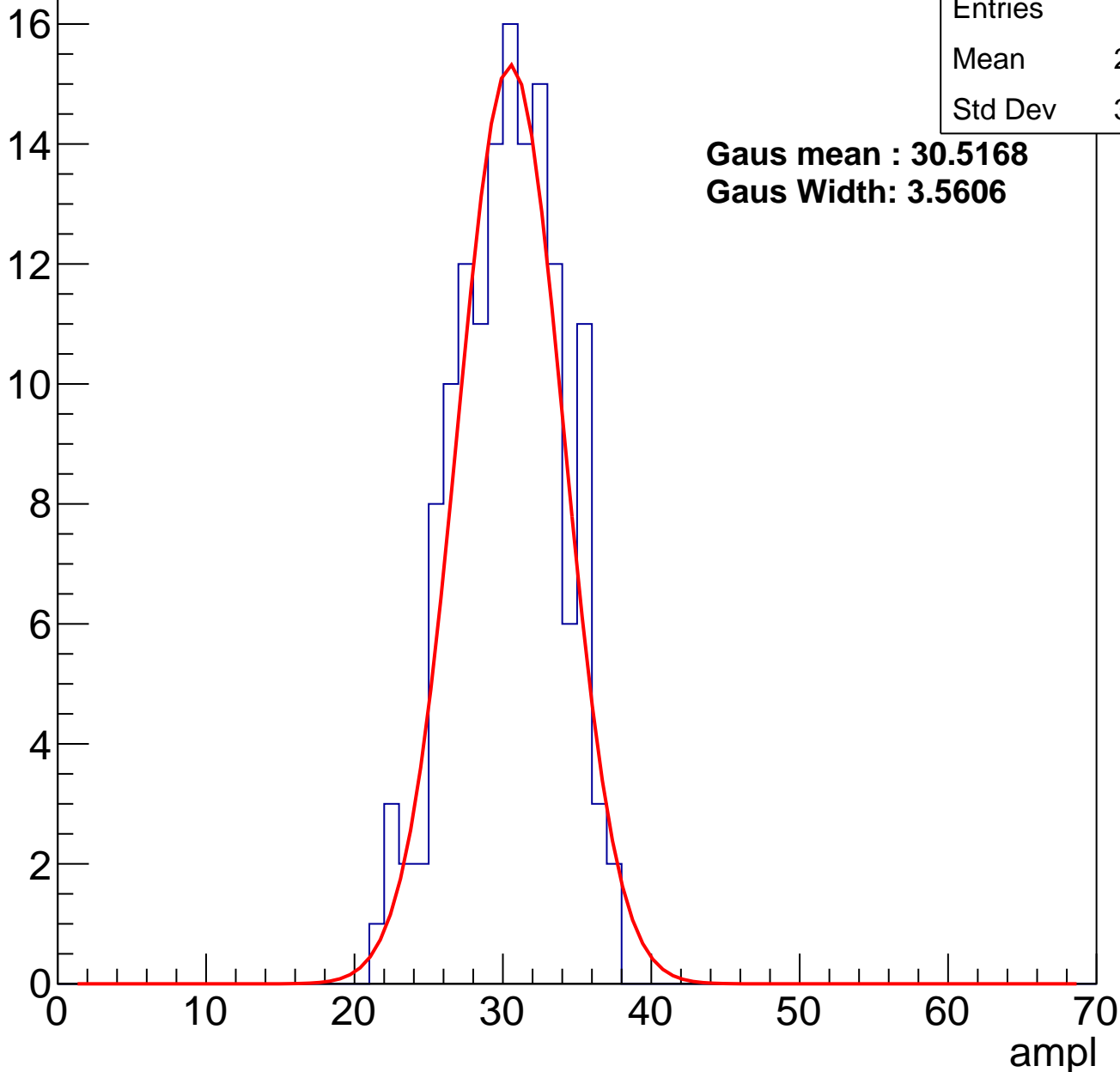
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	29.86
Std Dev	3.486

**Gaus mean : 30.5168**

**Gaus Width: 3.5606**

Entry



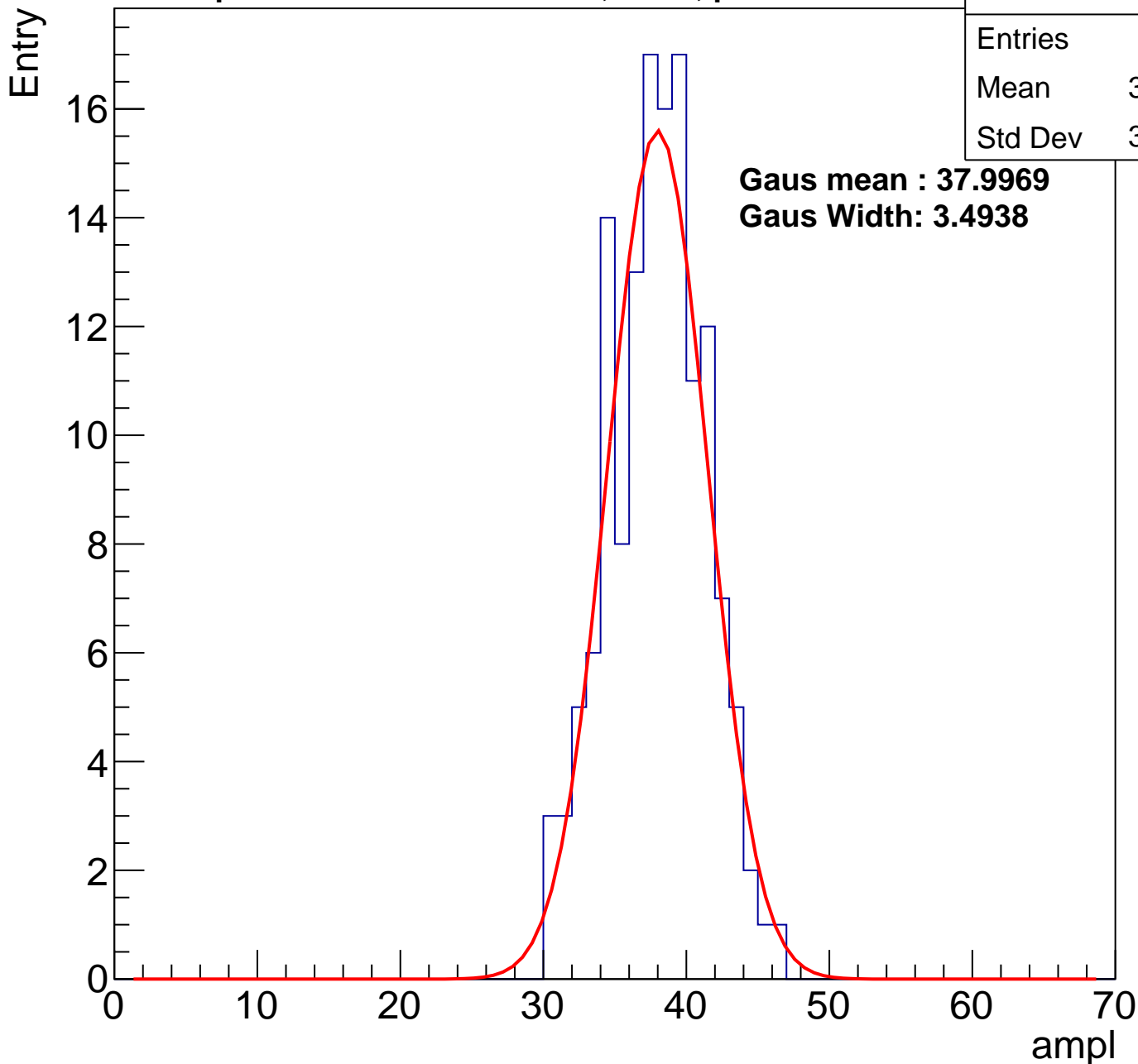
# B0L101S, U26-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	37.48
Std Dev	3.355

**Gaus mean : 37.9969**

**Gaus Width: 3.4938**



# B0L101S, U26-ch69, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

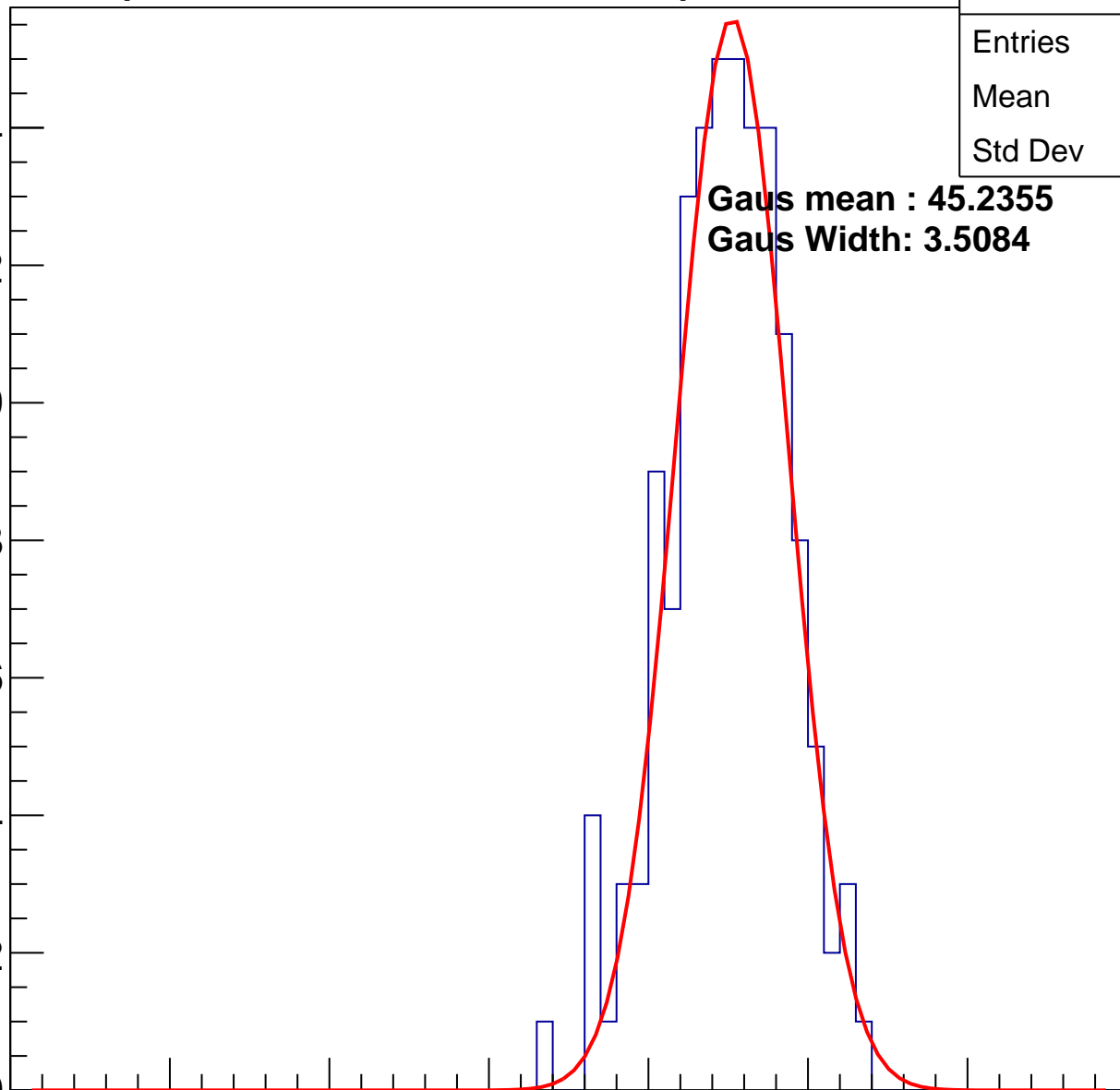
70

ampl

Entries	143
Mean	44.46
Std Dev	3.717

**Gaus mean : 45.2355**

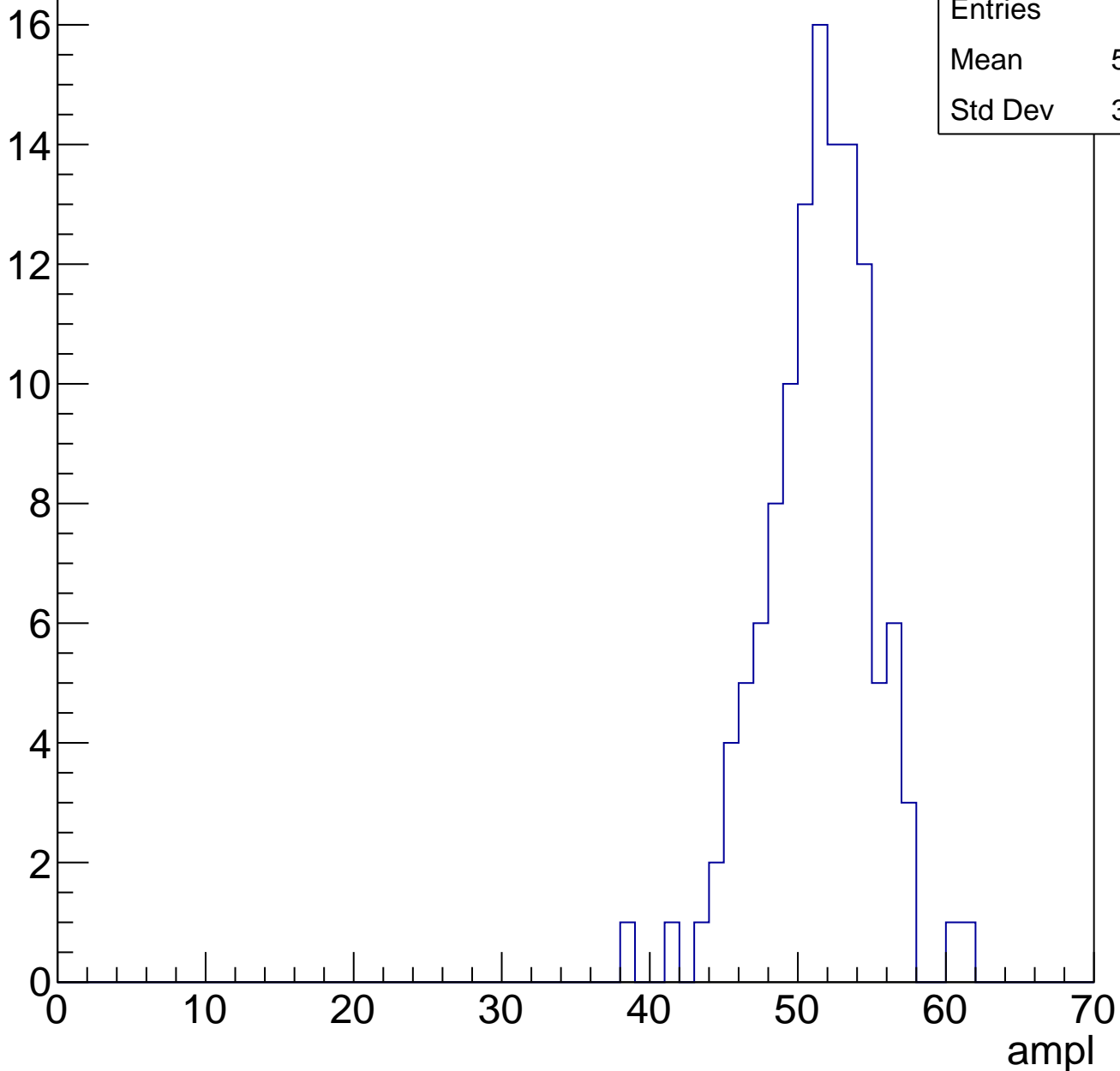
**Gaus Width: 3.5084**



# B0L101S, U26-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	123
Mean	50.92
Std Dev	3.626

# B0L101S, U26-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	104
Mean	56.39
Std Dev	3.248

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

40

50

60

70

ampl

# B0L101S, U26-ch69, adc5

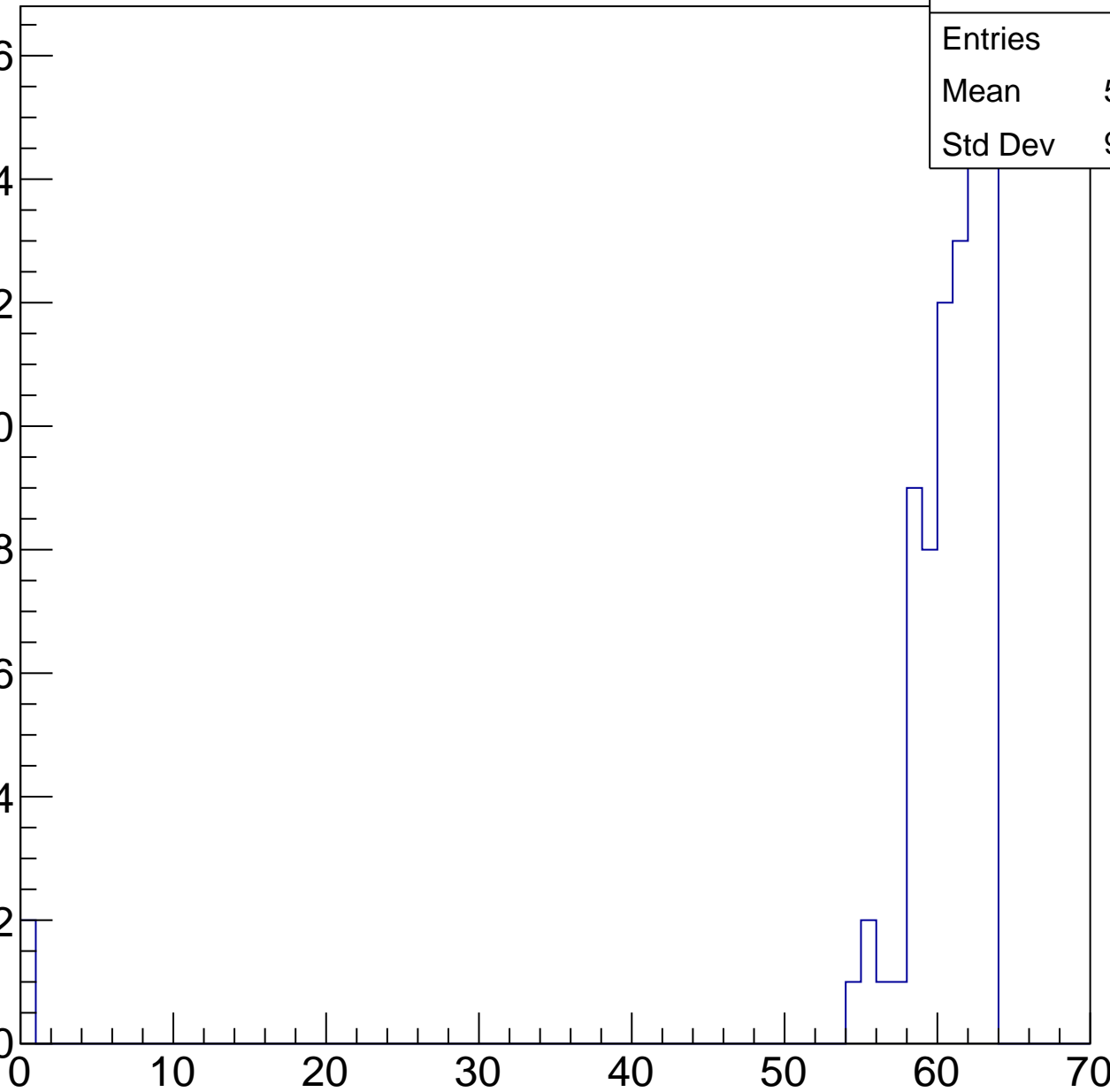
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	59.06
Std Dev	9.626

ampl



# B0L101S, U26-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

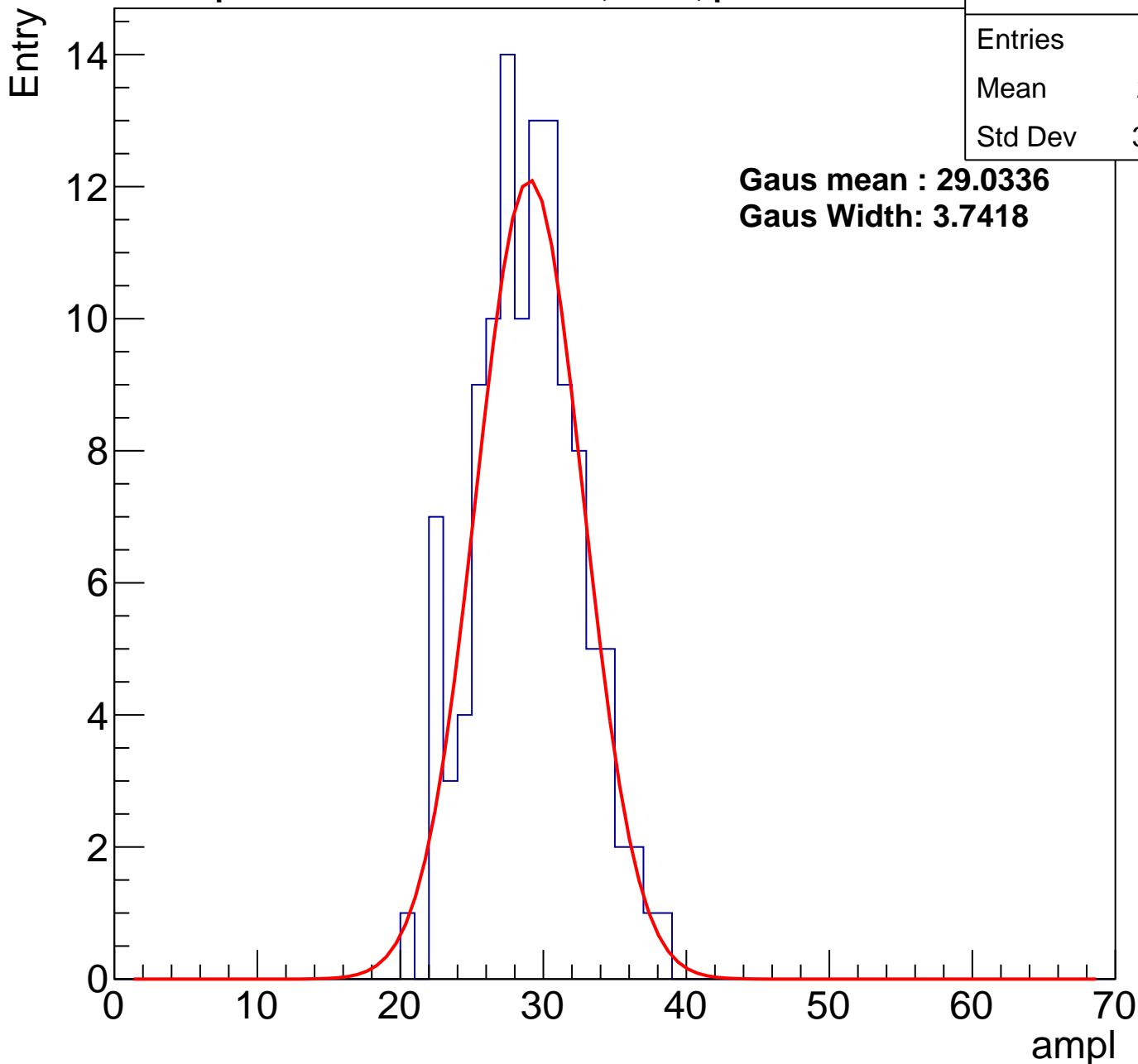
# B0L101S, U26-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	28.51
Std Dev	3.634

**Gaus mean : 29.0336**

**Gaus Width: 3.7418**



# B0L101S, U26-ch70, adc1

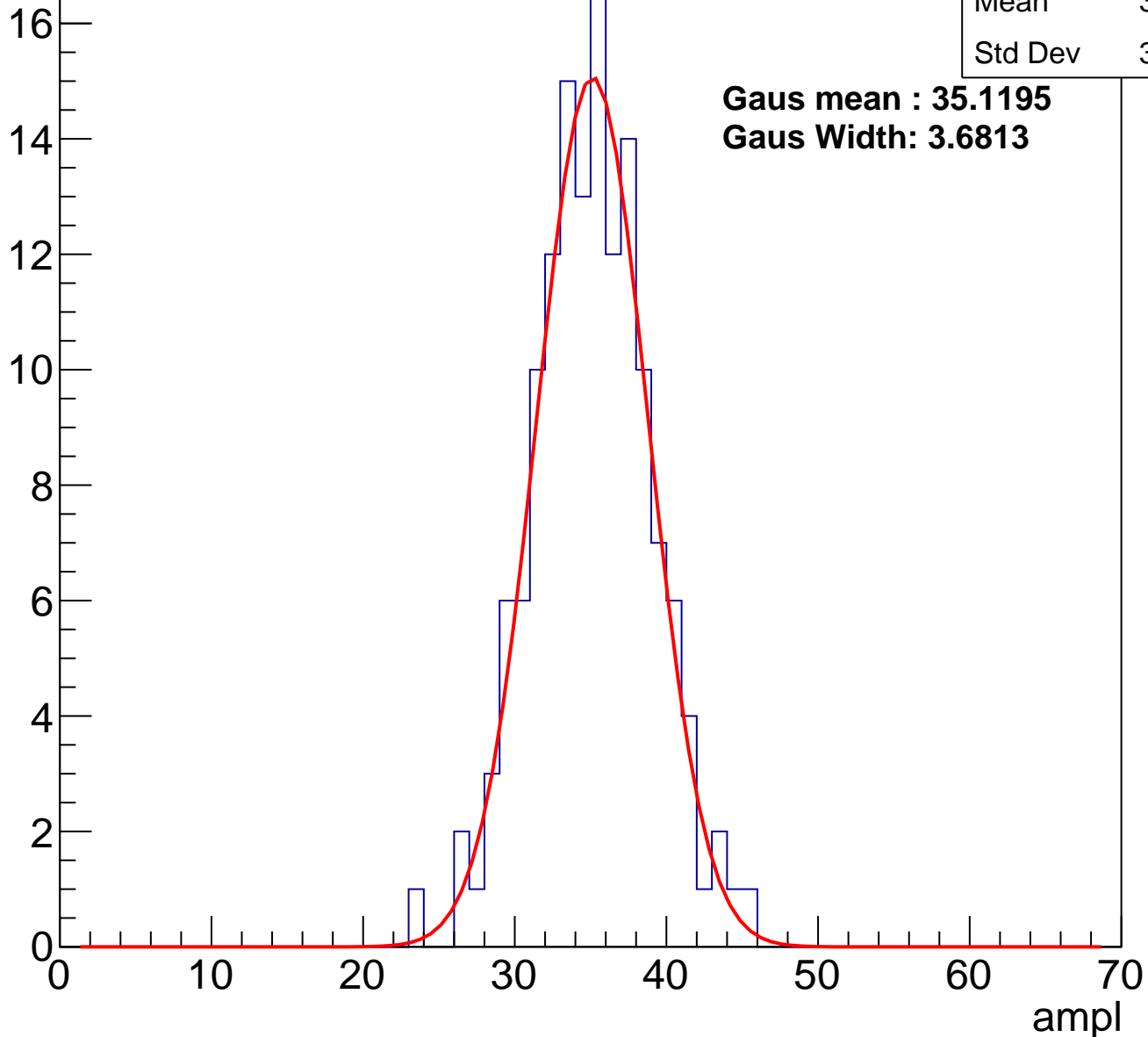
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	34.65
Std Dev	3.845

**Gaus mean : 35.1195**

**Gaus Width: 3.6813**

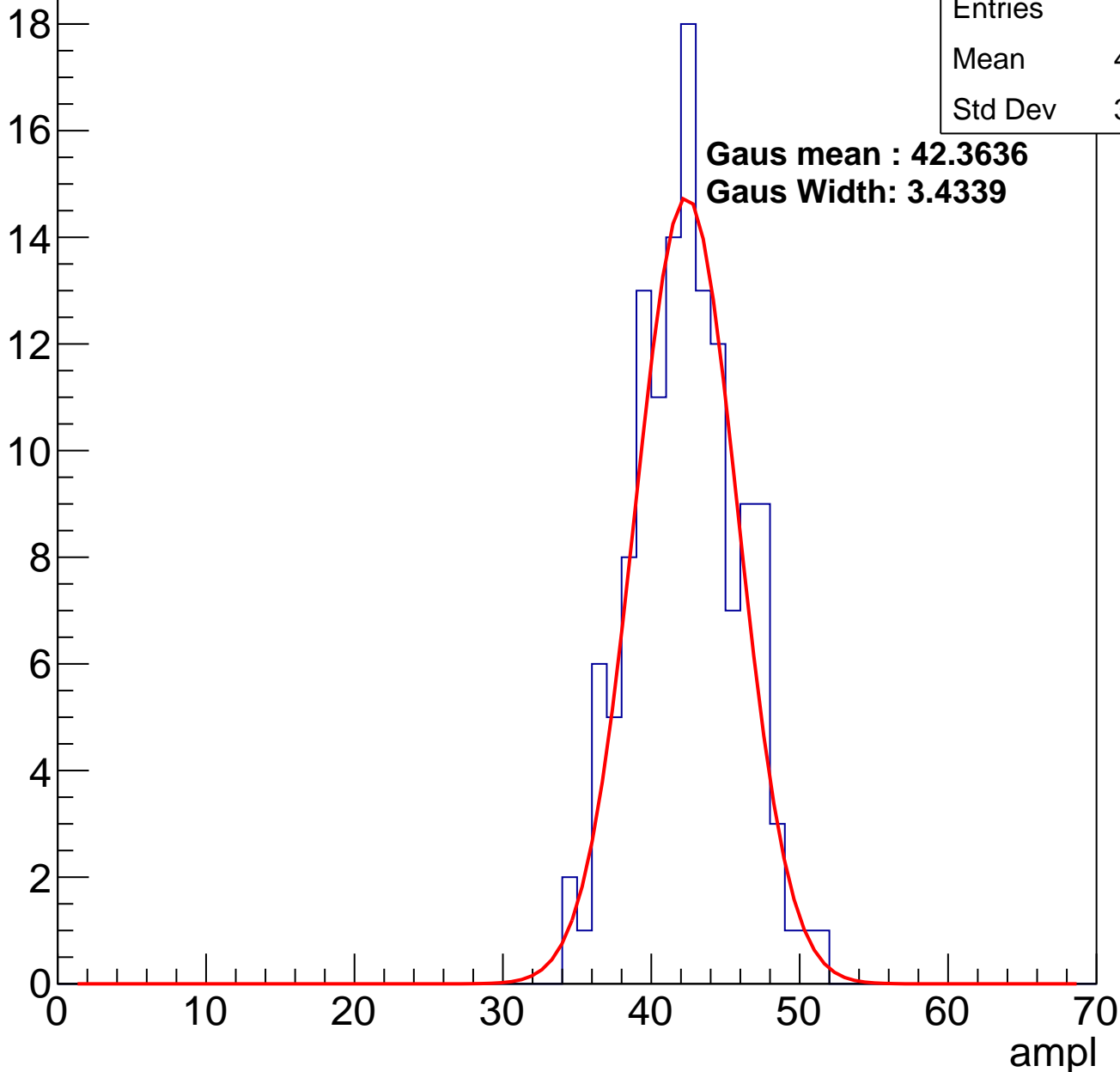
Entry



# B0L101S, U26-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

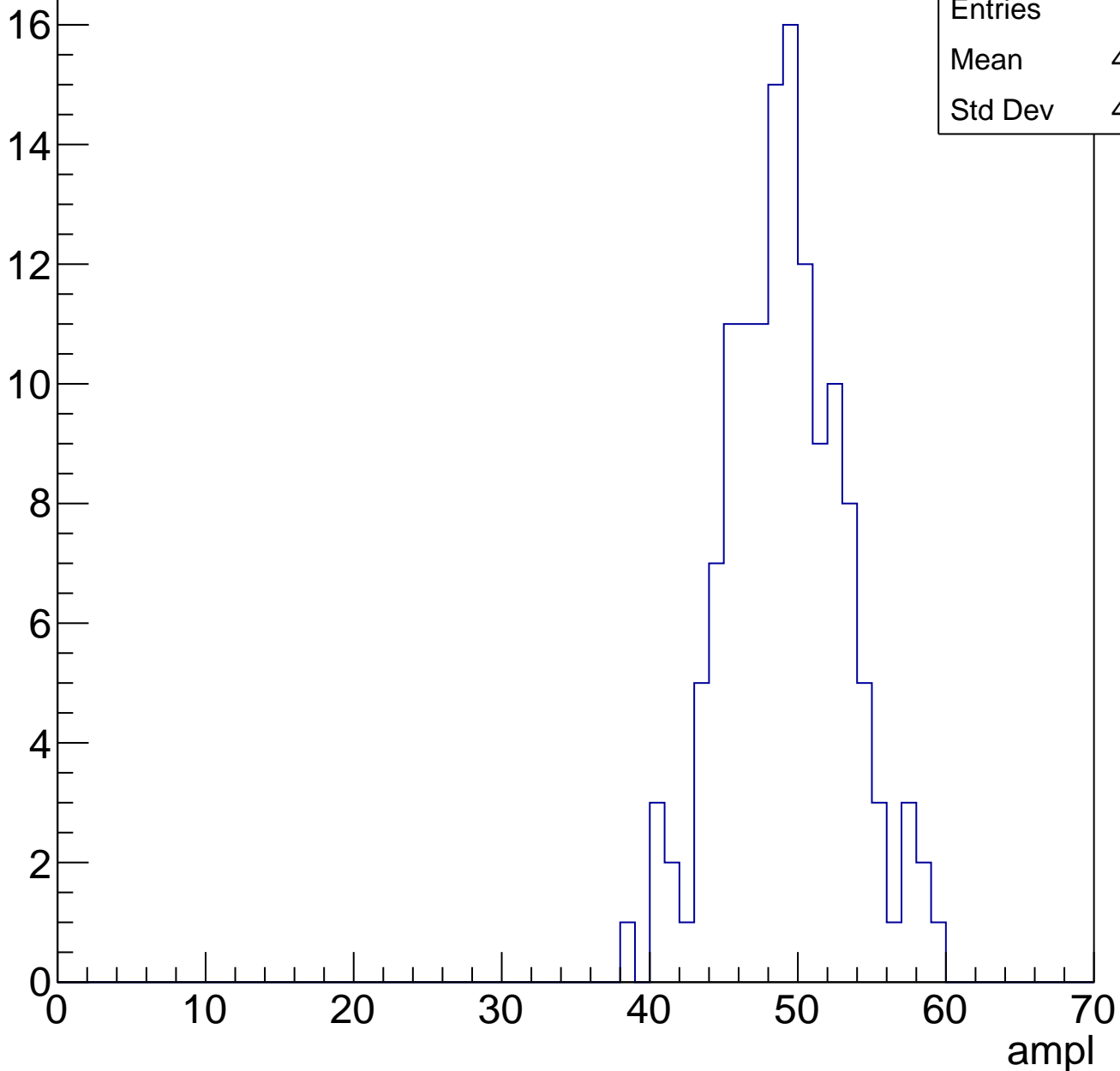
Entry



# B0L101S, U26-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

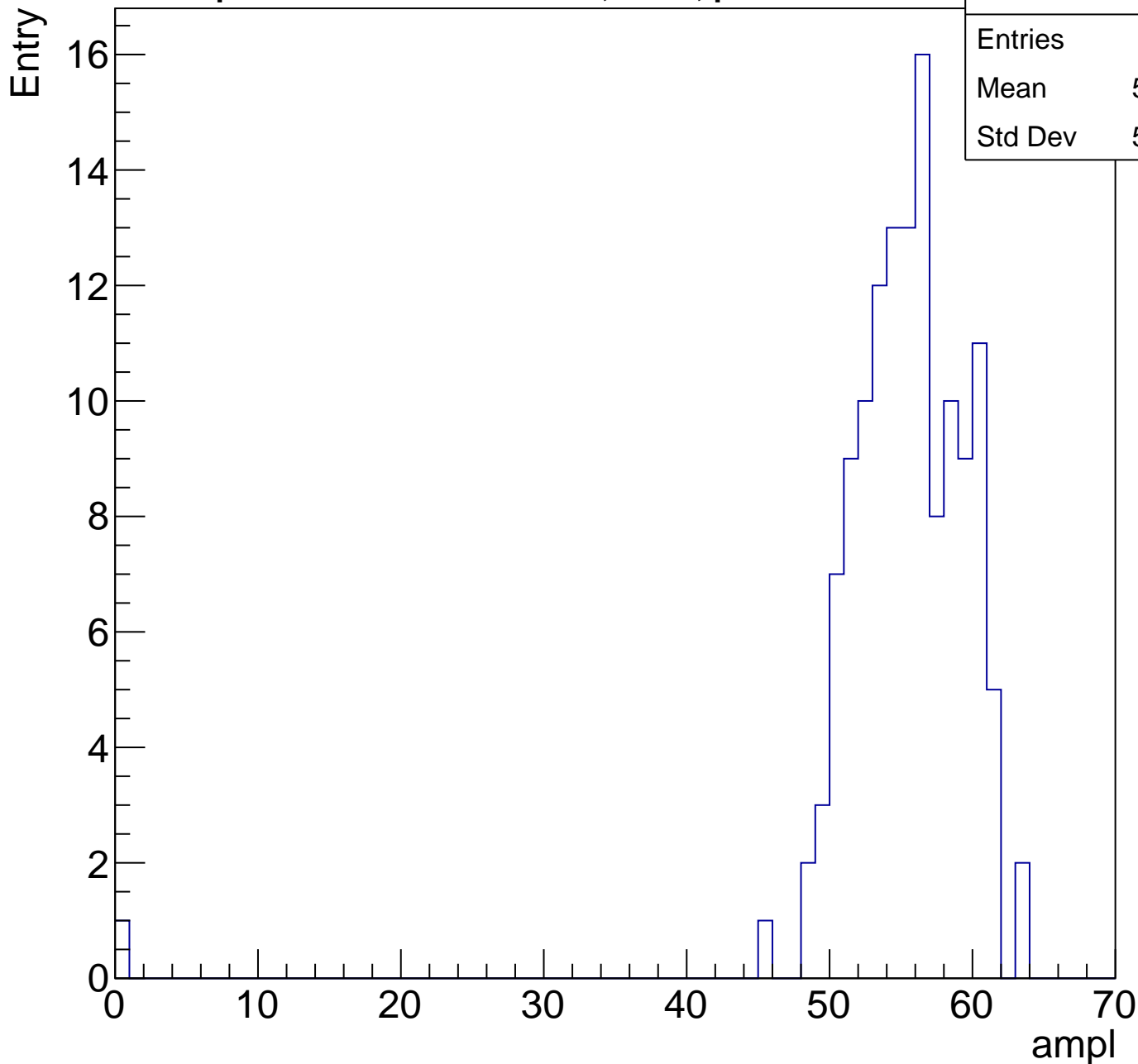
Entry



# B0L101S, U26-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	54.73
Std Dev	5.939



# B0L101S, U26-ch70, adc5

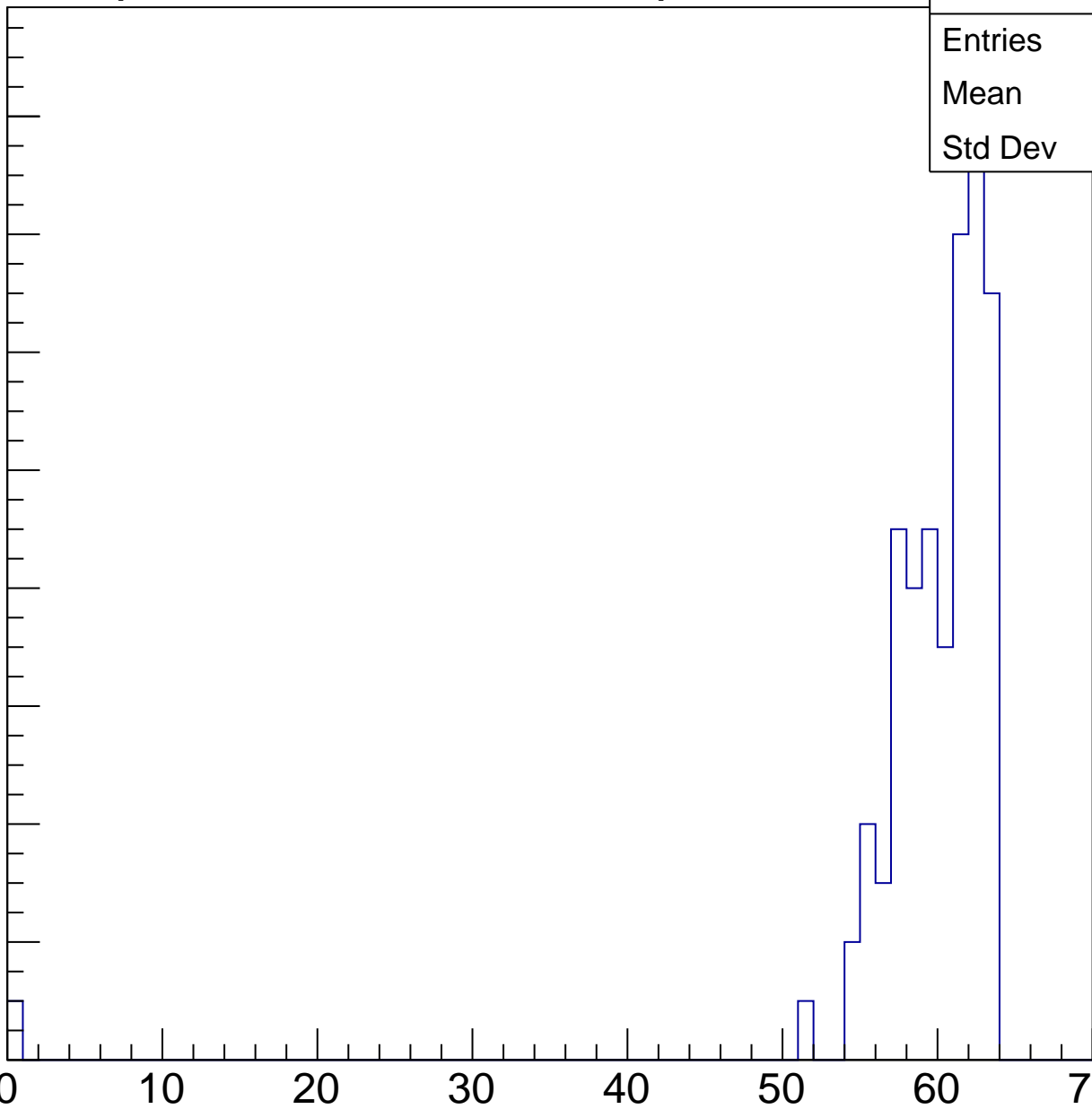
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	88
Mean	59.11
Std Dev	6.87

ampl

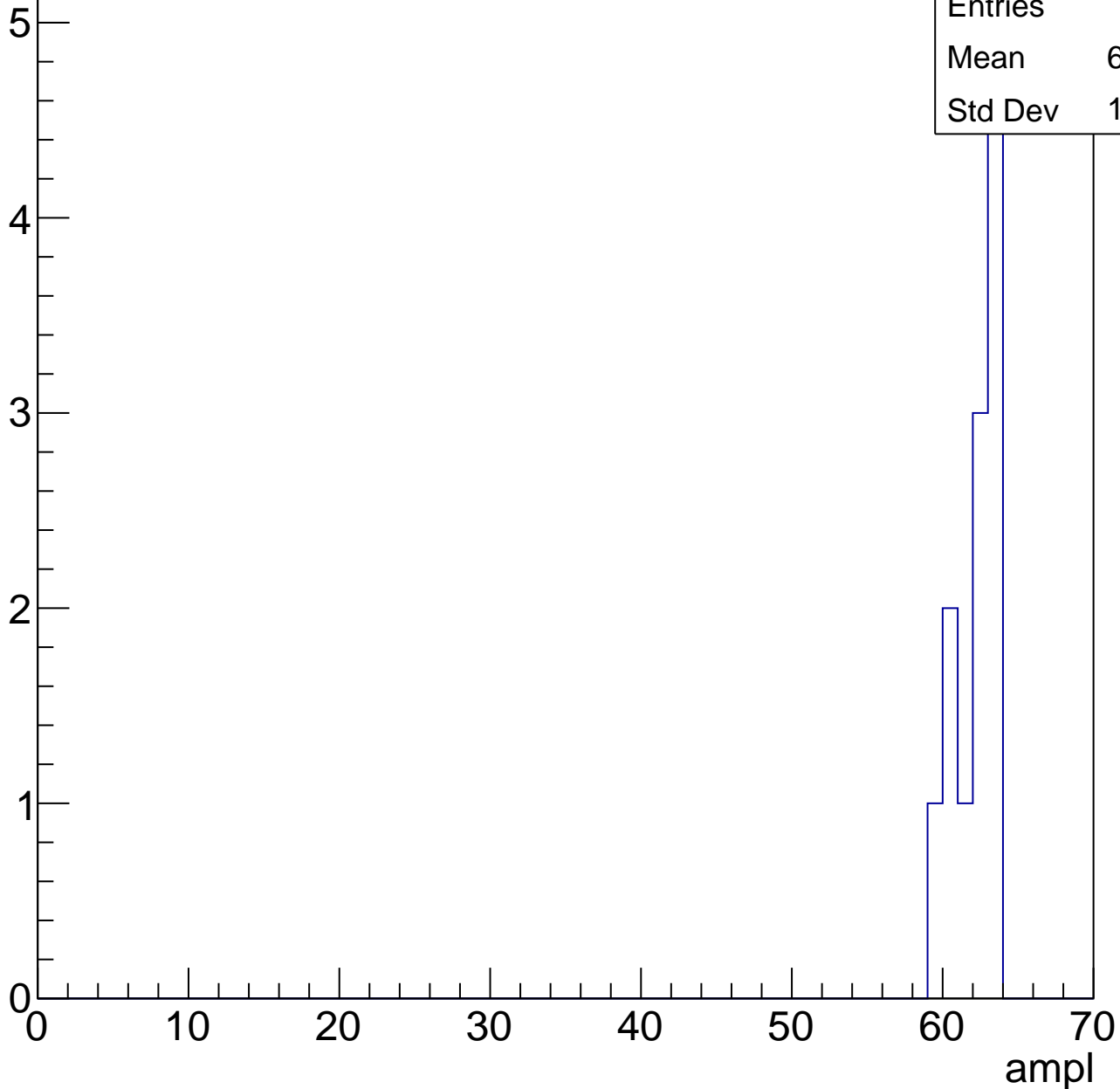


# B0L101S, U26-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.75
Std Dev	1.362





# B0L101S, U26-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch71, adc0

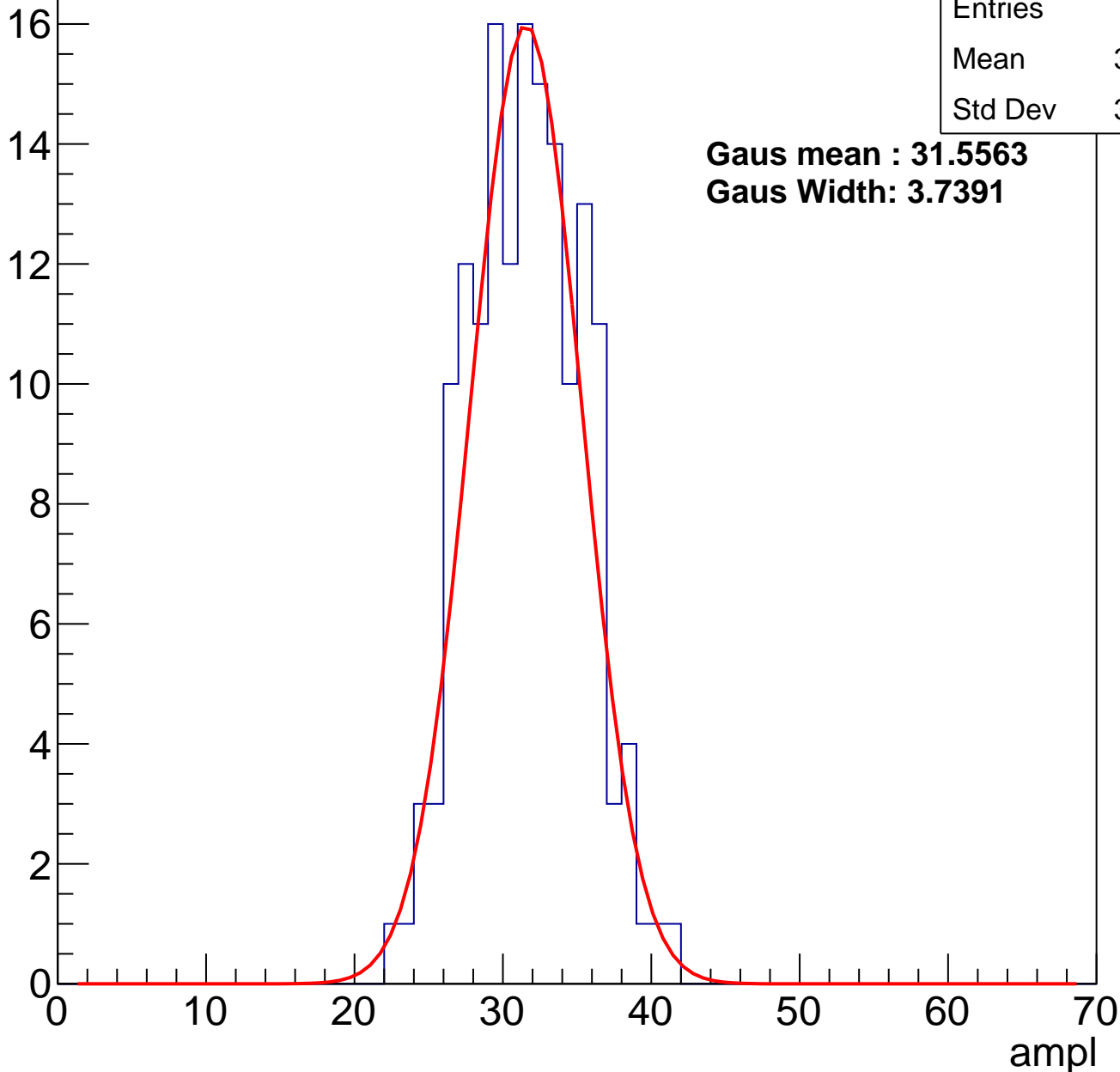
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	31.14
Std Dev	3.737

**Gaus mean : 31.5563**

**Gaus Width: 3.7391**

Entry



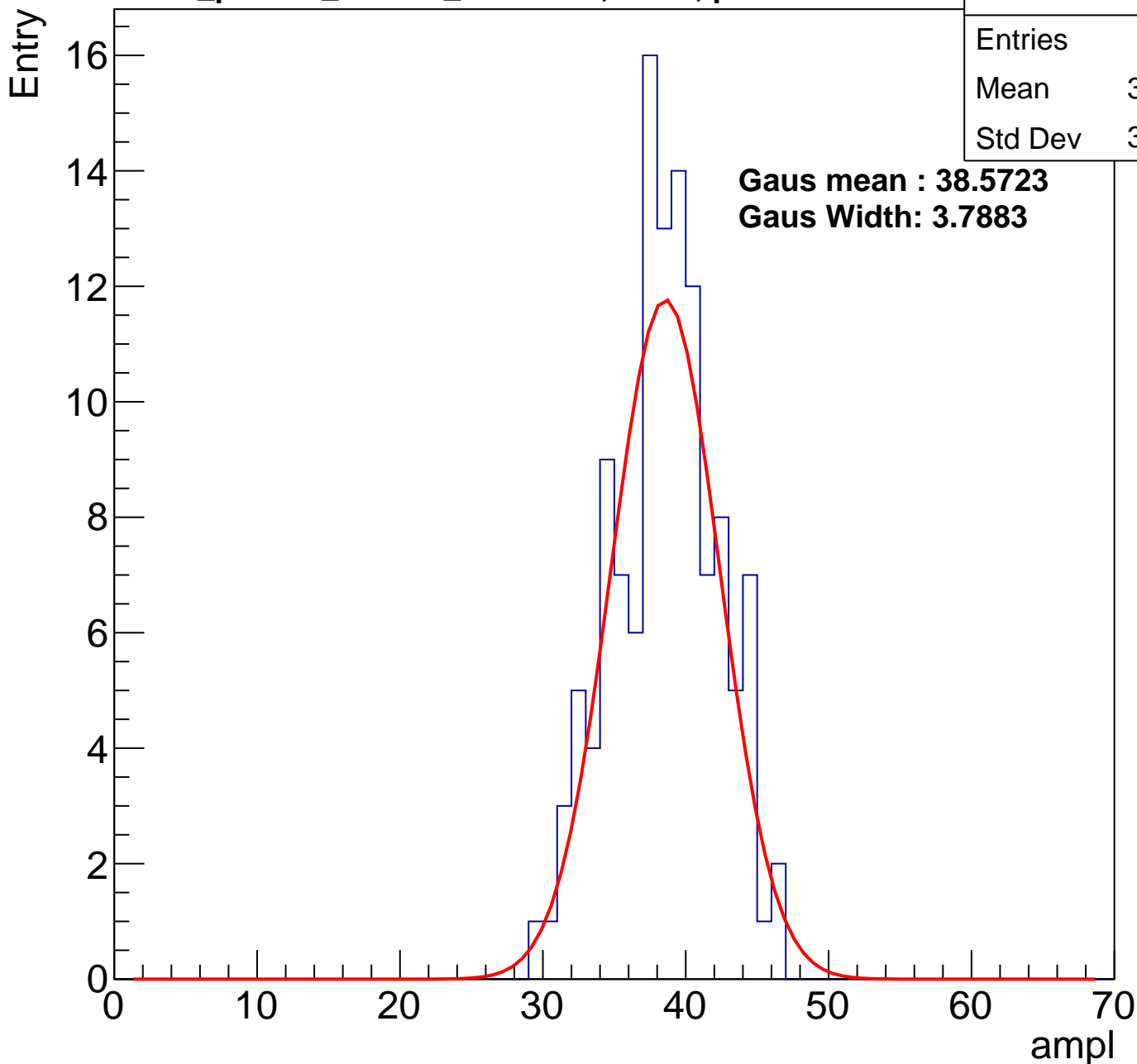
# B0L101S, U26-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	38.07
Std Dev	3.665

**Gaus mean : 38.5723**

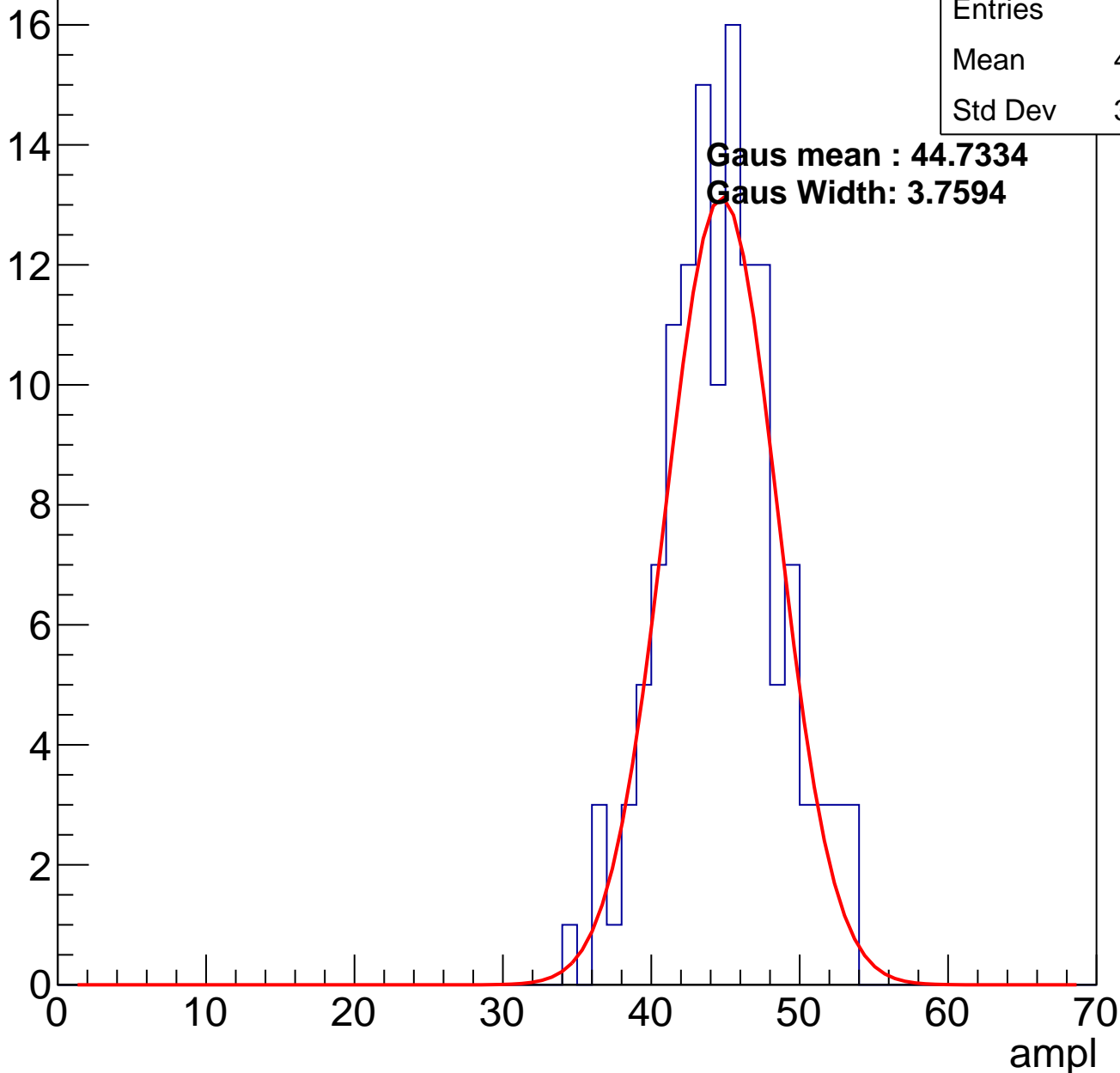
**Gaus Width: 3.7883**



# B0L101S, U26-ch71, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

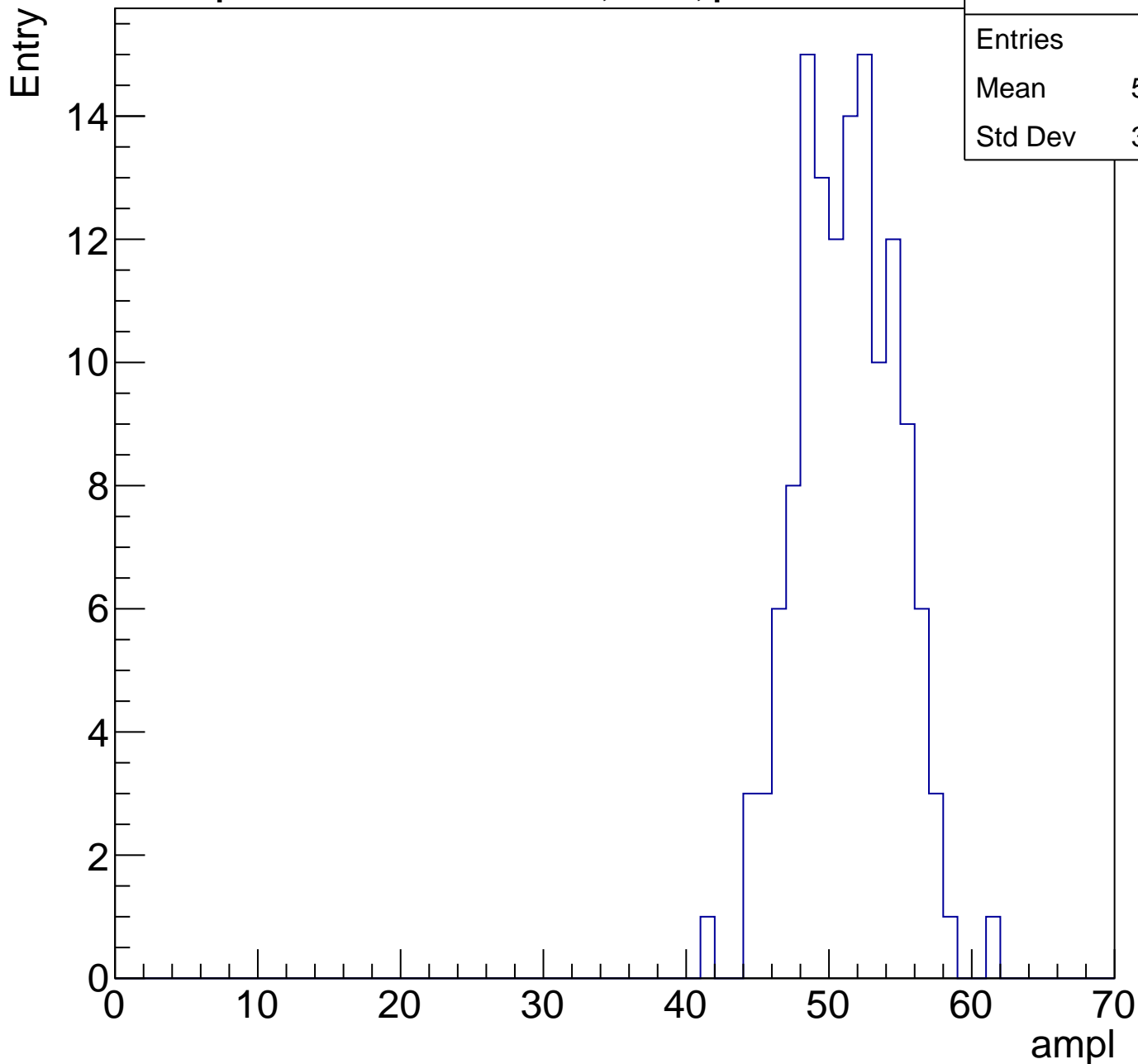
Entry



# B0L101S, U26-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

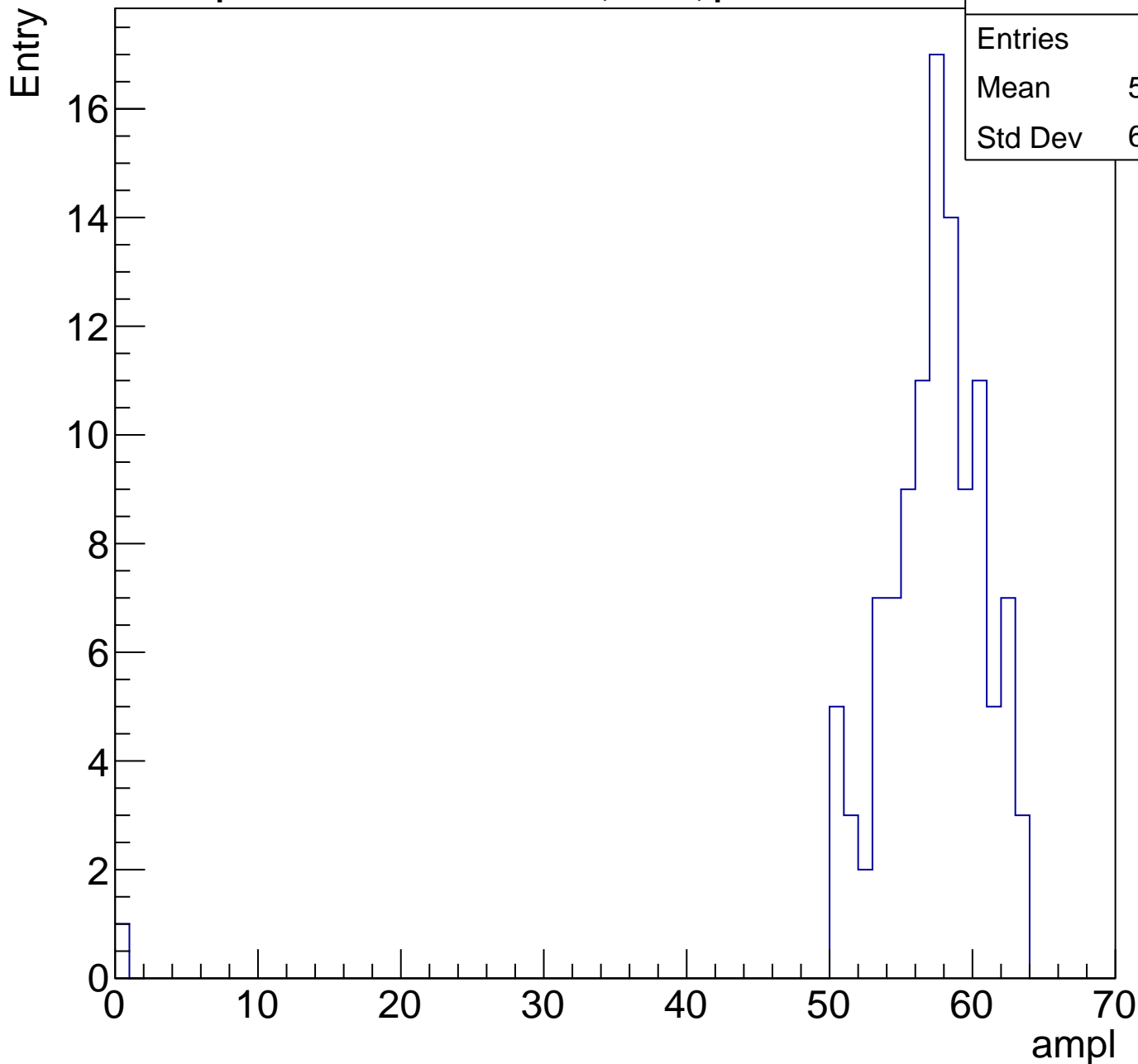
Entries	132
Mean	50.83
Std Dev	3.418



# B0L101S, U26-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	111
Mean	56.46
Std Dev	6.263



# B0L101S, U26-ch71, adc5

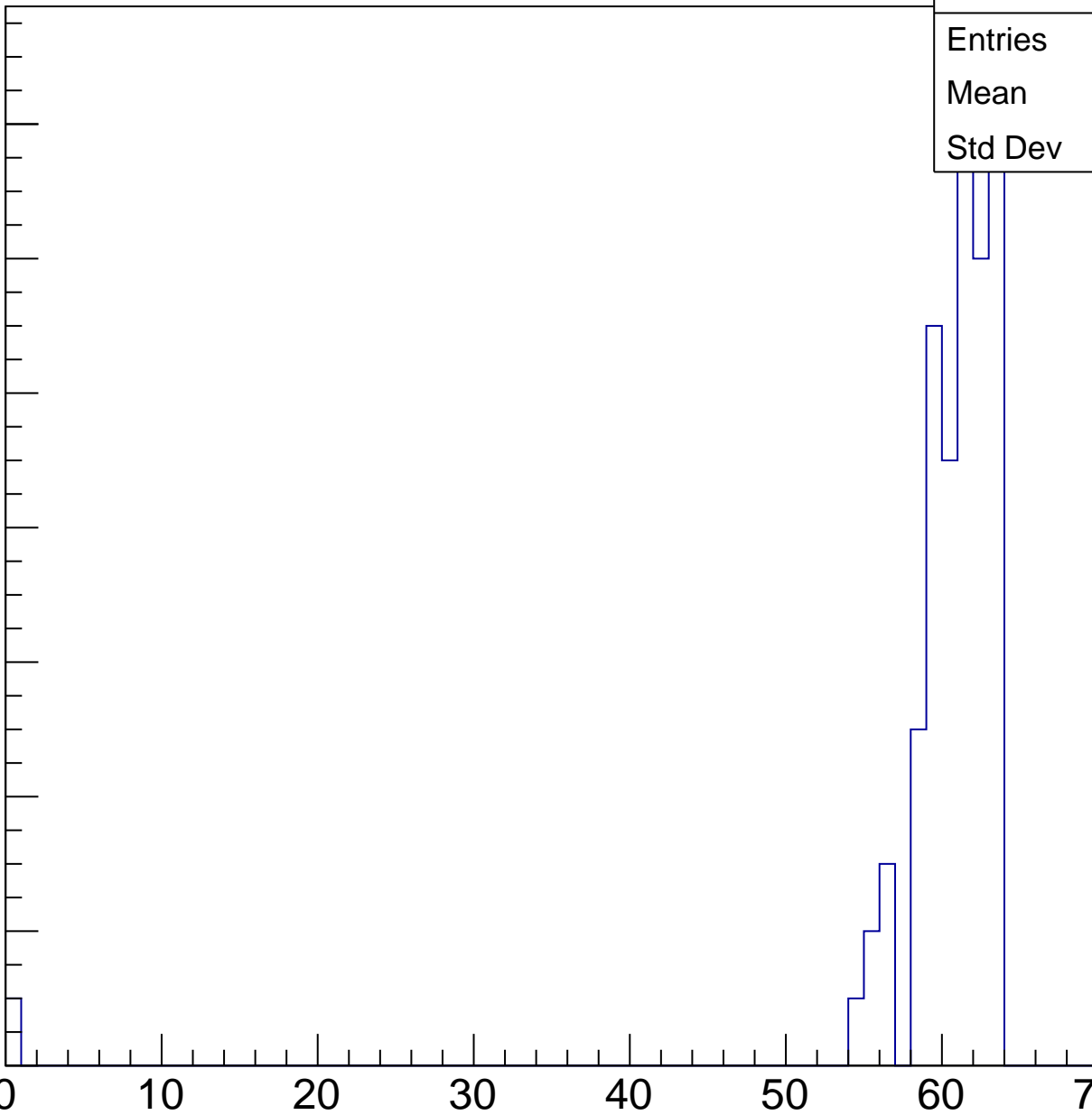
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	59.64
Std Dev	7.358

ampl



# B0L101S, U26-ch71, adc6

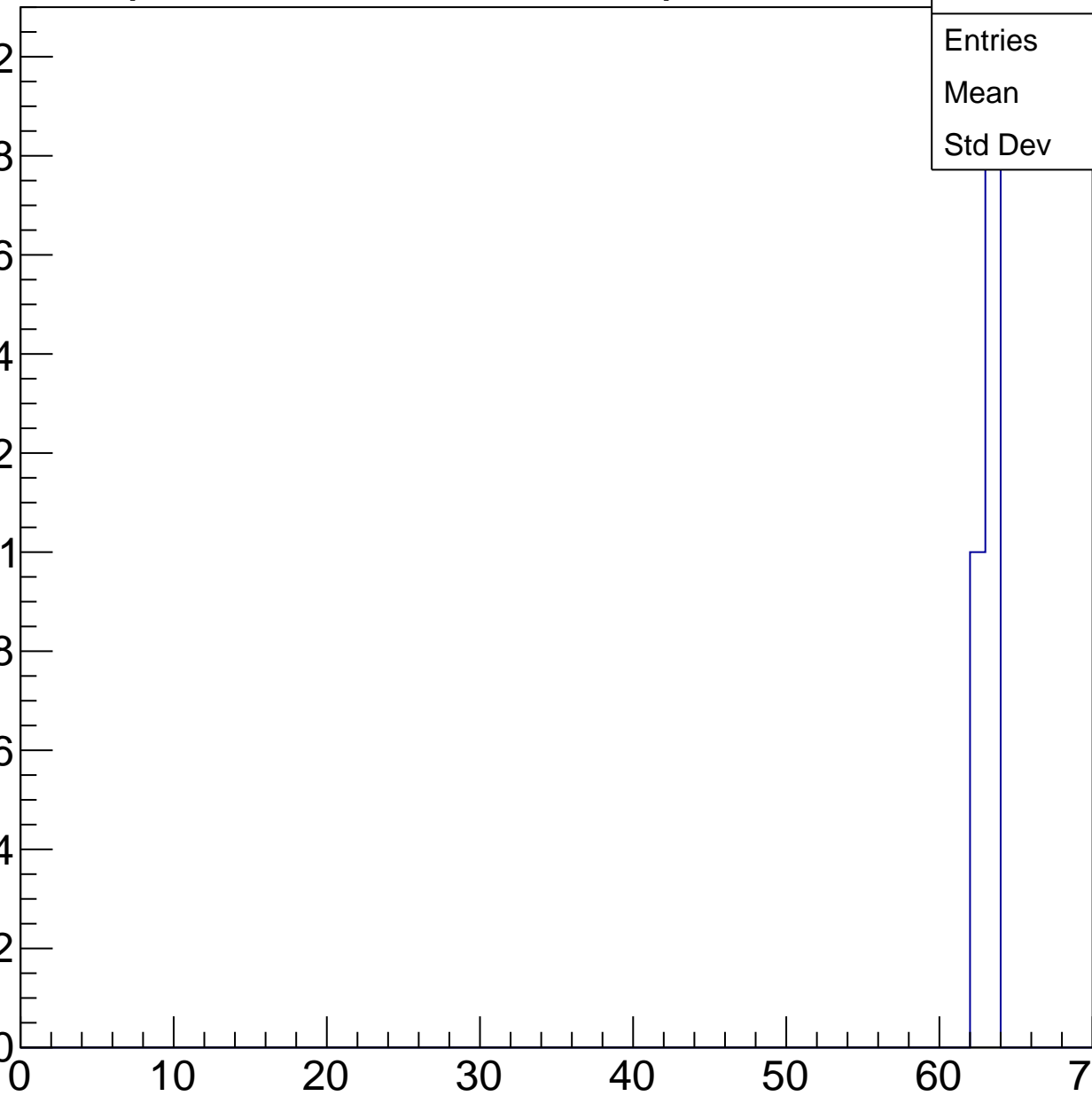
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl





# B0L101S, U26-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L101S, U26-ch72, adc0

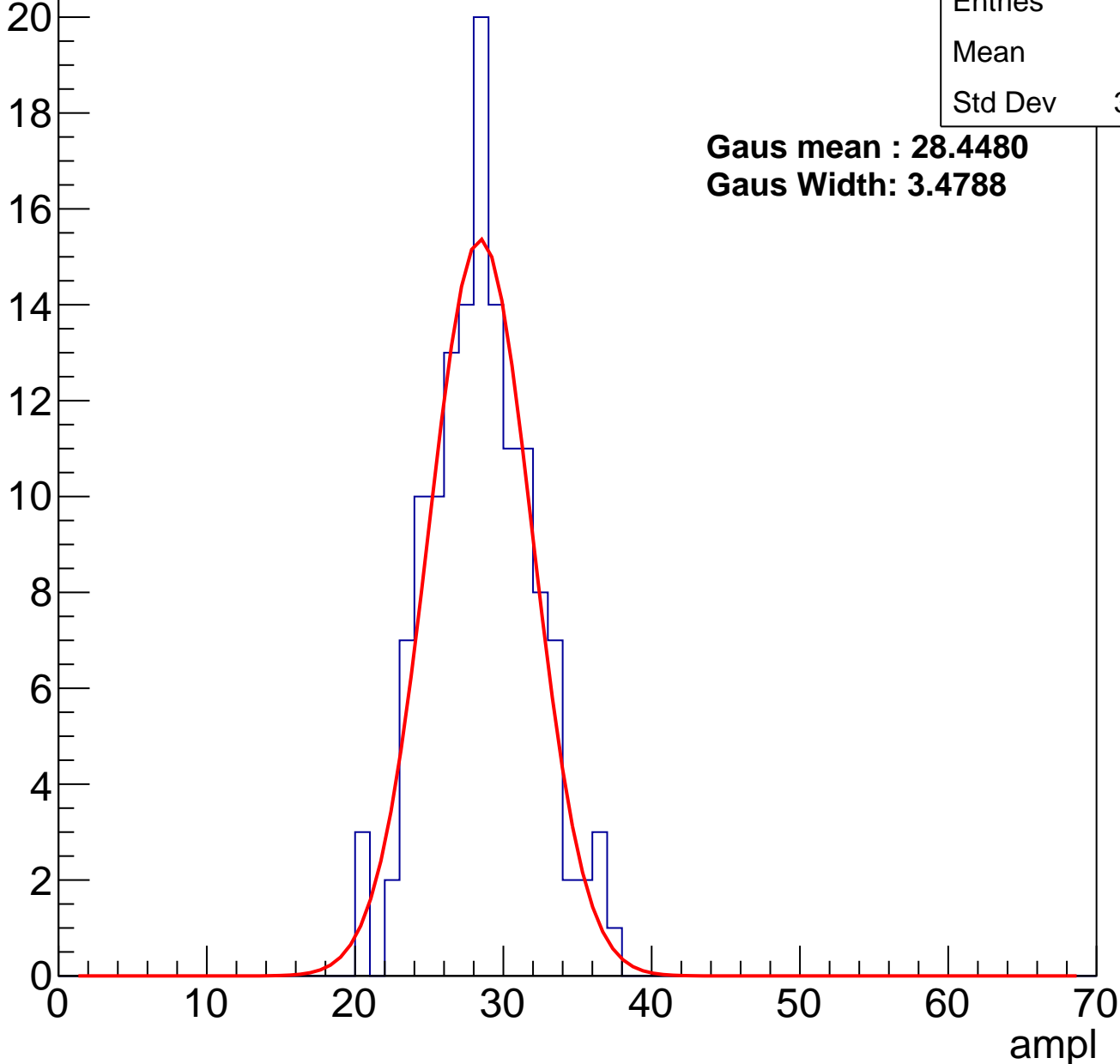
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	28.1
Std Dev	3.465

**Gaus mean : 28.4480**

**Gaus Width: 3.4788**

Entry



# B0L101S, U26-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

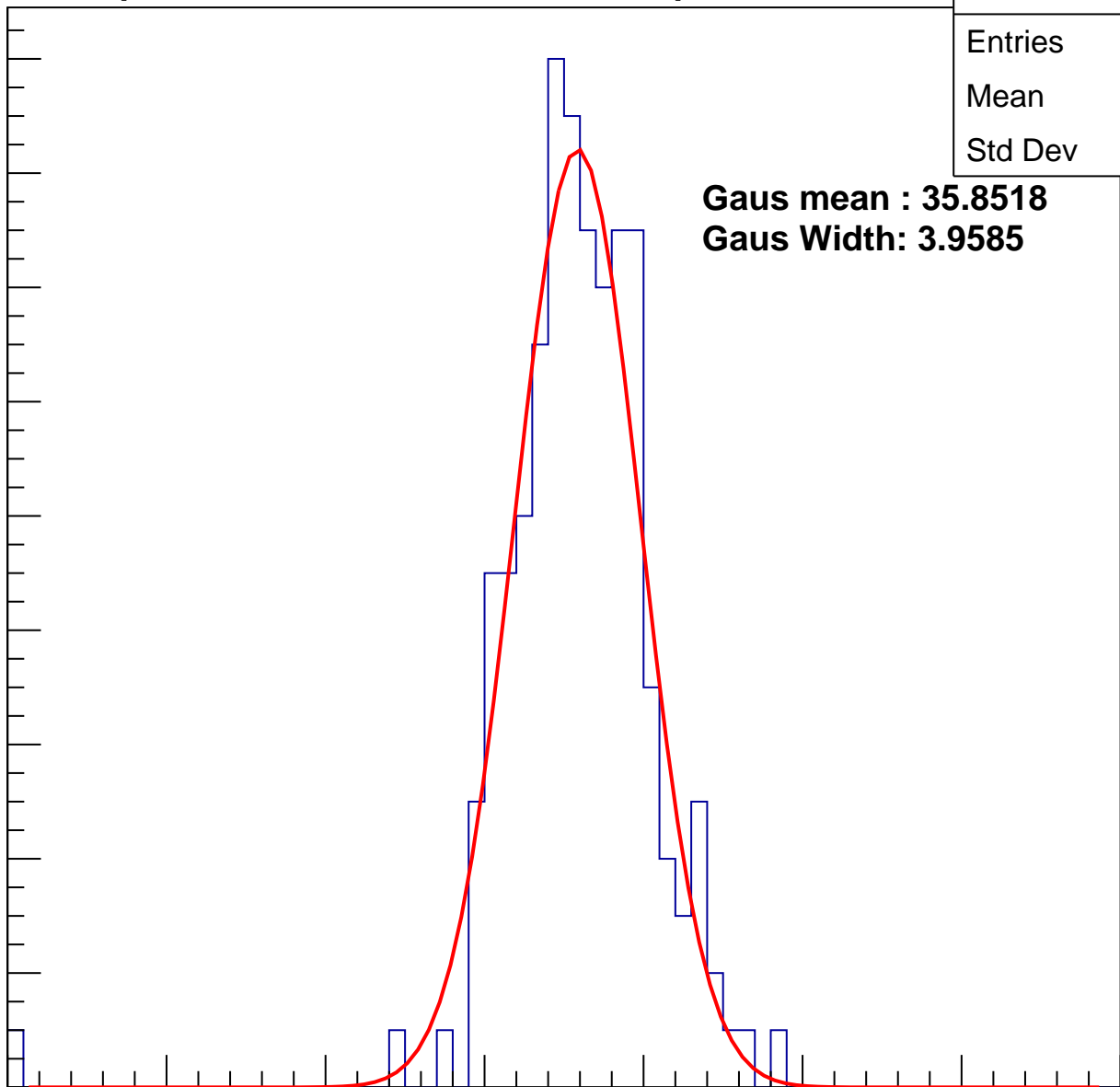
Entries	167
Mean	35.49
Std Dev	4.81

**Gaus mean : 35.8518**

**Gaus Width: 3.9585**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch72, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

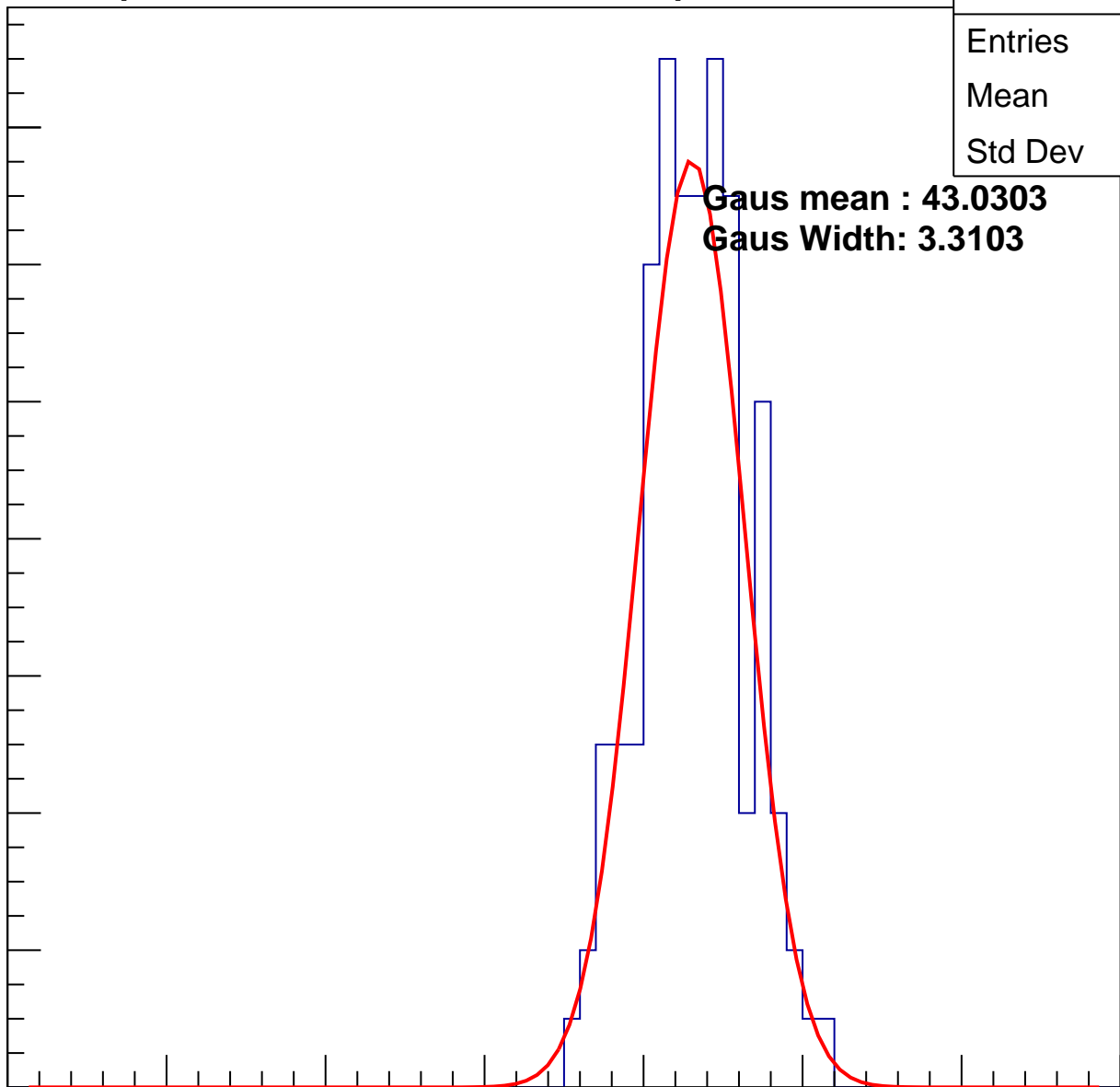
Entries	121
Mean	42.7
Std Dev	3.234

**Gaus mean : 43.0303**

**Gaus Width: 3.3103**

0 10 20 30 40 50 60 70

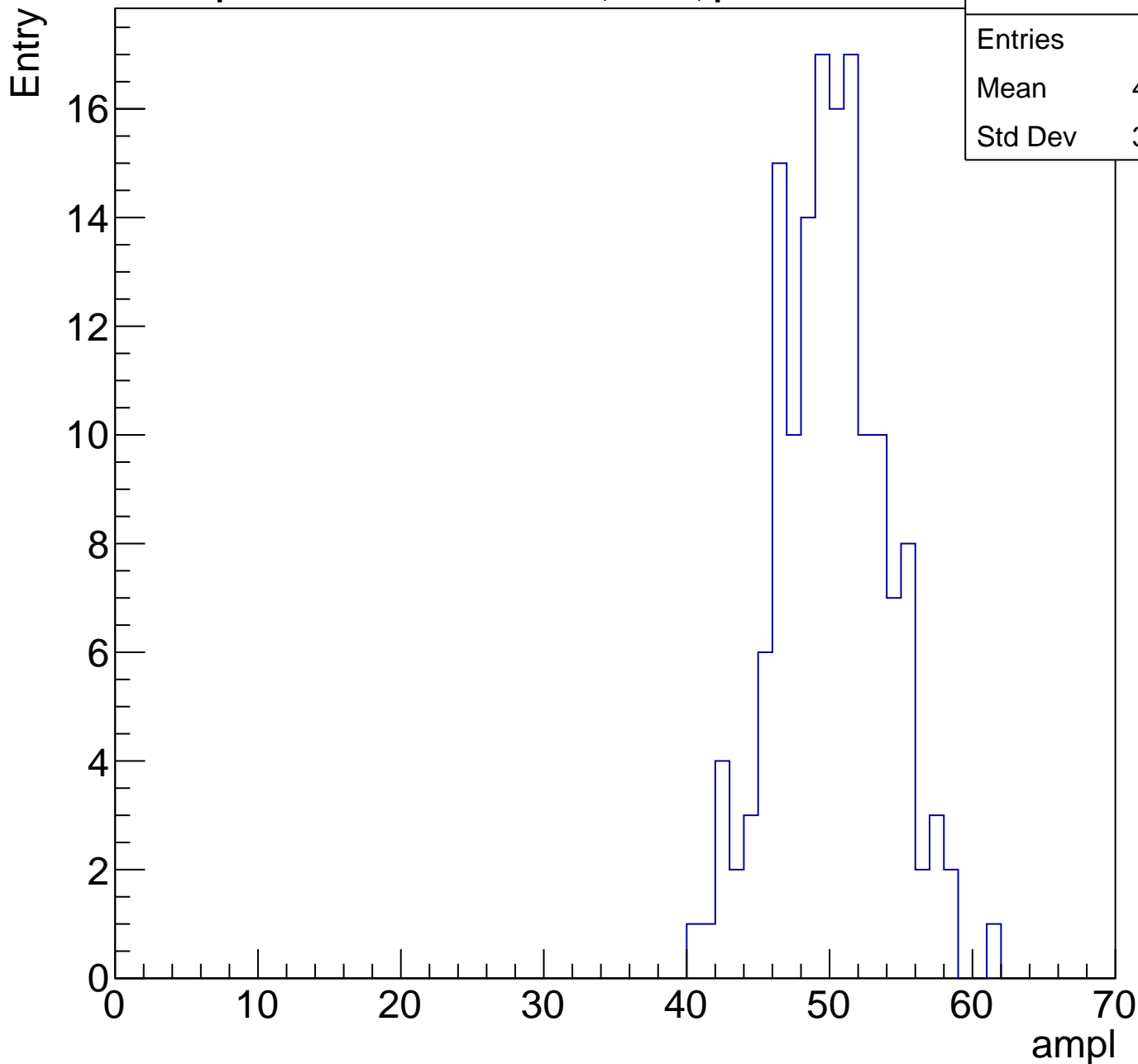
ampl



# B0L101S, U26-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	49.64
Std Dev	3.758



# B0L101S, U26-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

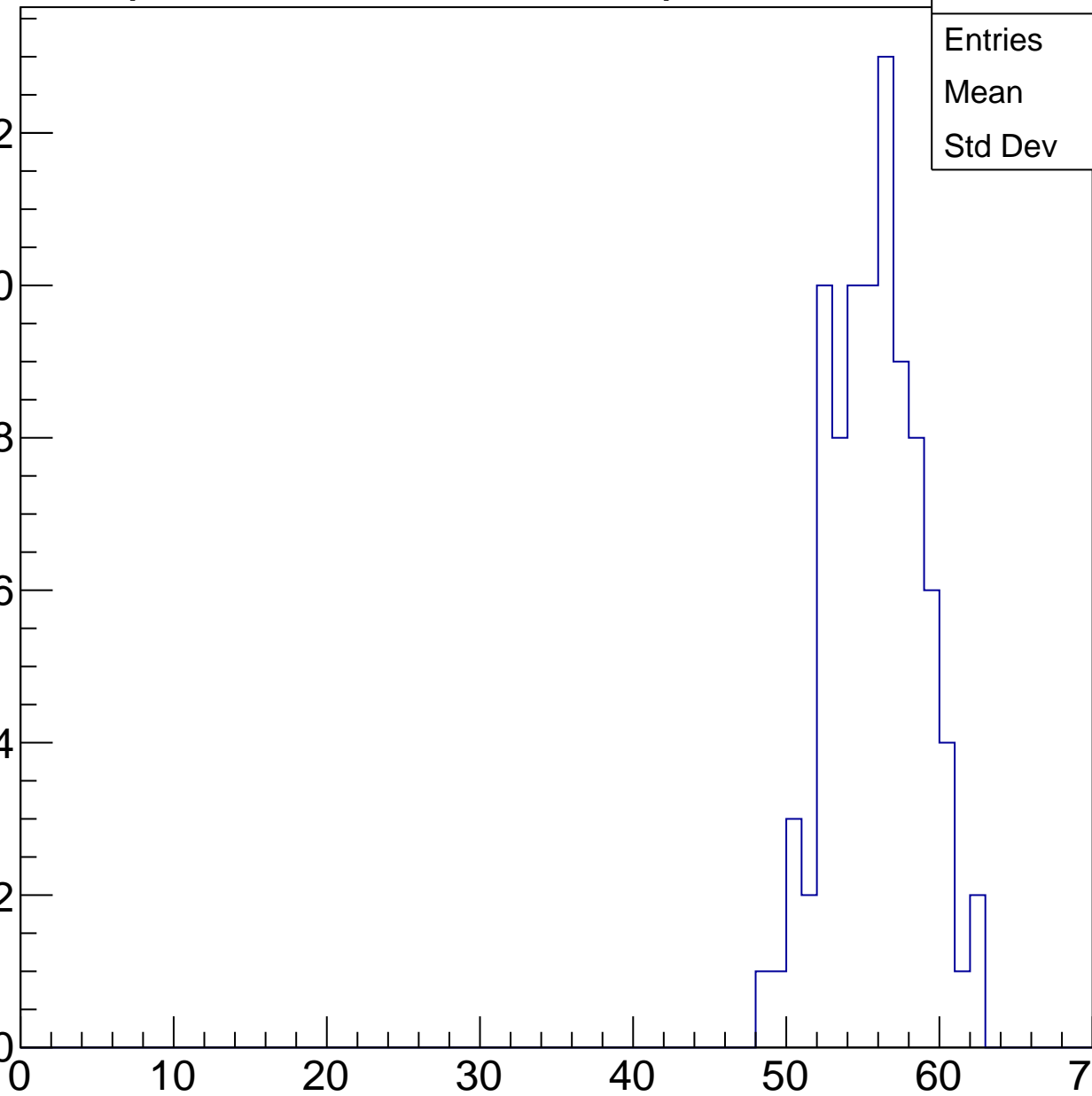
4

2

0

Entries	88
Mean	55.31
Std Dev	2.952

ampl



# B0L101S, U26-ch72, adc5

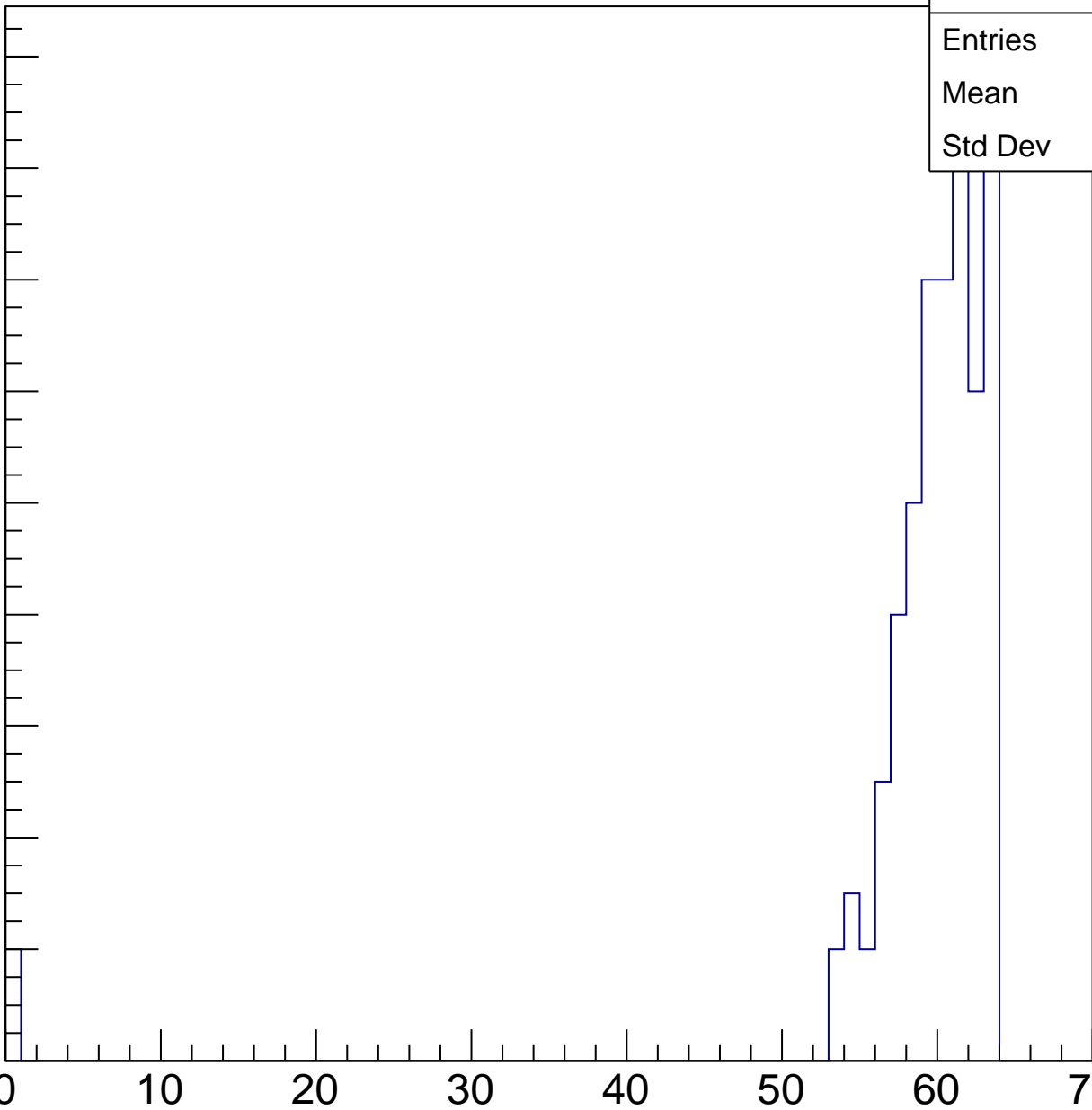
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	106
Mean	58.58
Std Dev	8.501

ampl

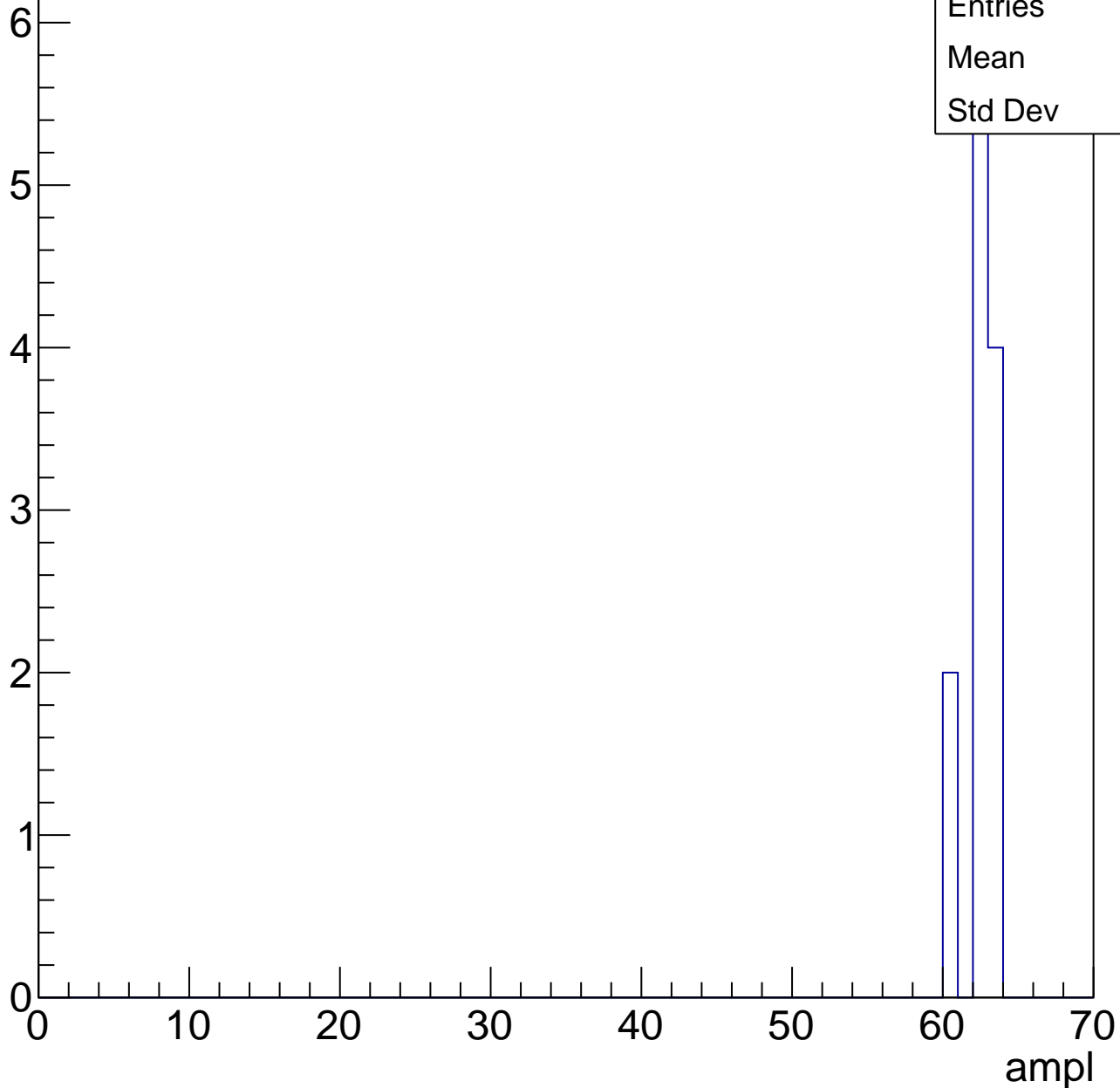


# B0L101S, U26-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	62
Std Dev	1





# B0L101S, U26-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch73, adc0

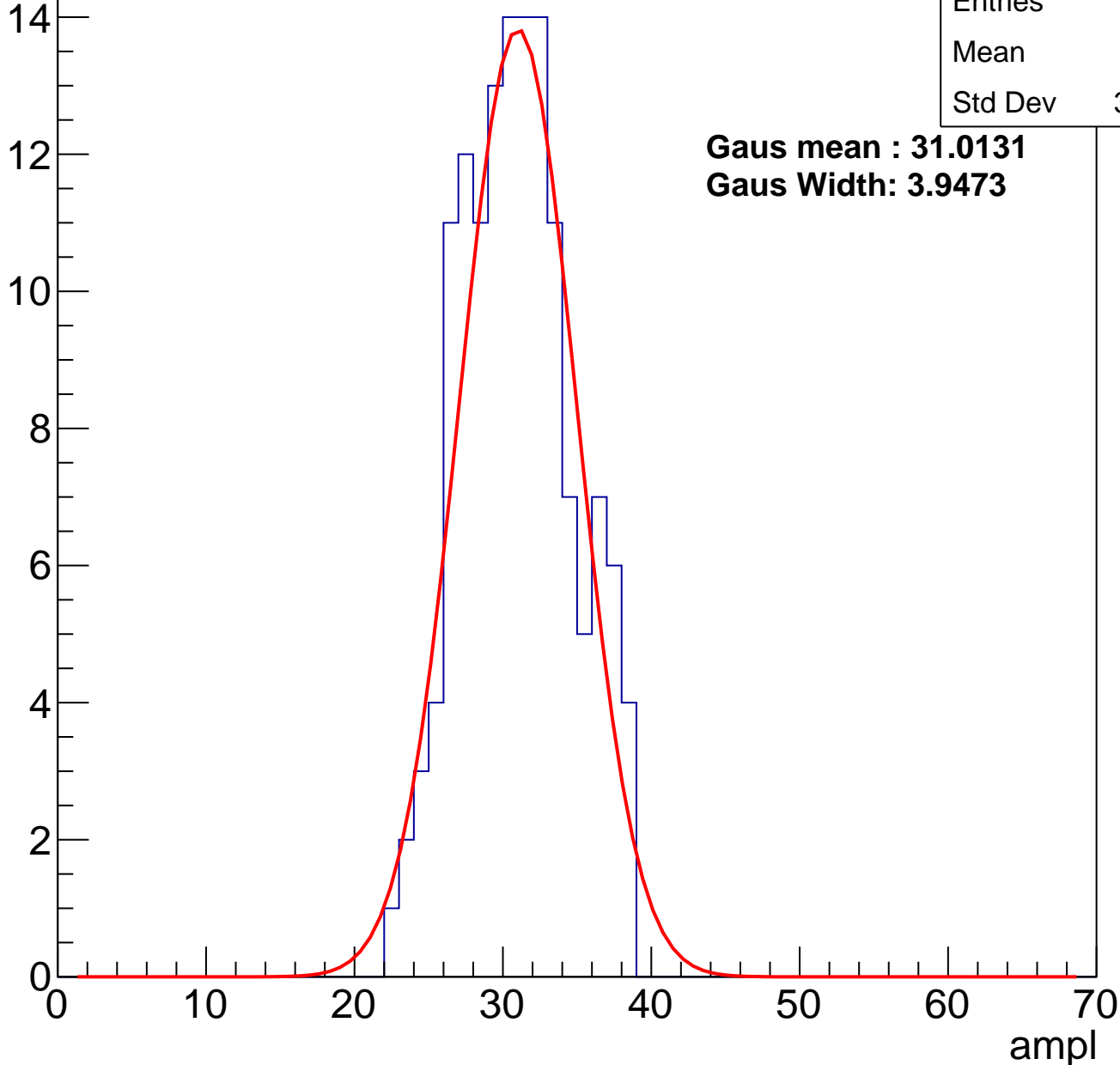
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	30.5
Std Dev	3.682

**Gaus mean : 31.0131**

**Gaus Width: 3.9473**

Entry



# B0L101S, U26-ch73, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	36.52
Std Dev	3.962

**Gaus mean : 37.4179**

**Gaus Width: 4.1079**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch73, adc2

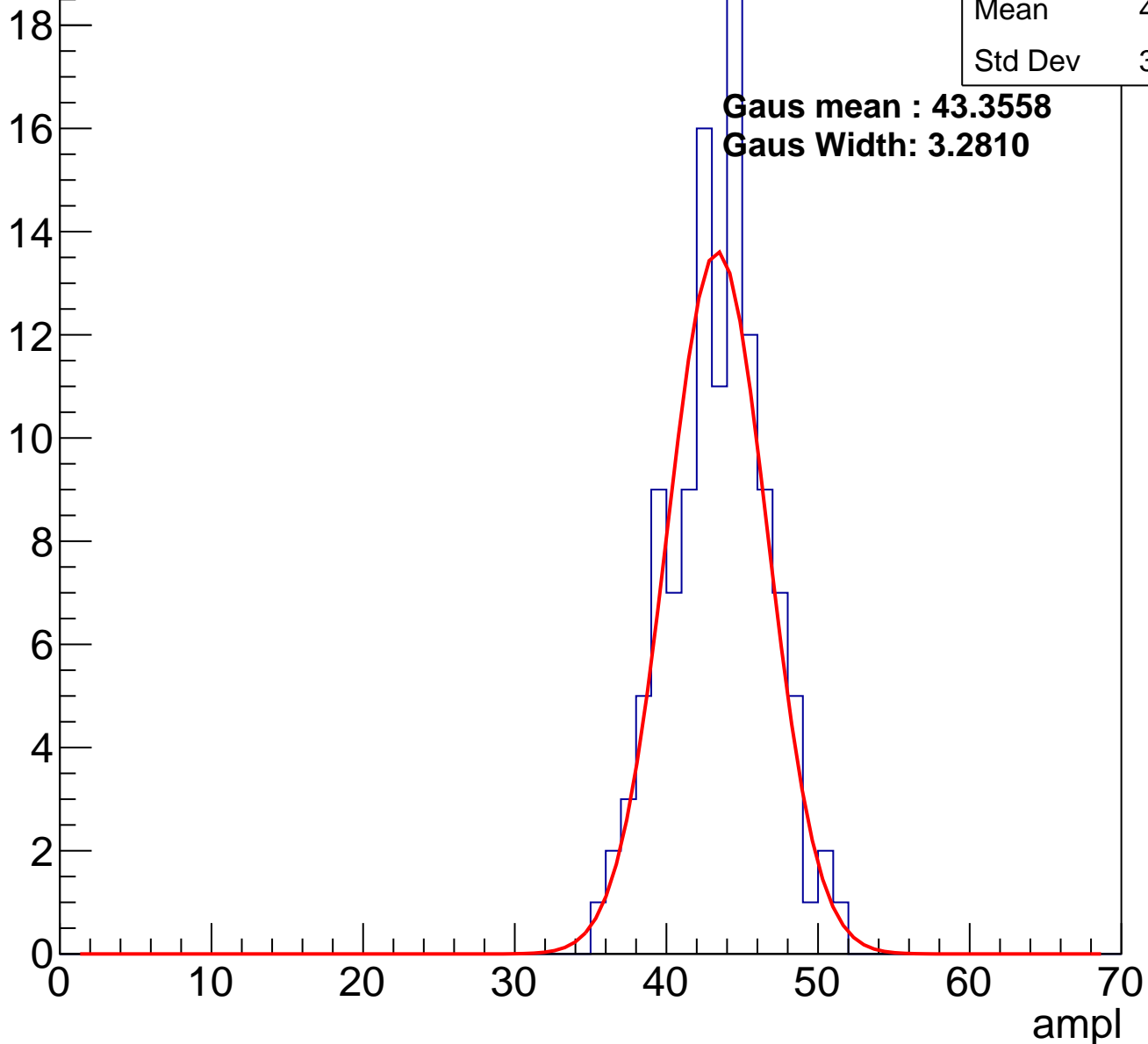
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	42.96
Std Dev	3.224

**Gaus mean : 43.3558**

**Gaus Width: 3.2810**

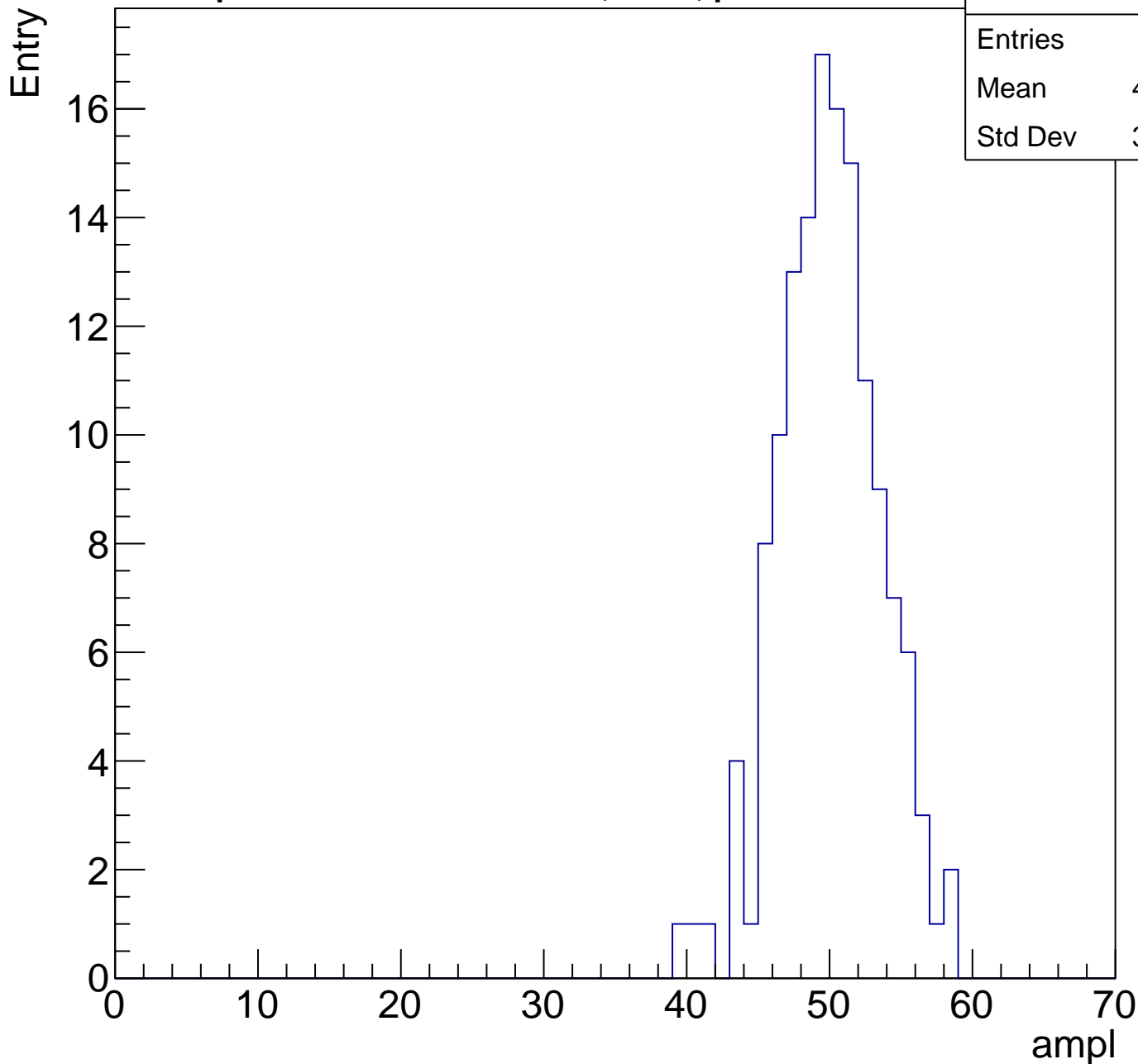
Entry



# B0L101S, U26-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	49.54
Std Dev	3.526



# B0L101S, U26-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

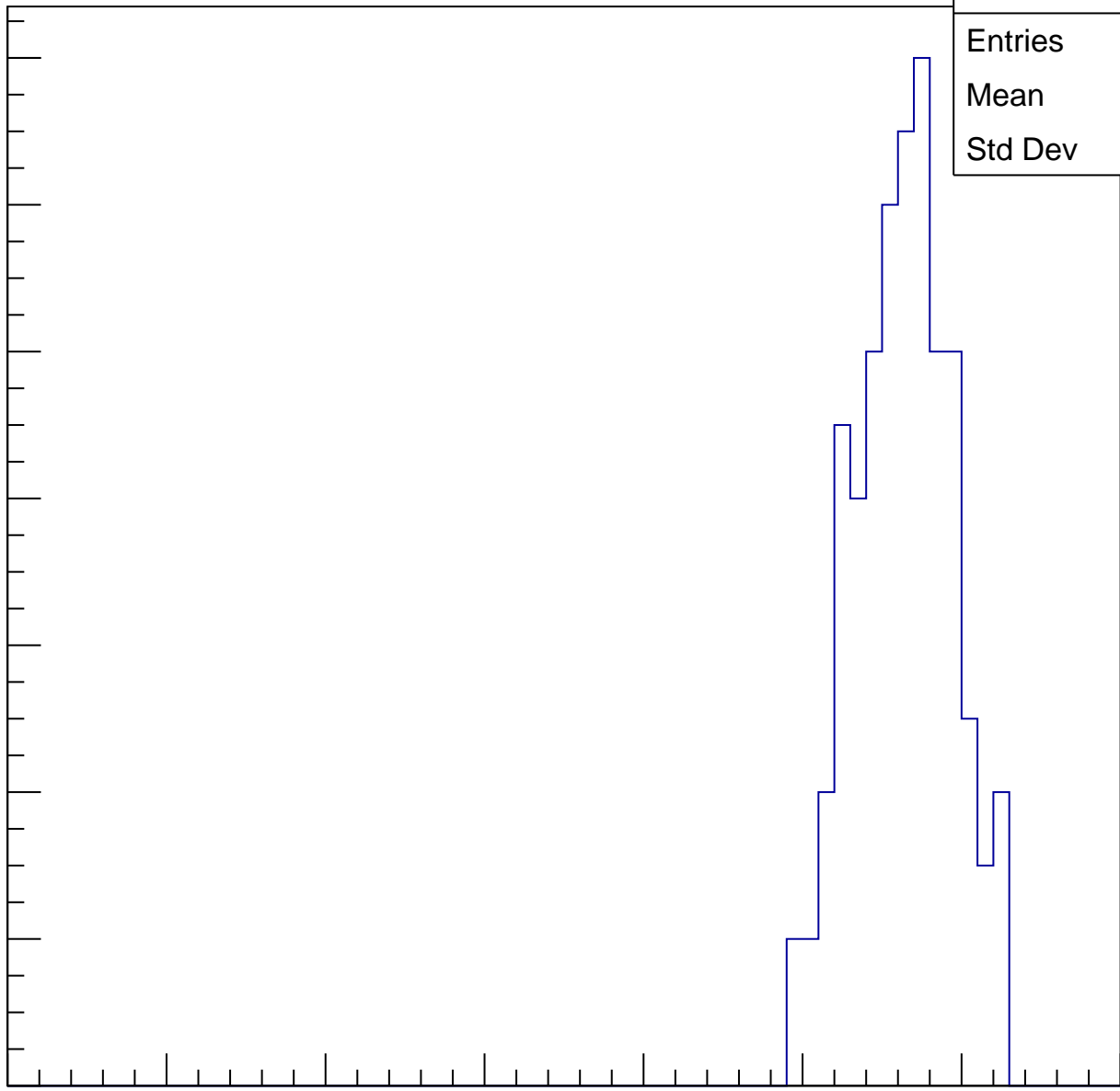
Entries	106
Mean	55.86
Std Dev	3.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch73, adc5

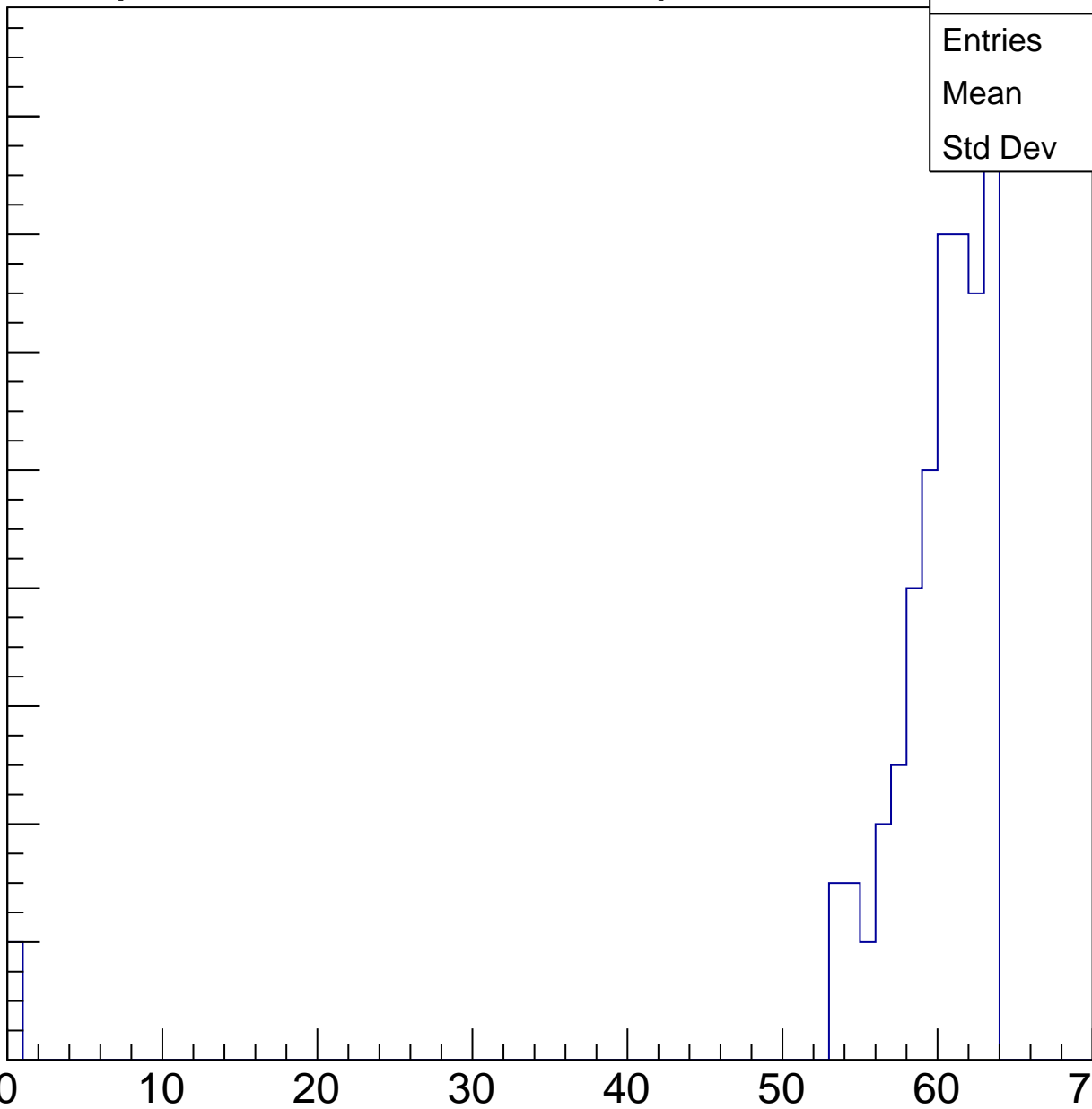
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	95
Mean	58.58
Std Dev	8.993

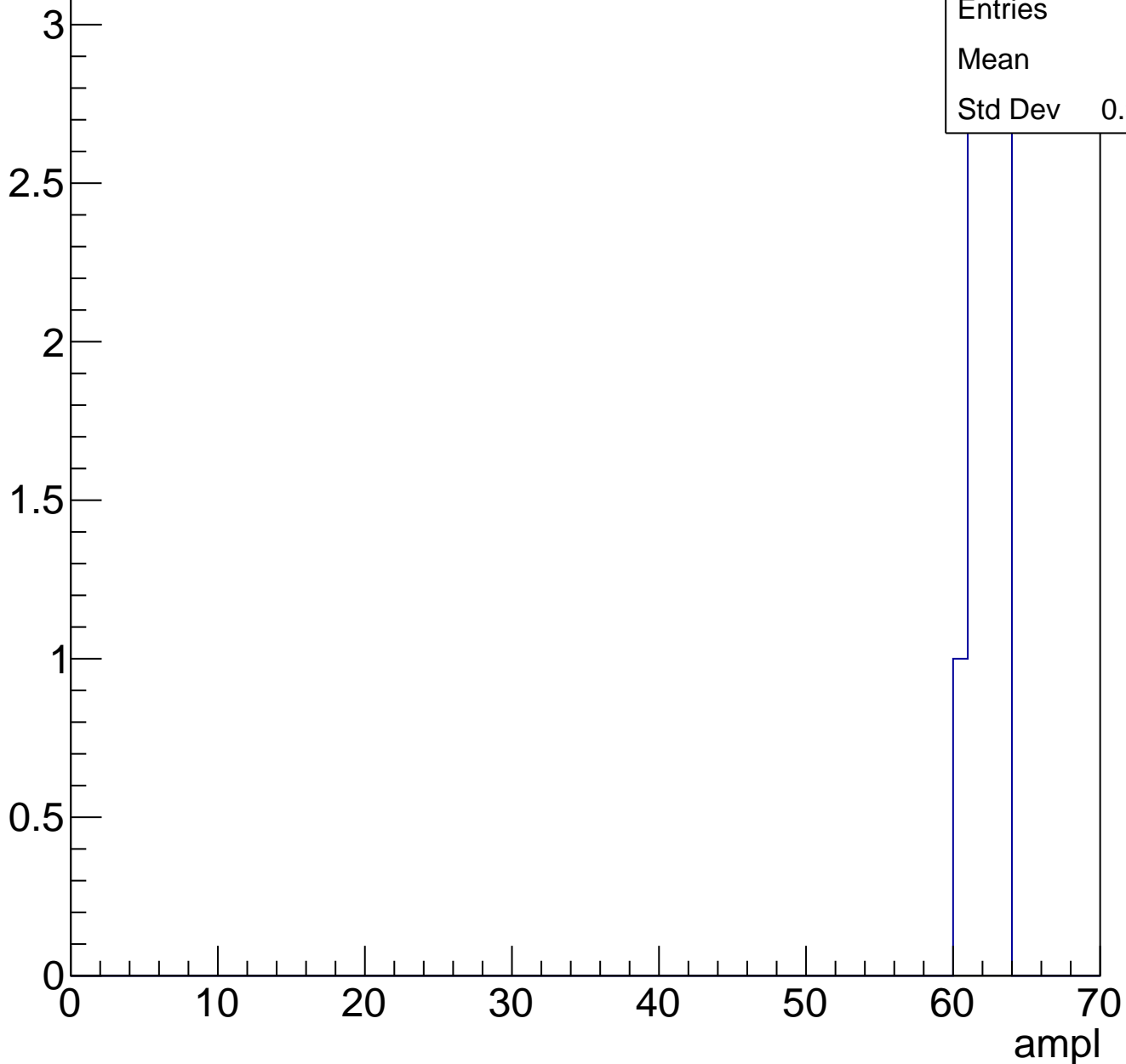
ampl



# B0L101S, U26-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	159
Mean	29.16
Std Dev	6.184

**Gaus mean : 30.3312**

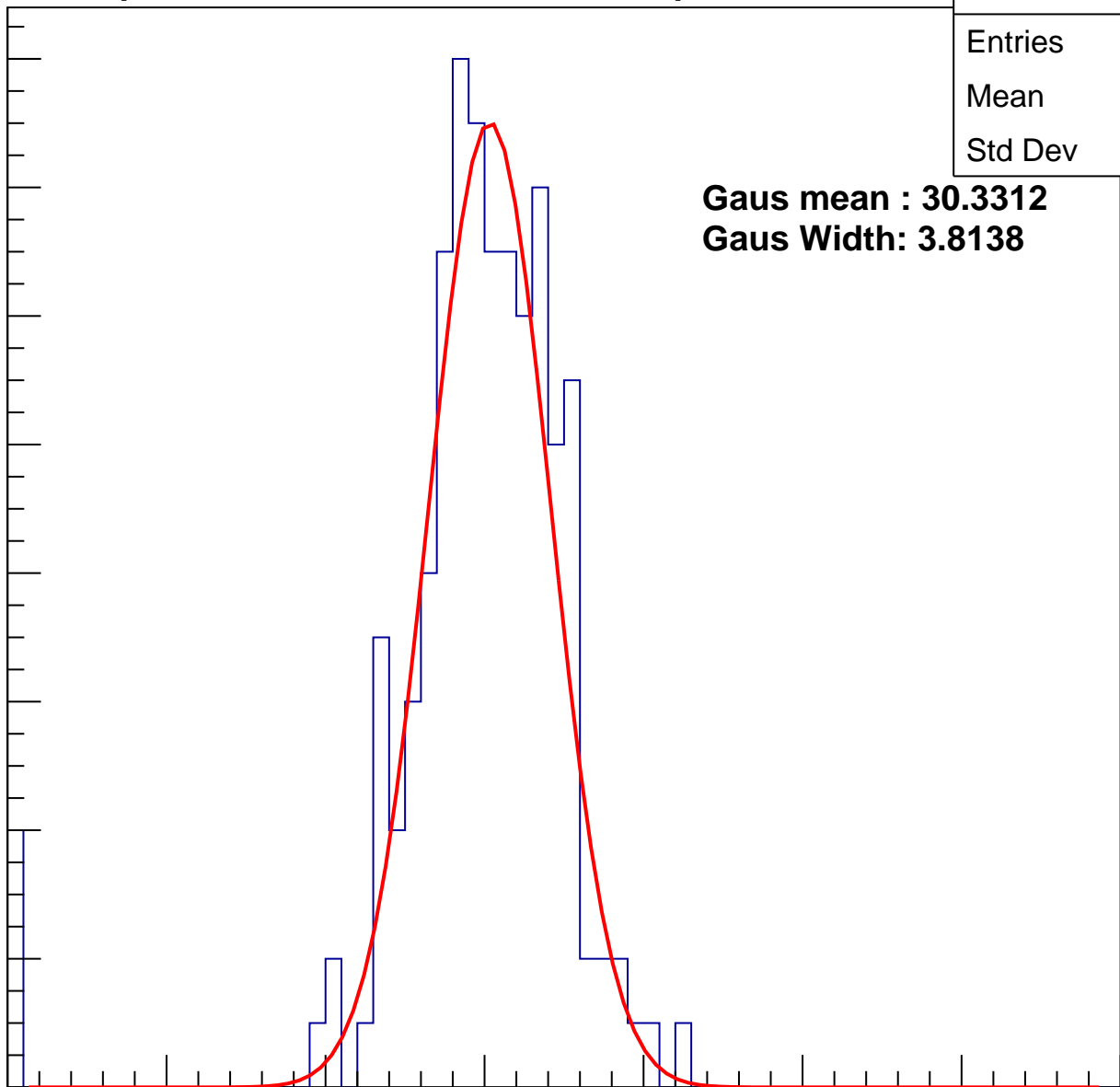
**Gaus Width: 3.8138**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

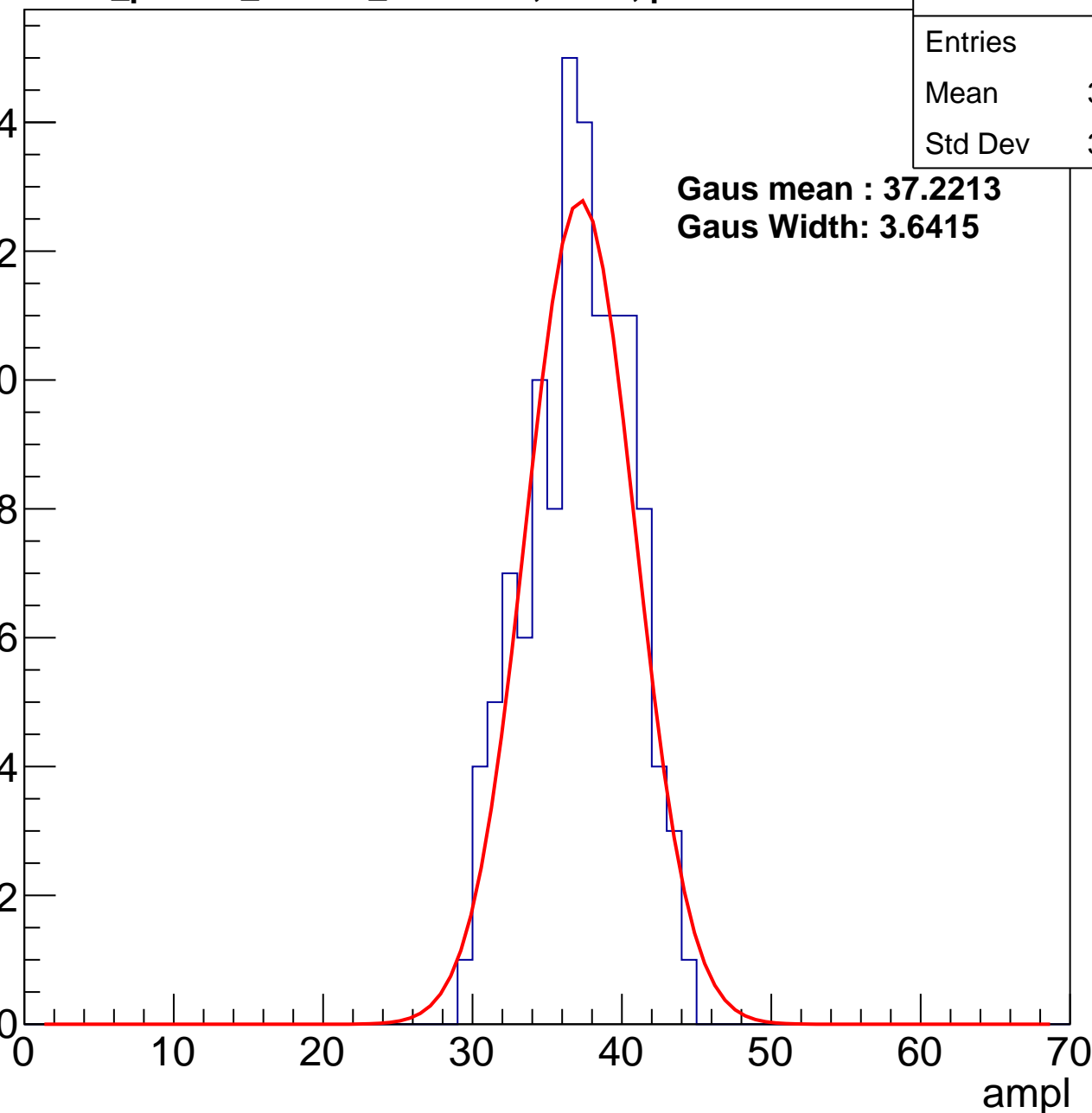
Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	36.64
Std Dev	3.395

**Gaus mean : 37.2213**

**Gaus Width: 3.6415**



# B0L101S, U26-ch74, adc2

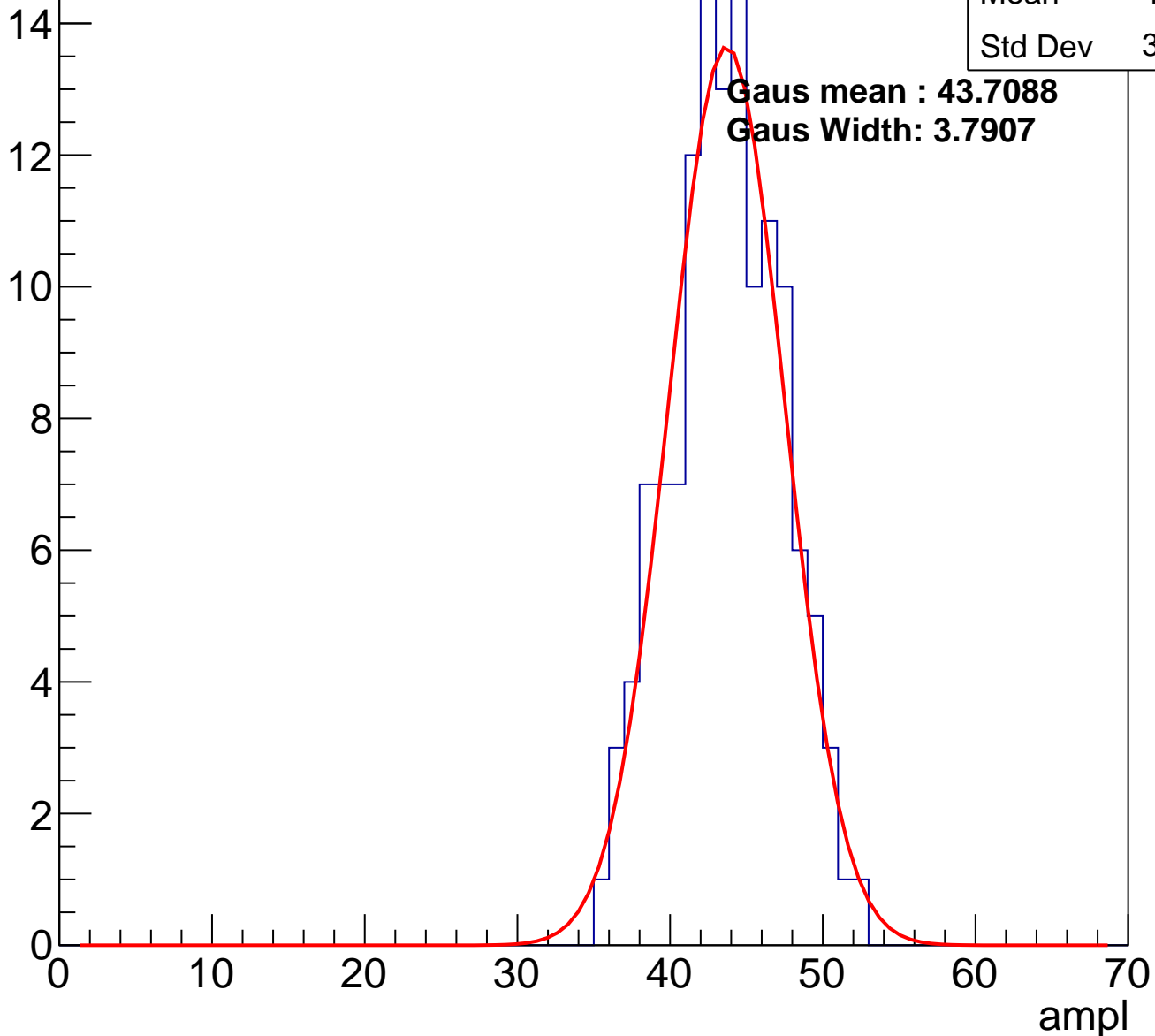
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	43.23
Std Dev	3.599

**Gaus mean : 43.7088**

**Gaus Width: 3.7907**

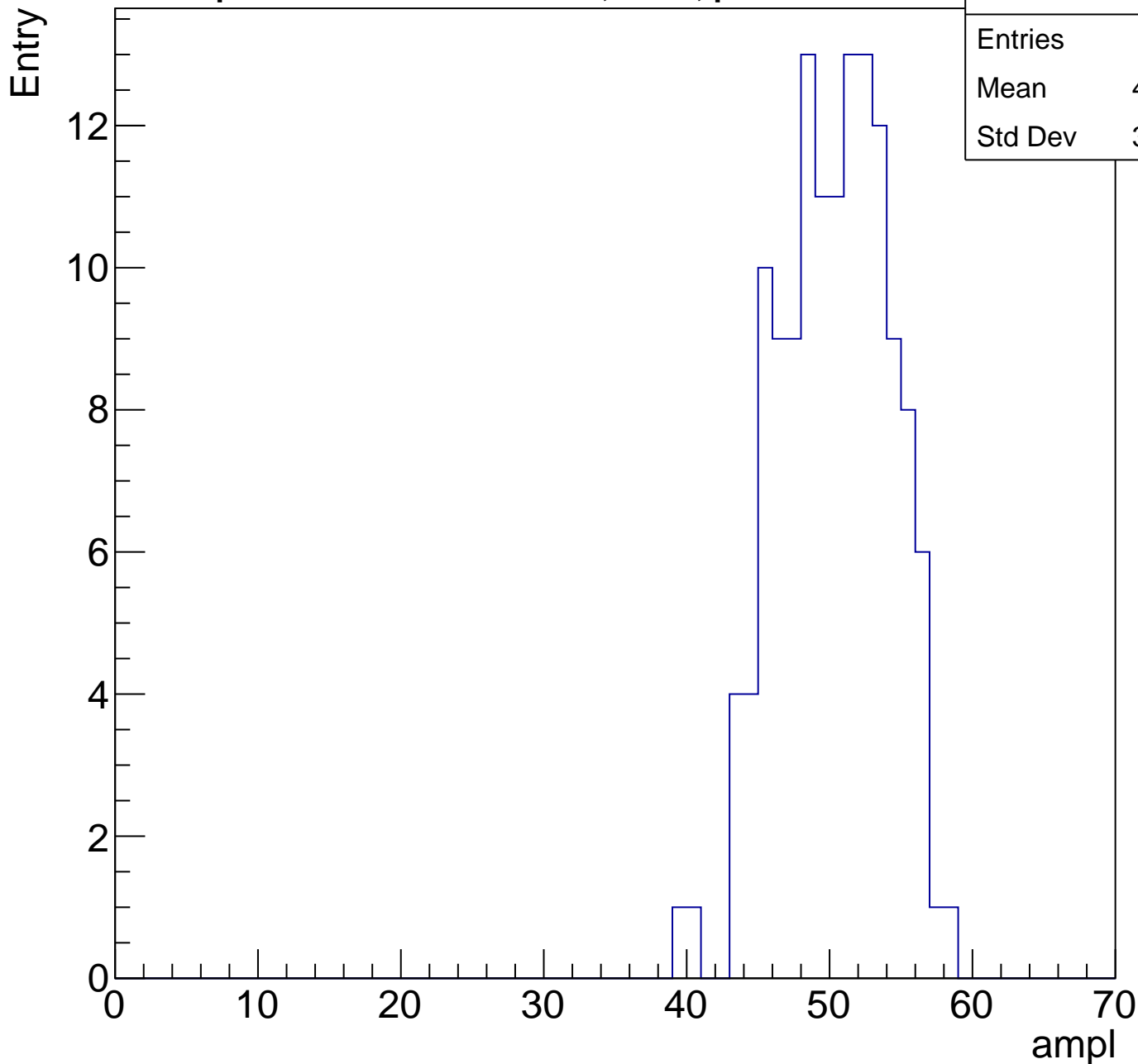
Entry



# B0L101S, U26-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	49.85
Std Dev	3.779



# B0L101S, U26-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

122

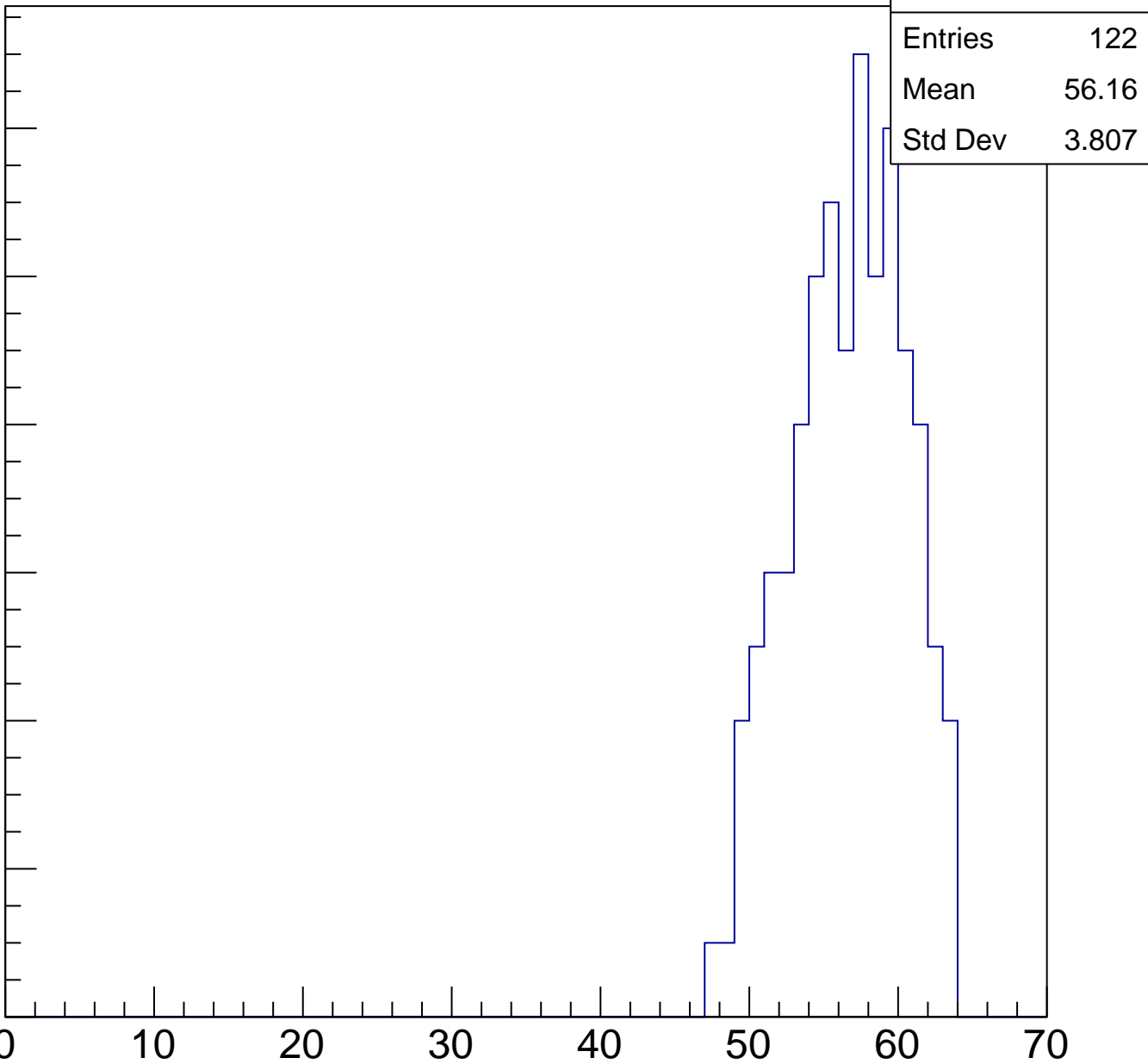
Mean

56.16

Std Dev

3.807

ampl



# B0L101S, U26-ch74, adc5

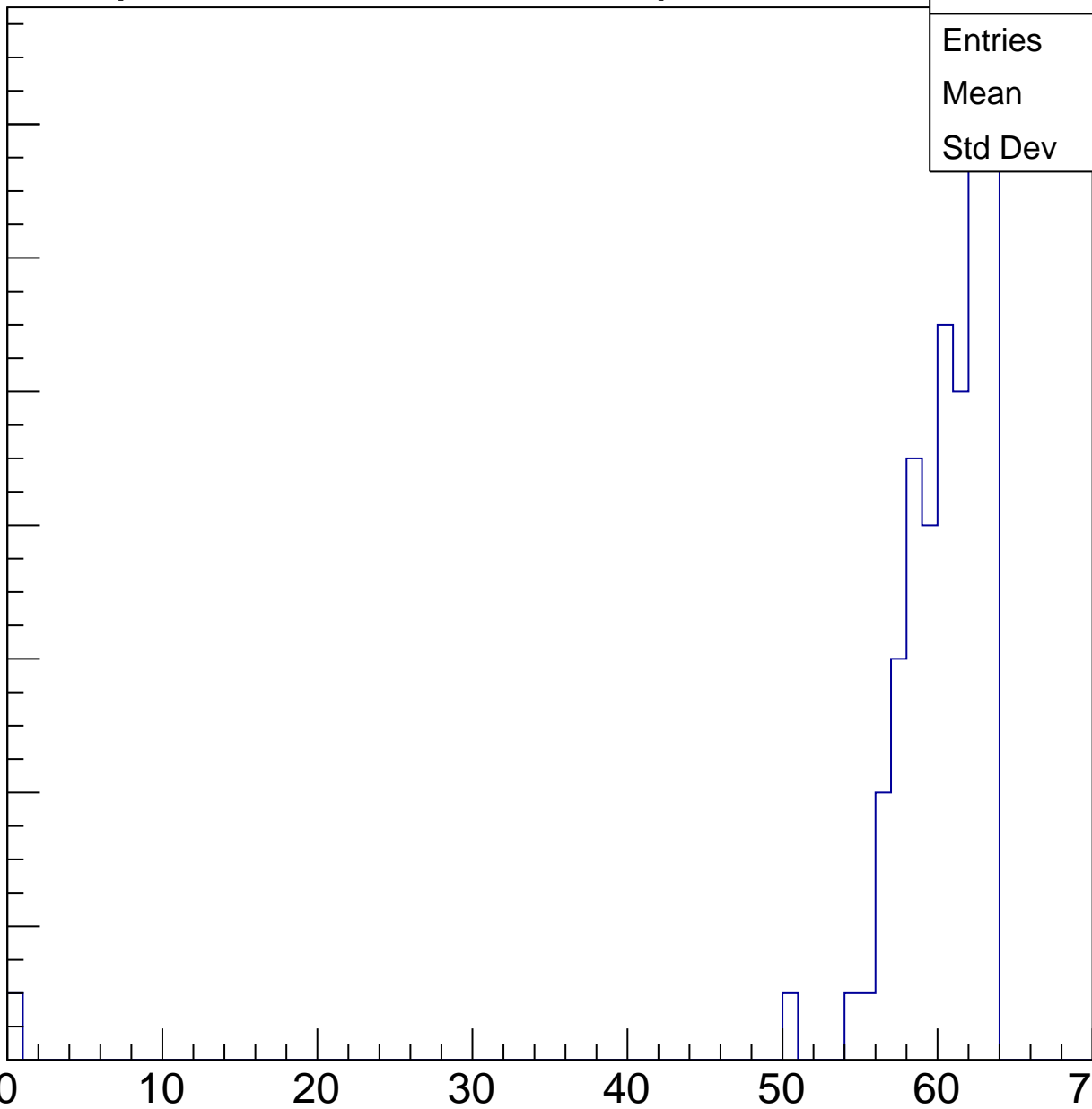
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	59.28
Std Dev	7.099

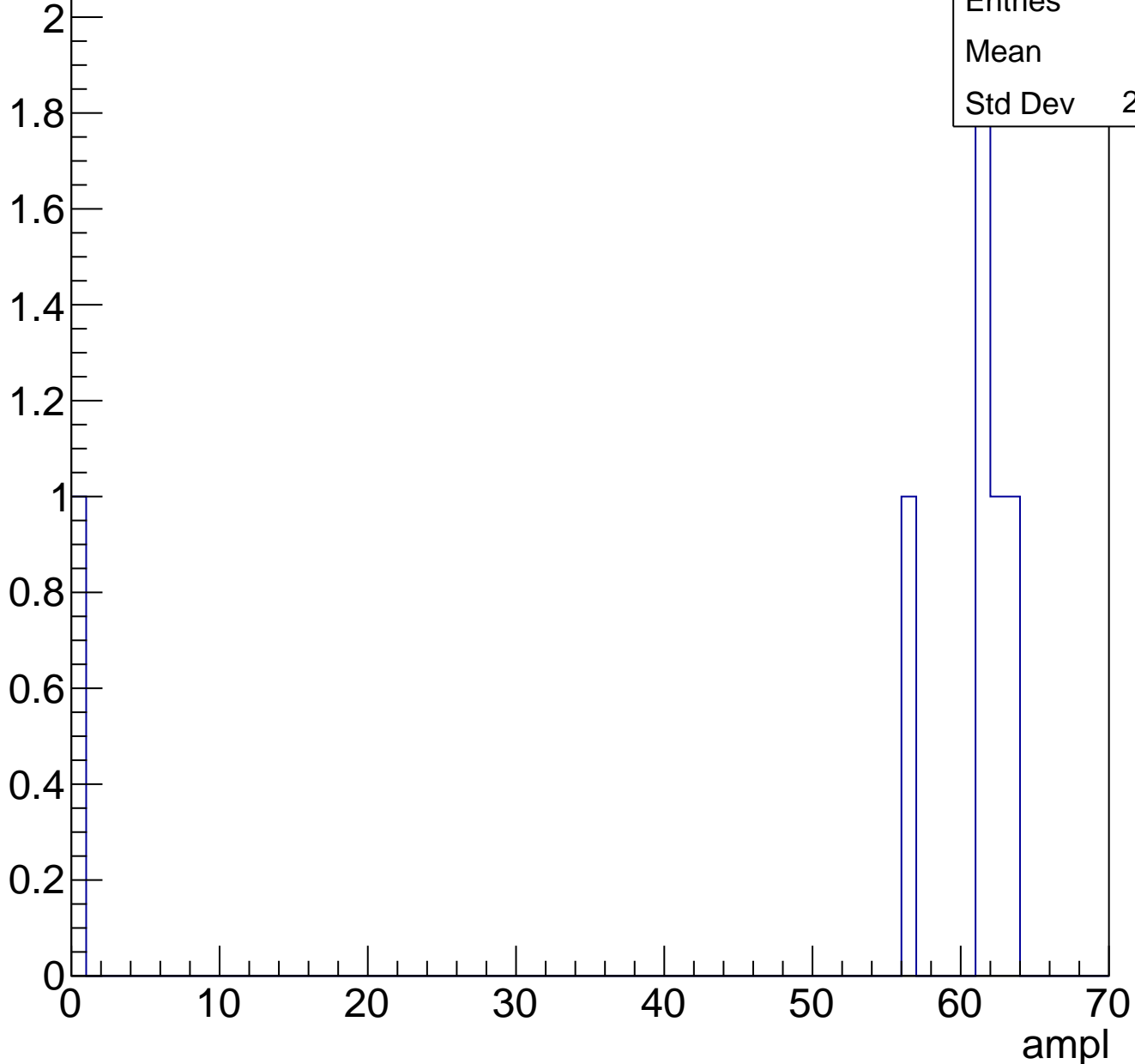
ampl



# B0L101S, U26-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch75, adc0

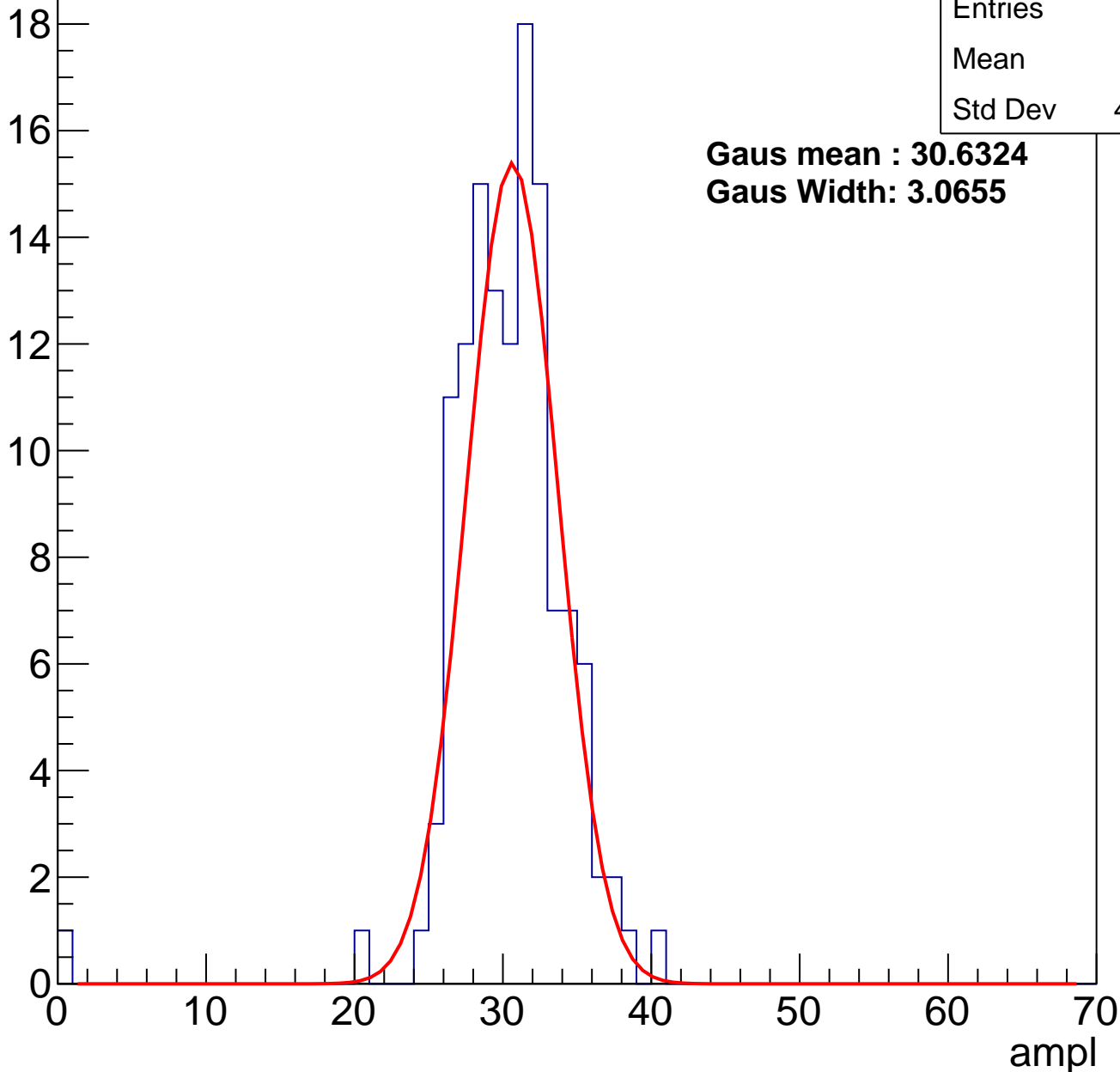
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	29.9
Std Dev	4.149

**Gaus mean : 30.6324**

**Gaus Width: 3.0655**

Entry



# B0L101S, U26-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	141
Mean	36.97
Std Dev	3.337

**Gaus mean : 37.4949**

**Gaus Width: 3.4427**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

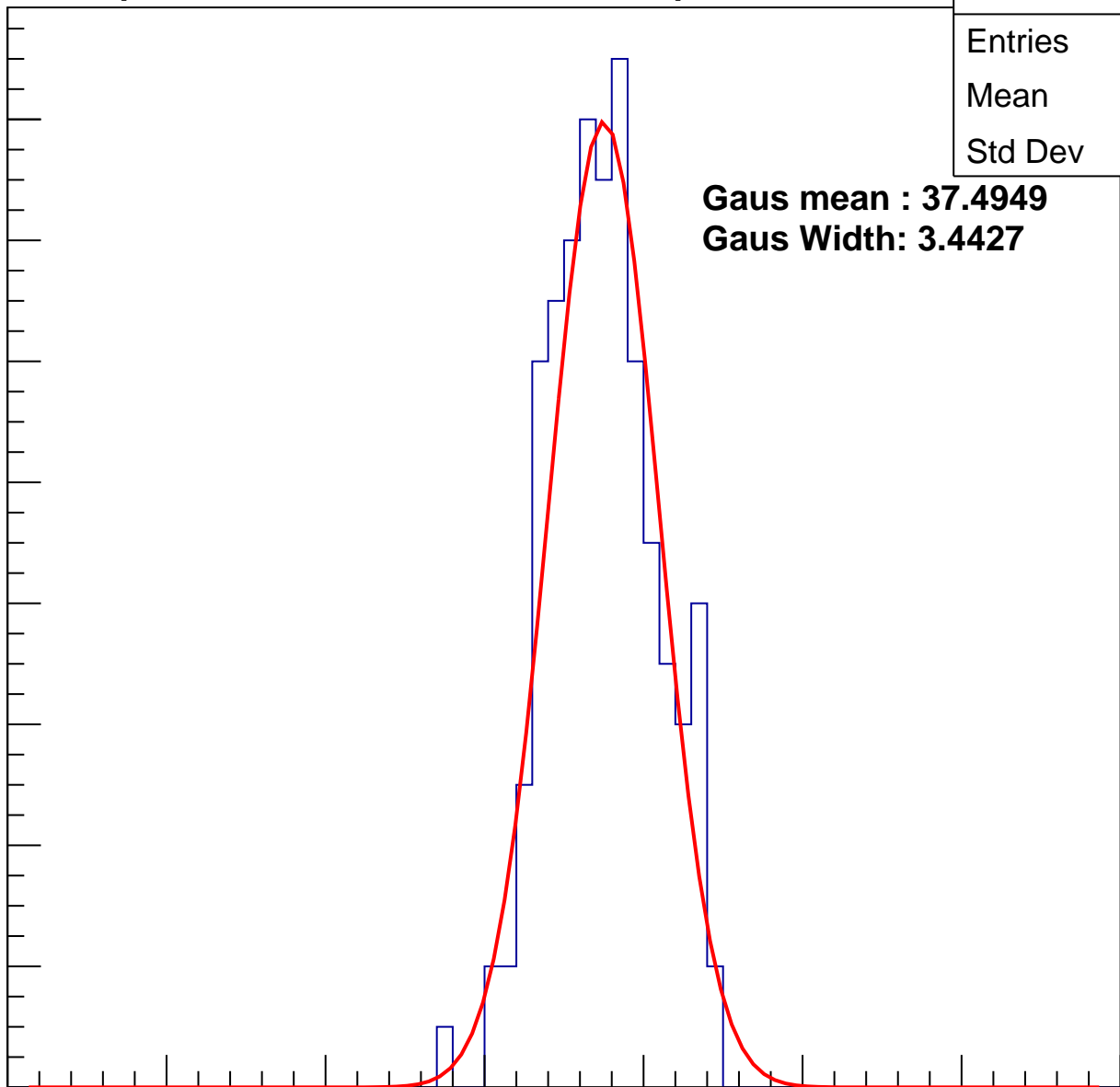
40

50

60

70

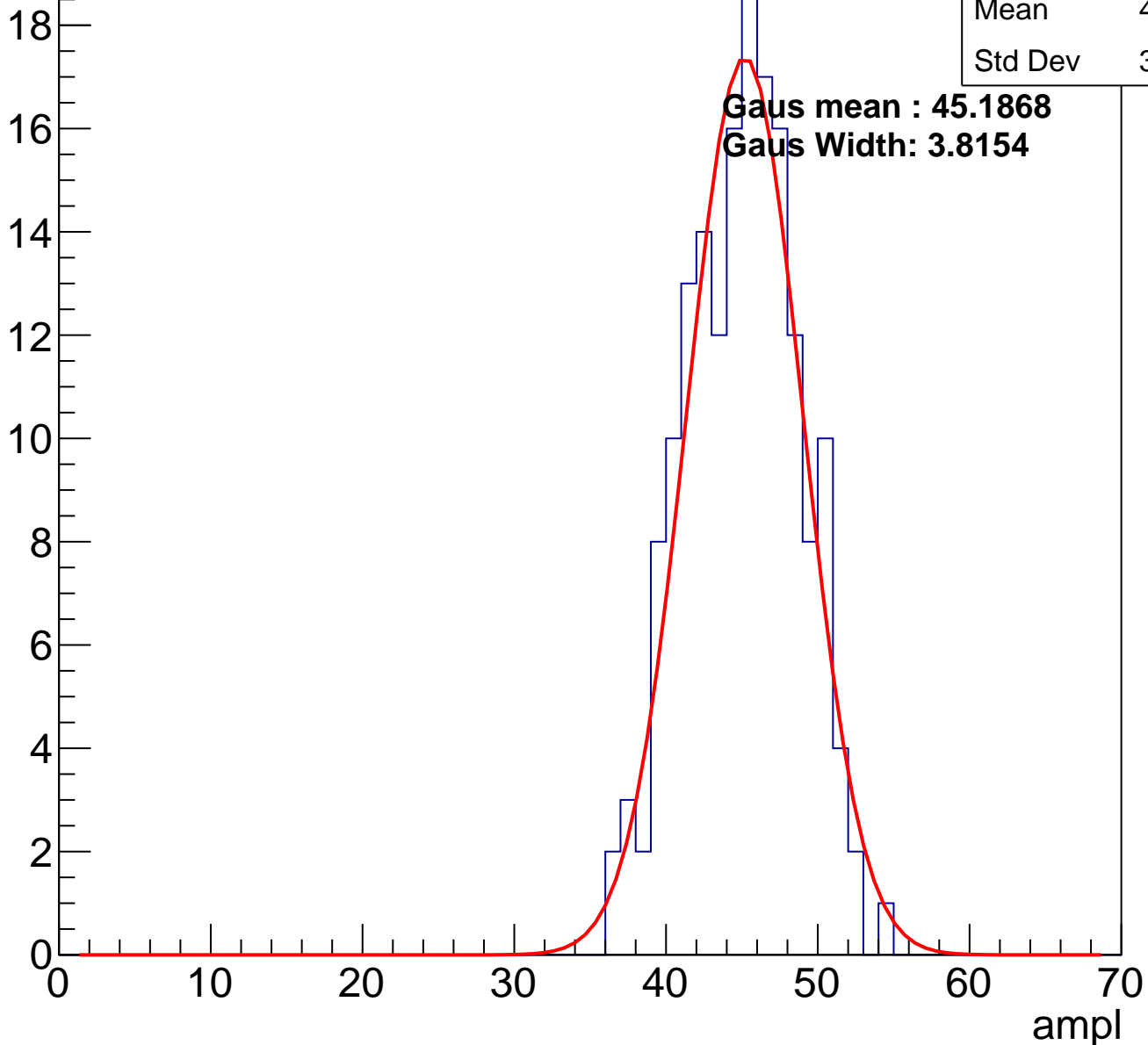
ampl



# B0L101S, U26-ch75, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

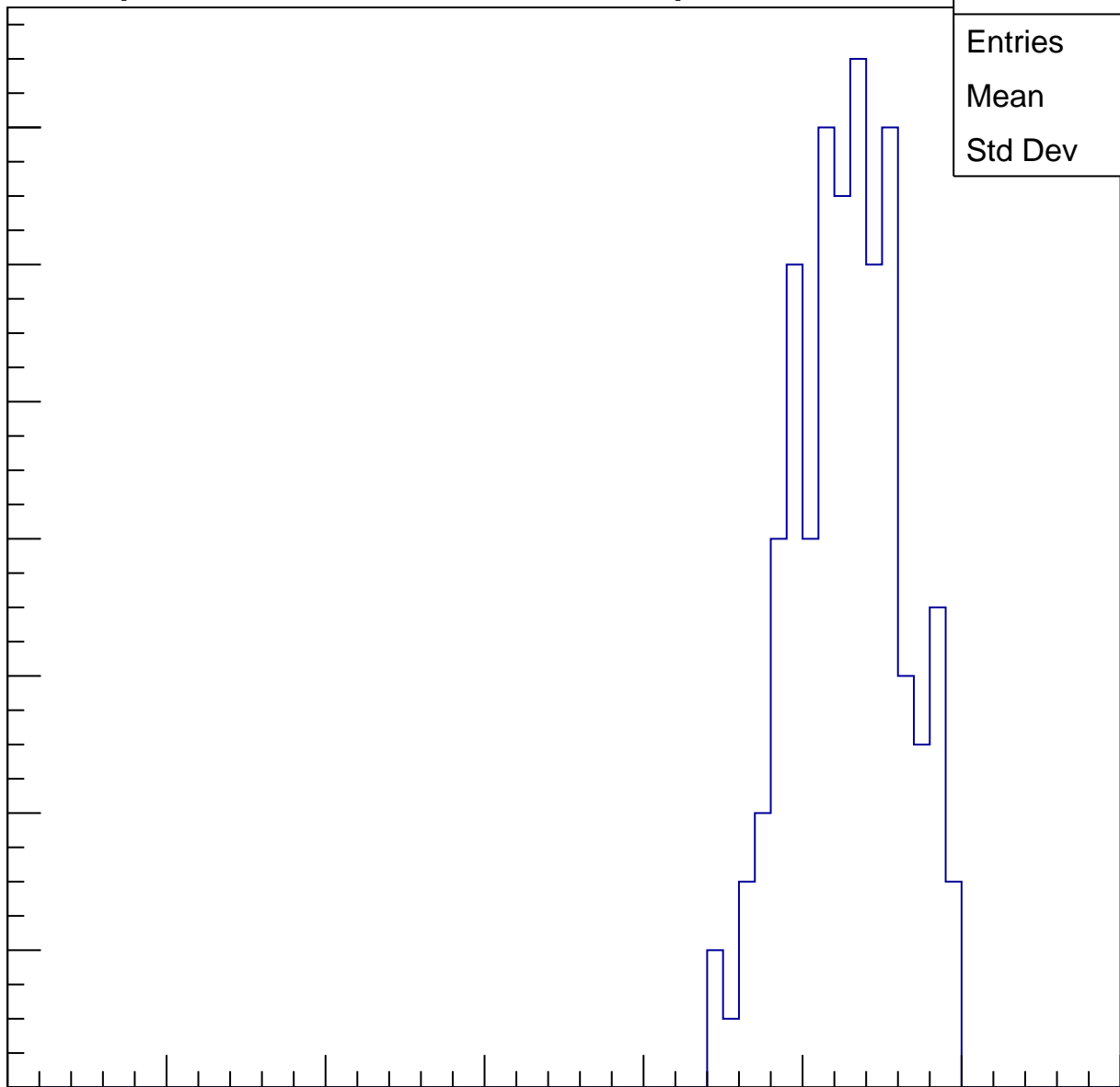
40

50

60

ampl

Entries	127
Mean	52.27
Std Dev	3.399



# B0L101S, U26-ch75, adc4

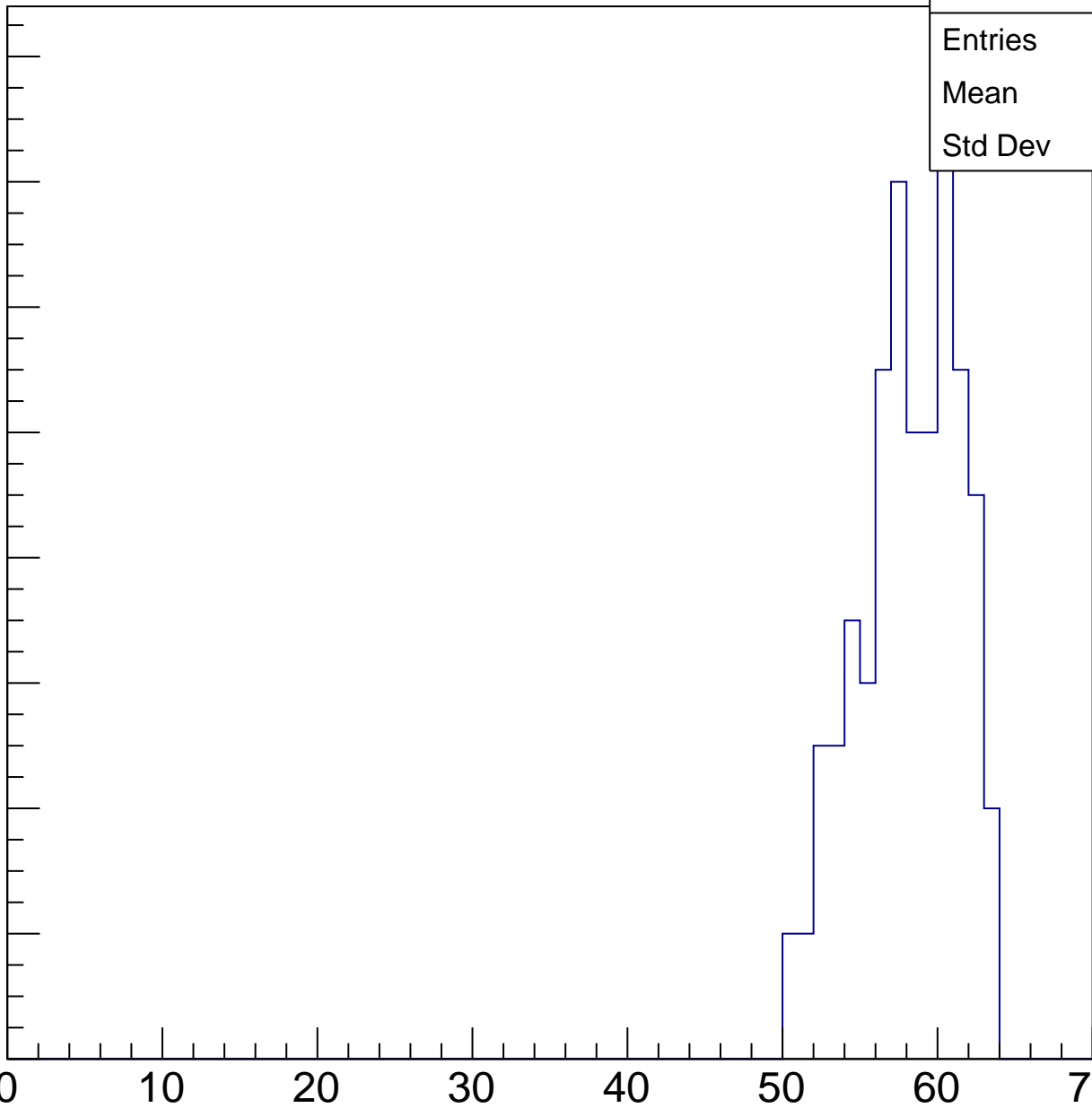
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	112
Mean	57.68
Std Dev	3.219

ampl



# B0L101S, U26-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

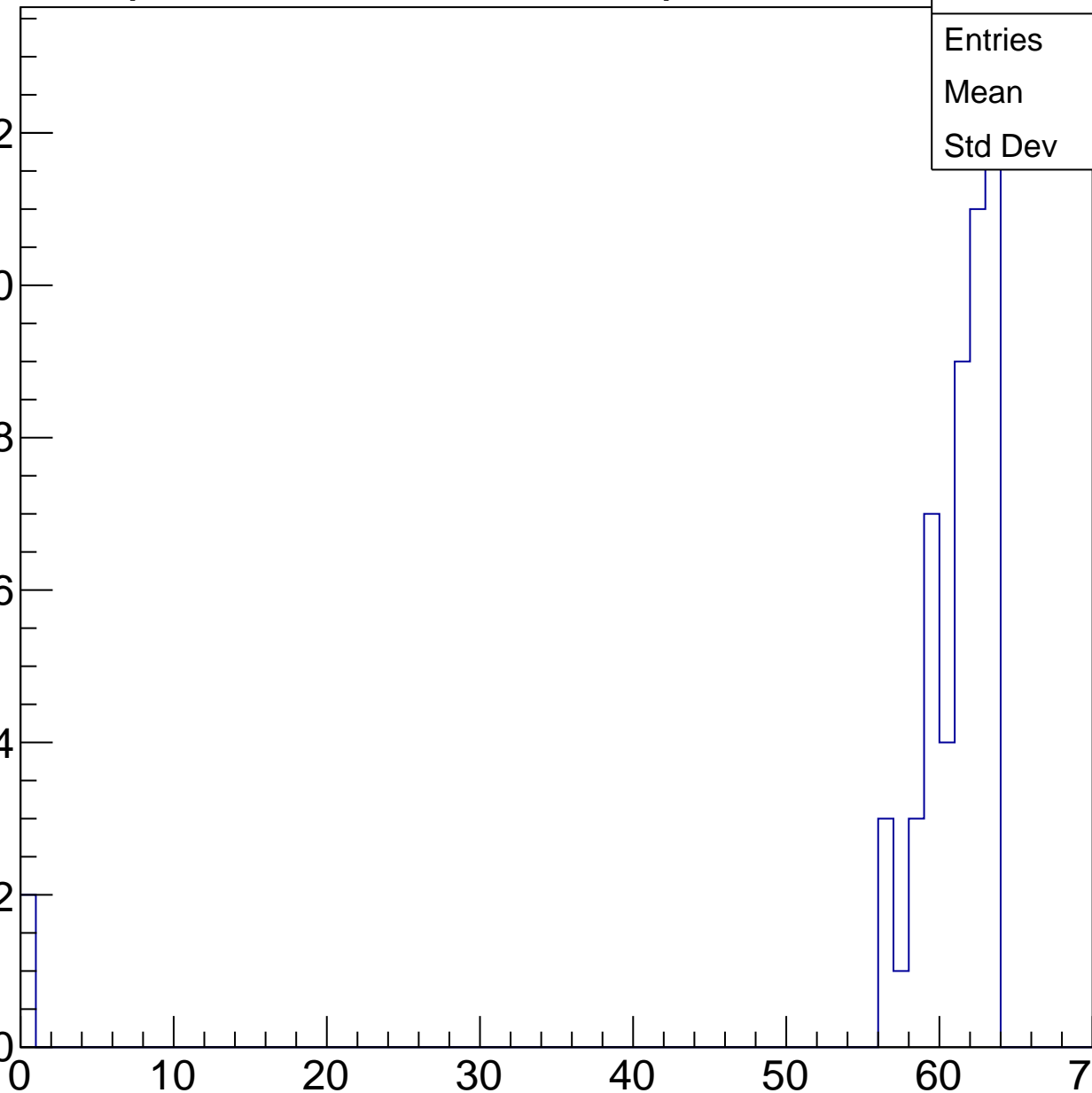
4

2

0

Entries	53
Mean	58.53
Std Dev	11.76

ampl



# B0L101S, U26-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch75, adc7

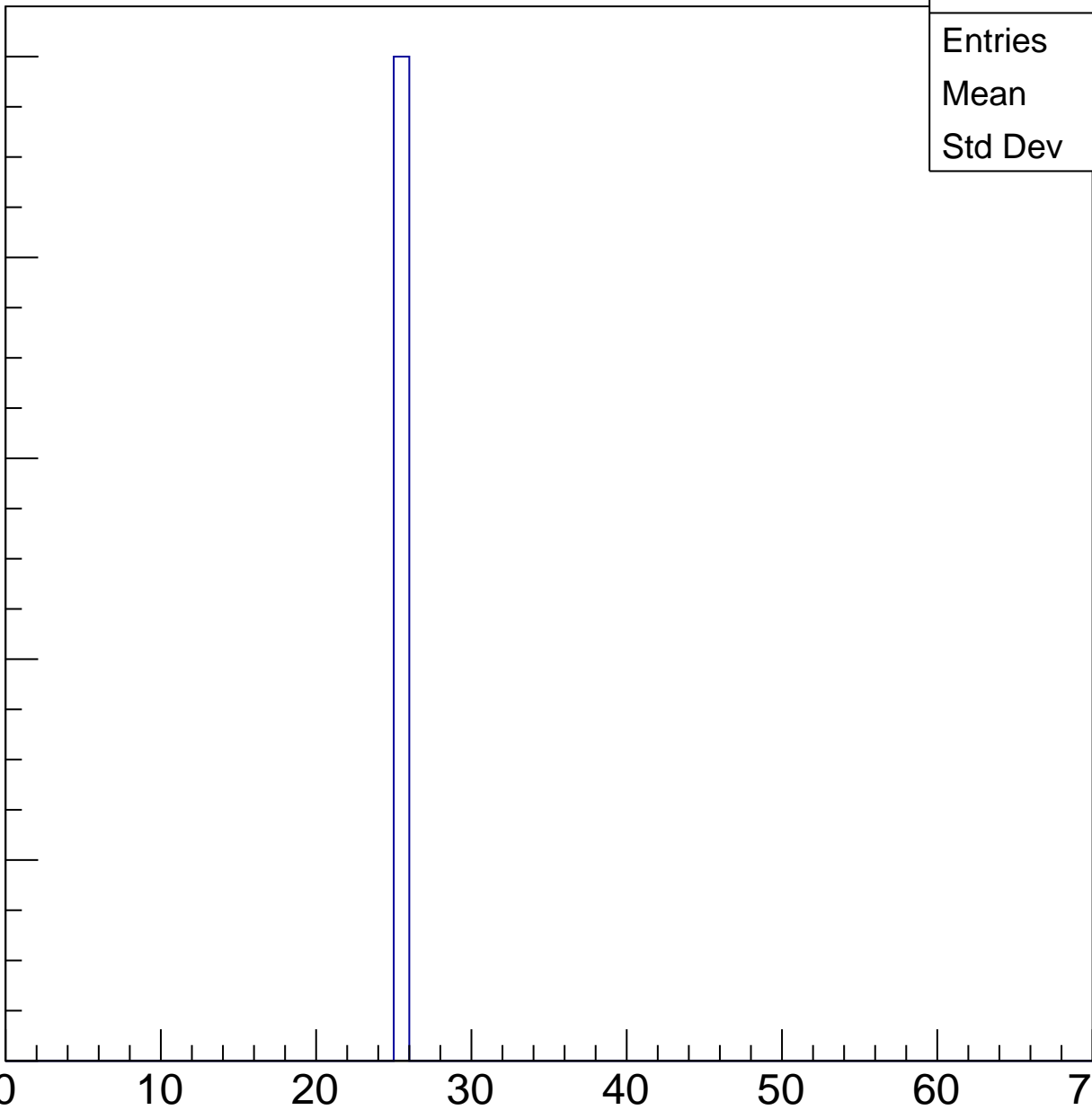
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl



# B0L101S, U26-ch76, adc0

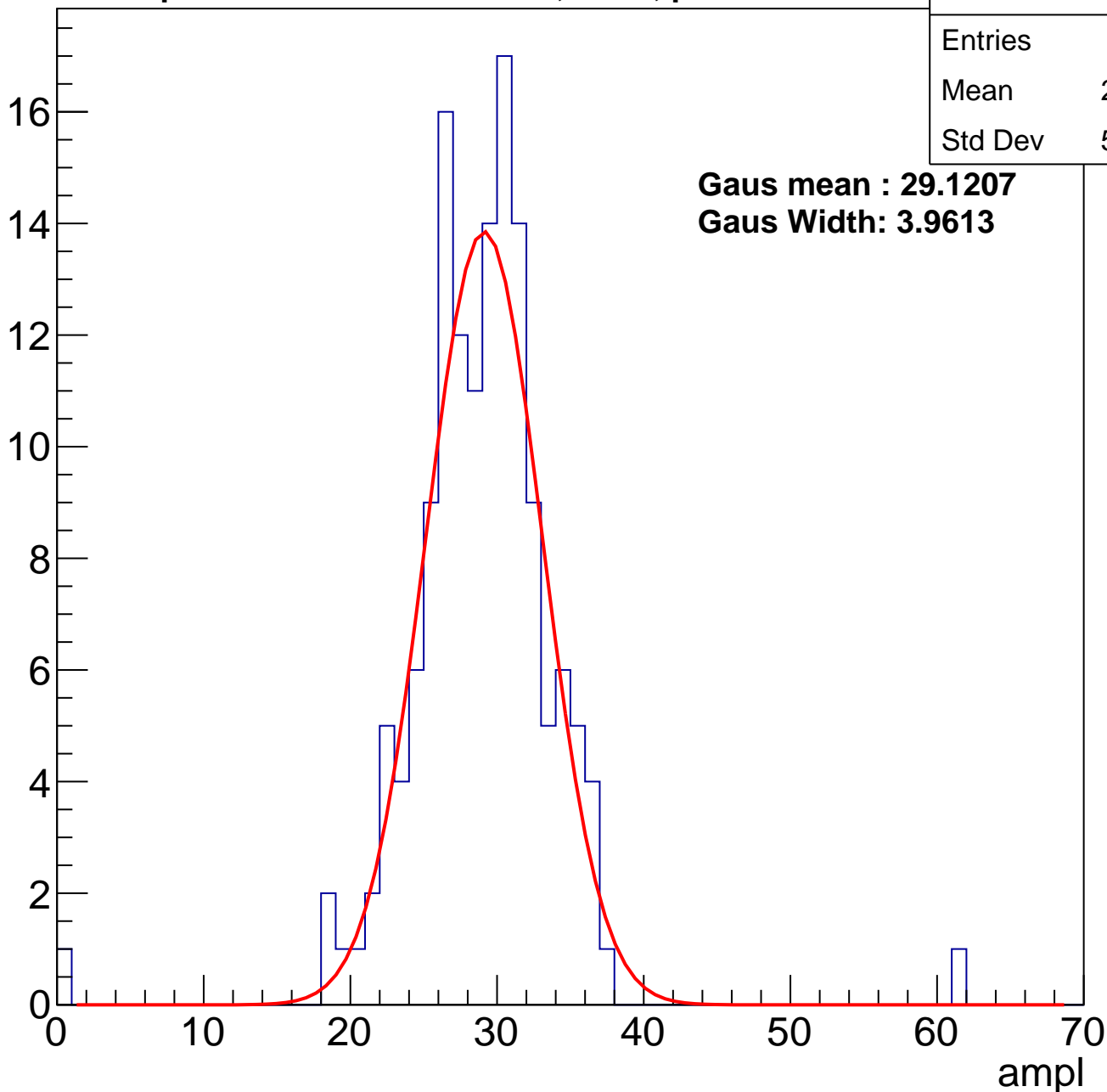
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	28.49
Std Dev	5.292

**Gaus mean : 29.1207**

**Gaus Width: 3.9613**

Entry



# B0L101S, U26-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	35.17
Std Dev	3.715

**Gaus mean : 35.5621**

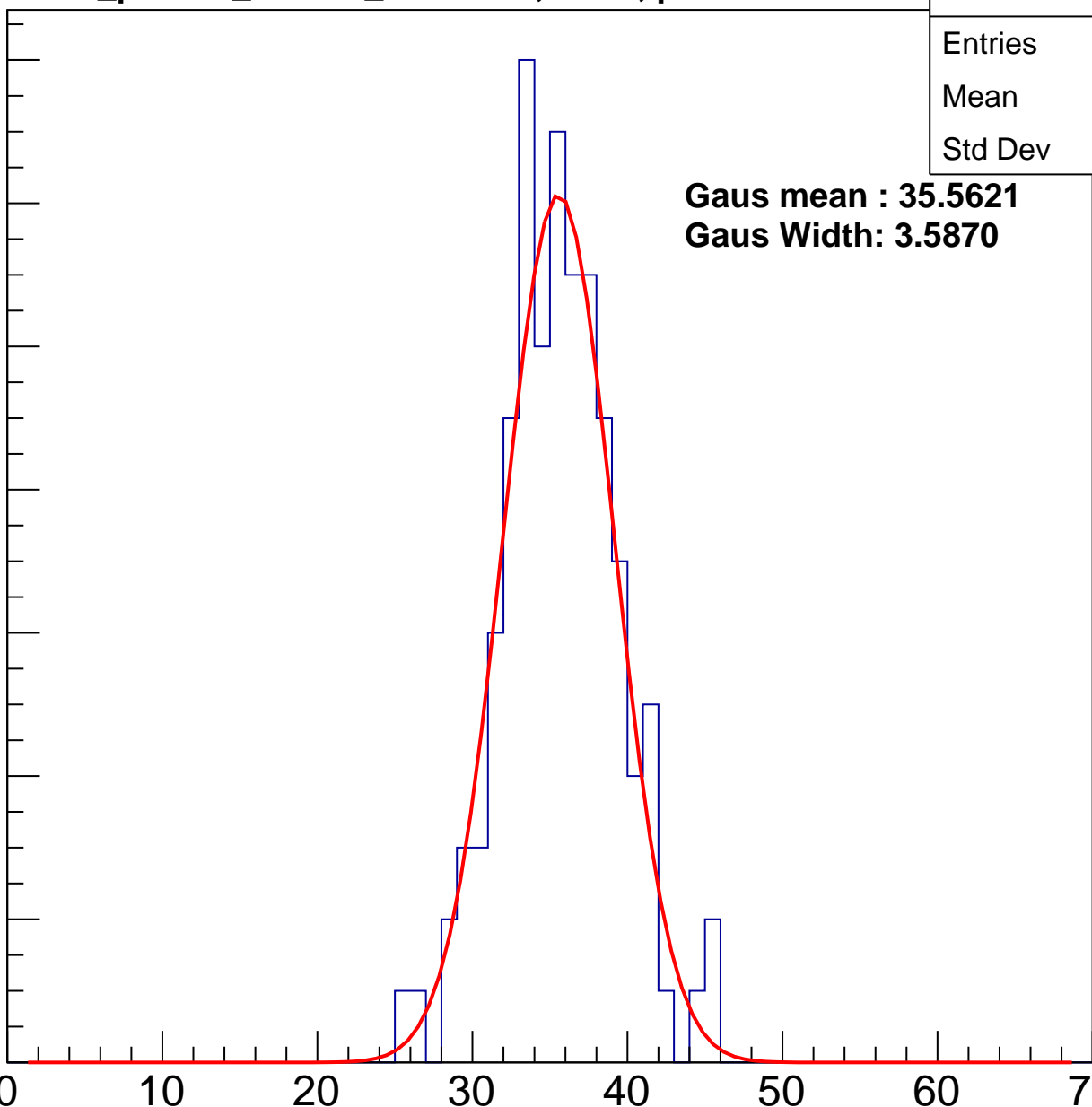
**Gaus Width: 3.5870**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

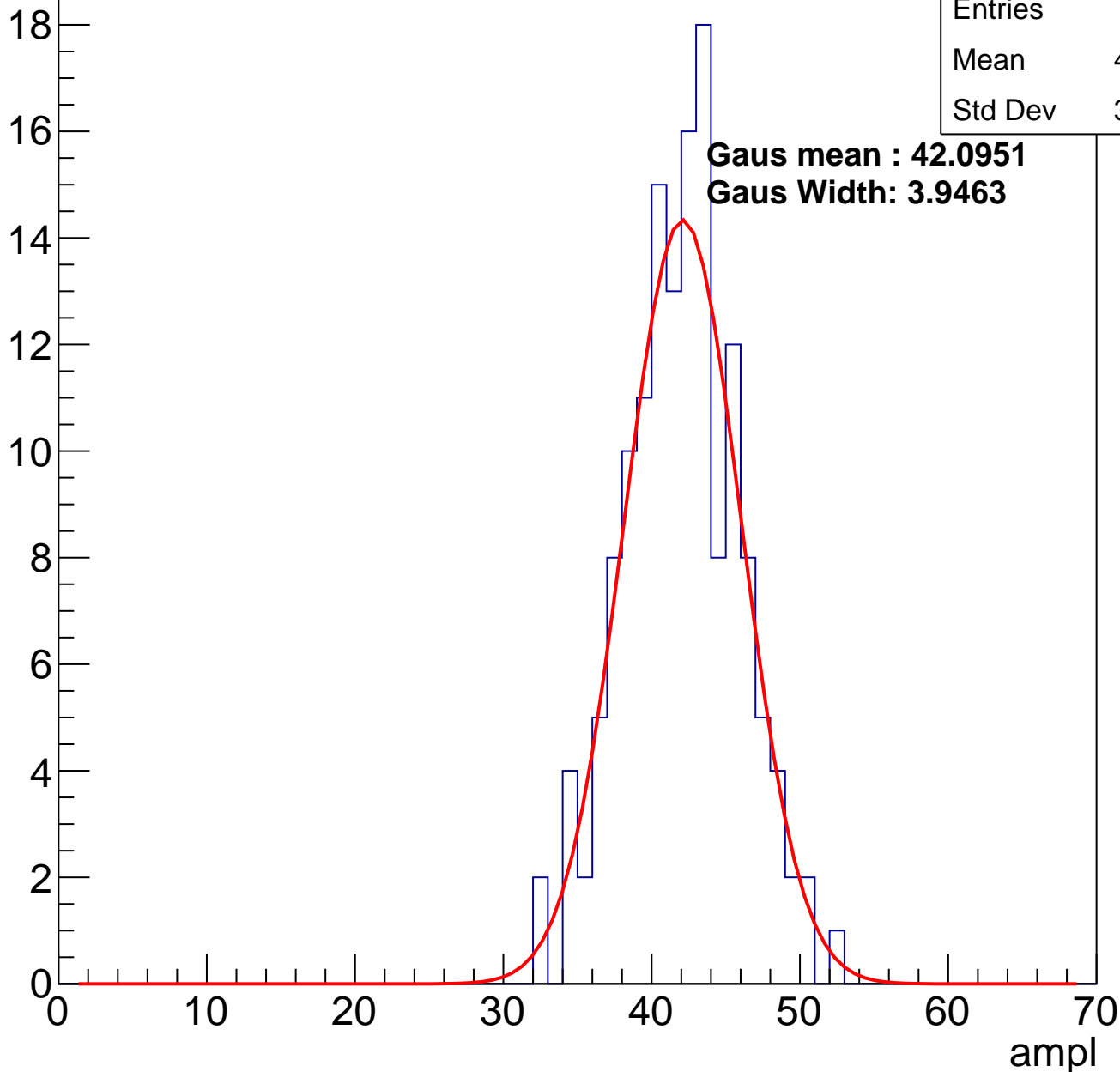
0 10 20 30 40 50 60 70



# B0L101S, U26-ch76, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch76, adc3

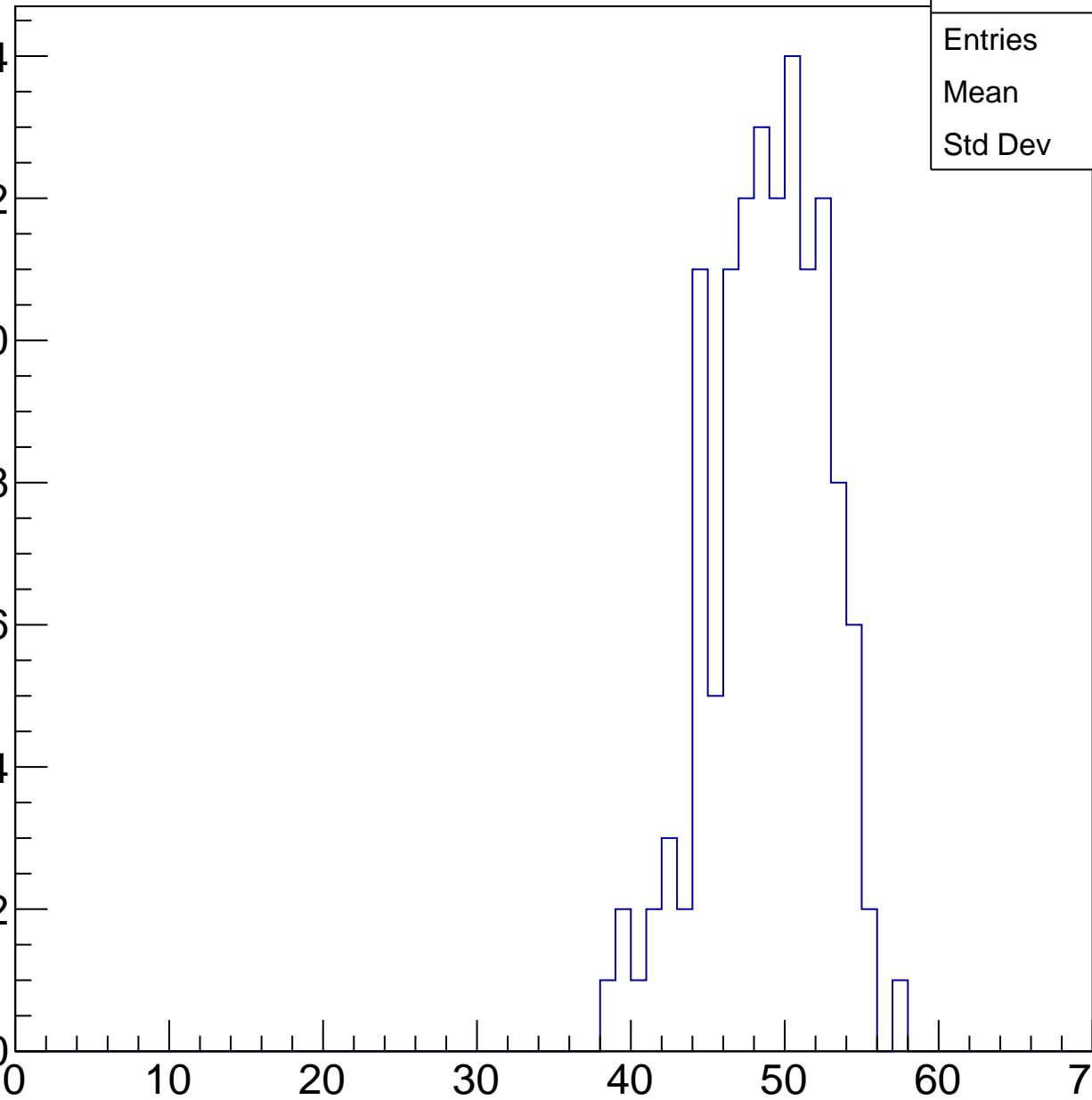
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	129
Mean	48.38
Std Dev	3.73

ampl



# B0L101S, U26-ch76, adc4

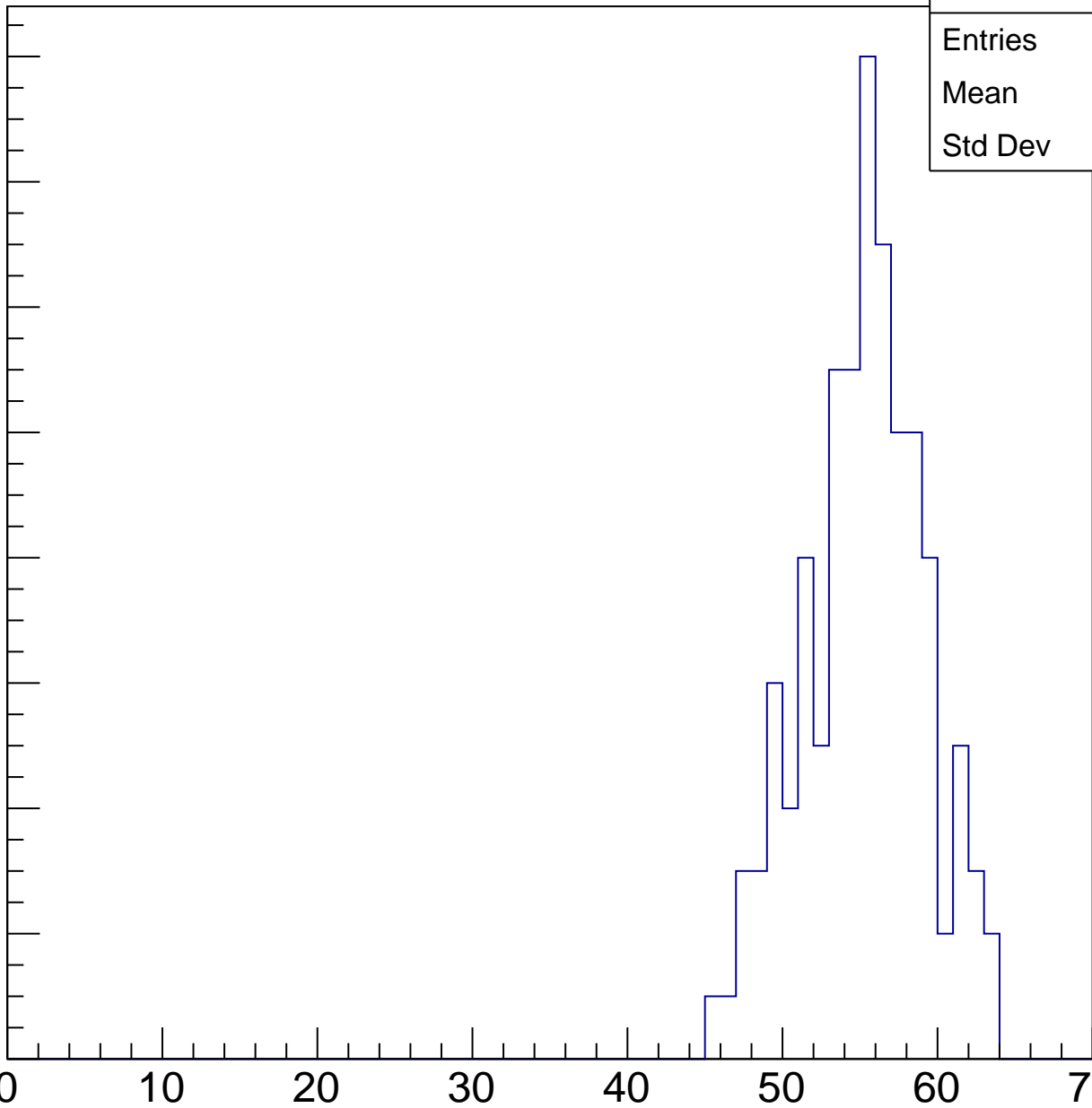
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	54.77
Std Dev	3.877

ampl



# B0L101S, U26-ch76, adc5

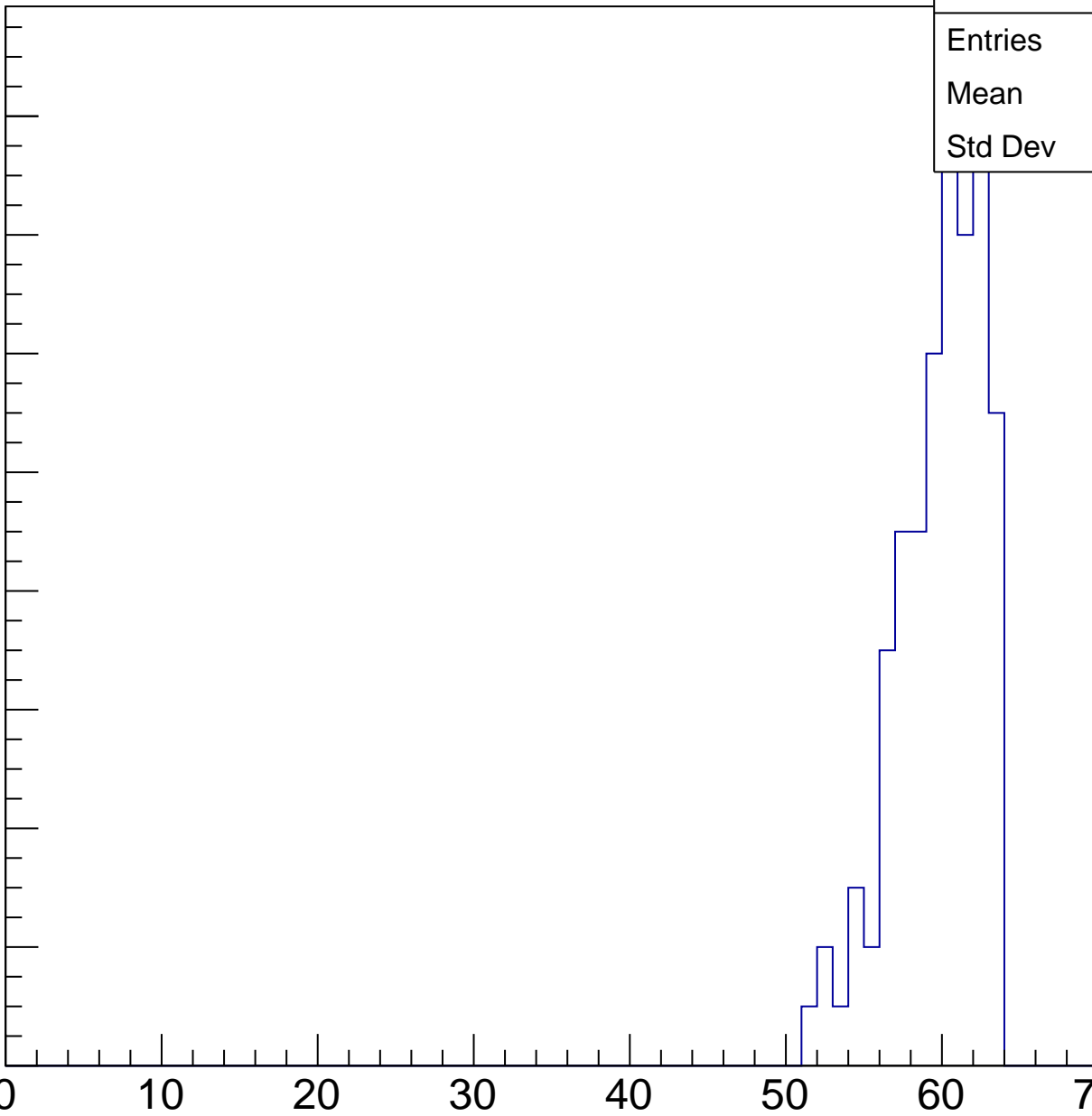
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	104
Mean	59.37
Std Dev	2.76

ampl

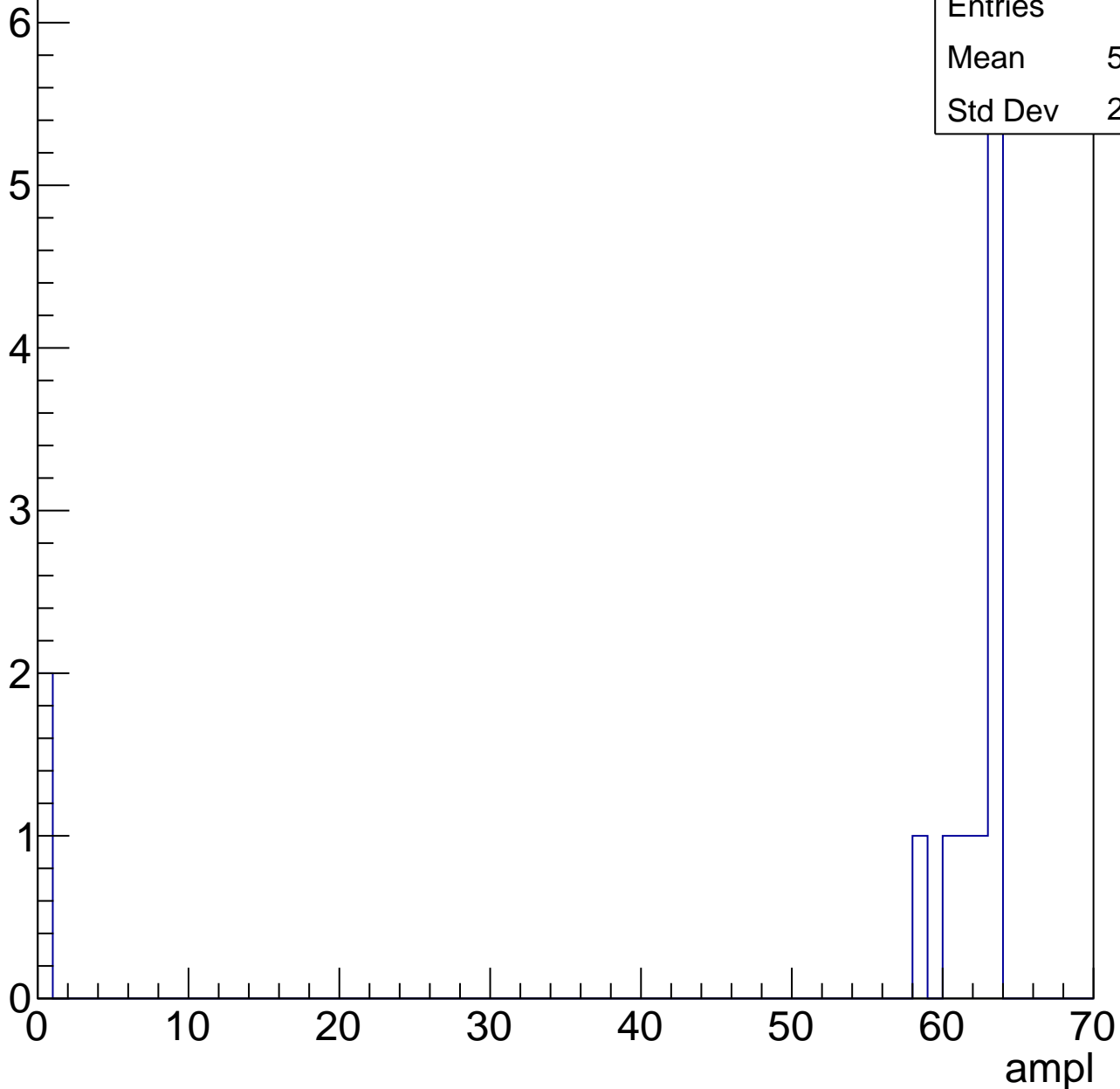


# B0L101S, U26-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	51.58
Std Dev	23.12





# B0L101S, U26-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch77, adc0

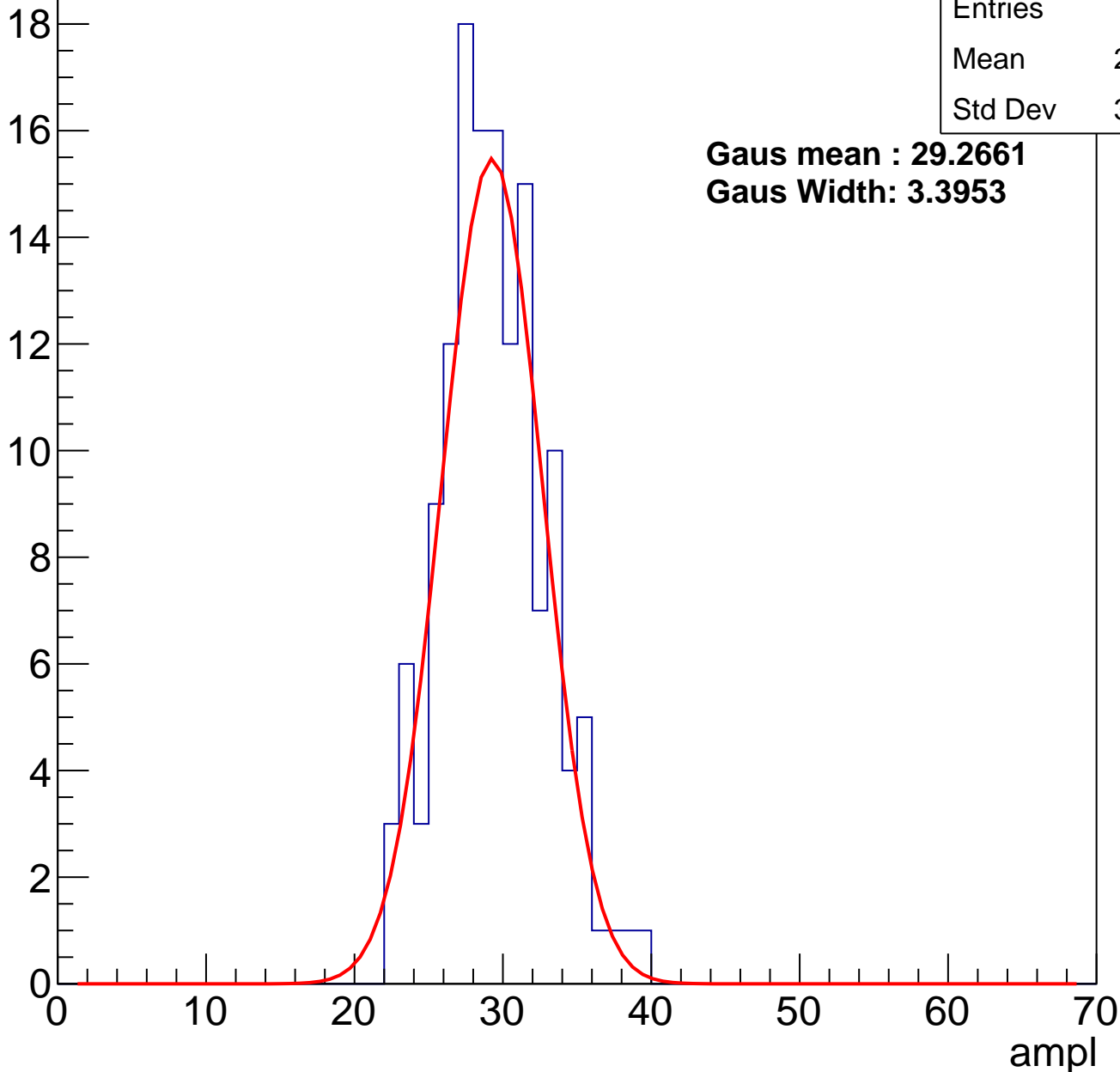
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	28.94
Std Dev	3.429

**Gaus mean : 29.2661**

**Gaus Width: 3.3953**

Entry

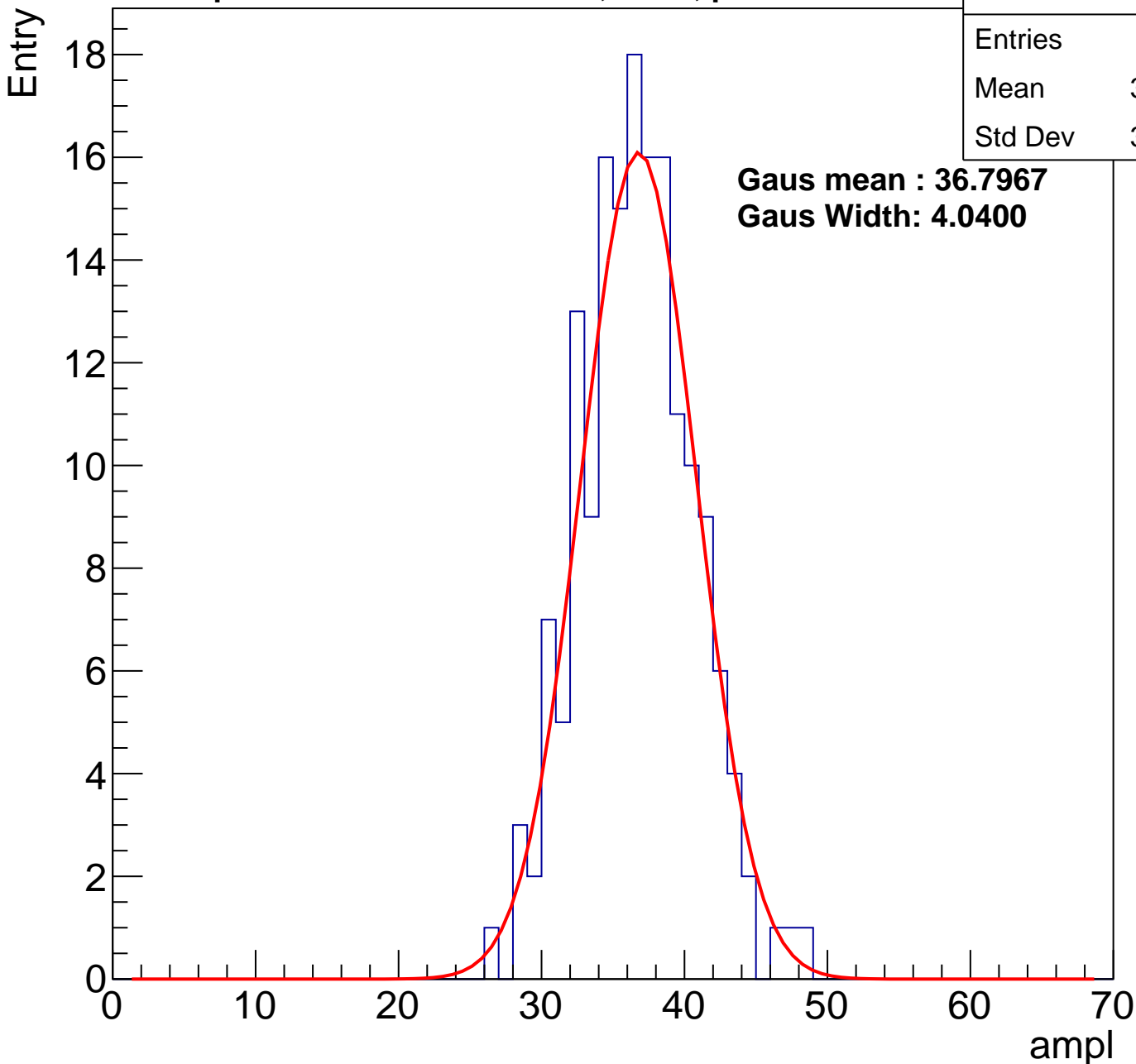


# B0L101S, U26-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	166
Mean	36.23
Std Dev	3.954

**Gaus mean : 36.7967**  
**Gaus Width: 4.0400**



# B0L101S, U26-ch77, adc2

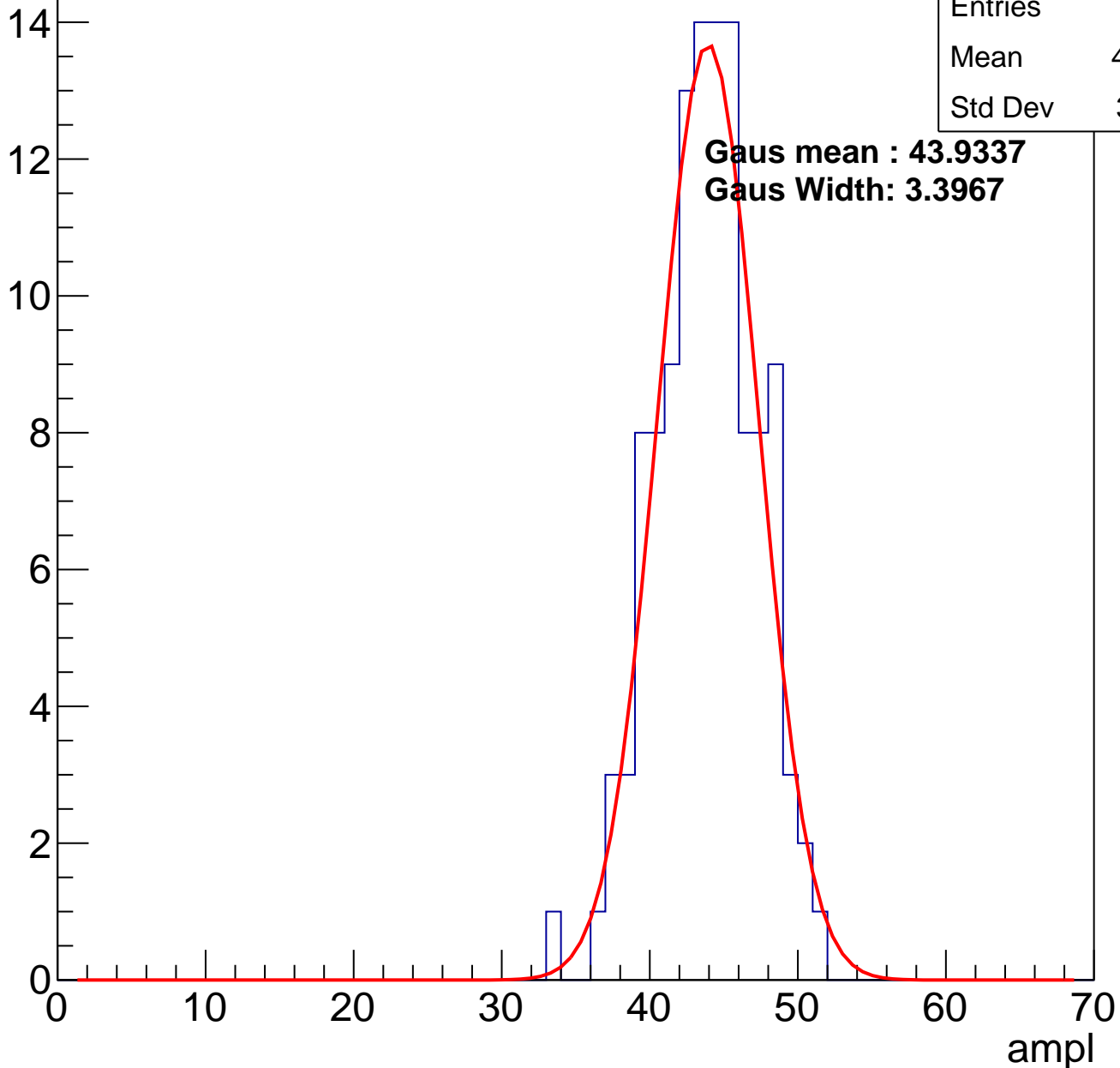
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	119
Mean	43.39
Std Dev	3.341

**Gaus mean : 43.9337**

**Gaus Width: 3.3967**

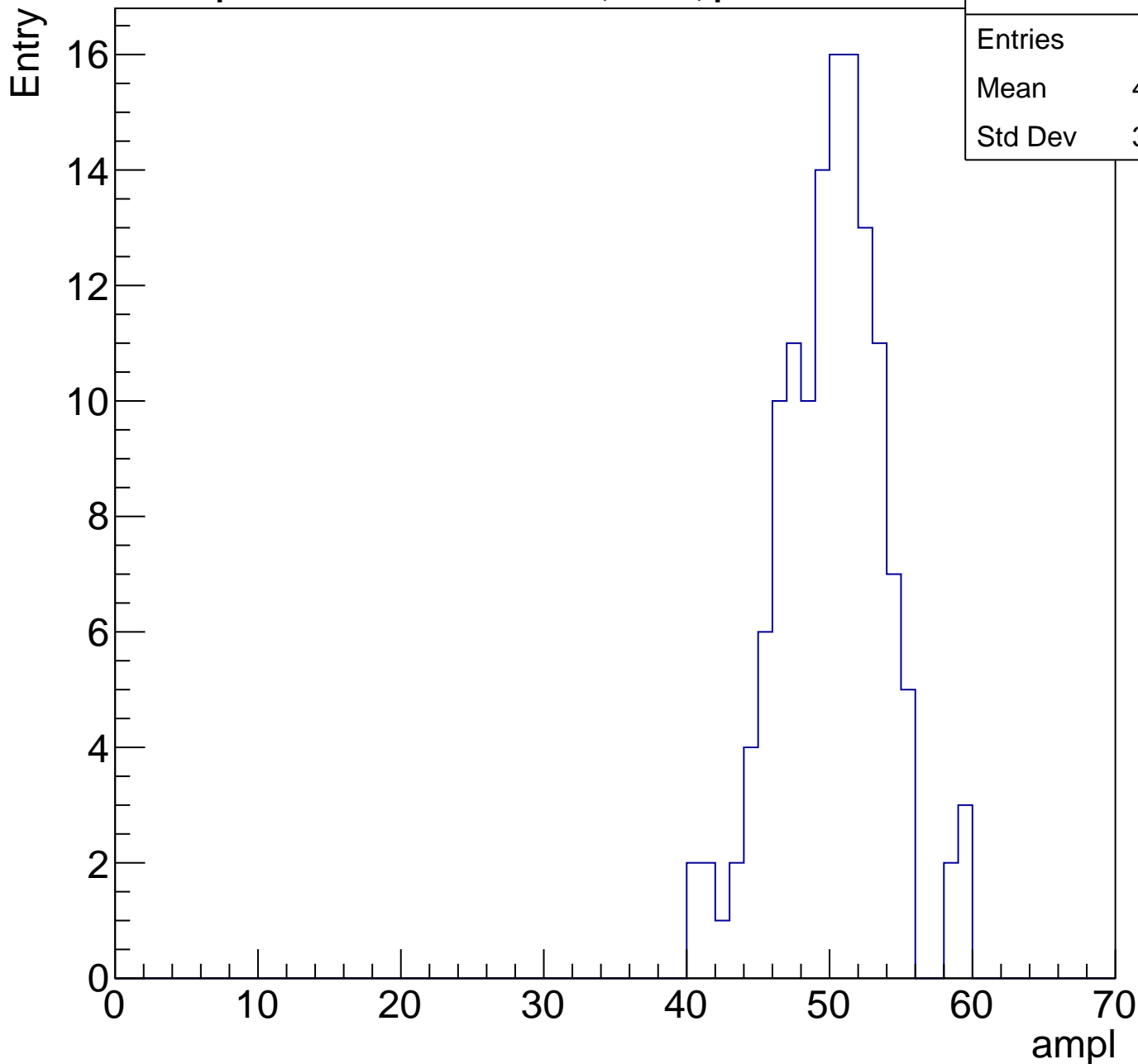
Entry



# B0L101S, U26-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	49.63
Std Dev	3.739

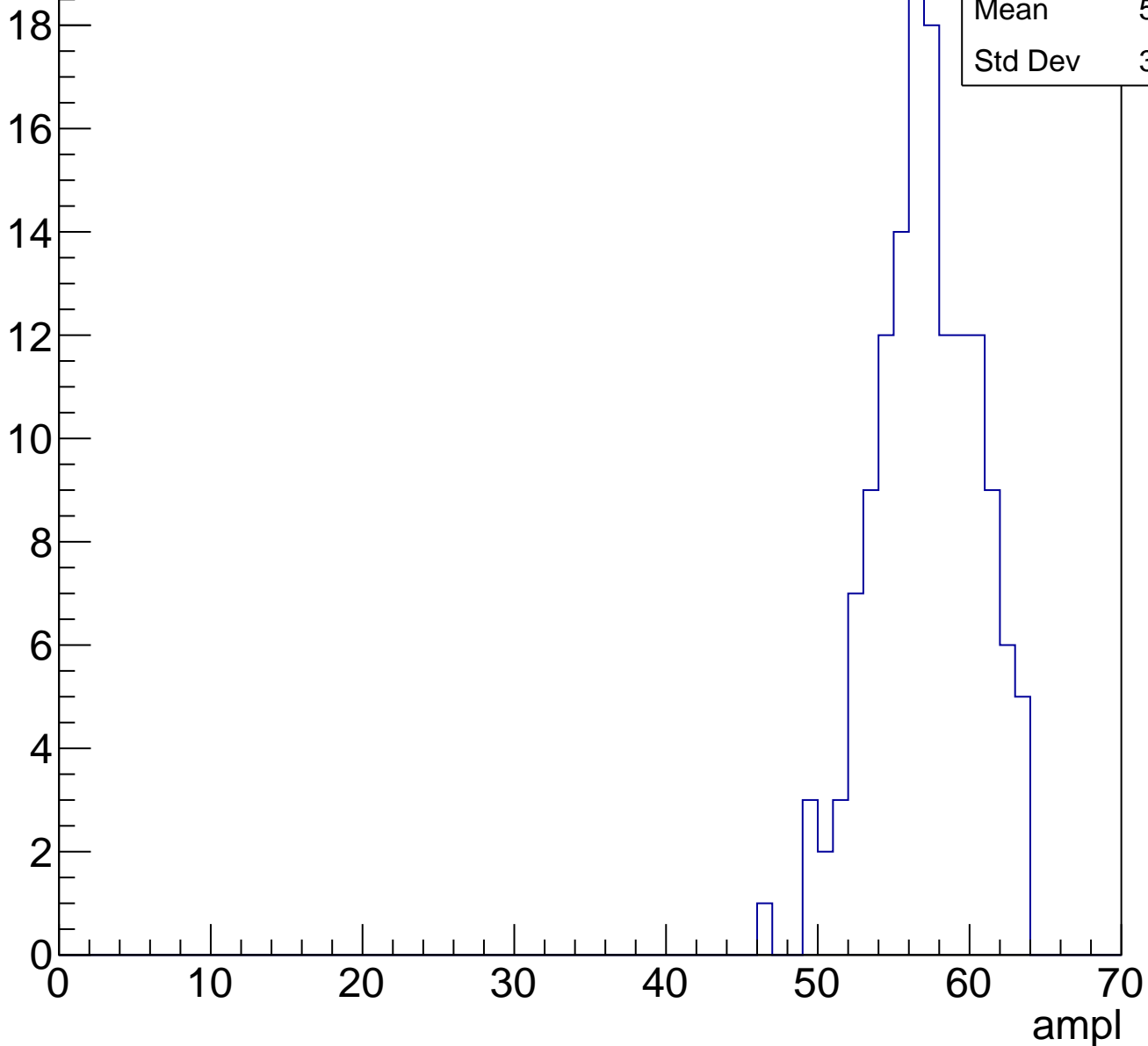


# B0L101S, U26-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	56.63
Std Dev	3.378

Entry



# B0L101S, U26-ch77, adc5

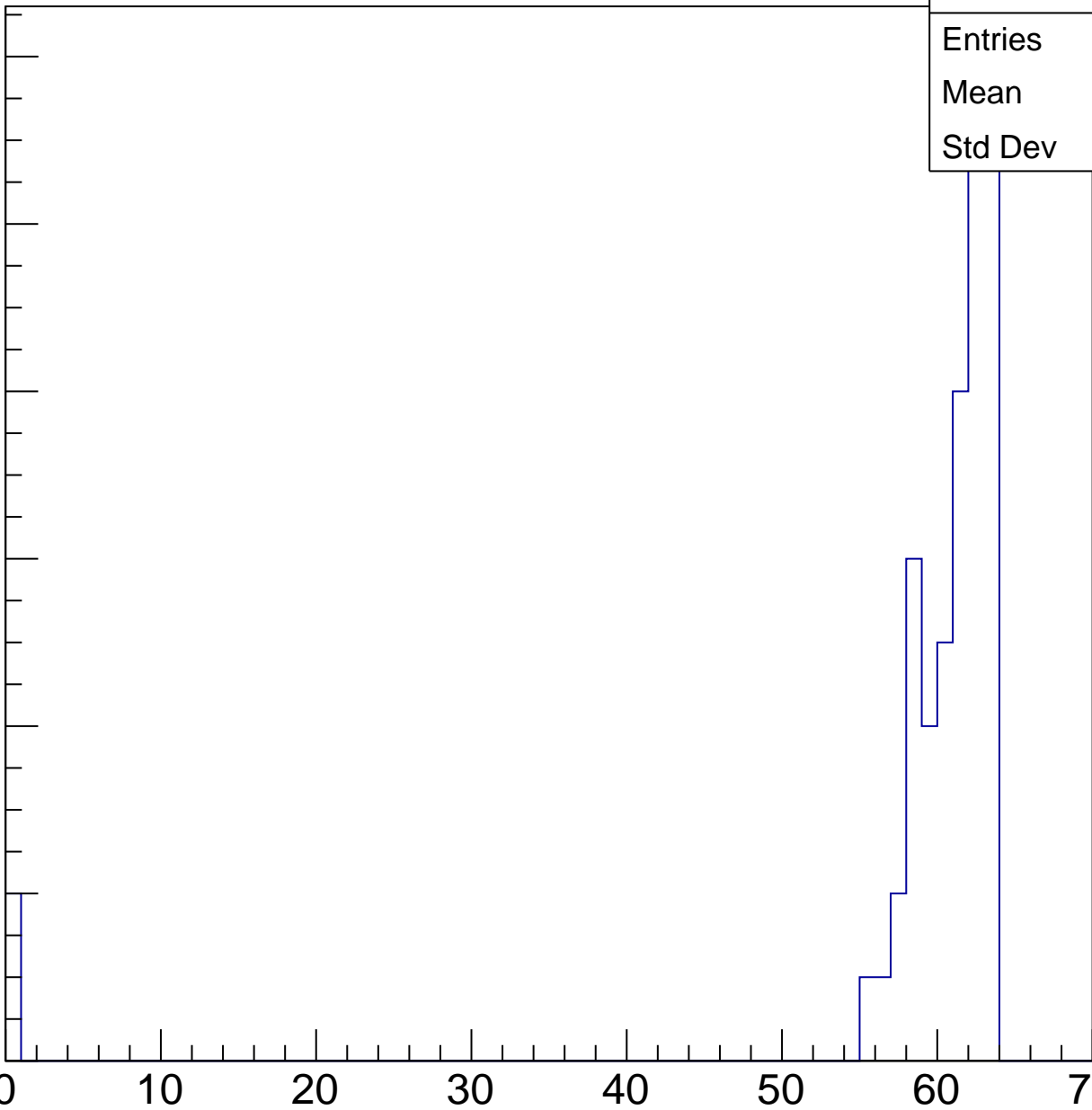
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	53
Mean	58.43
Std Dev	11.75

ampl



# B0L101S, U26-ch77, adc6

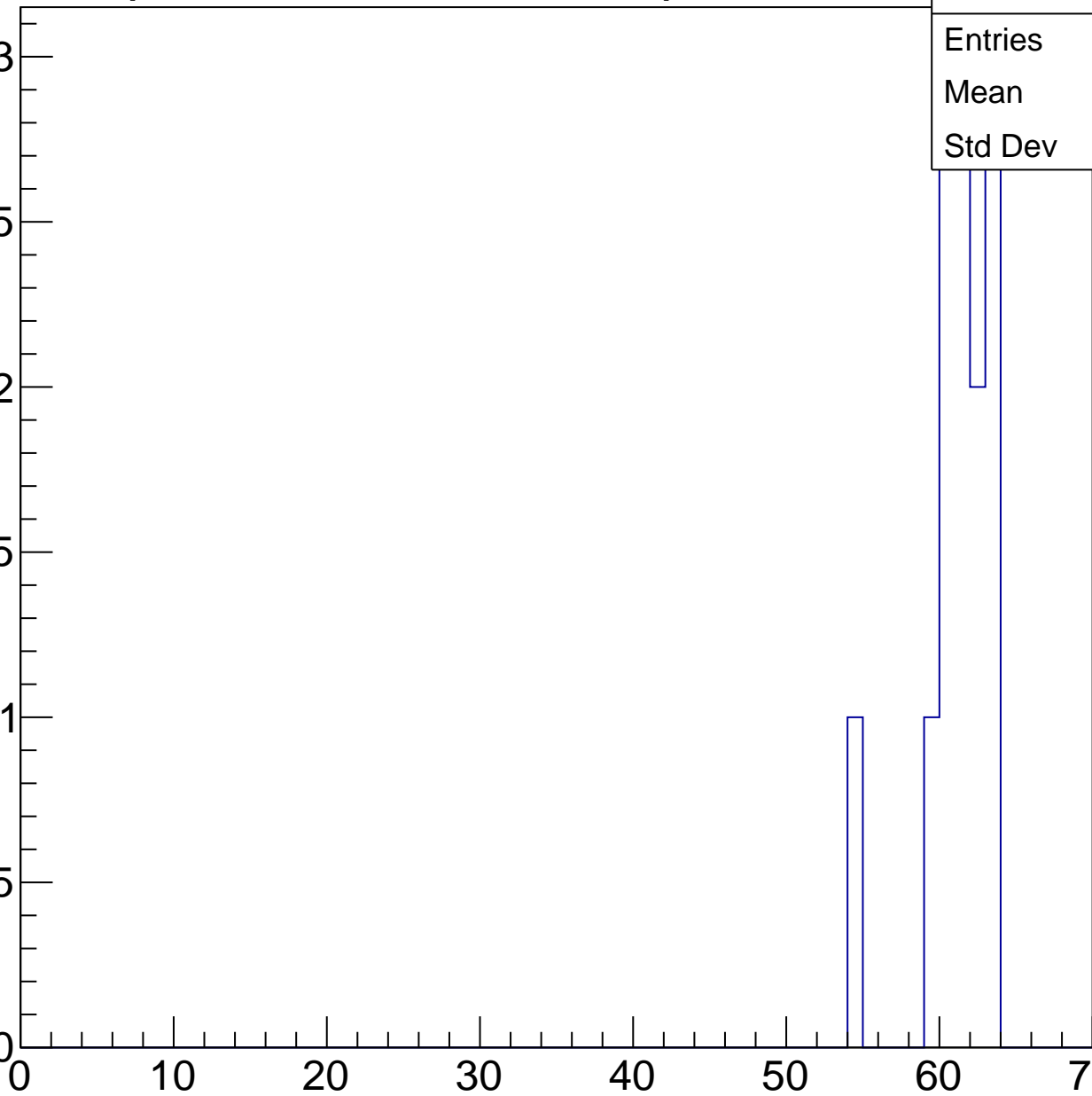
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	13
Mean	60.69
Std Dev	2.3

ampl





# B0L101S, U26-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

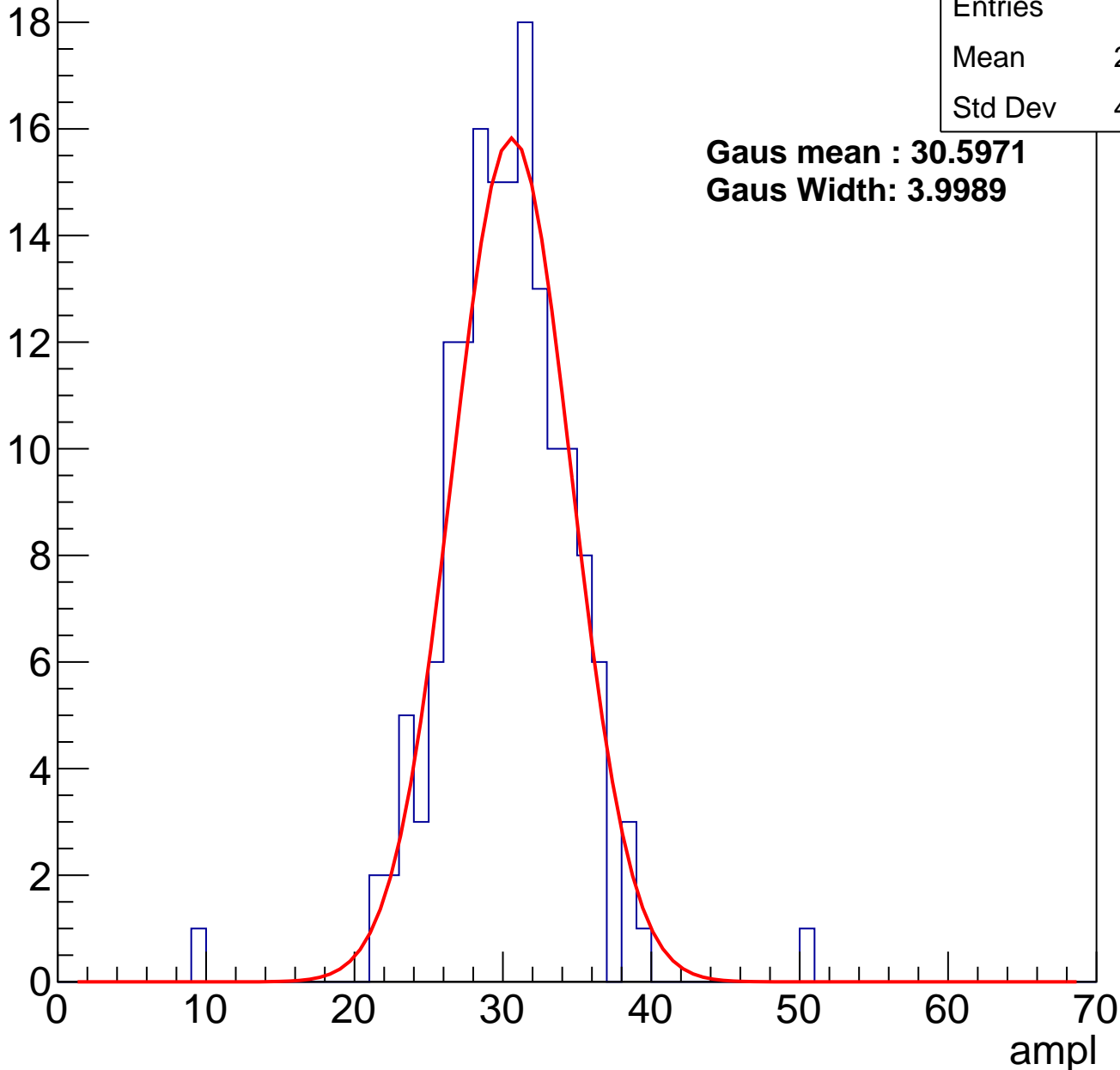
Entry



# B0L101S, U26-ch78, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	37.74
Std Dev	3.802

**Gaus mean : 38.2093**

**Gaus Width: 3.6496**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch78, adc2

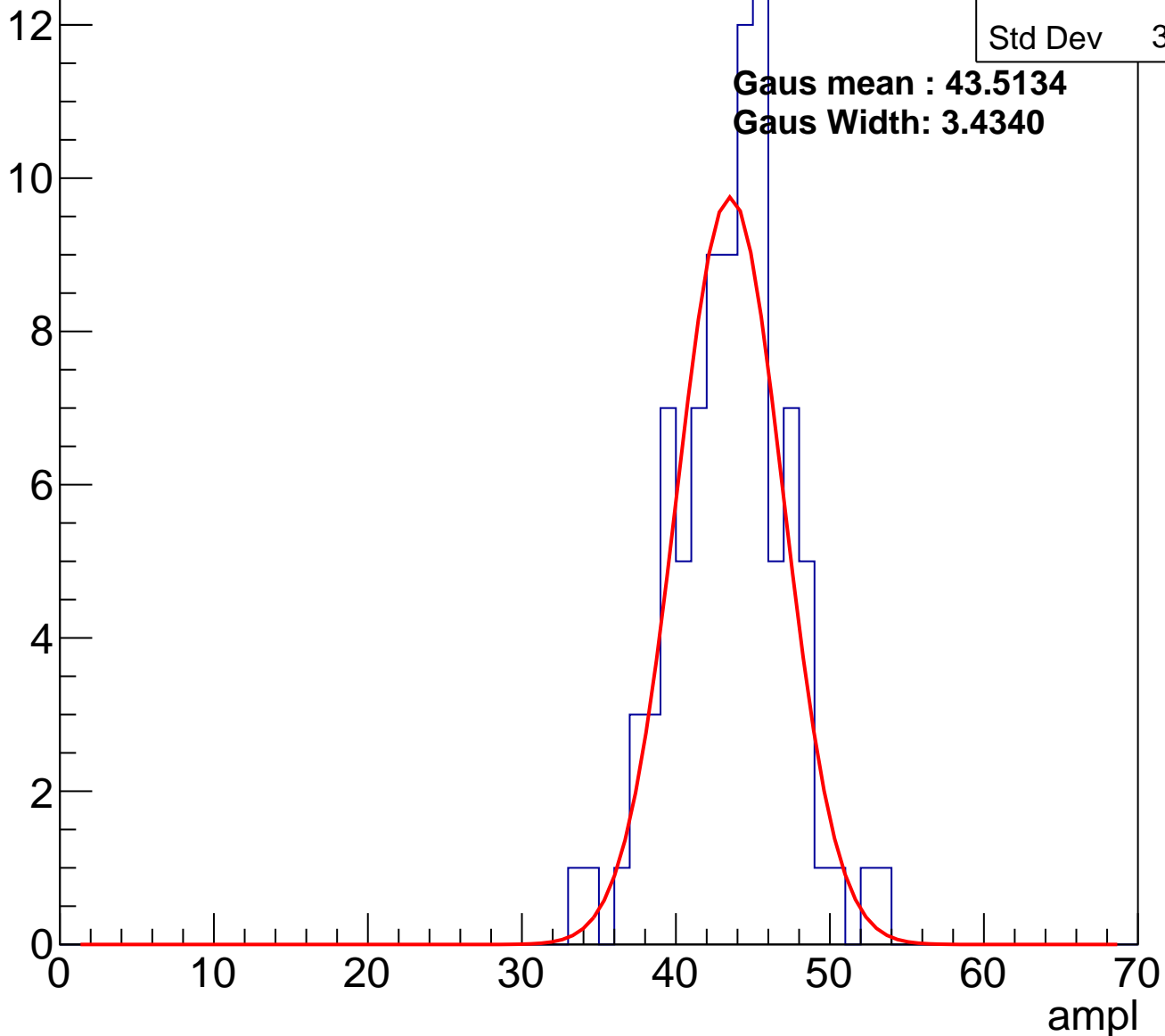
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	92
Mean	43.14
Std Dev	3.637

**Gaus mean : 43.5134**

**Gaus Width: 3.4340**

Entry

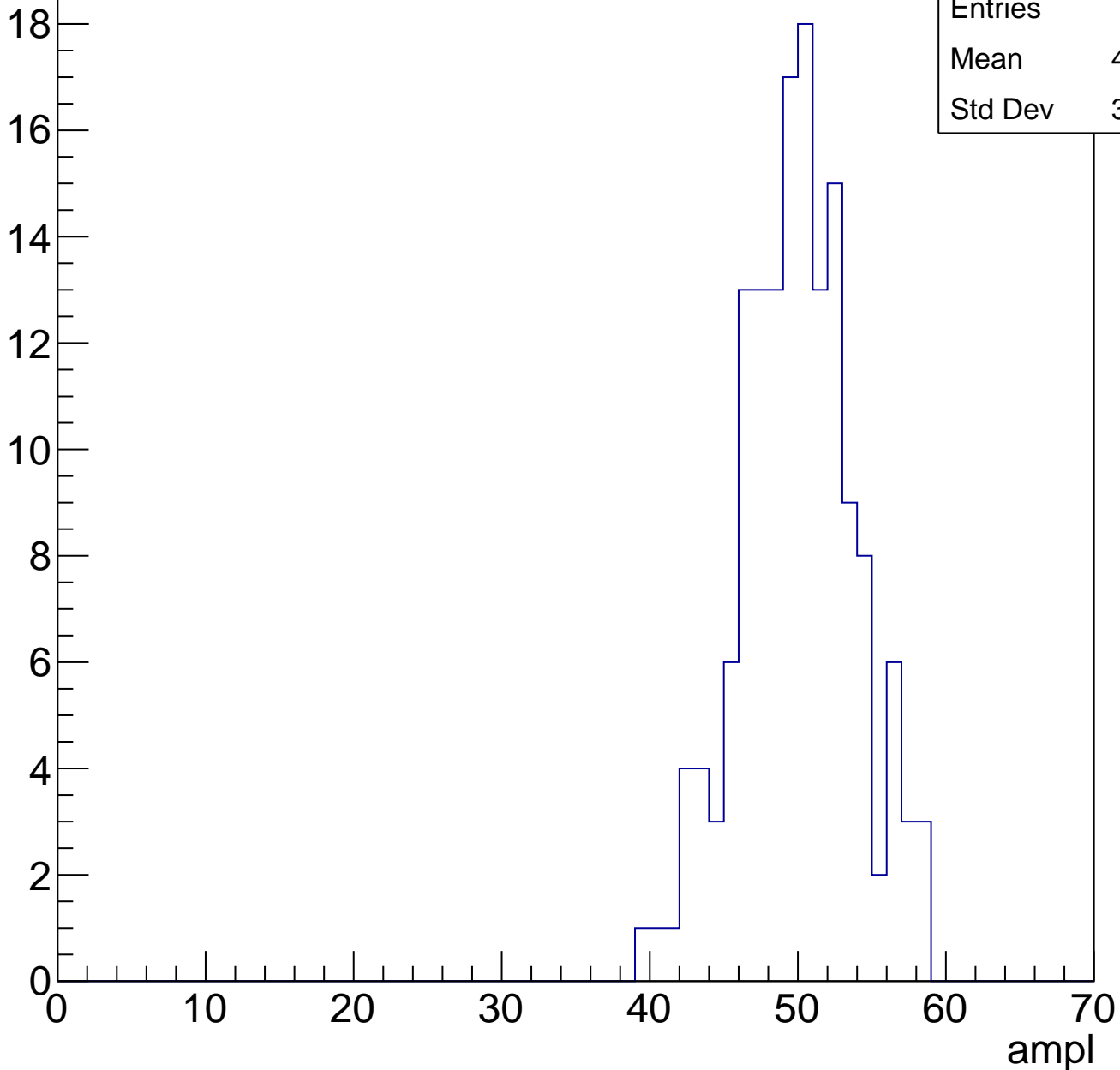


# B0L101S, U26-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	49.48
Std Dev	3.837

Entry



# B0L101S, U26-ch78, adc4

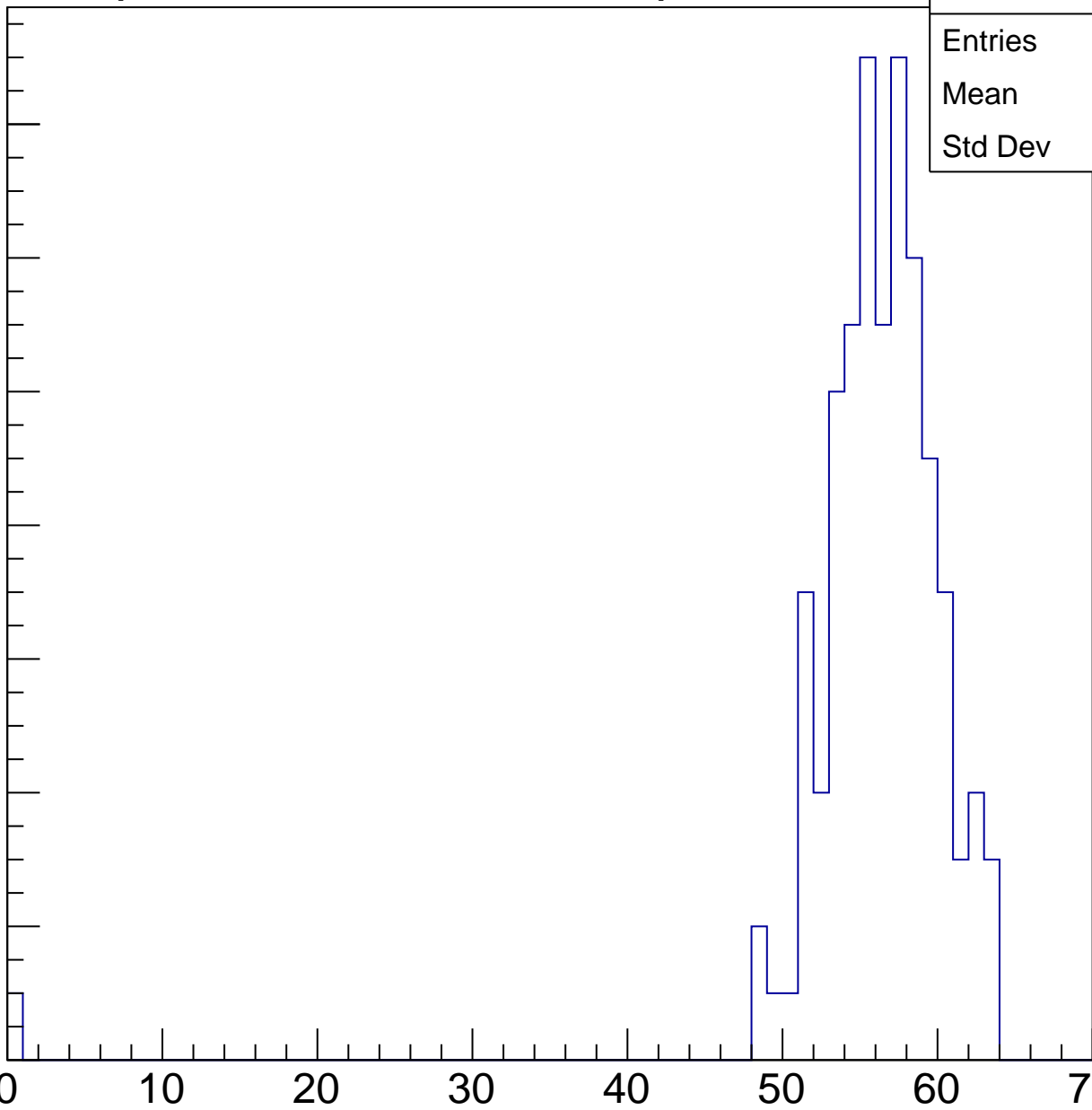
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	116
Mean	55.58
Std Dev	6.115

ampl



# B0L101S, U26-ch78, adc5

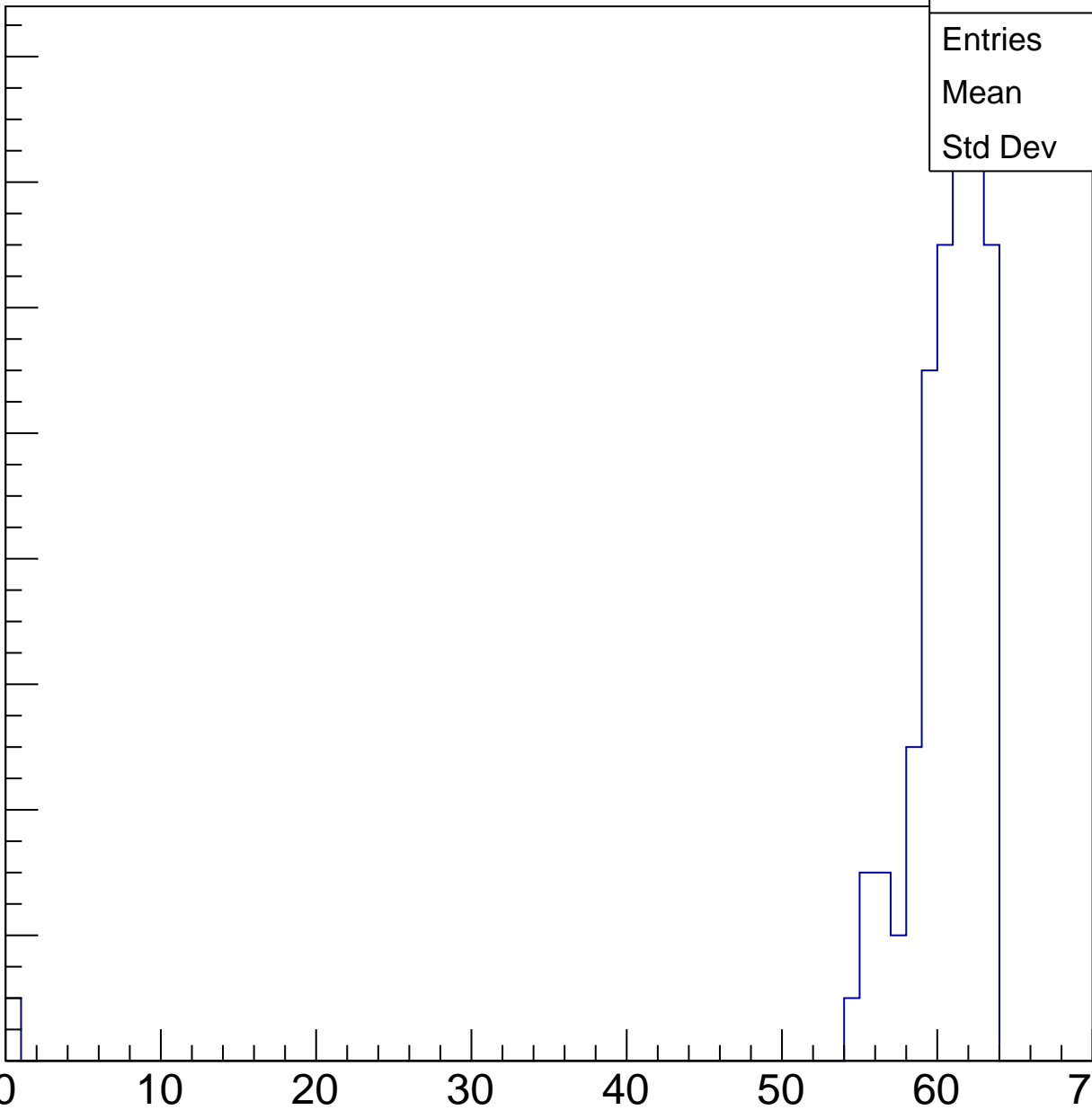
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	83
Mean	59.58
Std Dev	6.93

ampl



# B0L101S, U26-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

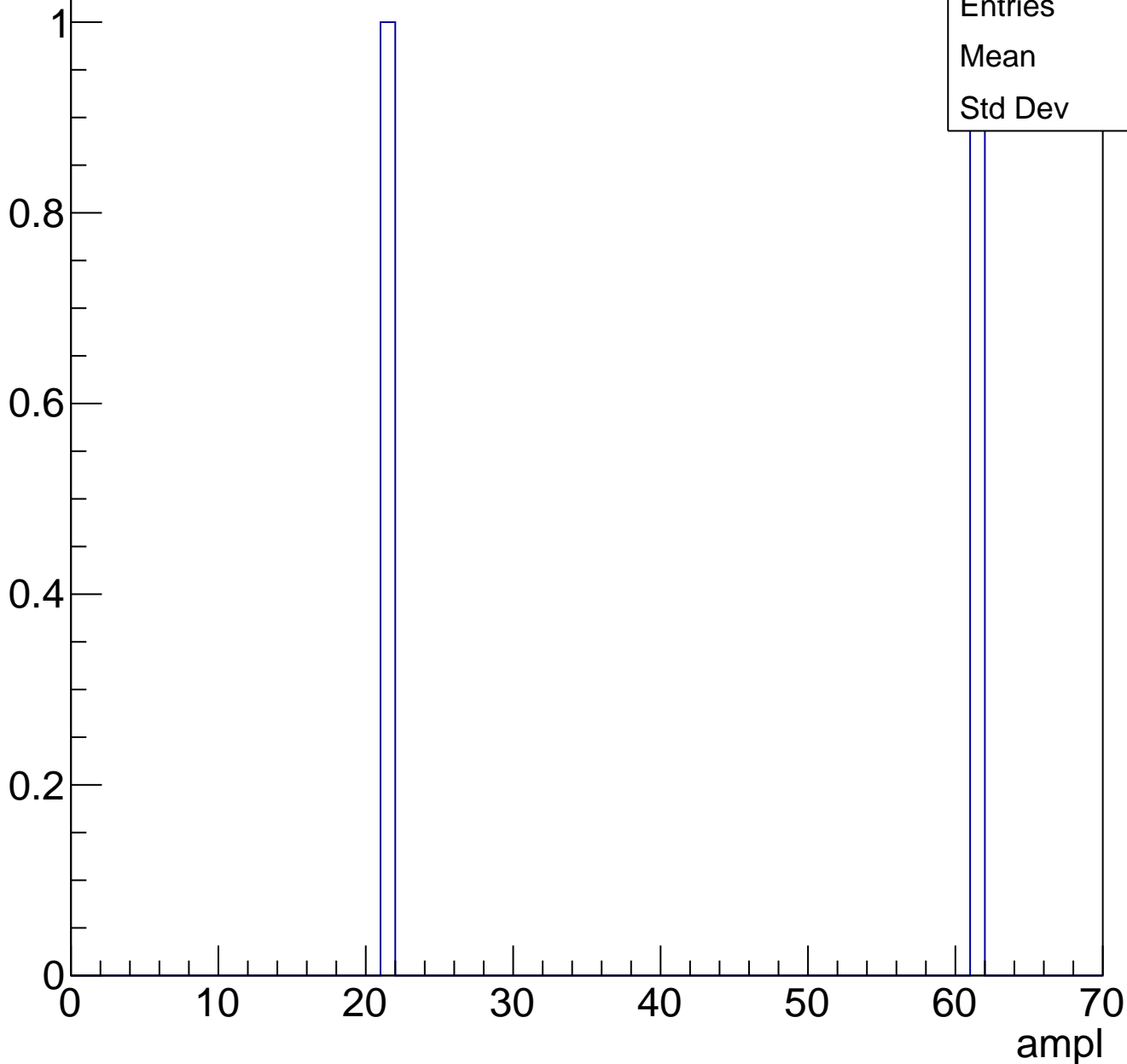




# B0L101S, U26-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch79, adc0

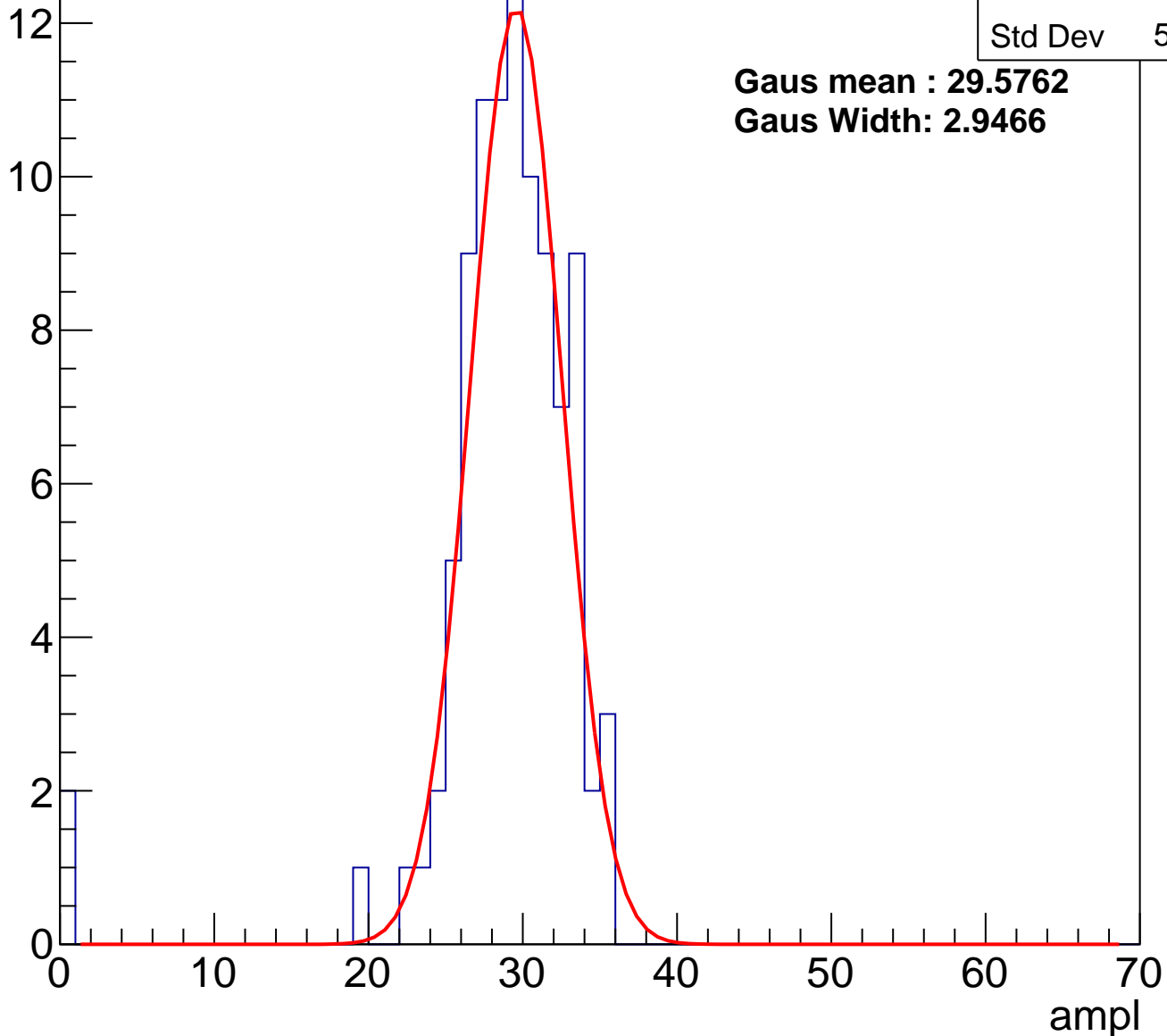
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	96
Mean	28.4
Std Dev	5.112

**Gaus mean : 29.5762**

**Gaus Width: 2.9466**

Entry



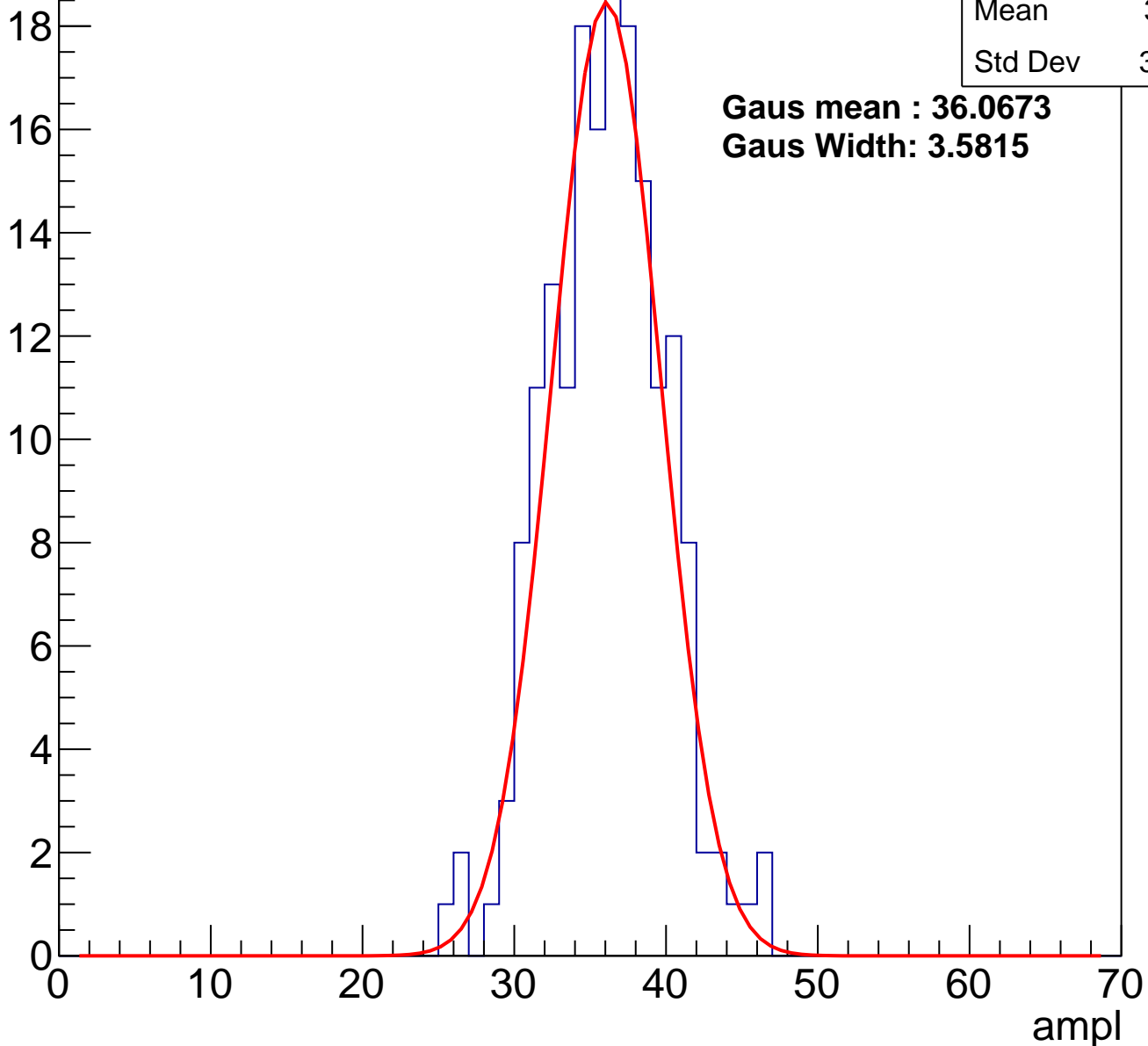
# B0L101S, U26-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	35.61
Std Dev	3.815

**Gaus mean : 36.0673**  
**Gaus Width: 3.5815**

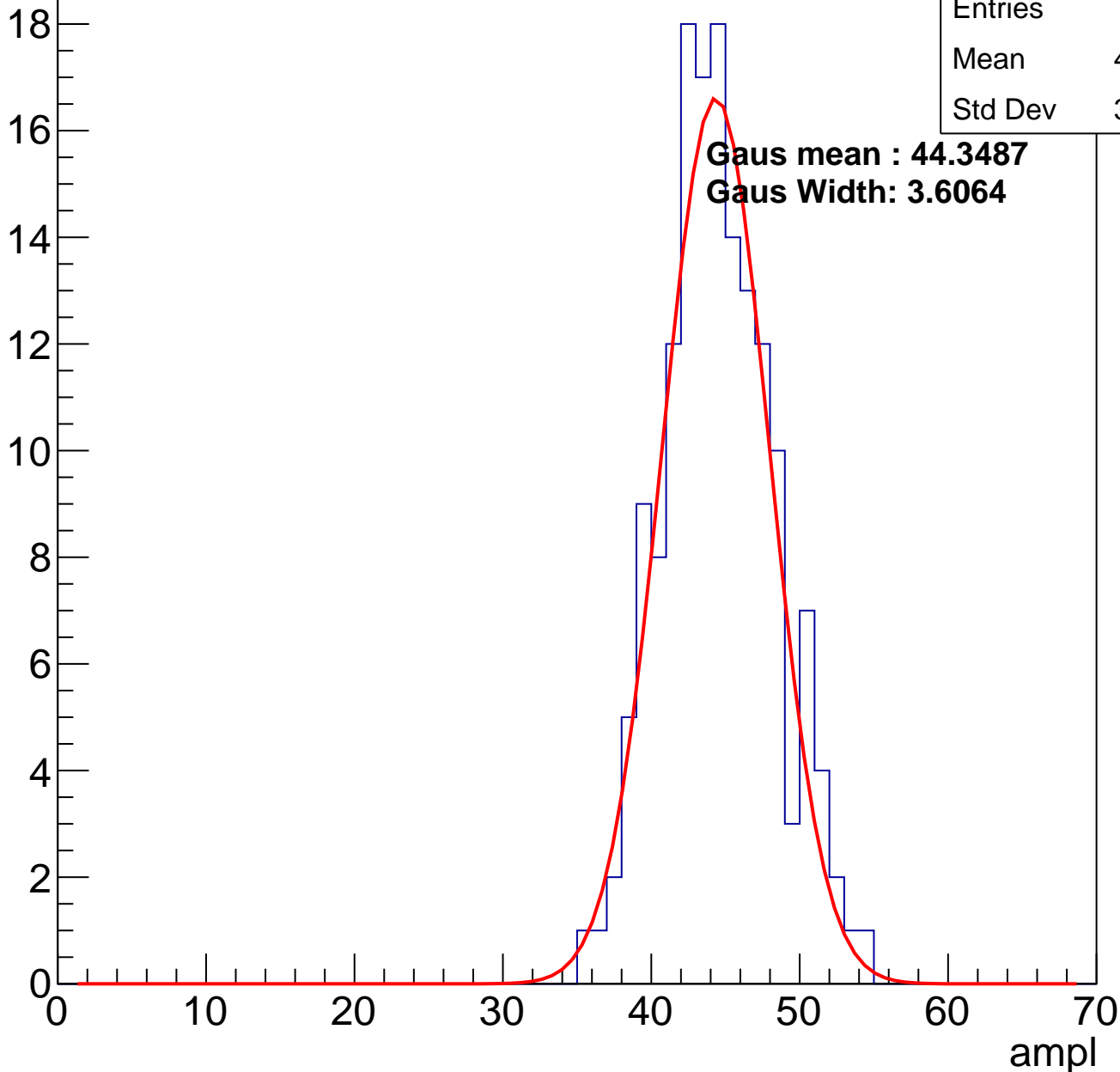
Entry



# B0L101S, U26-ch79, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

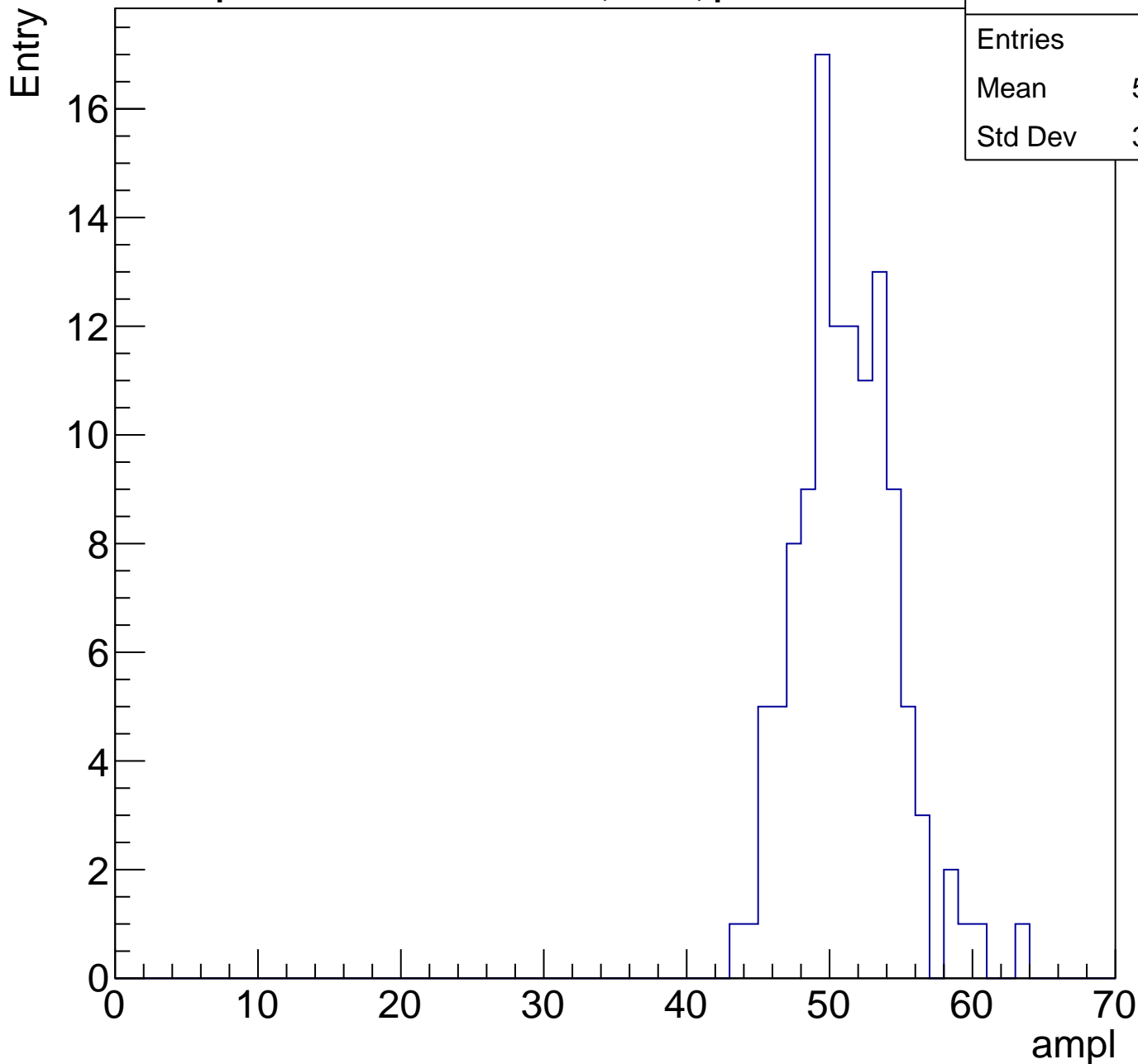
Entry



# B0L101S, U26-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	50.72
Std Dev	3.454

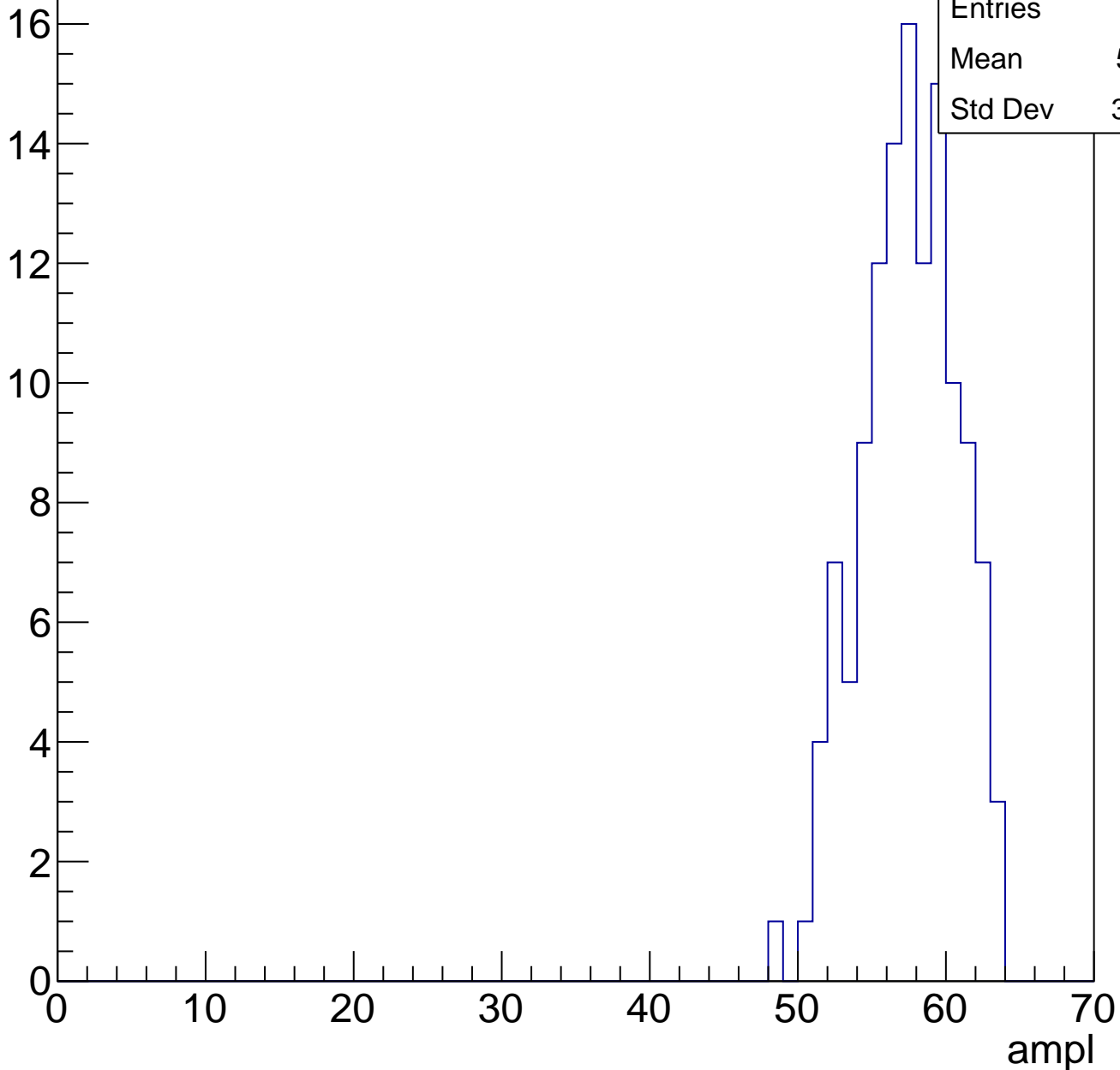


# B0L101S, U26-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	57.01
Std Dev	3.176

Entry



# B0L101S, U26-ch79, adc5

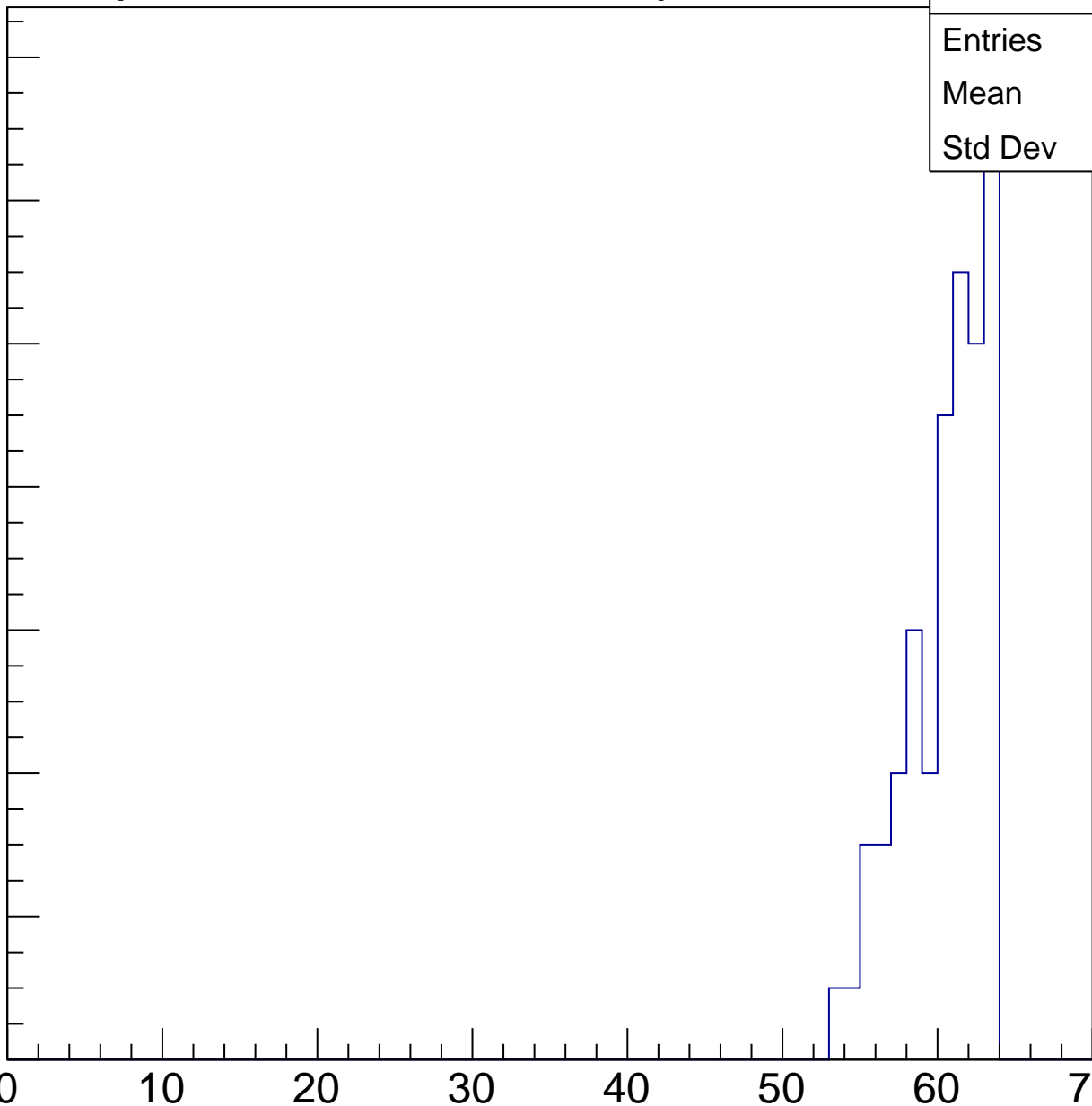
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	66
Mean	60.08
Std Dev	2.607

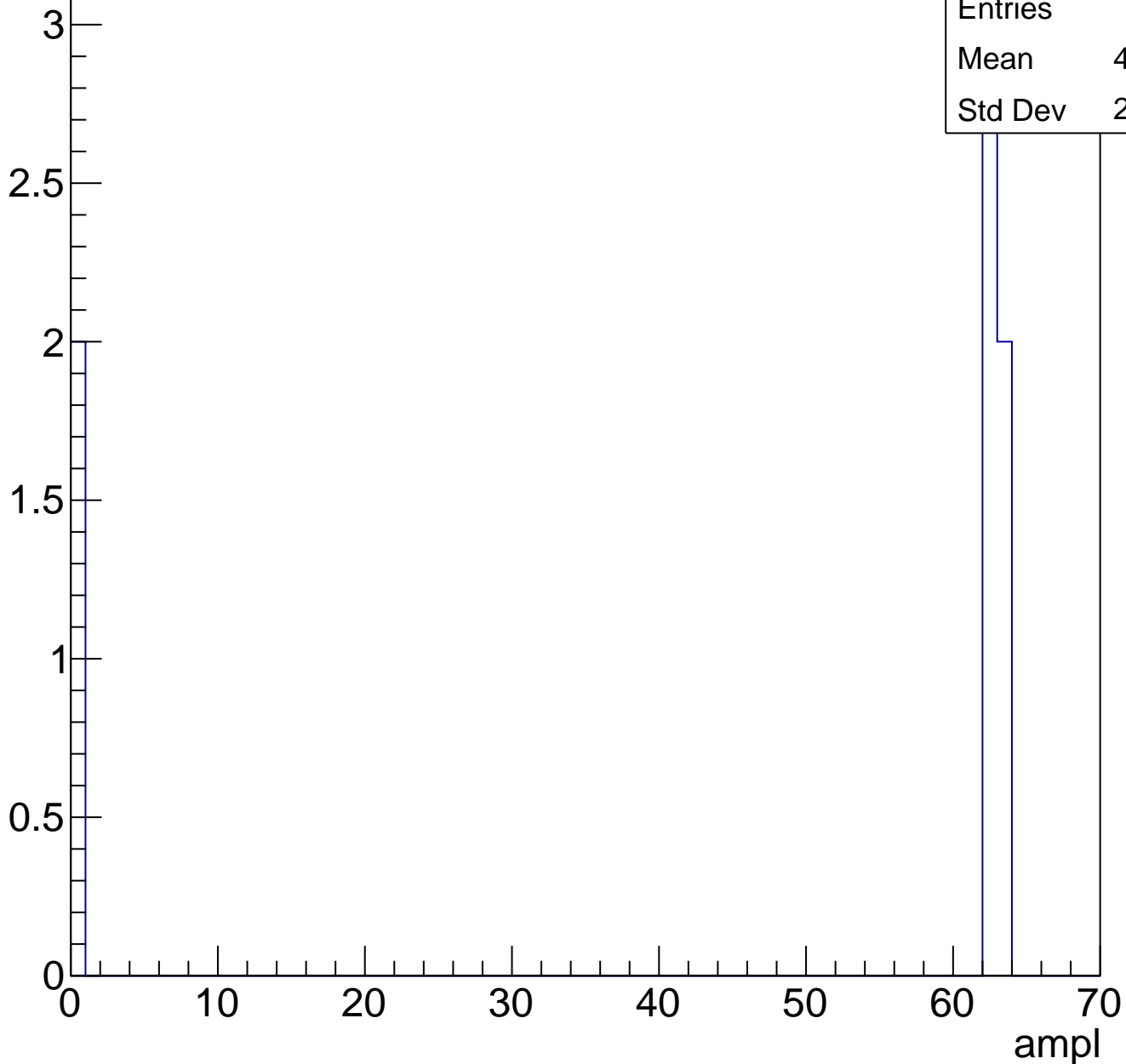
ampl



# B0L101S, U26-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	27.47
Std Dev	4.516

**Gaus mean : 28.1983**

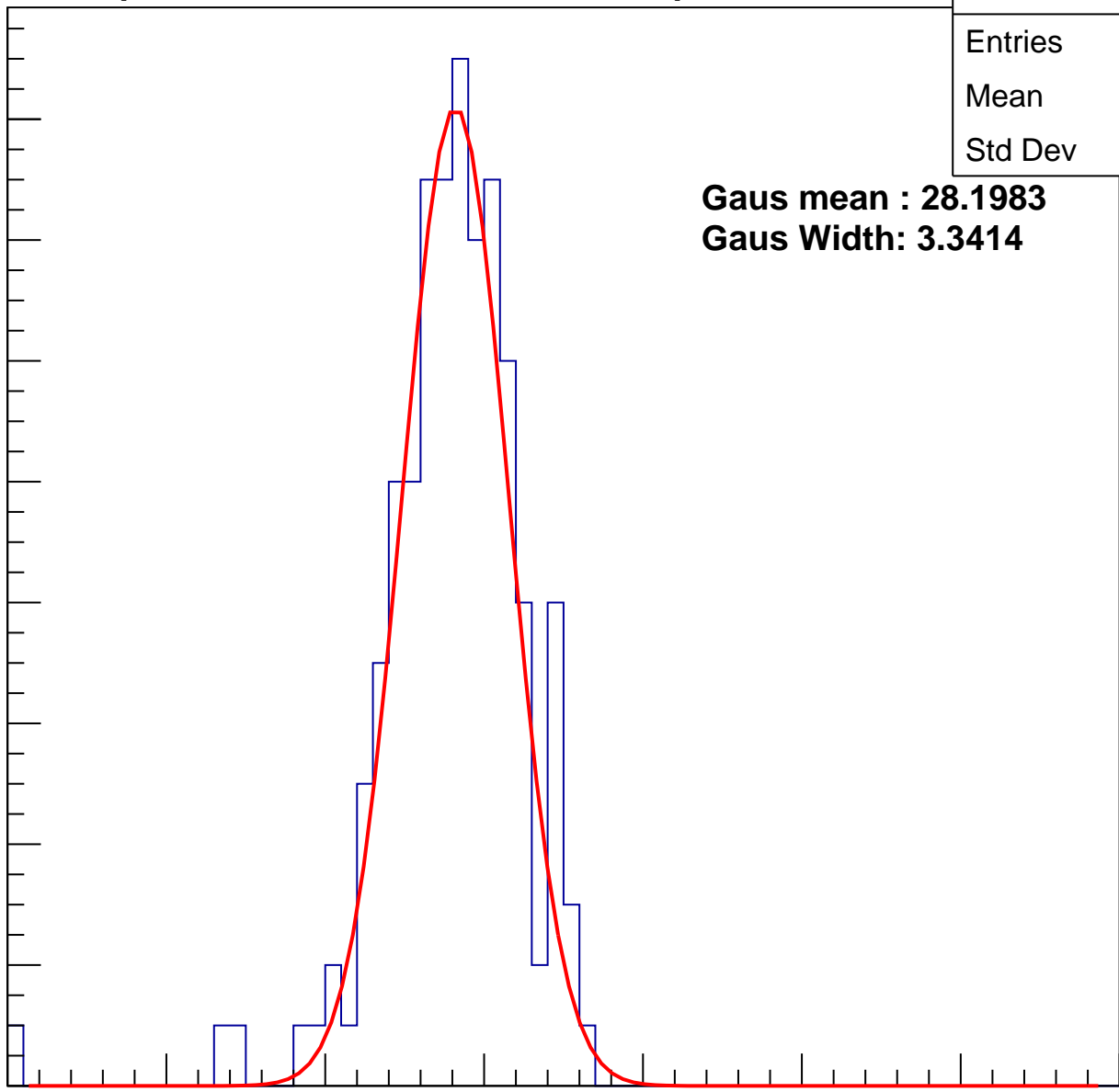
**Gaus Width: 3.3414**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch80, adc1

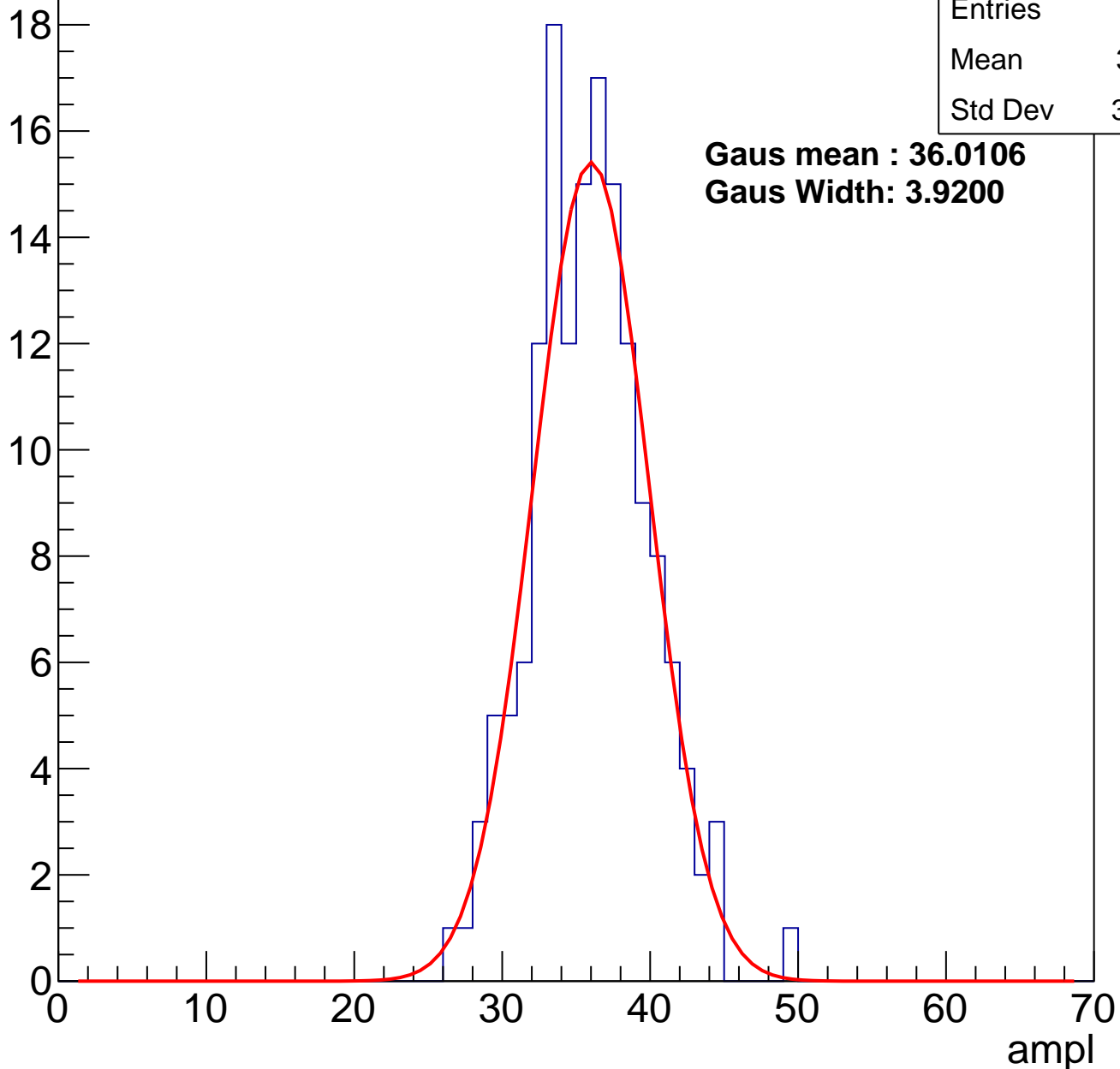
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	35.51
Std Dev	3.896

**Gaus mean : 36.0106**

**Gaus Width: 3.9200**

Entry



# B0L101S, U26-ch80, adc2

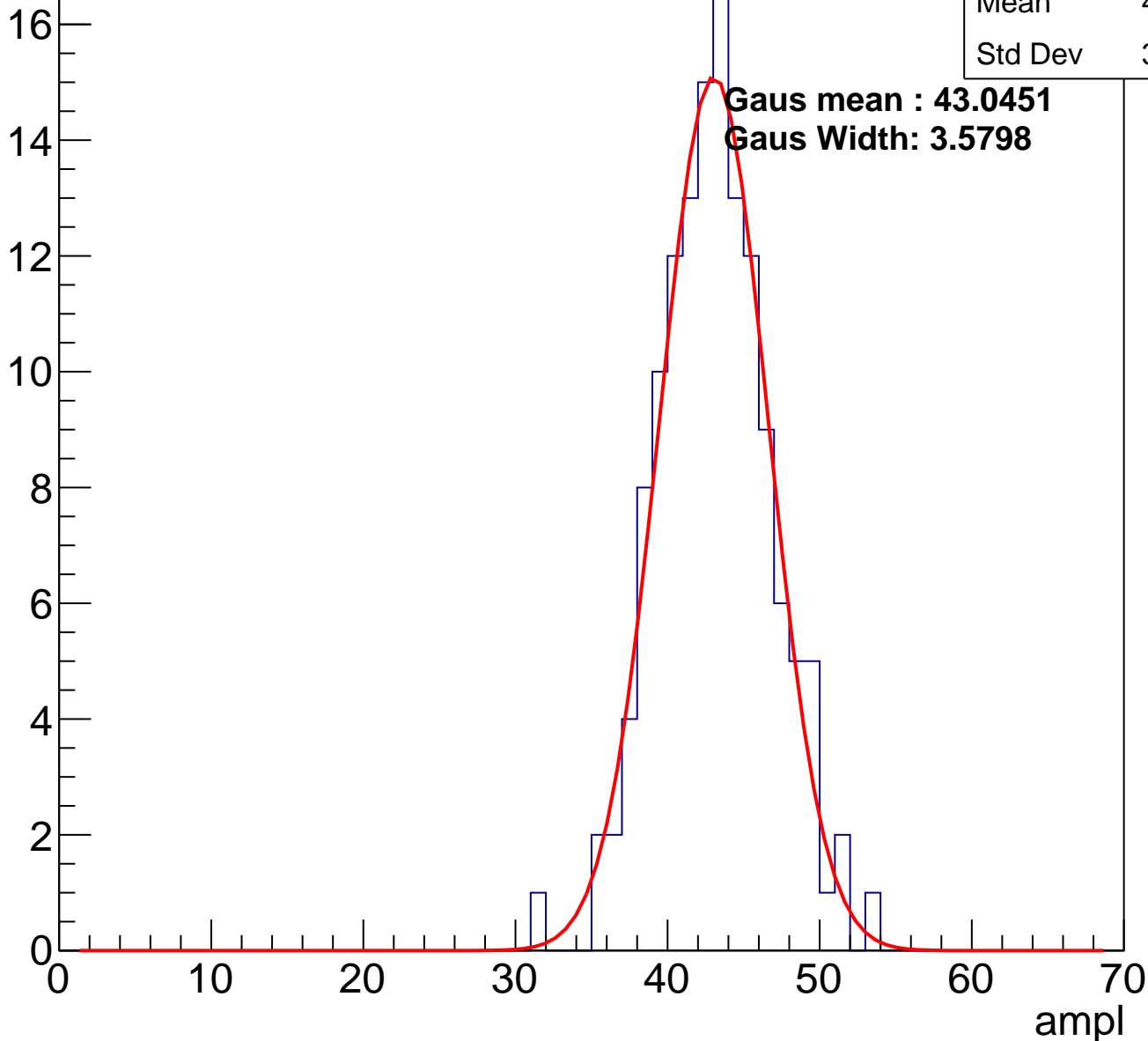
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	138
Mean	42.66
Std Dev	3.664

**Gaus mean : 43.0451**

**Gaus Width: 3.5798**



# B0L101S, U26-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	49.77
Std Dev	4.048

Entry

14

12

10

8

6

4

2

0

0

10

20

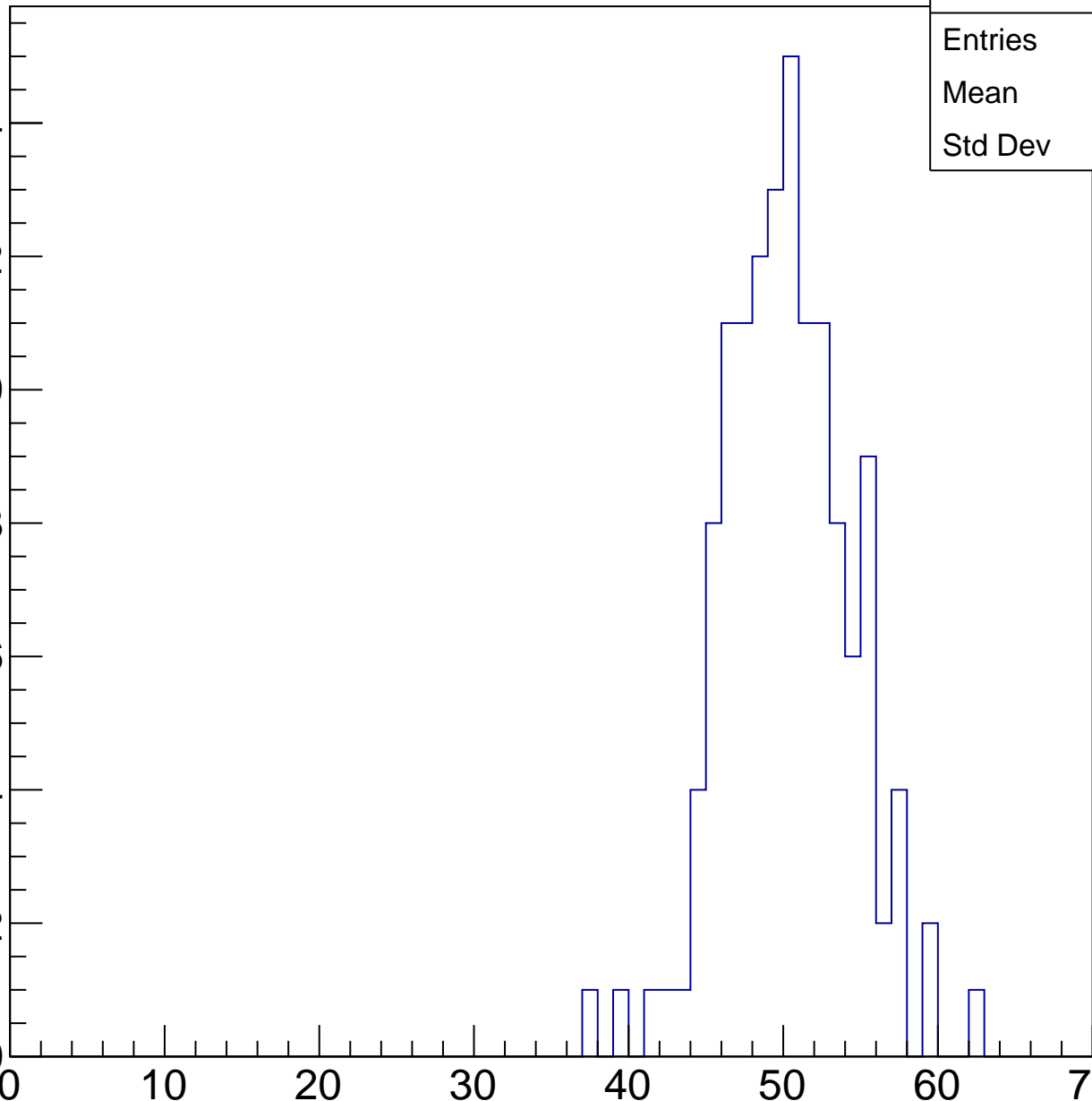
30

40

50

60

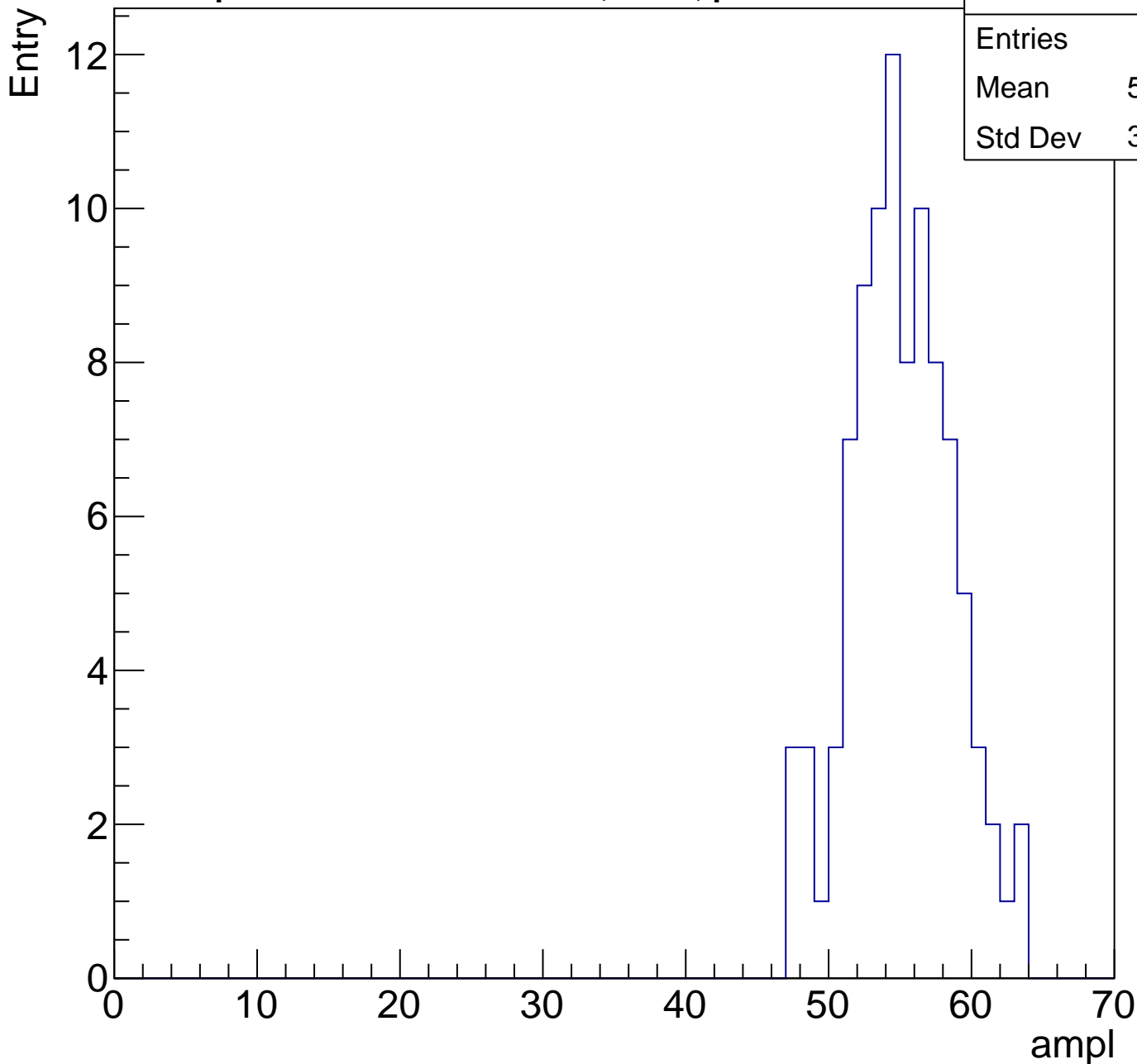
ampl



# B0L101S, U26-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	94
Mean	54.62
Std Dev	3.558



# B0L101S, U26-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

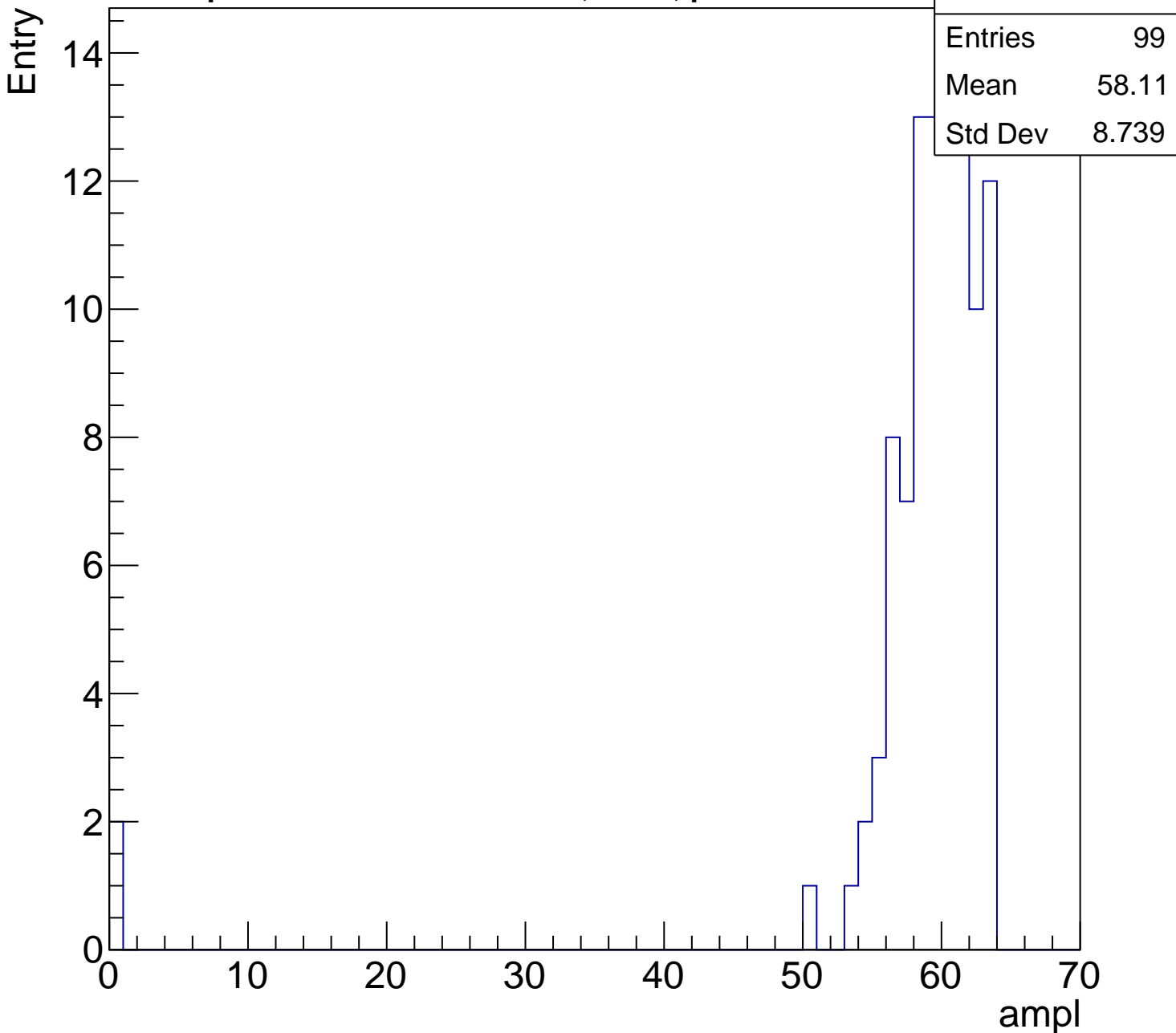
Entries	99
Mean	58.11
Std Dev	8.739

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

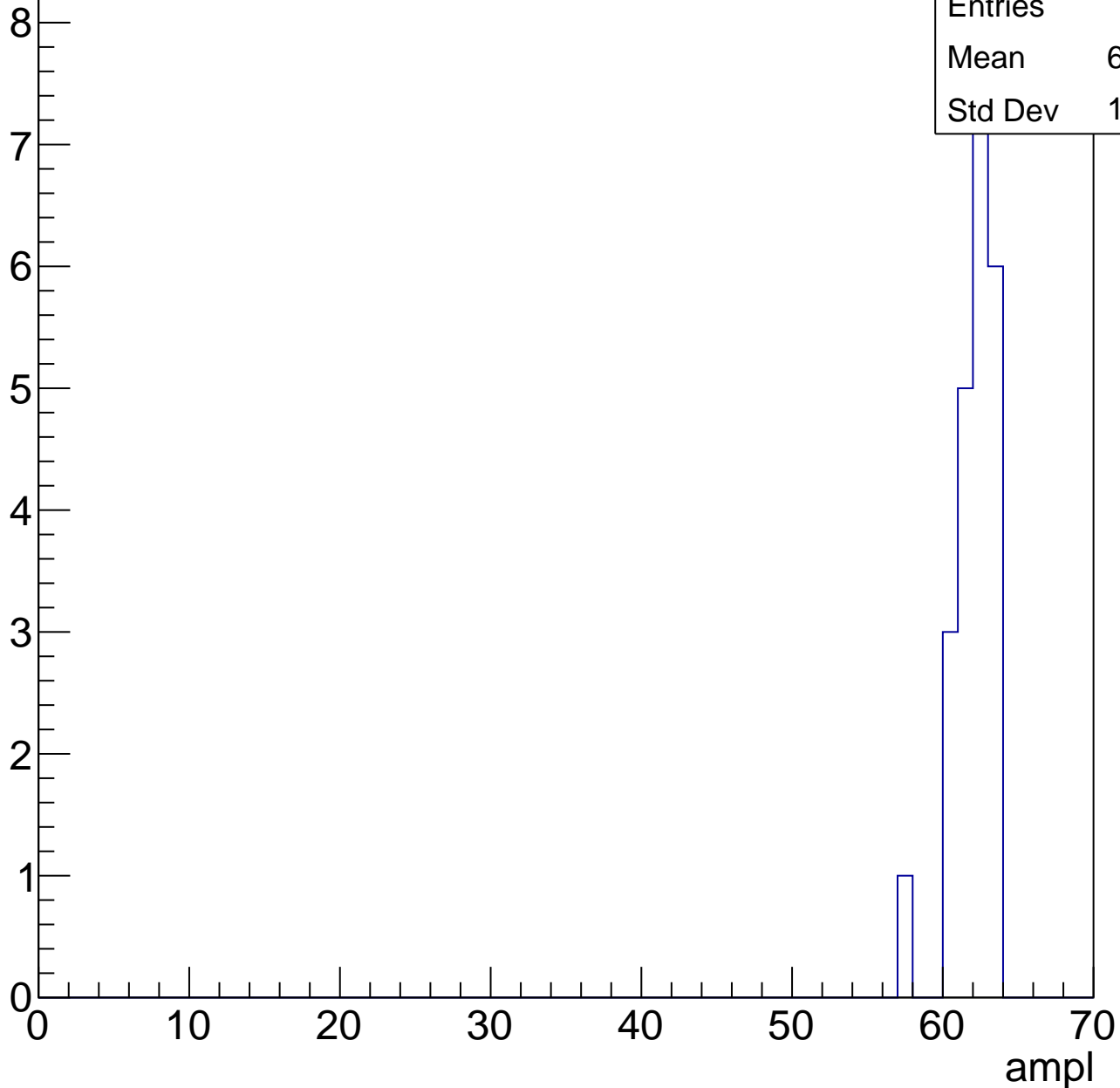


# B0L101S, U26-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	23
Mean	61.57
Std Dev	1.378





# B0L101S, U26-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch81, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	28.36
Std Dev	4.988

**Gaus mean : 28.9140**

**Gaus Width: 3.7055**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

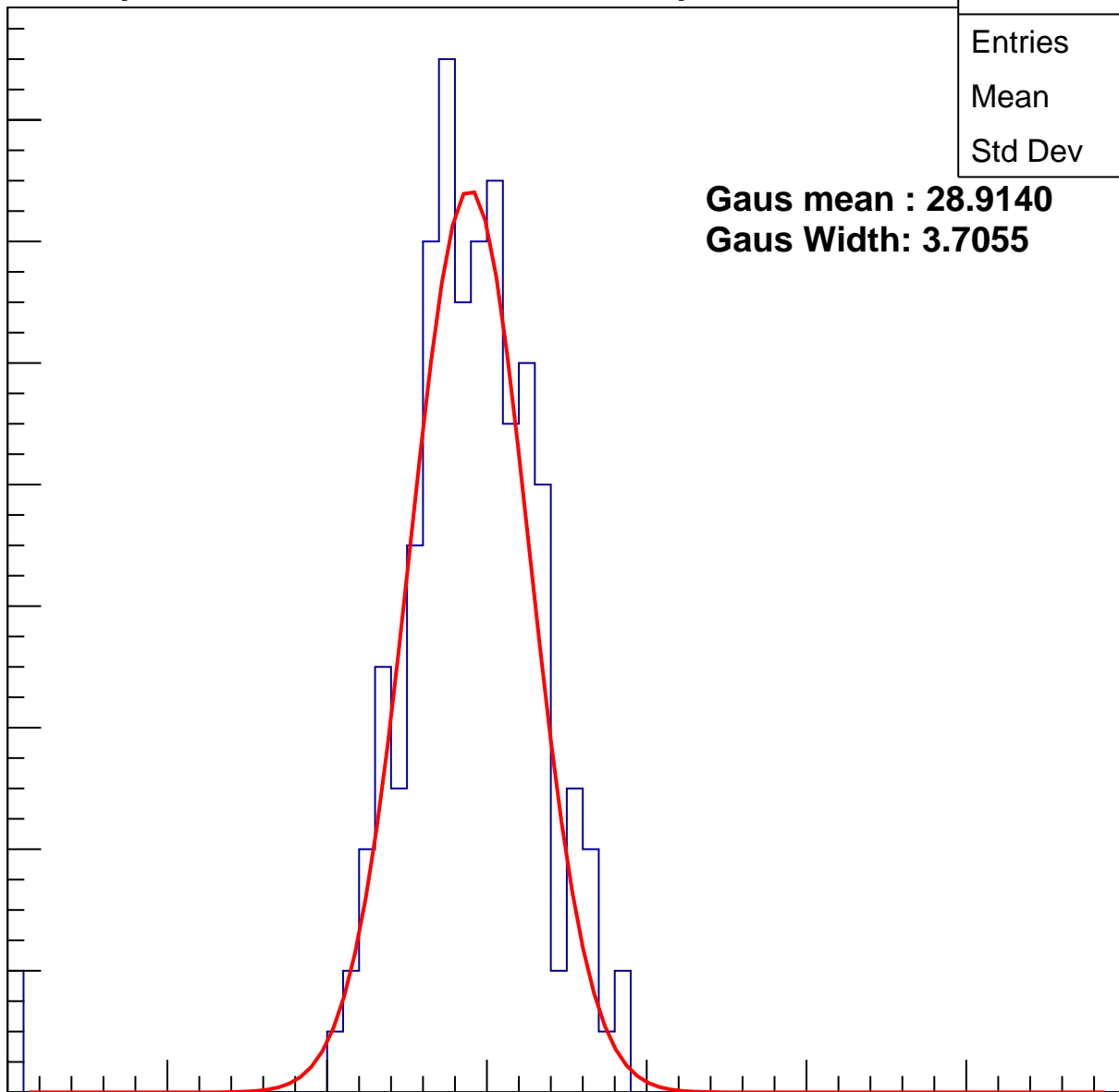
40

50

60

70

ampl

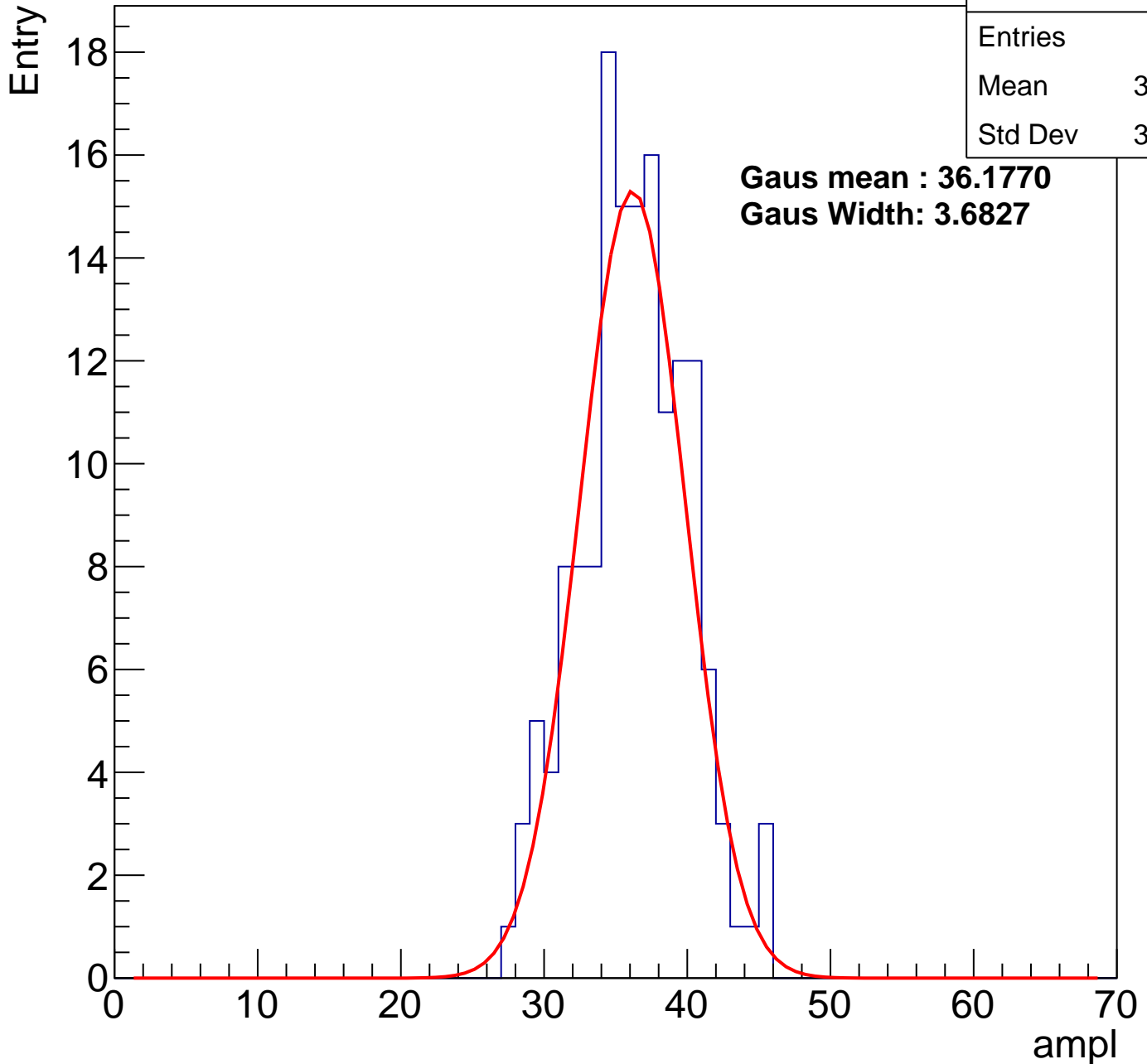


# B0L101S, U26-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	35.82
Std Dev	3.758

**Gaus mean : 36.1770**  
**Gaus Width: 3.6827**



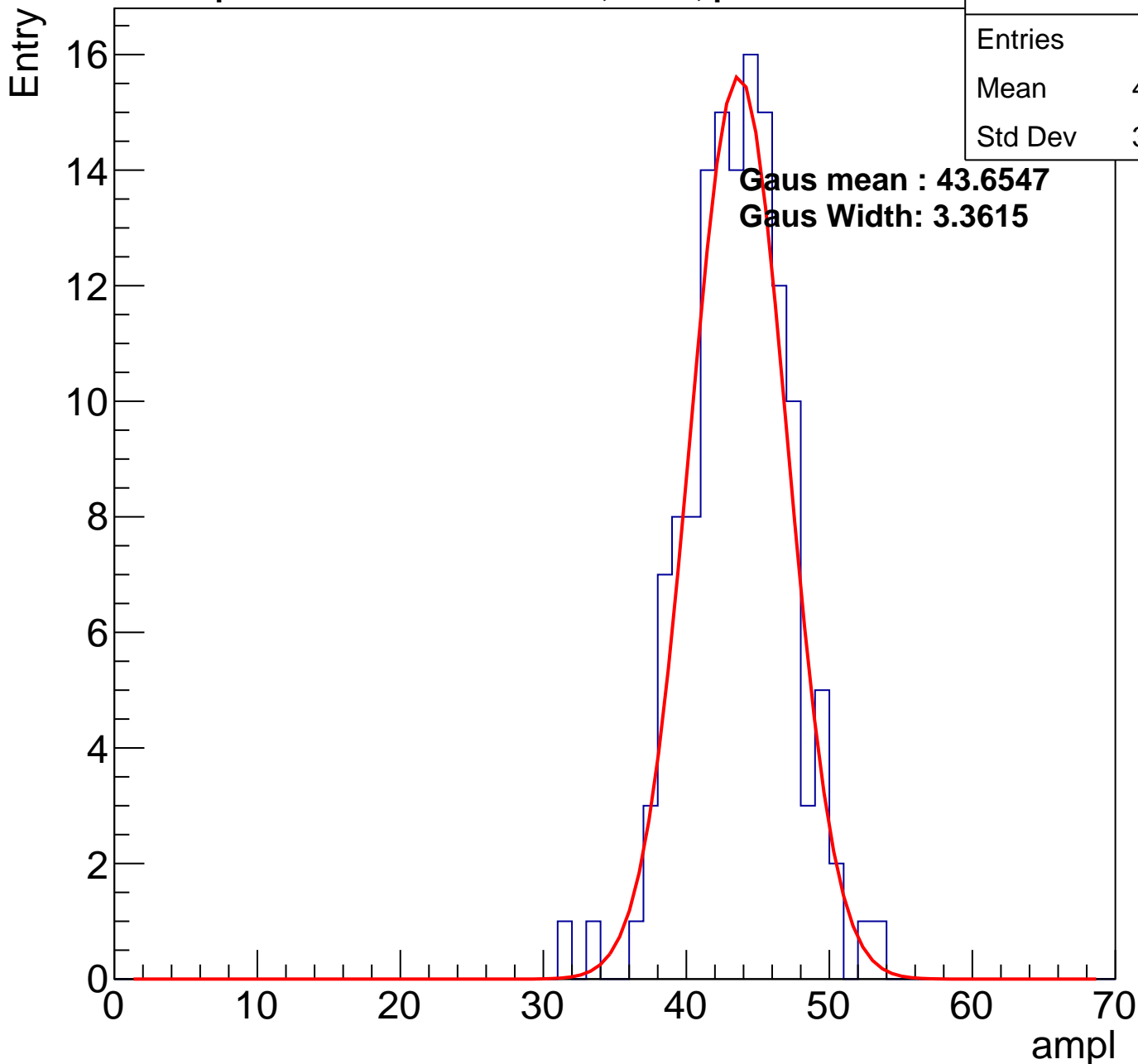
# B0L101S, U26-ch81, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	137
Mean	43.14
Std Dev	3.546

**Gaus mean : 43.6547**

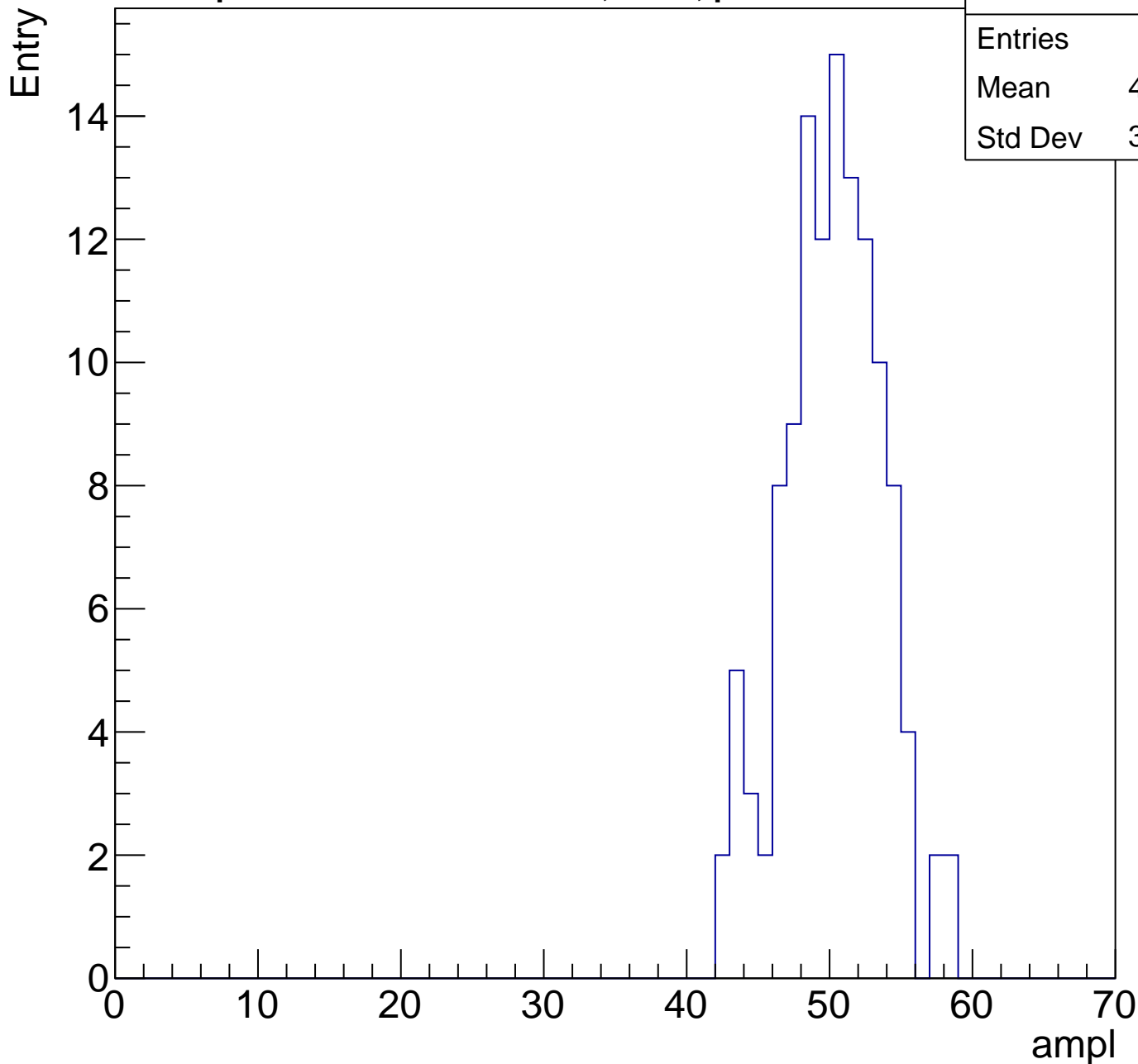
**Gaus Width: 3.3615**



# B0L101S, U26-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	121
Mean	49.76
Std Dev	3.409



# B0L101S, U26-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

107

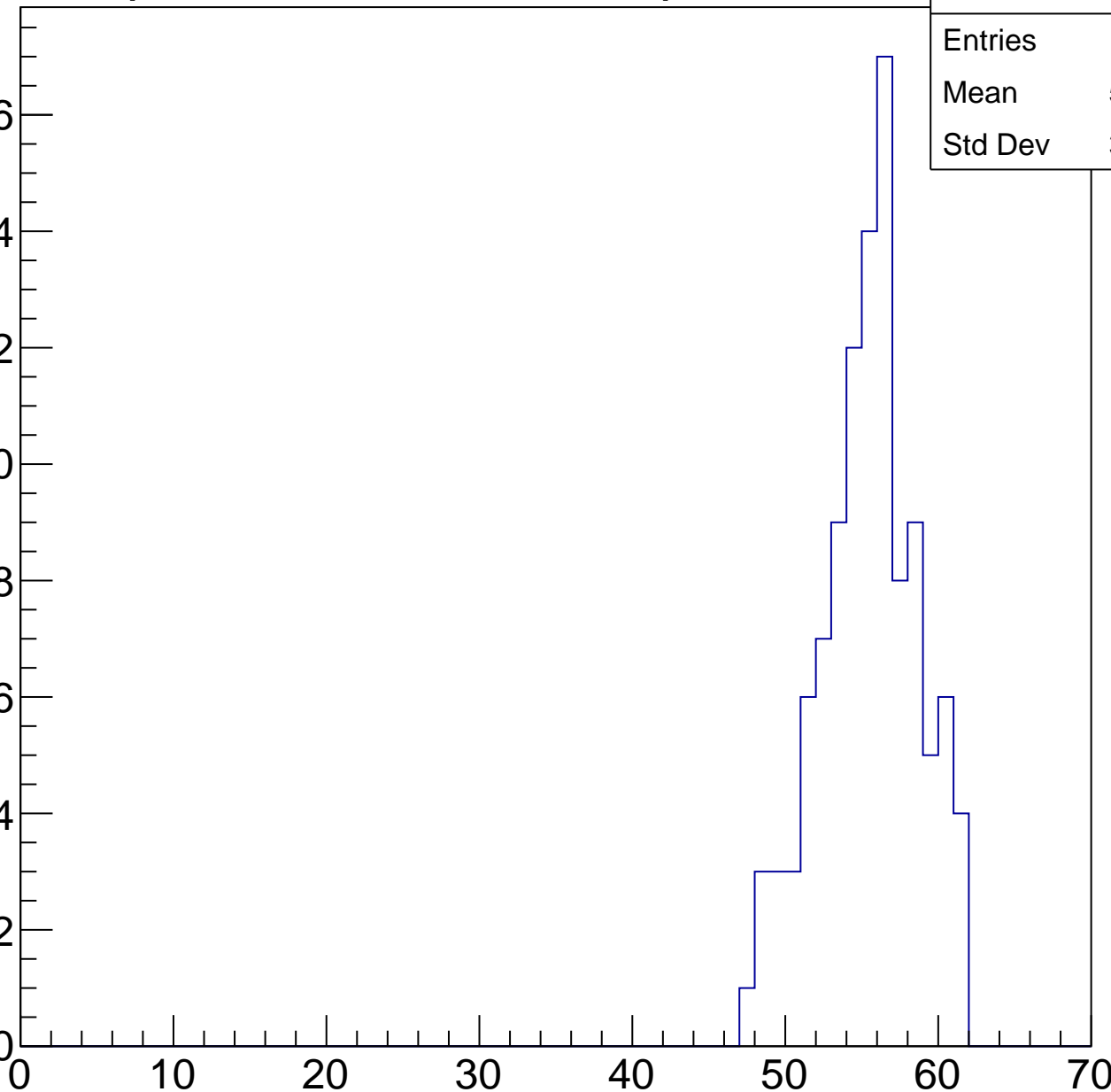
Mean

54.97

Std Dev

3.213

ampl



# B0L101S, U26-ch81, adc5

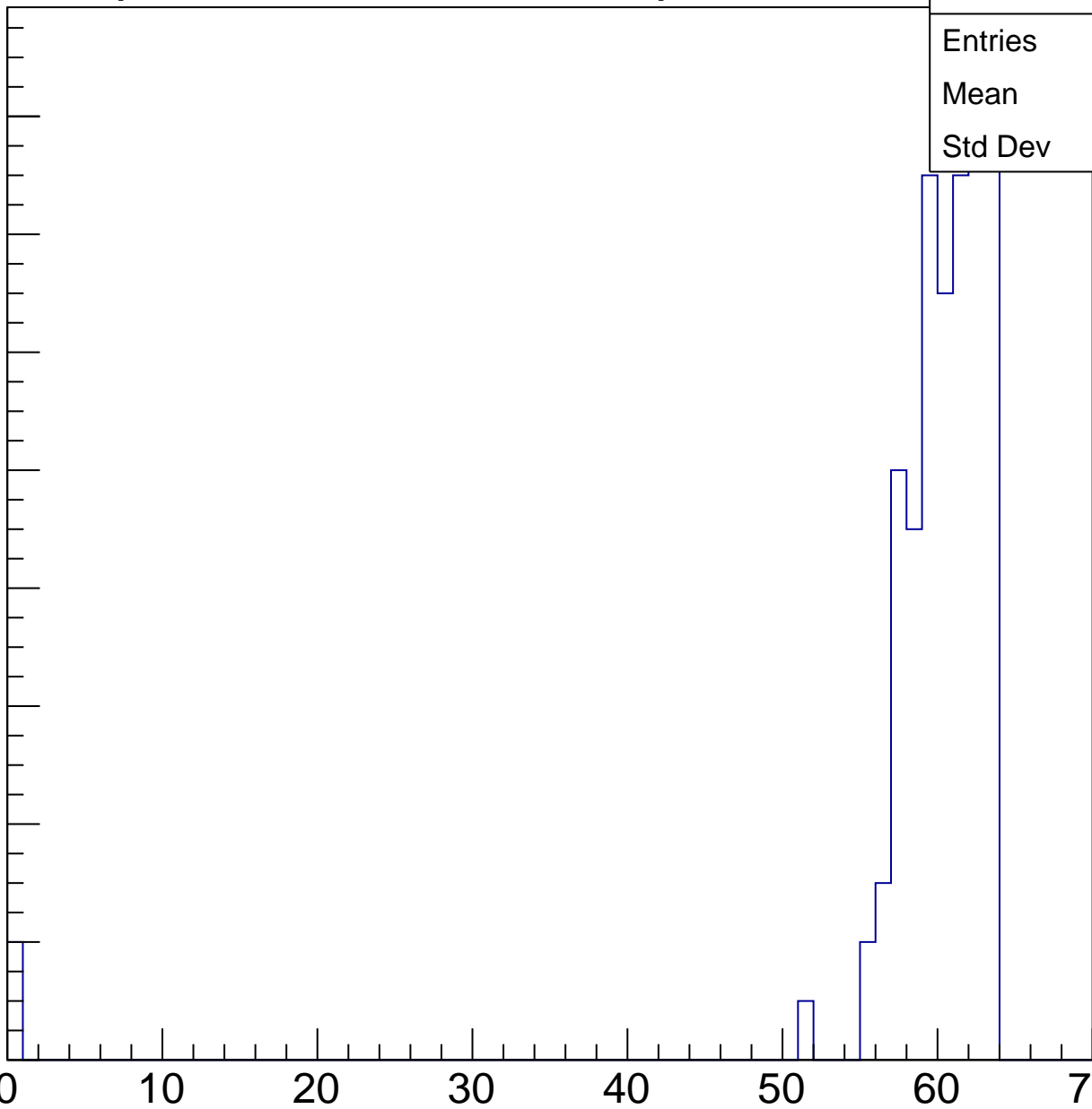
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	103
Mean	58.86
Std Dev	8.598

ampl



# B0L101S, U26-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch82, adc0

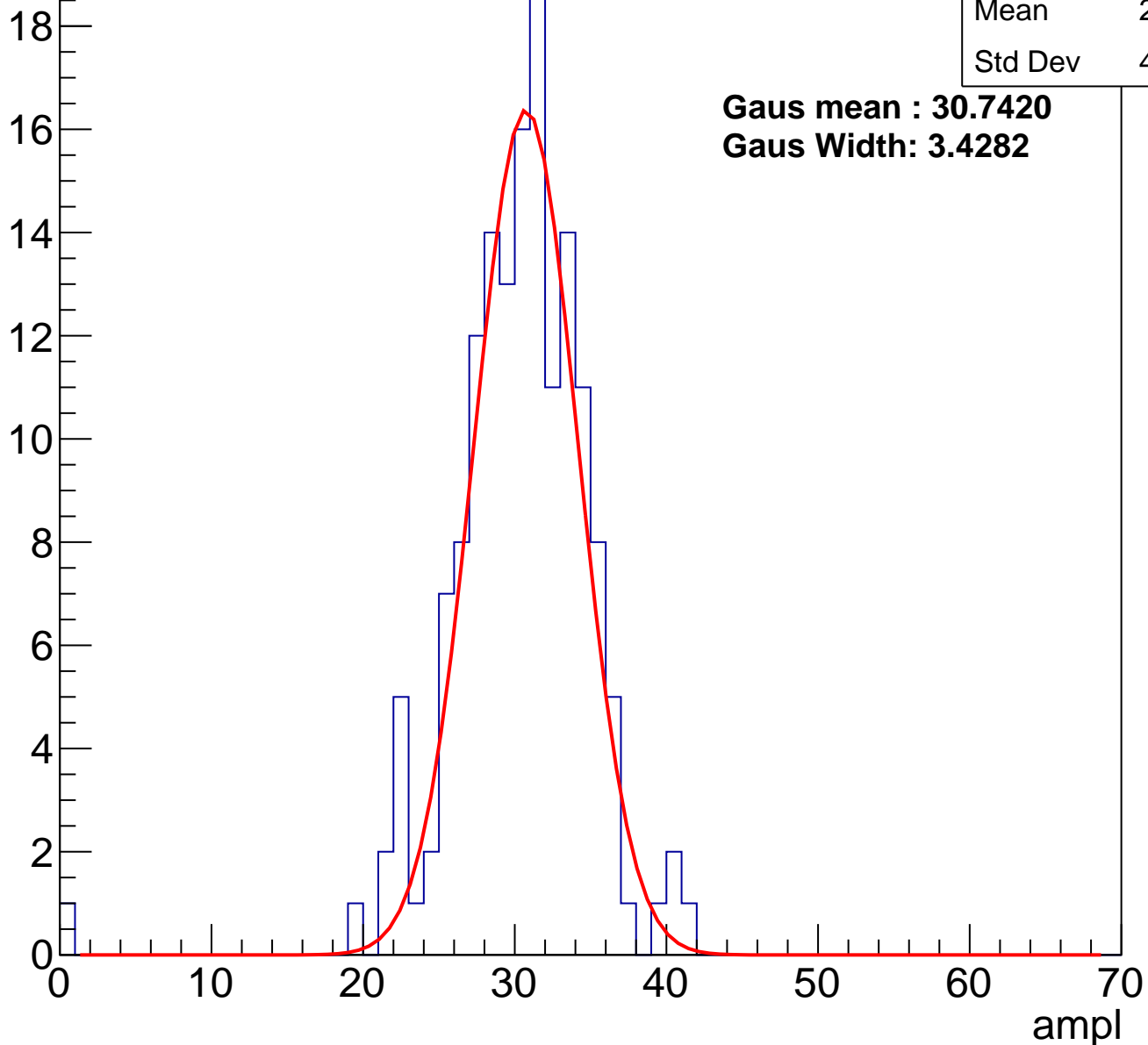
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	29.88
Std Dev	4.613

**Gaus mean : 30.7420**

**Gaus Width: 3.4282**

Entry



# B0L101S, U26-ch82, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	37.38
Std Dev	3.804

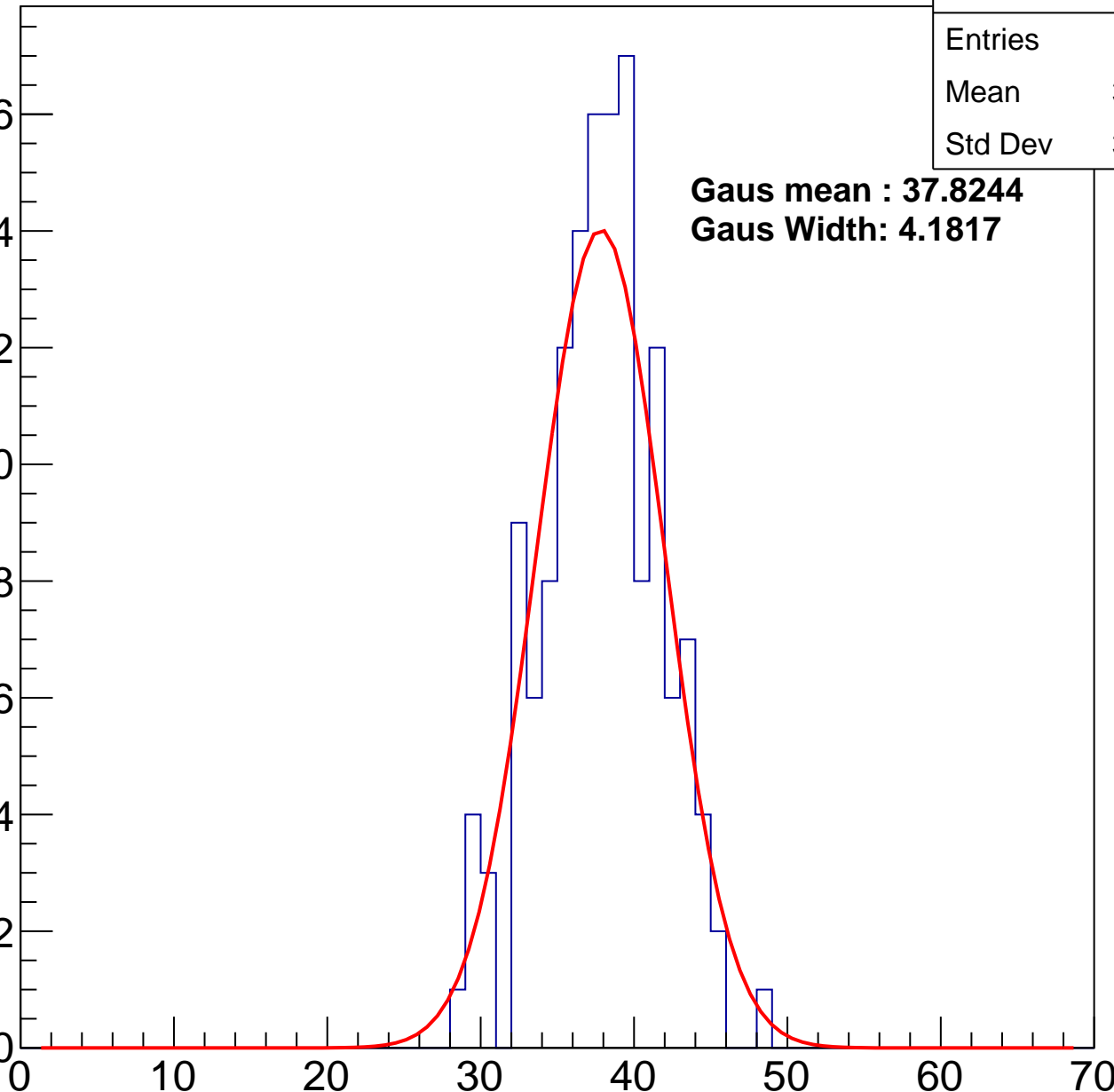
**Gaus mean : 37.8244**

**Gaus Width: 4.1817**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch82, adc2

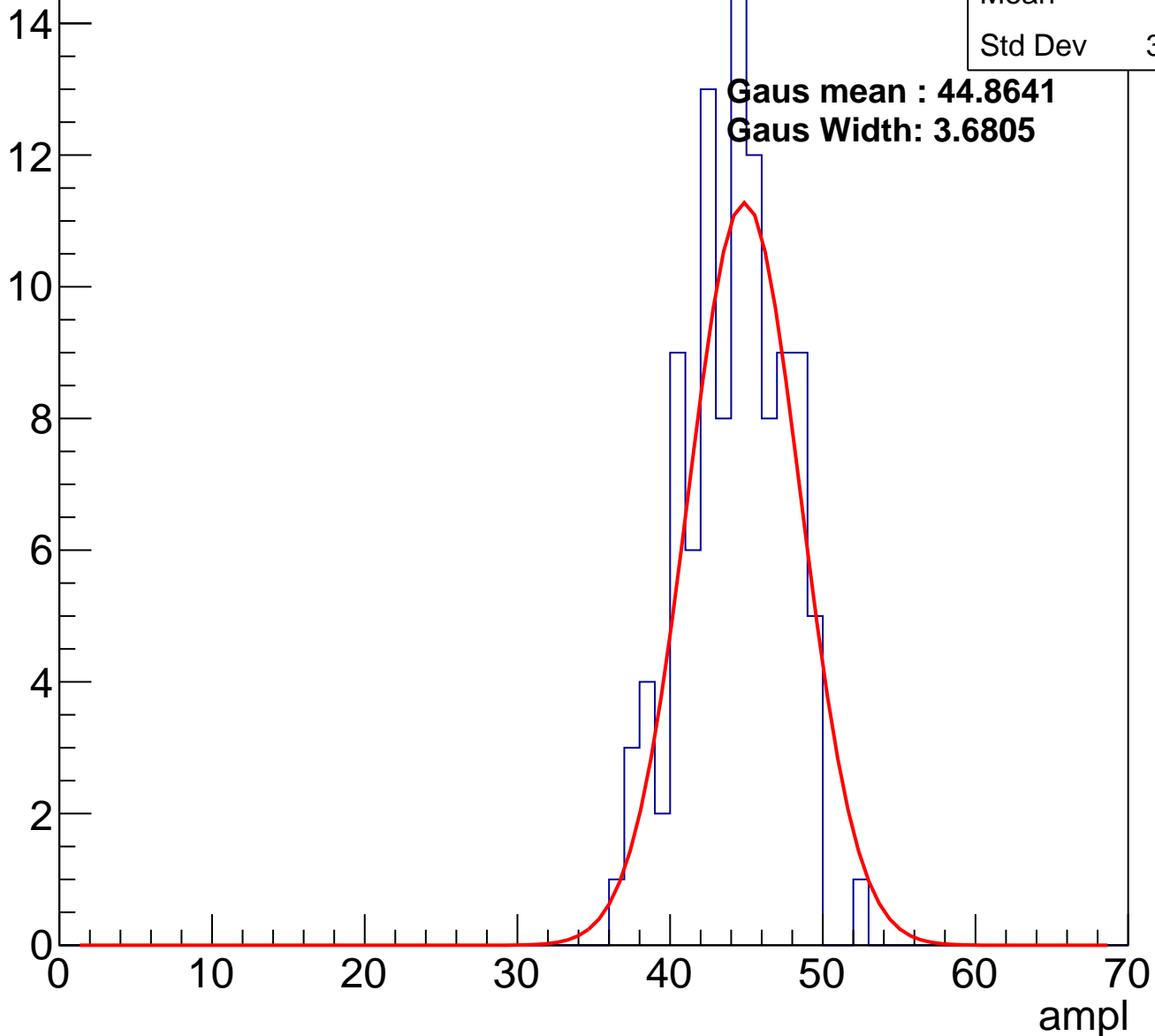
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	43.74
Std Dev	3.257

**Gaus mean : 44.8641**

**Gaus Width: 3.6805**

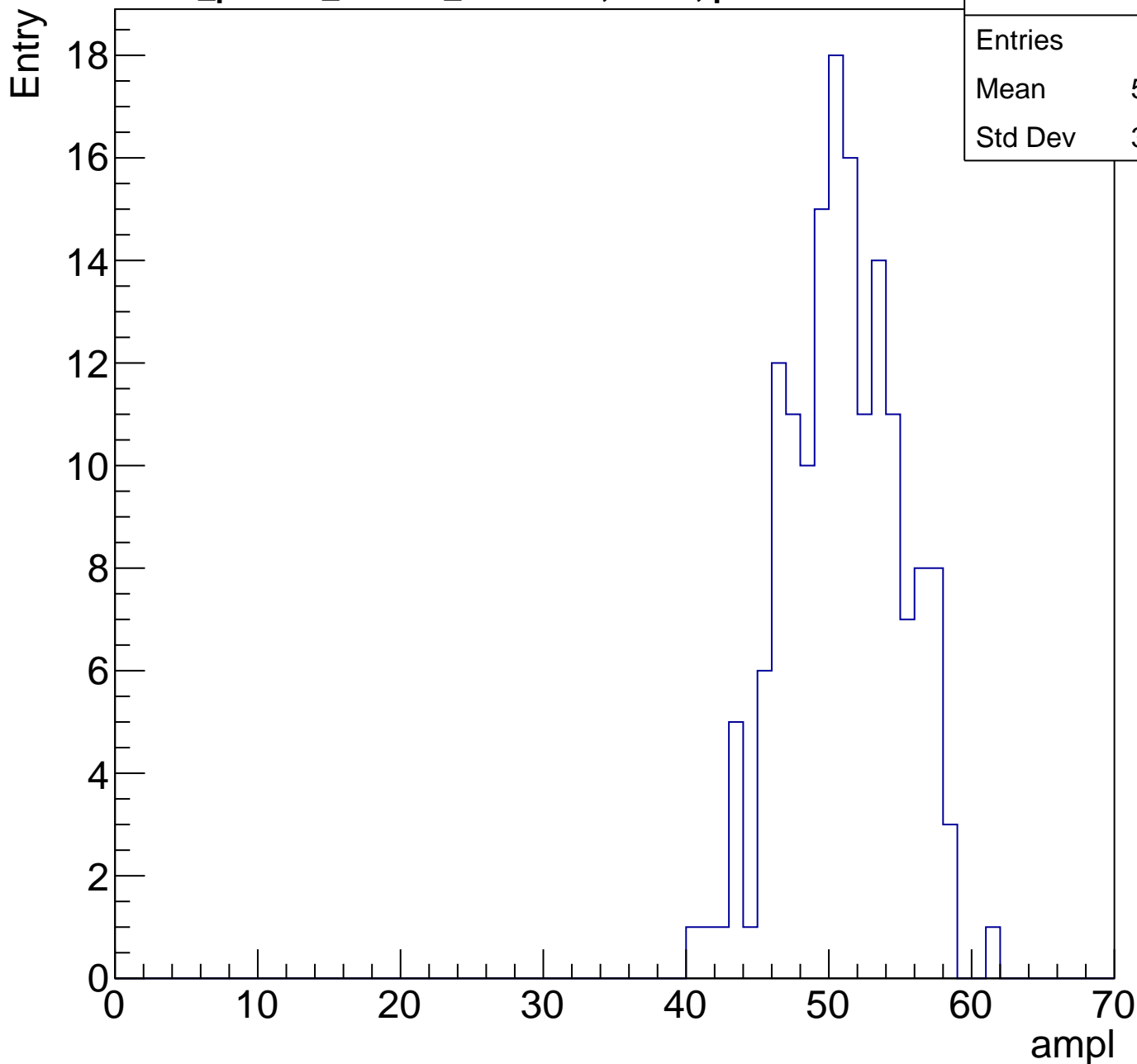
Entry



# B0L101S, U26-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	50.52
Std Dev	3.959

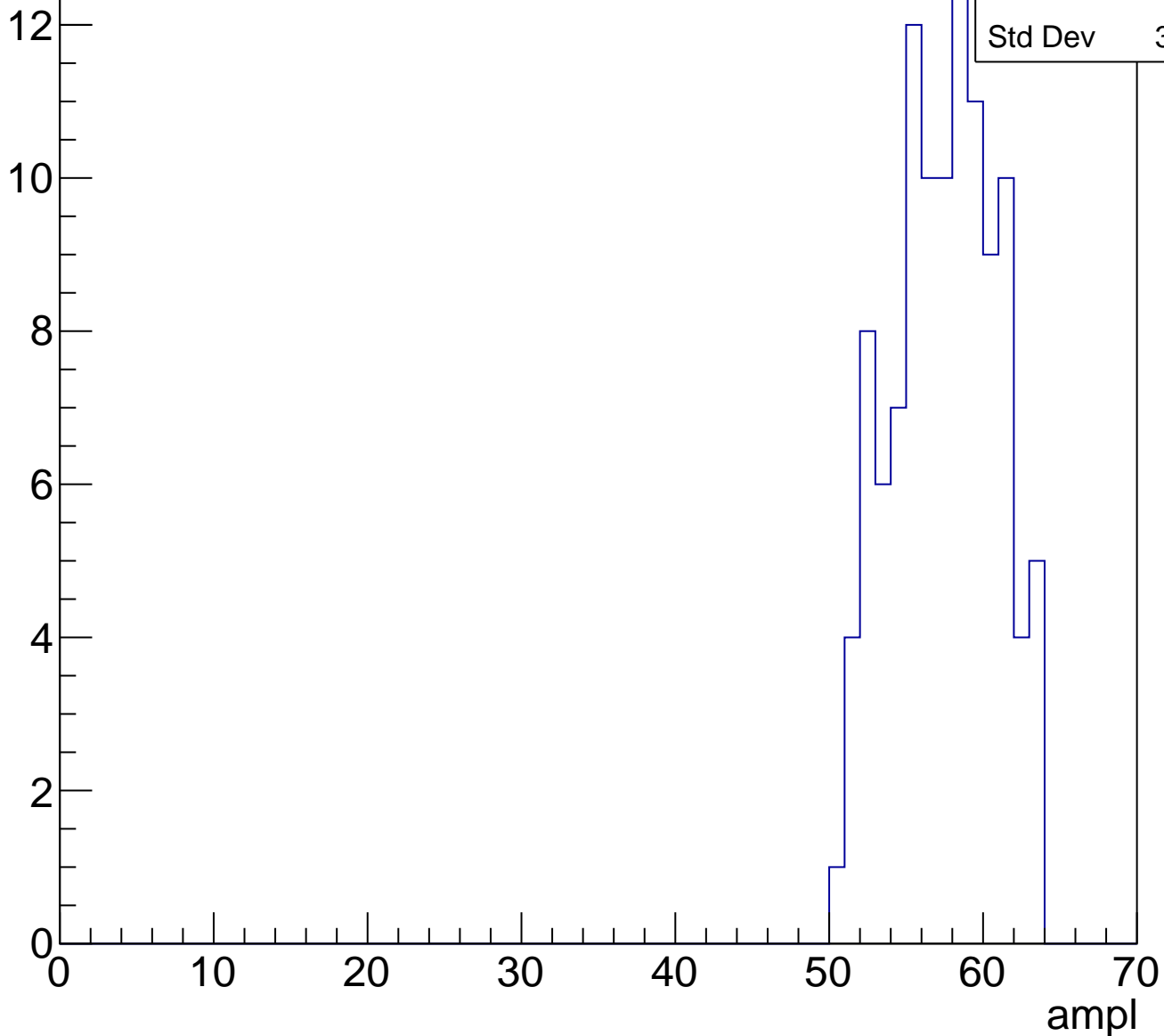


# B0L101S, U26-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	57.02
Std Dev	3.286

Entry



# B0L101S, U26-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

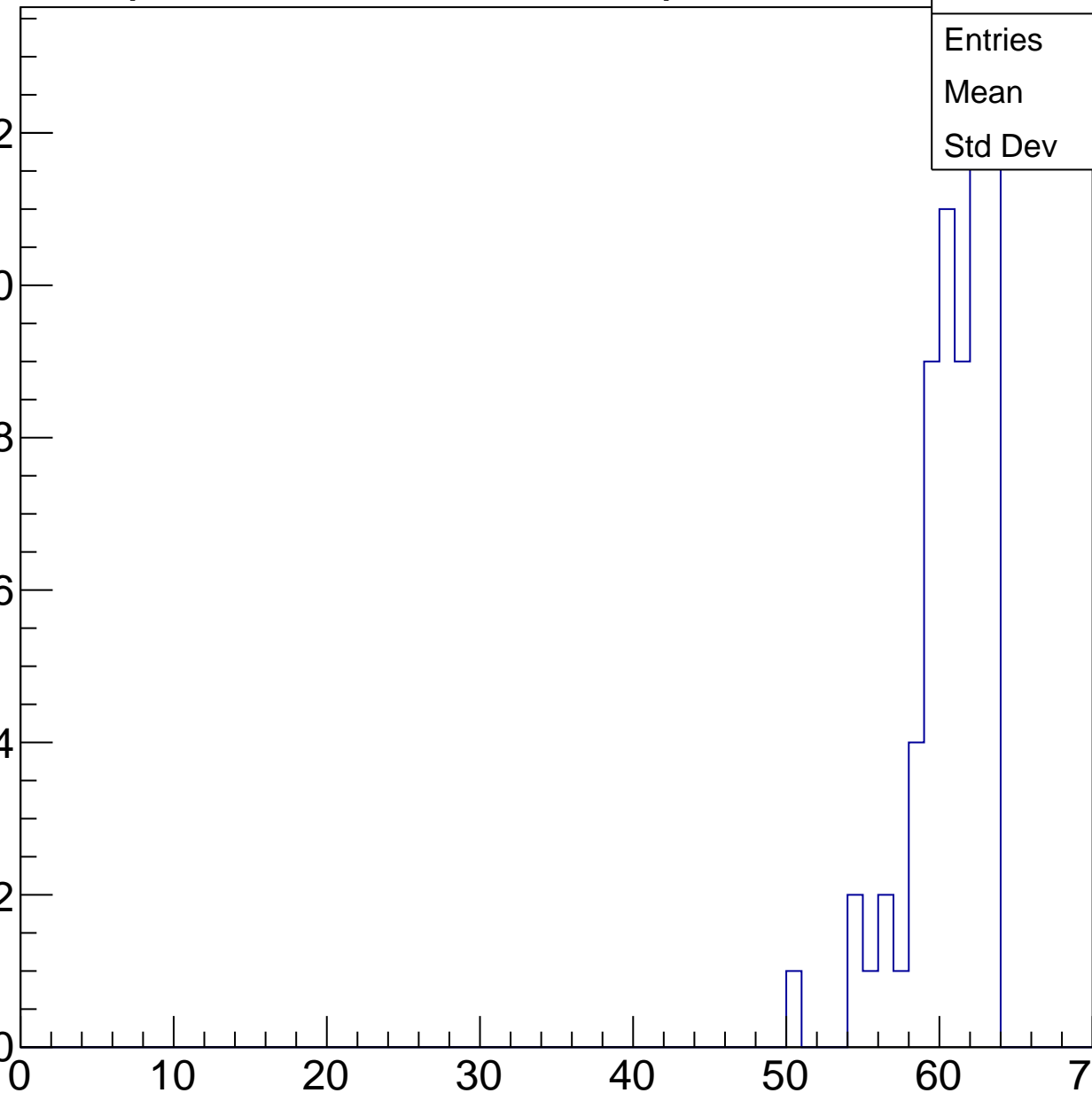
4

2

0

Entries	65
Mean	60.25
Std Dev	2.566

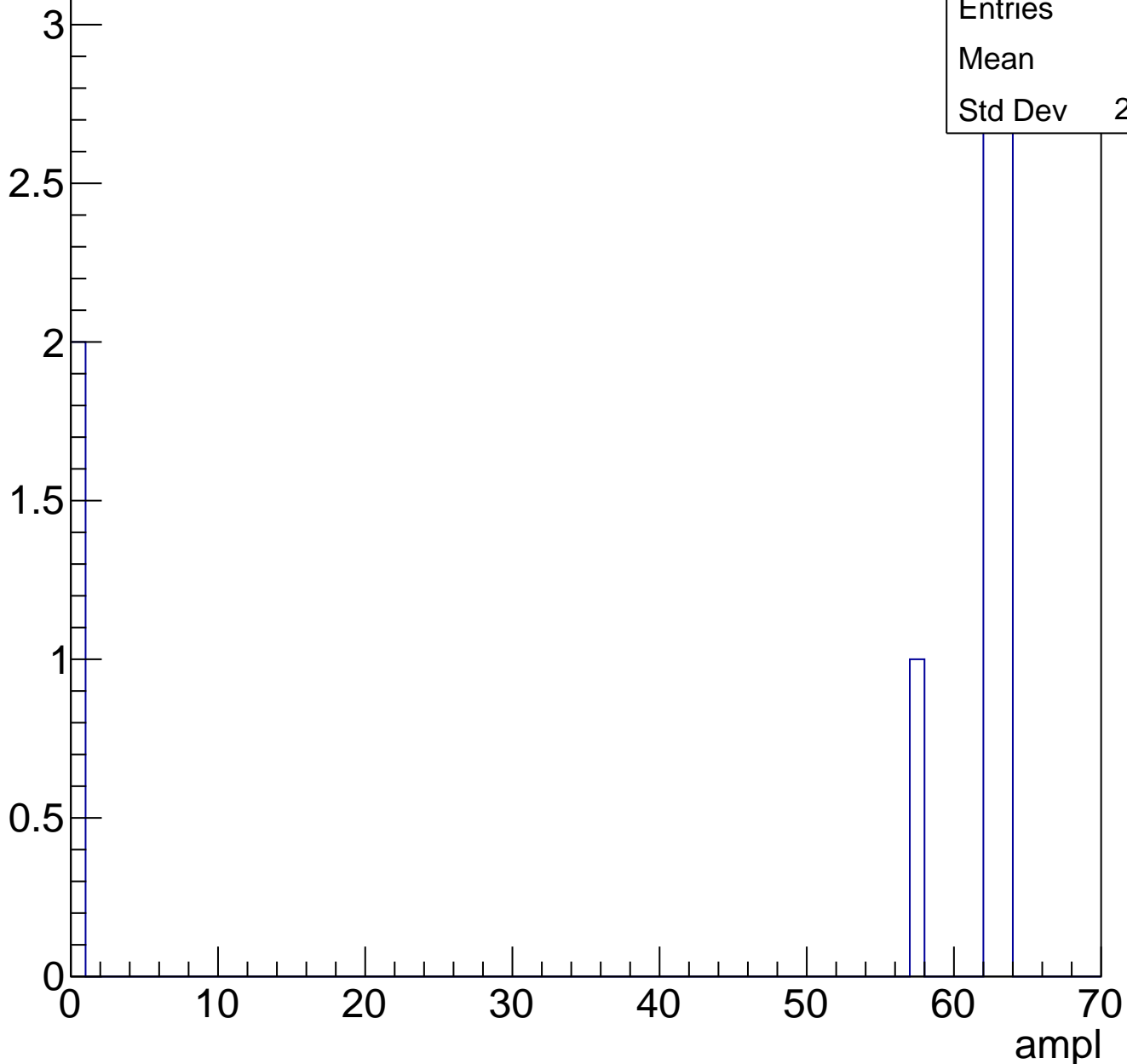
ampl



# B0L101S, U26-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch83, adc0

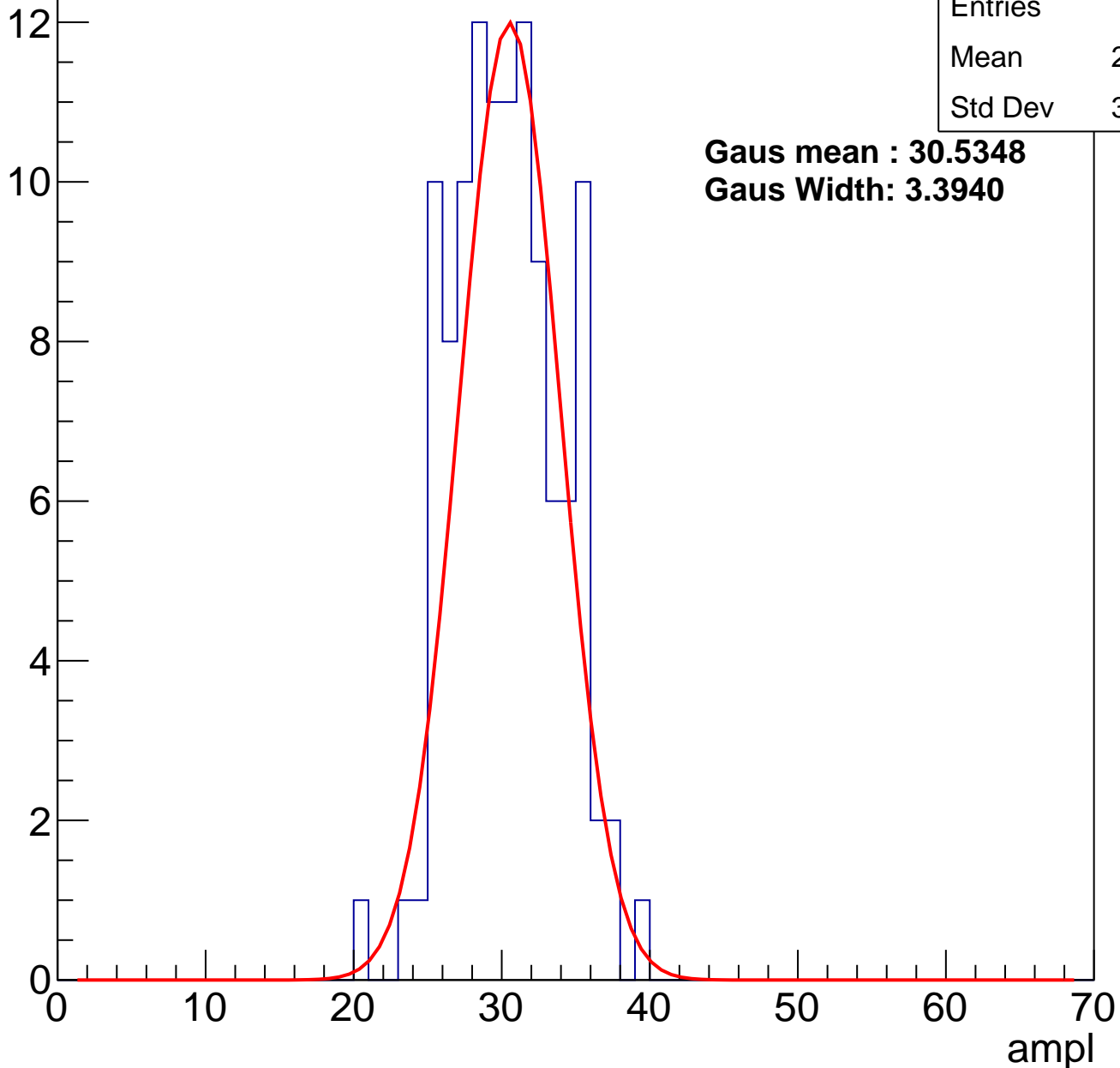
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	113
Mean	29.88
Std Dev	3.532

**Gaus mean : 30.5348**

**Gaus Width: 3.3940**

Entry



# B0L101S, U26-ch83, adc1

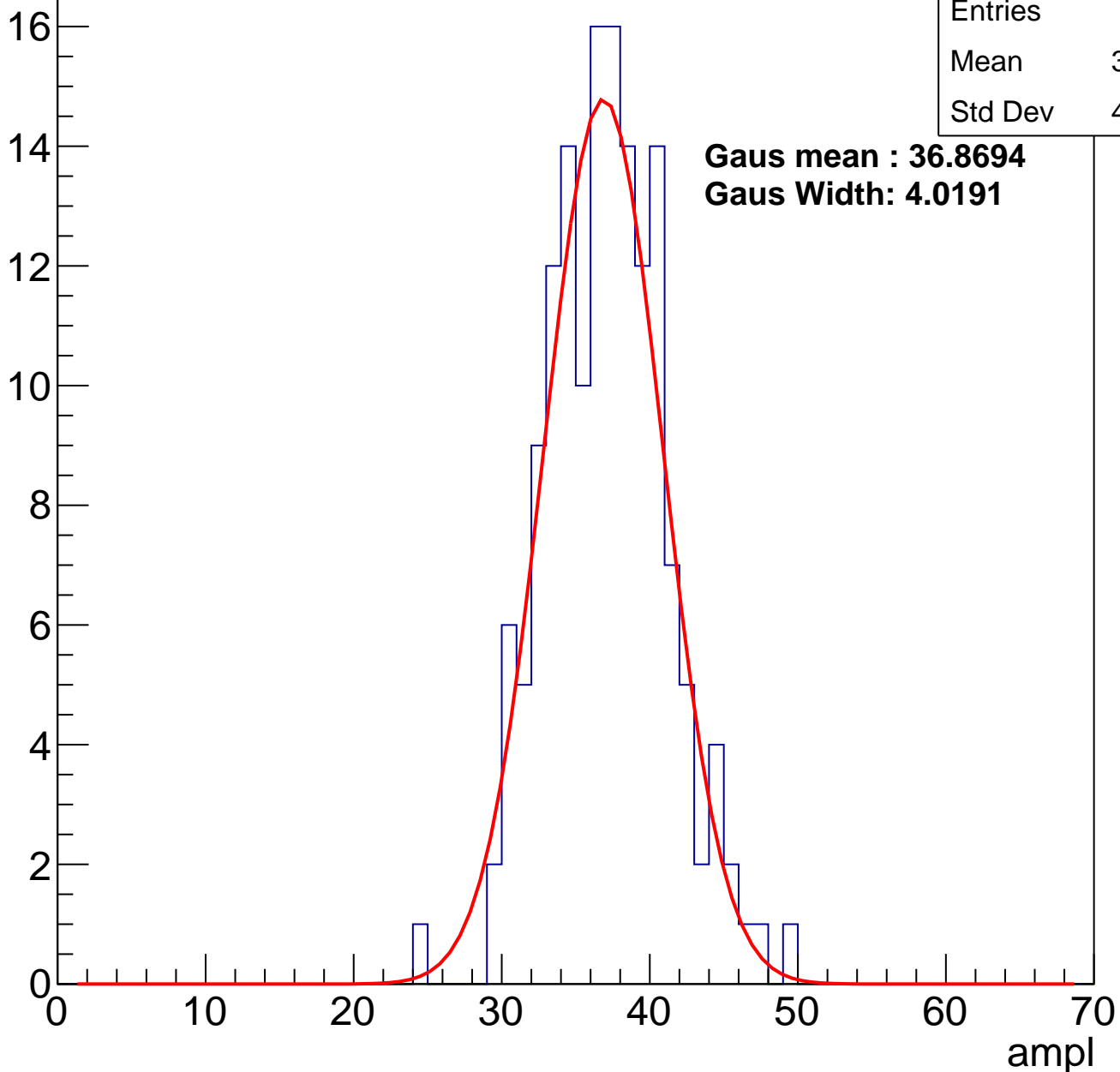
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	36.66
Std Dev	4.028

**Gaus mean : 36.8694**

**Gaus Width: 4.0191**

Entry



# B0L101S, U26-ch83, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	128
Mean	43.41
Std Dev	3.51

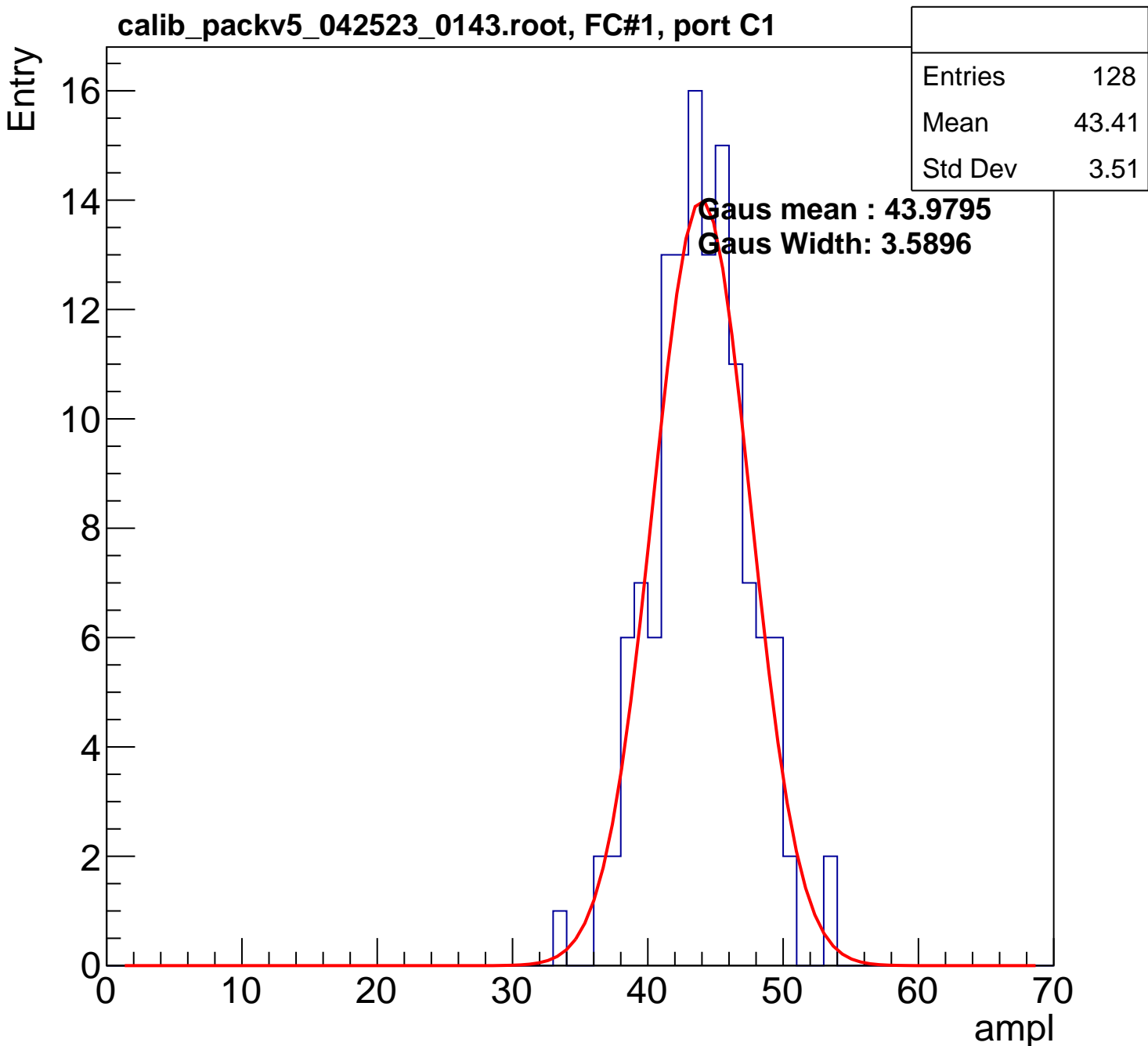
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

**Gaus mean : 43.9795**  
**Gaus Width: 3.5896**

ampl

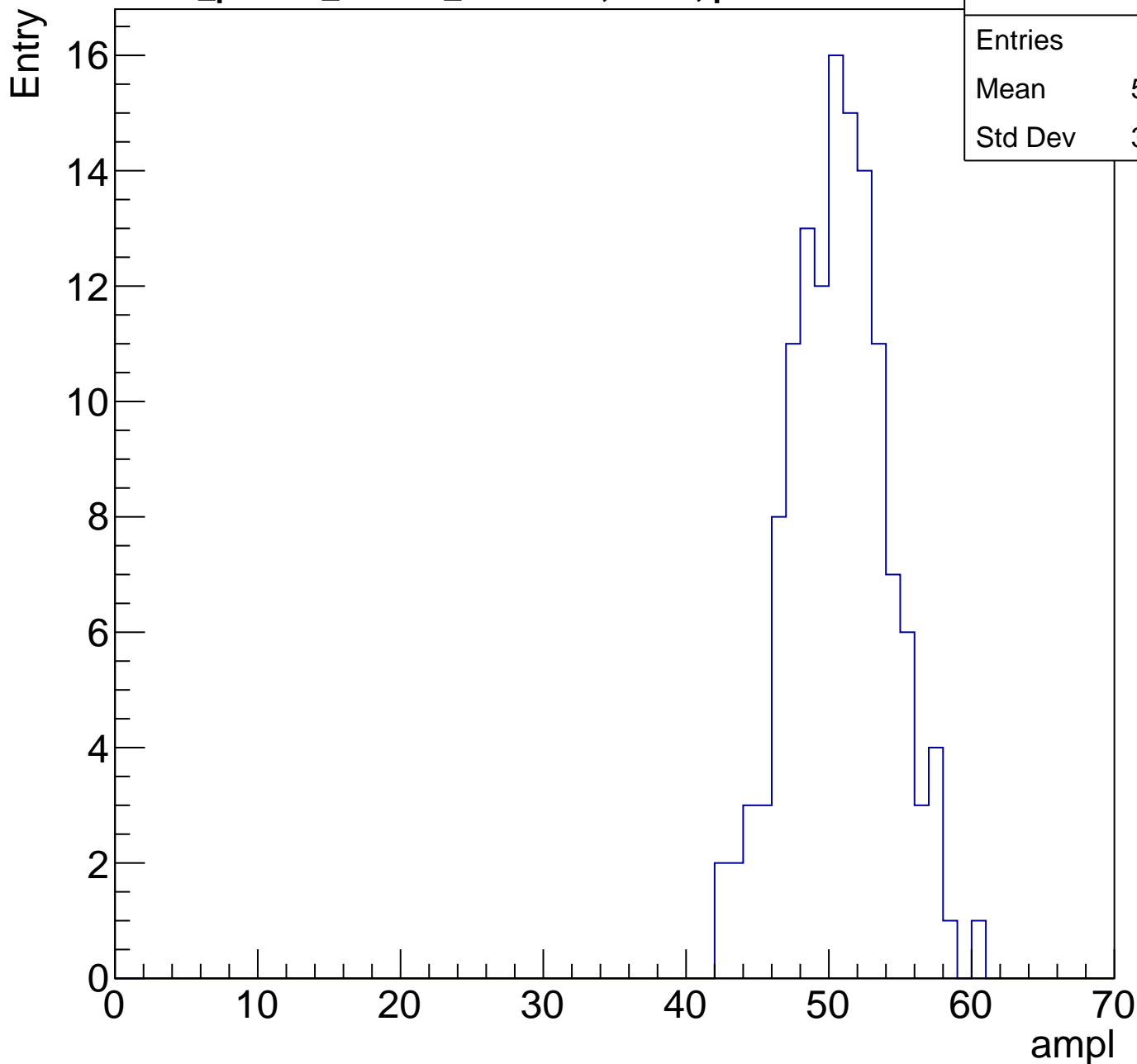
0 10 20 30 40 50 60 70



# B0L101S, U26-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

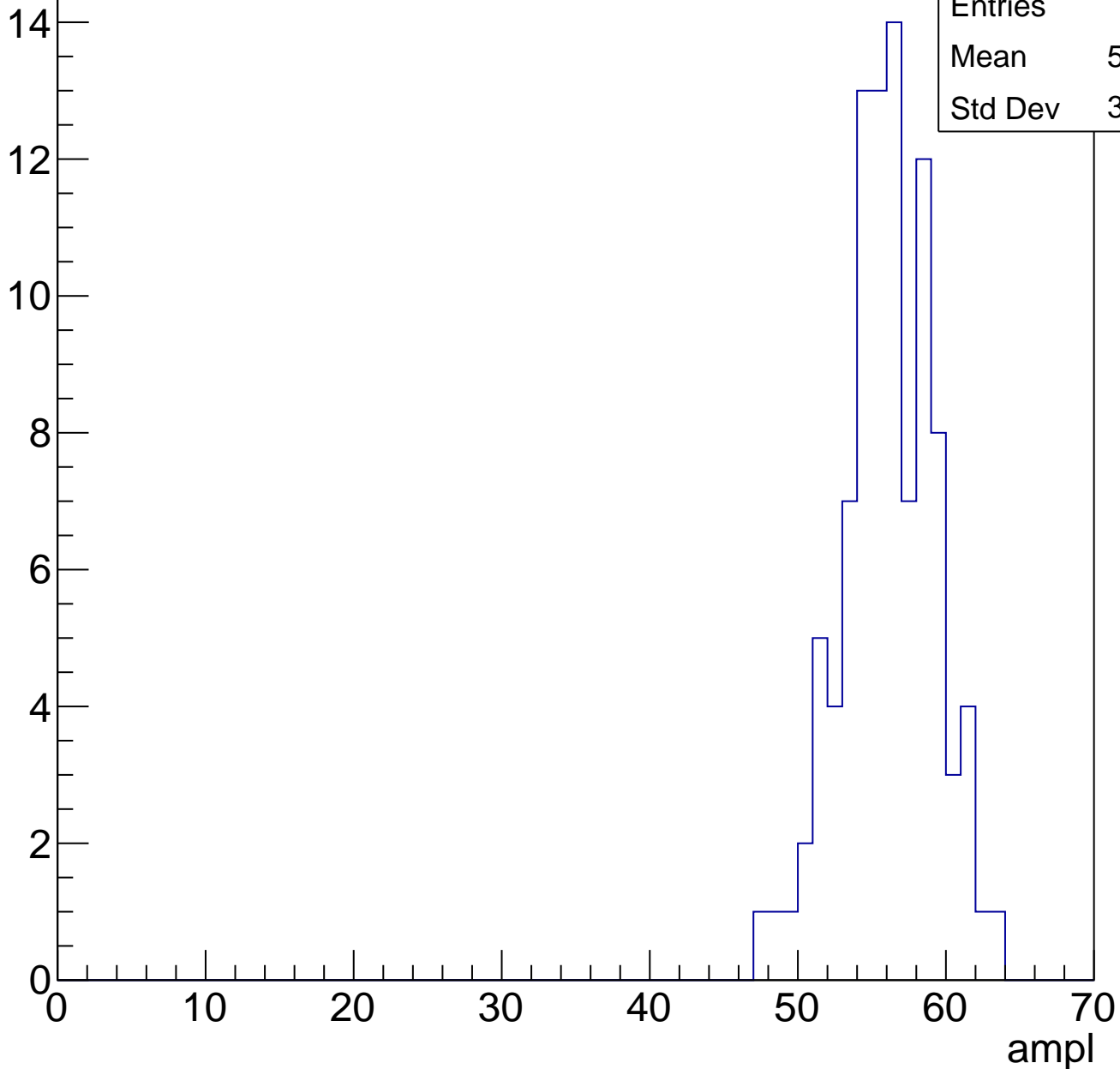
Entries	132
Mean	50.24
Std Dev	3.473



# B0L101S, U26-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

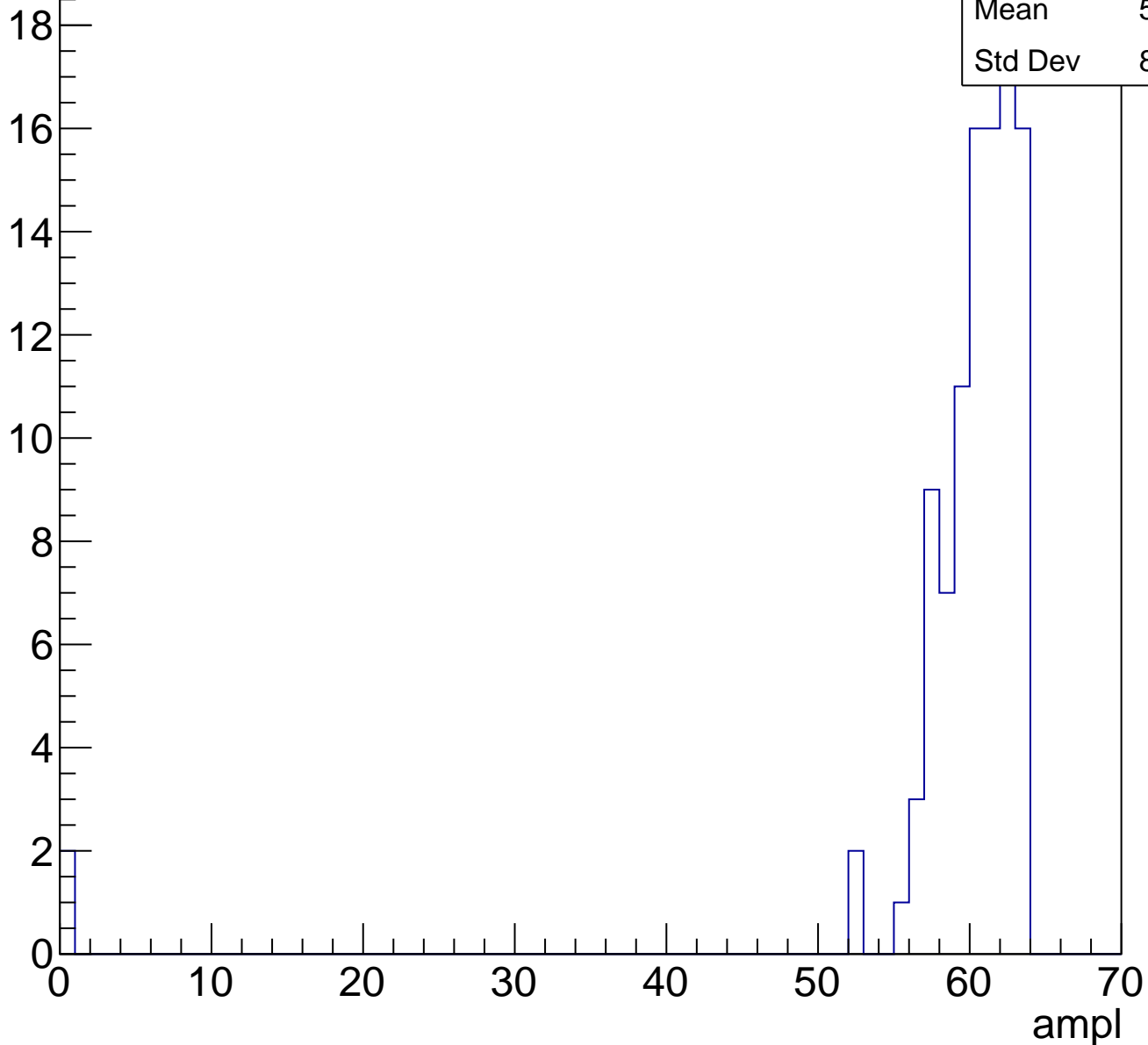
Entry



# B0L101S, U26-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

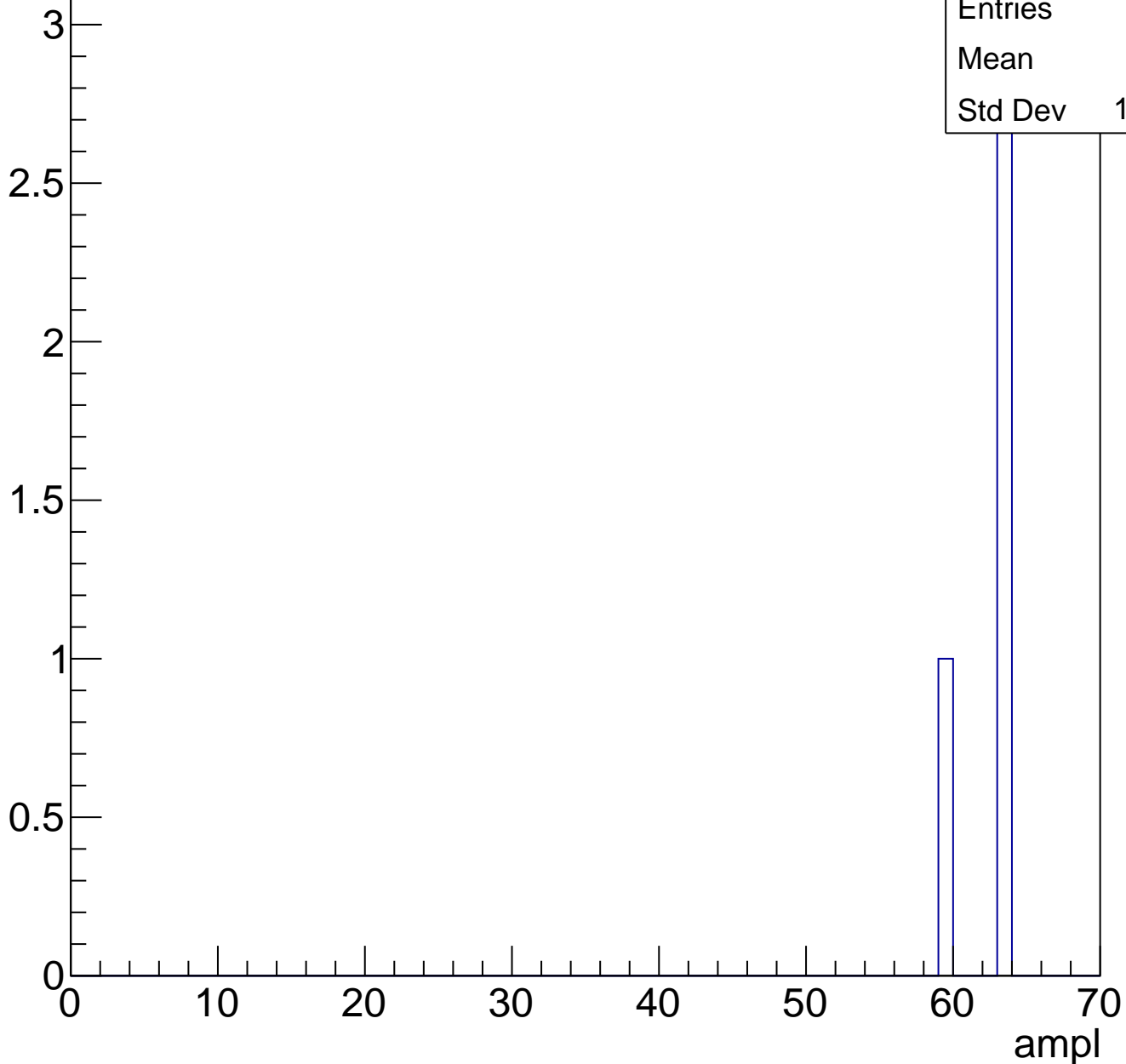
Entry



# B0L101S, U26-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

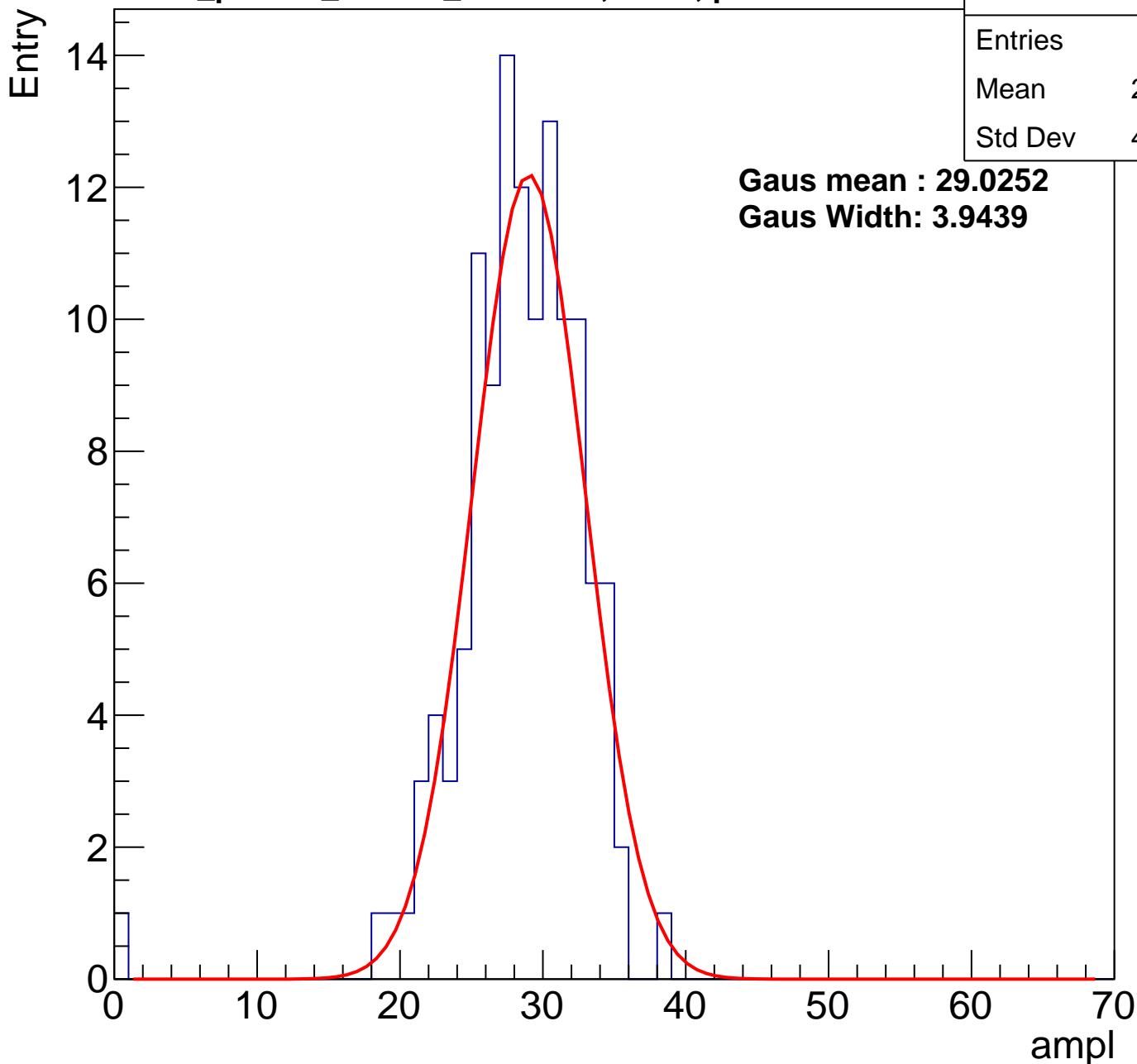
# B0L101S, U26-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	27.97
Std Dev	4.508

**Gaus mean : 29.0252**

**Gaus Width: 3.9439**



# B0L101S, U26-ch84, adc1

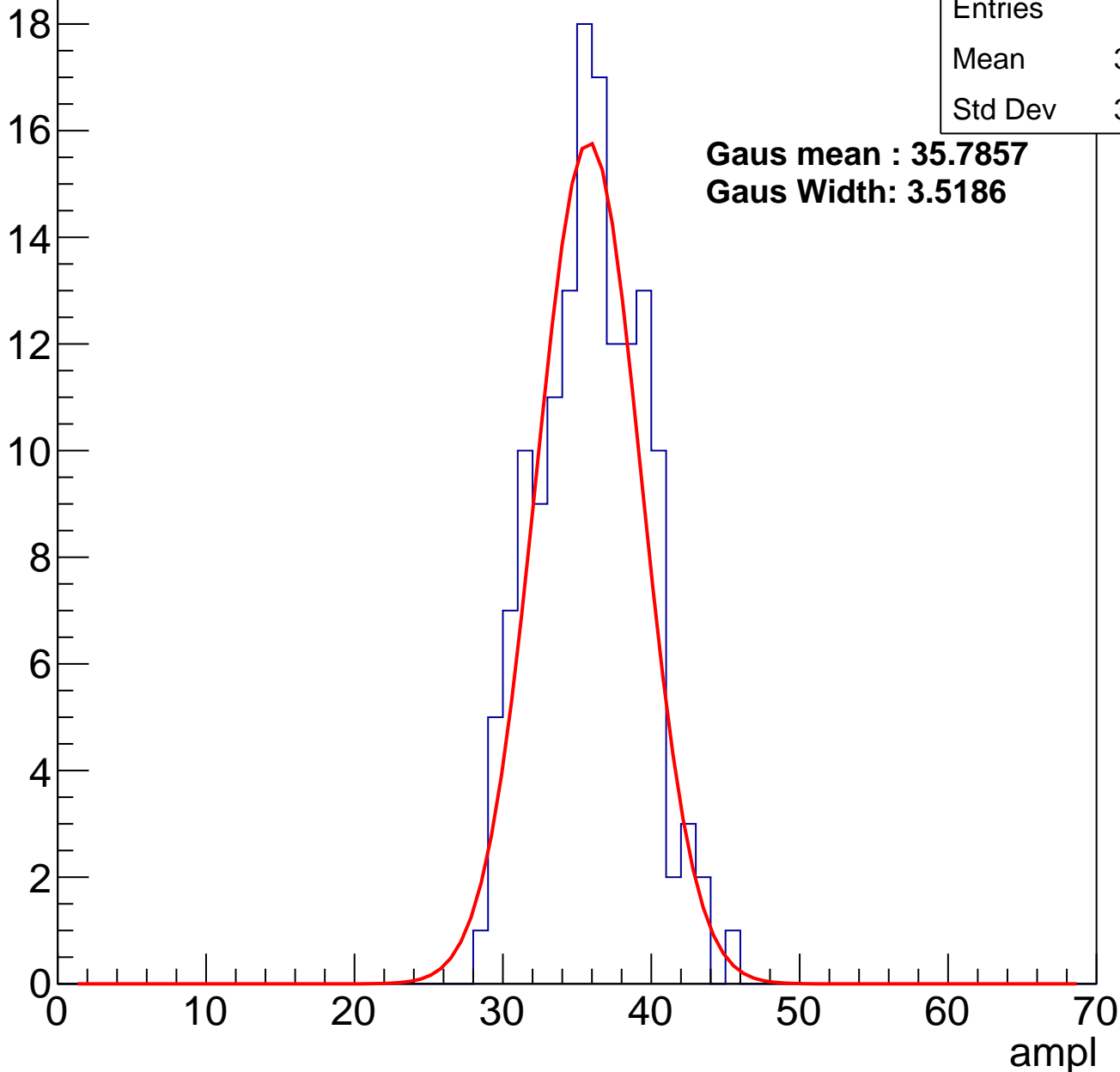
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	35.44
Std Dev	3.464

**Gaus mean : 35.7857**

**Gaus Width: 3.5186**

Entry



# B0L101S, U26-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	42.84
Std Dev	3.788

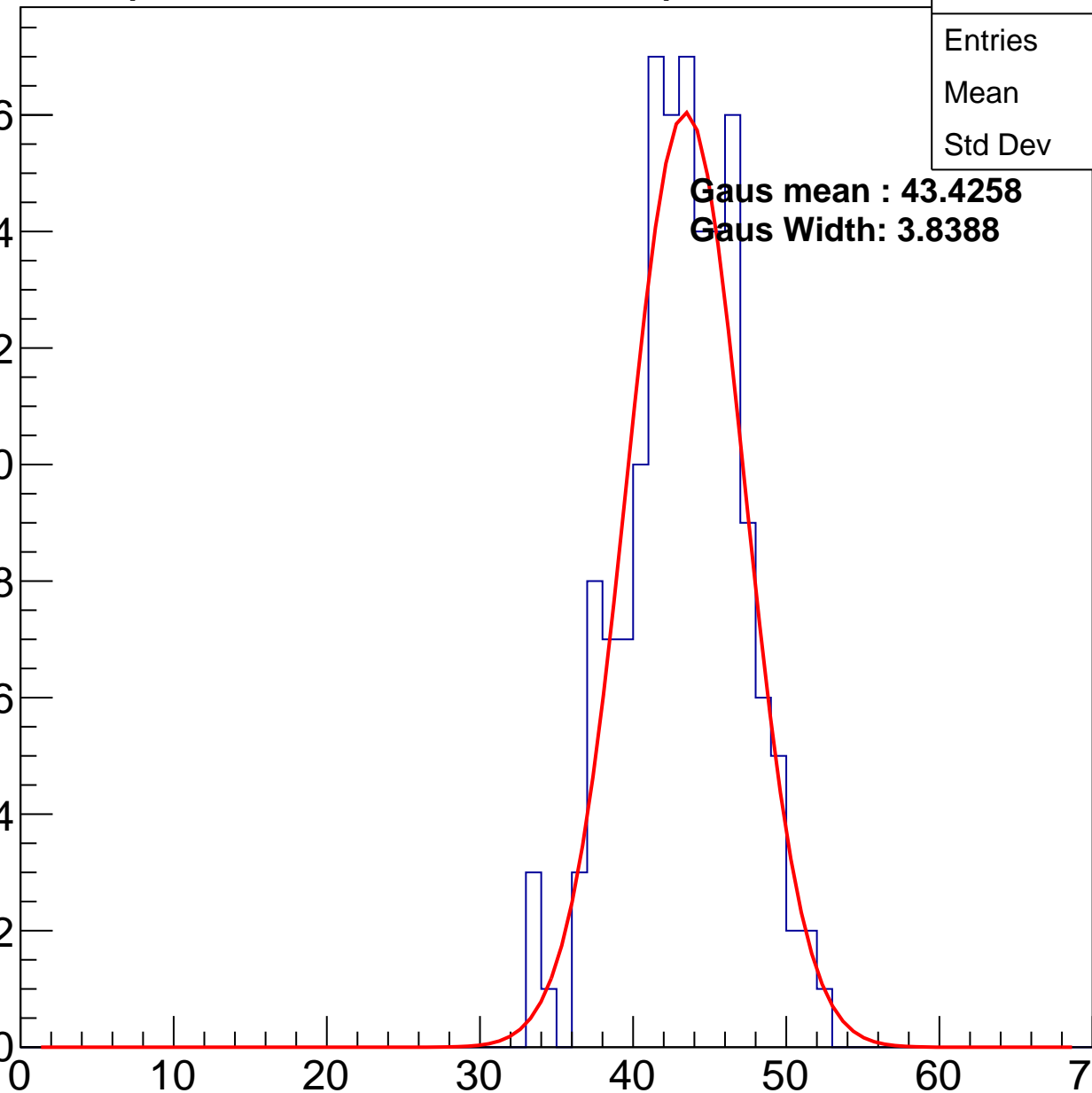
**Gaus mean : 43.4258**

**Gaus Width: 3.8388**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

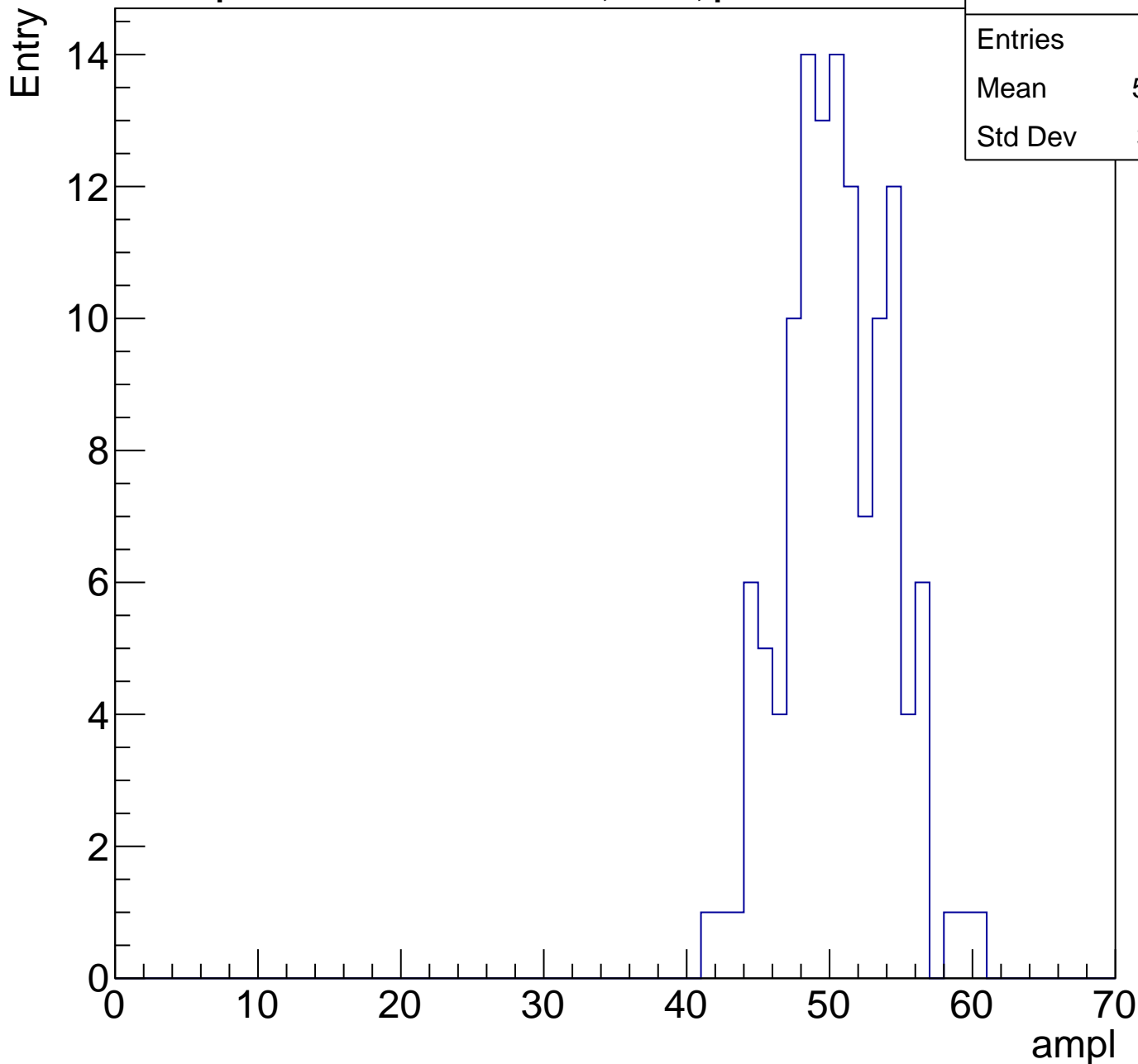
ampl



# B0L101S, U26-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	50.12
Std Dev	3.651



# B0L101S, U26-ch84, adc4

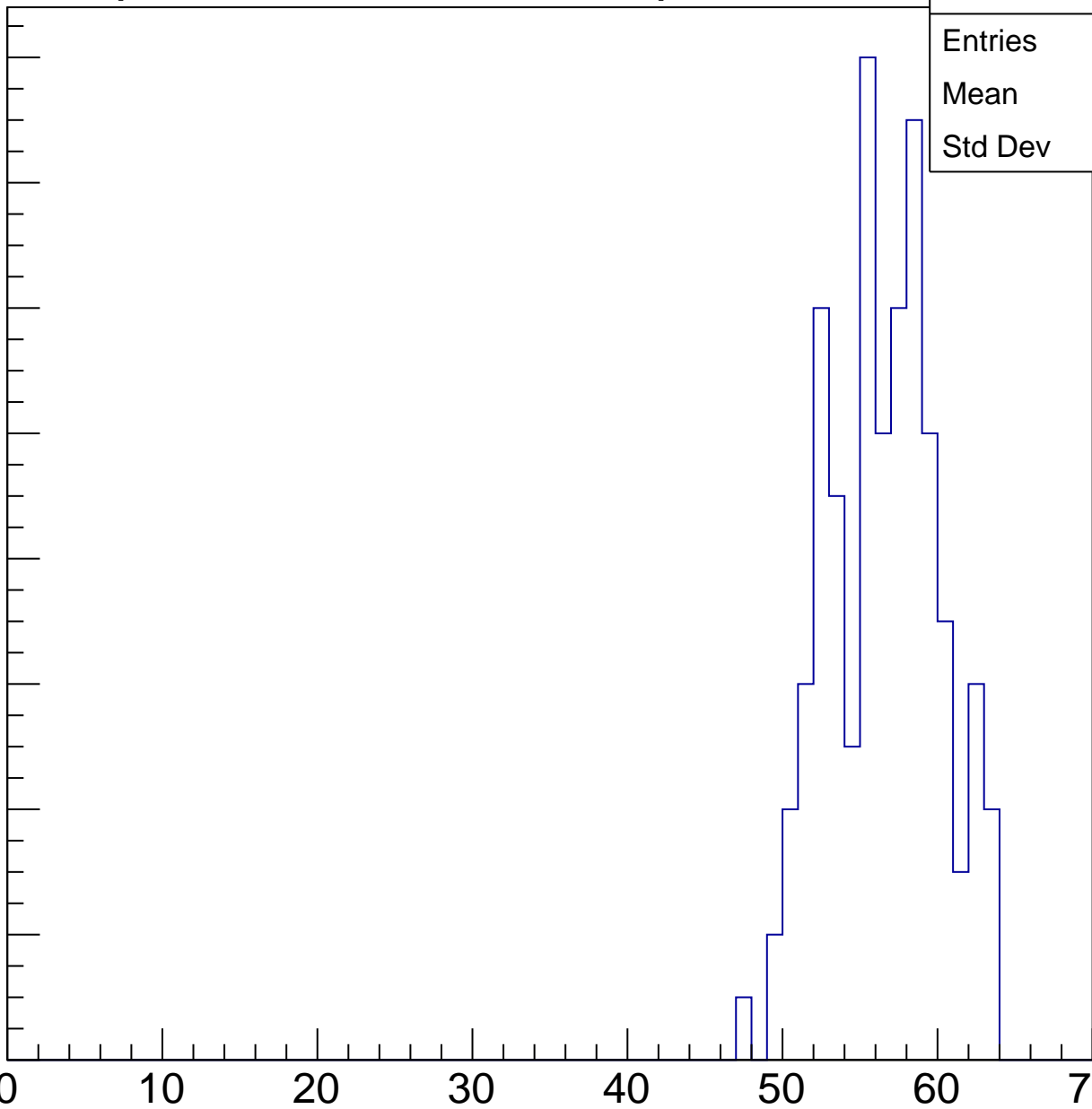
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	56.01
Std Dev	3.556

ampl



# B0L101S, U26-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	75
Mean	58.15
Std Dev	9.939

ampl

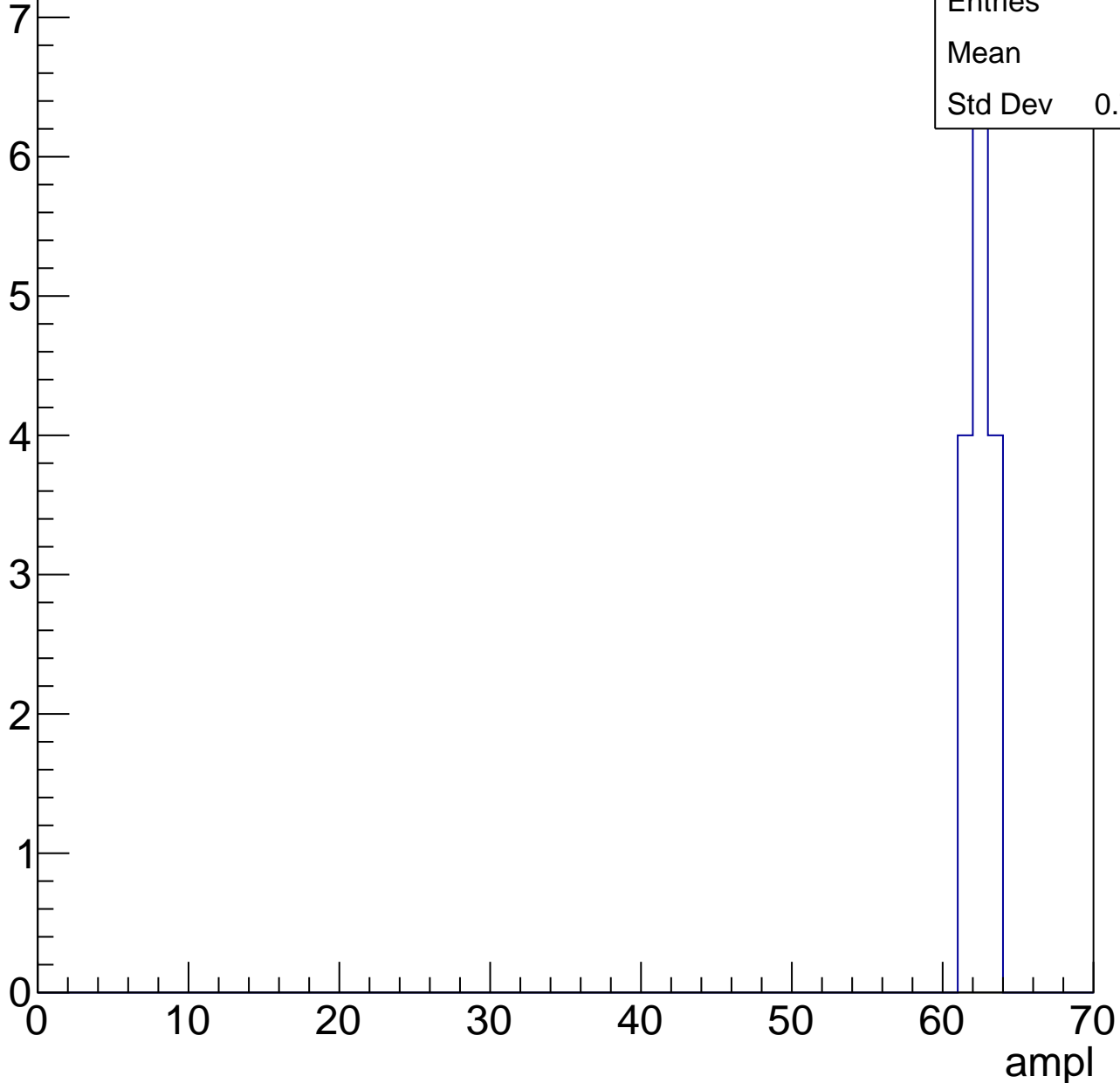
0 10 20 30 40 50 60 70

# B0L101S, U26-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	62
Std Dev	0.7303





# B0L101S, U26-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch85, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	28.01
Std Dev	5.421

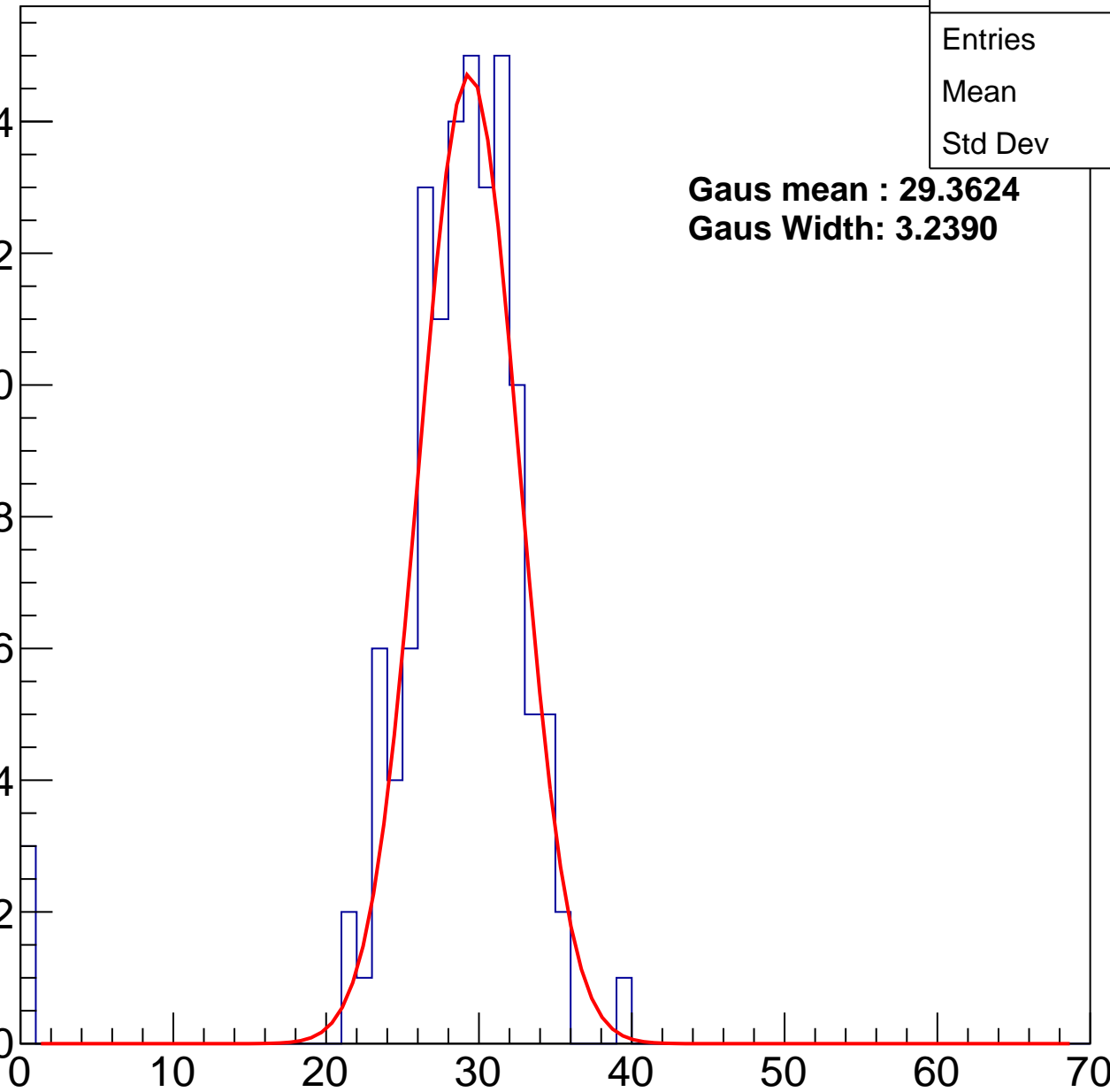
**Gaus mean : 29.3624**

**Gaus Width: 3.2390**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	190
Mean	36.43
Std Dev	4.829

**Gaus mean : 37.2838**

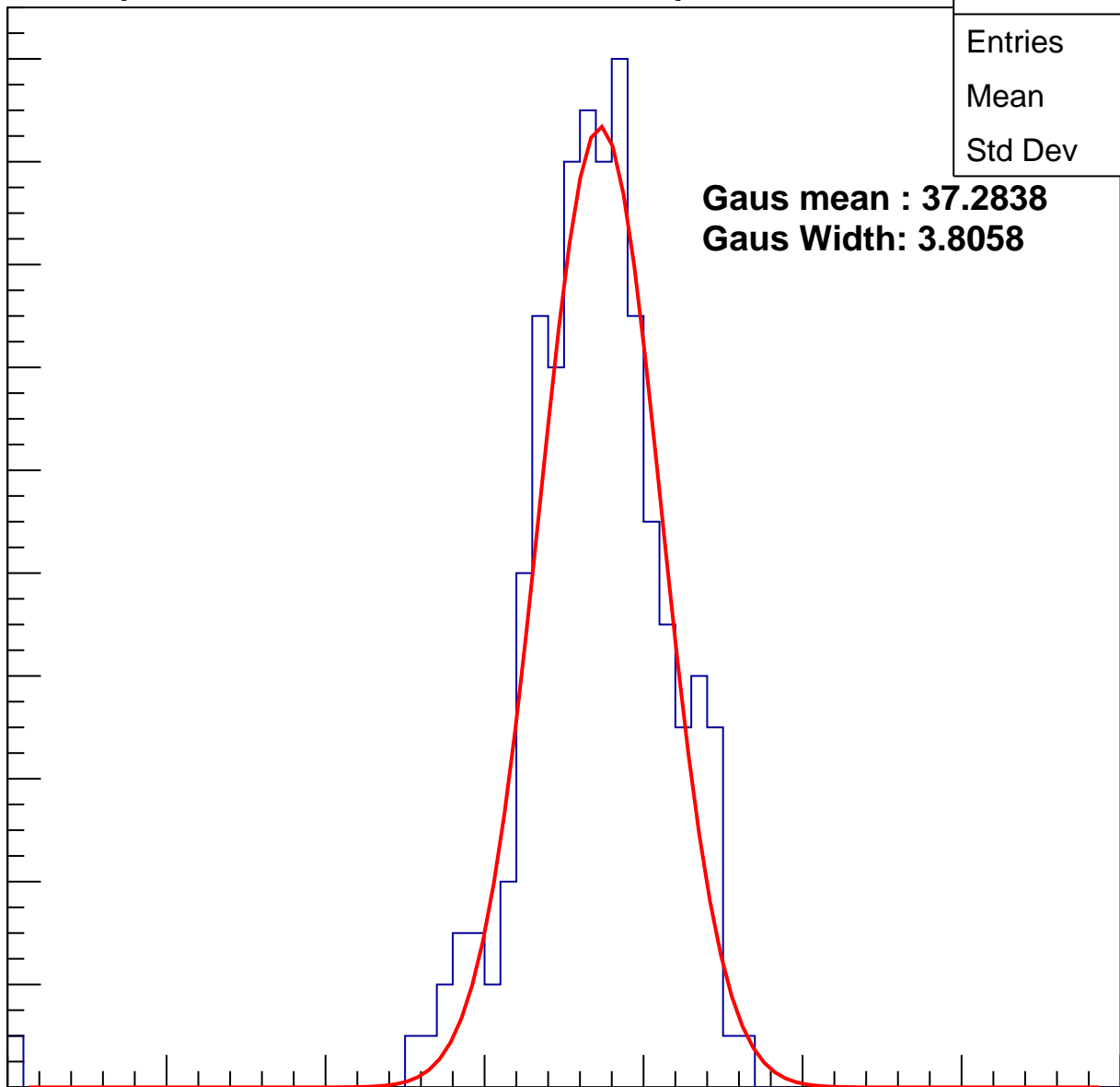
**Gaus Width: 3.8058**

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



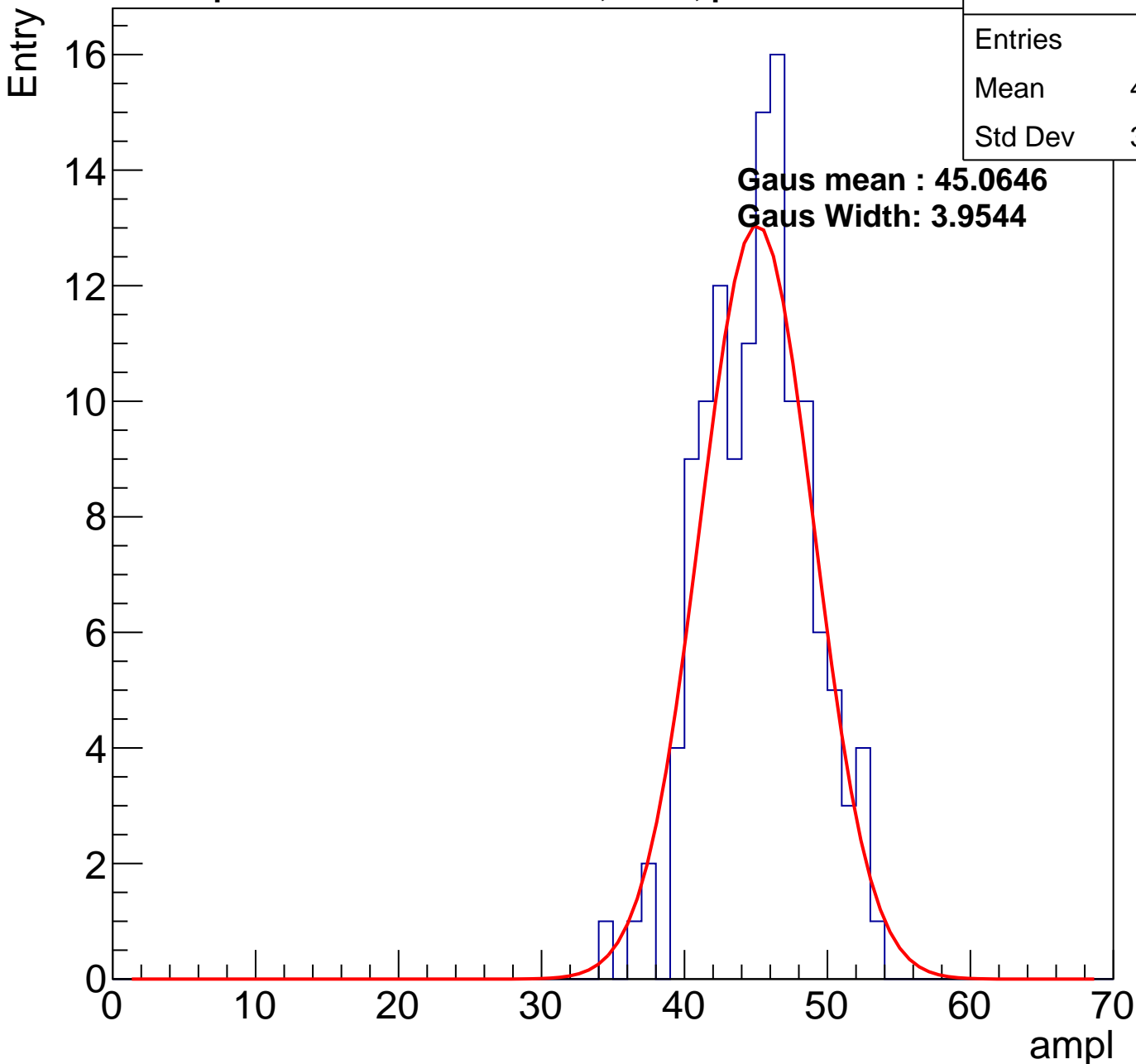
# B0L101S, U26-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	44.68
Std Dev	3.668

**Gaus mean : 45.0646**

**Gaus Width: 3.9544**

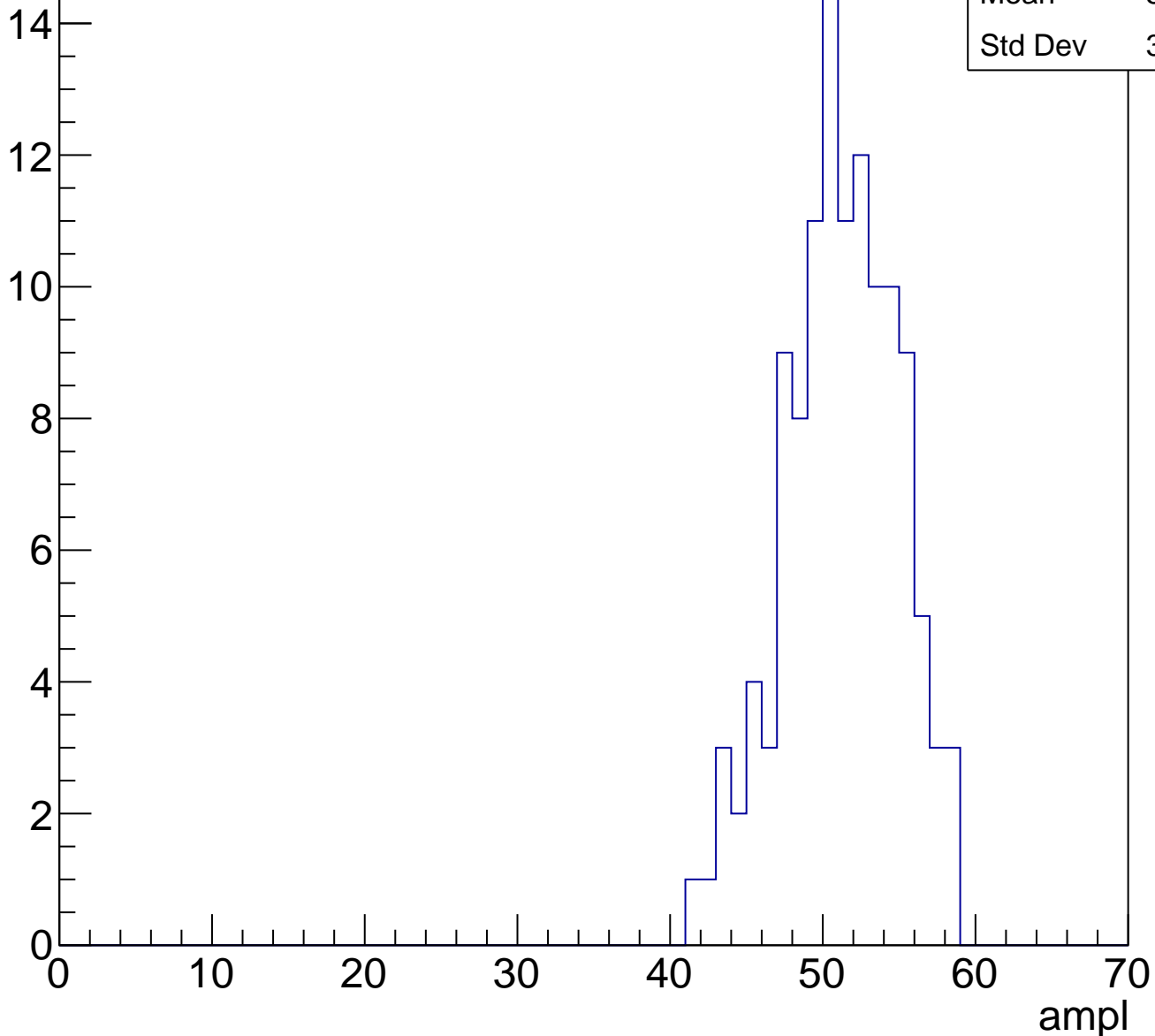


# B0L101S, U26-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	50.74
Std Dev	3.678

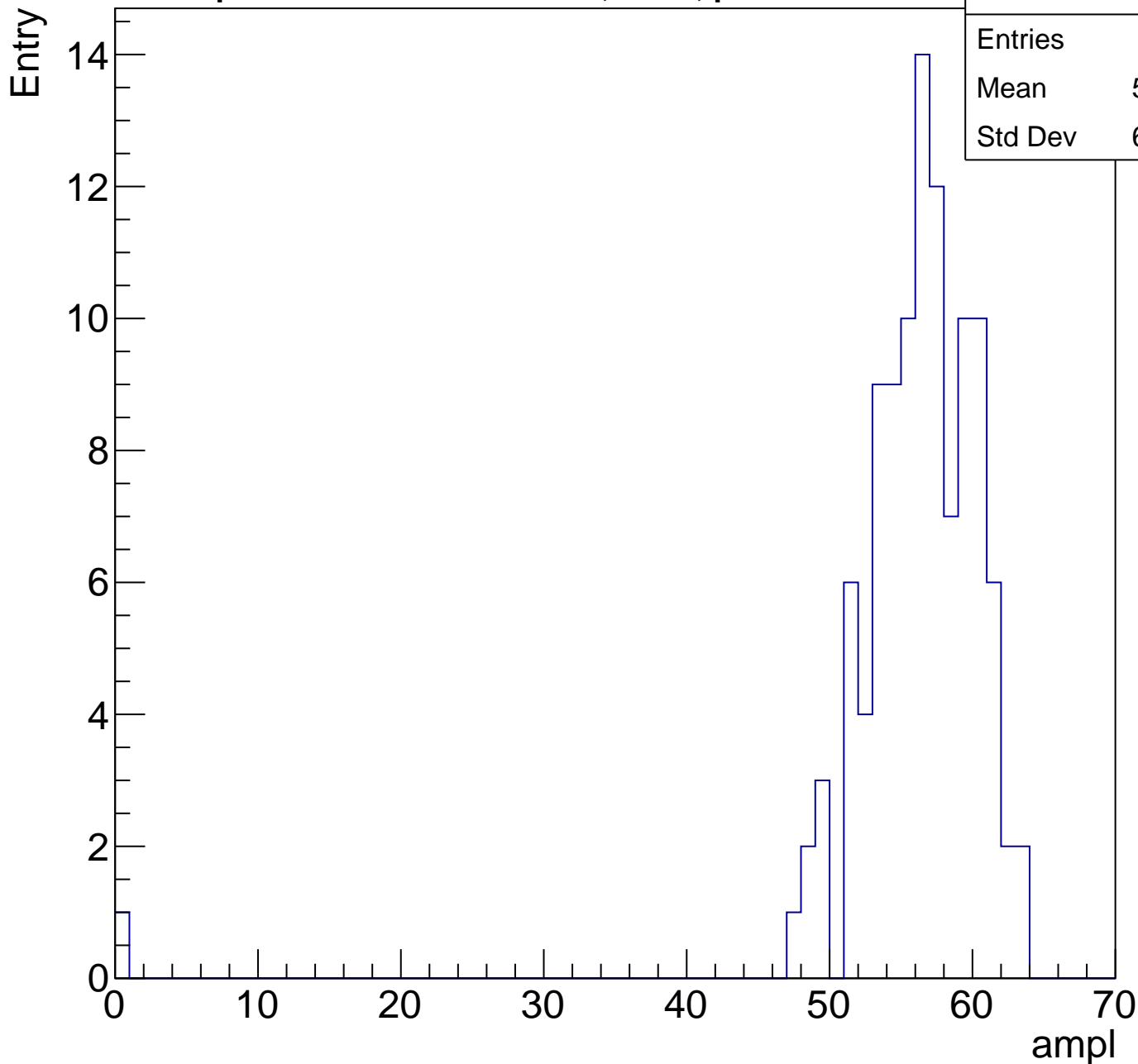
Entry



# B0L101S, U26-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	55.53
Std Dev	6.397



# B0L101S, U26-ch85, adc5

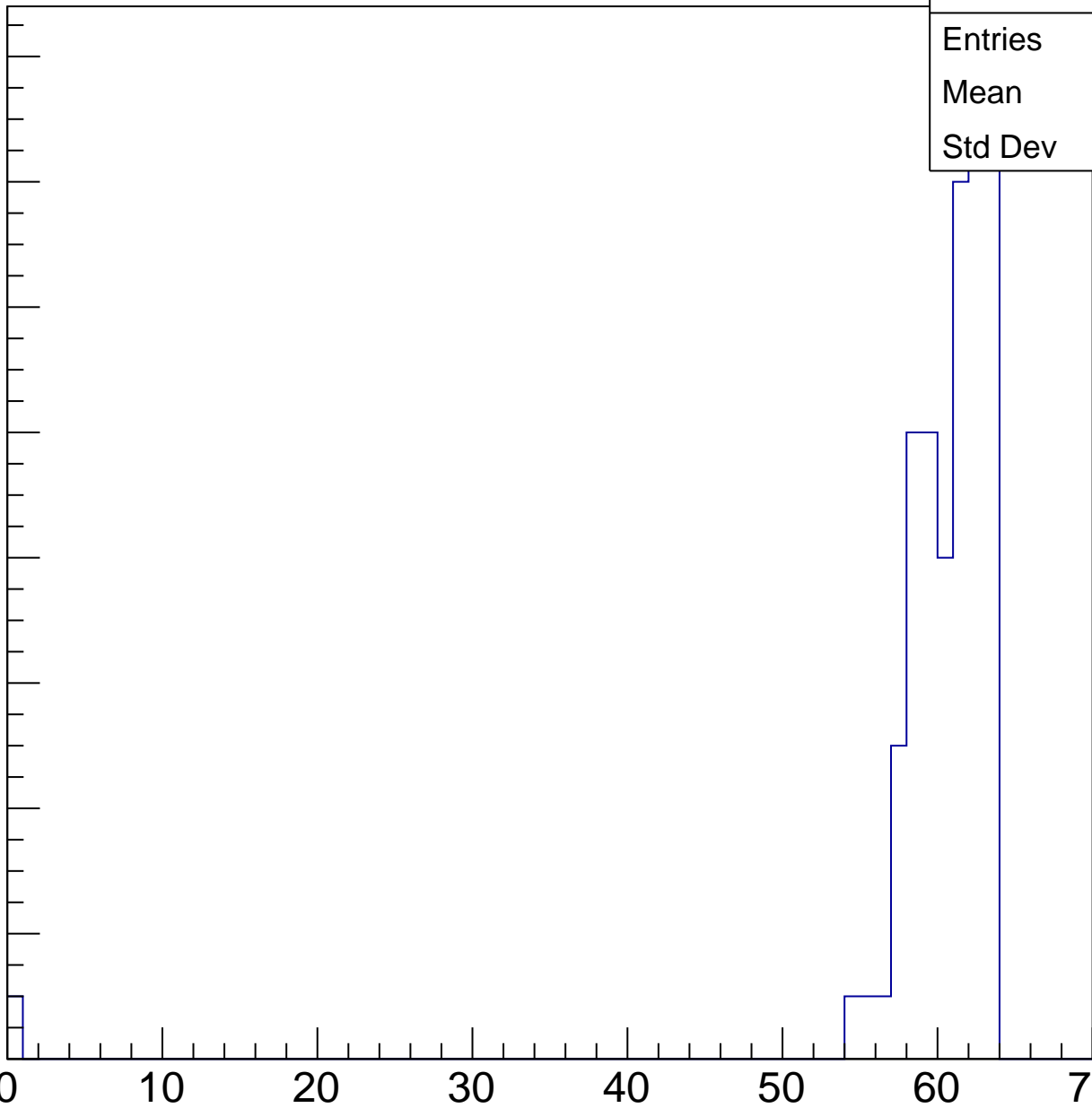
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	82
Mean	59.65
Std Dev	6.962

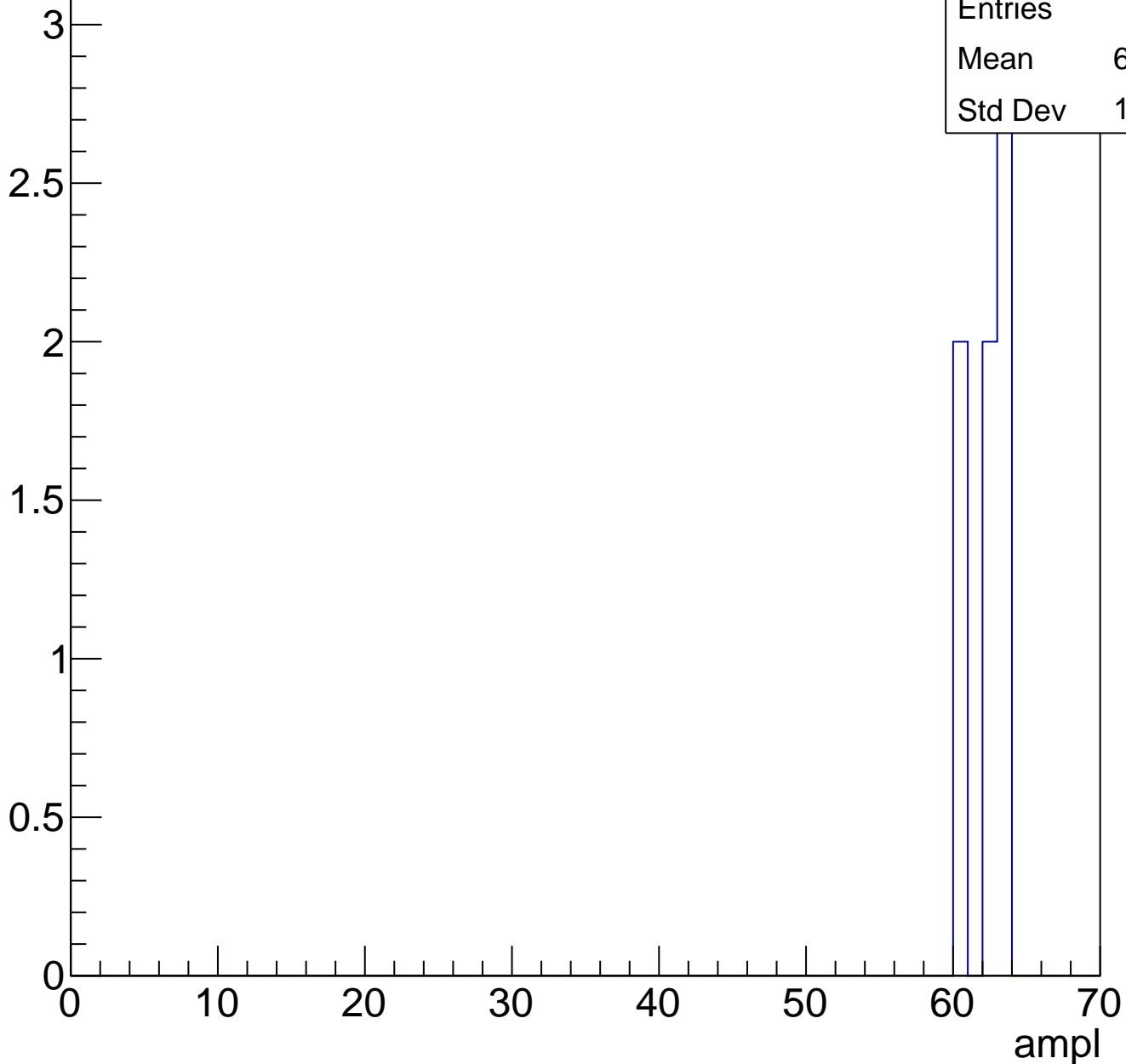
ampl



# B0L101S, U26-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	28.59
Std Dev	3.79

**Gaus mean : 29.4426**

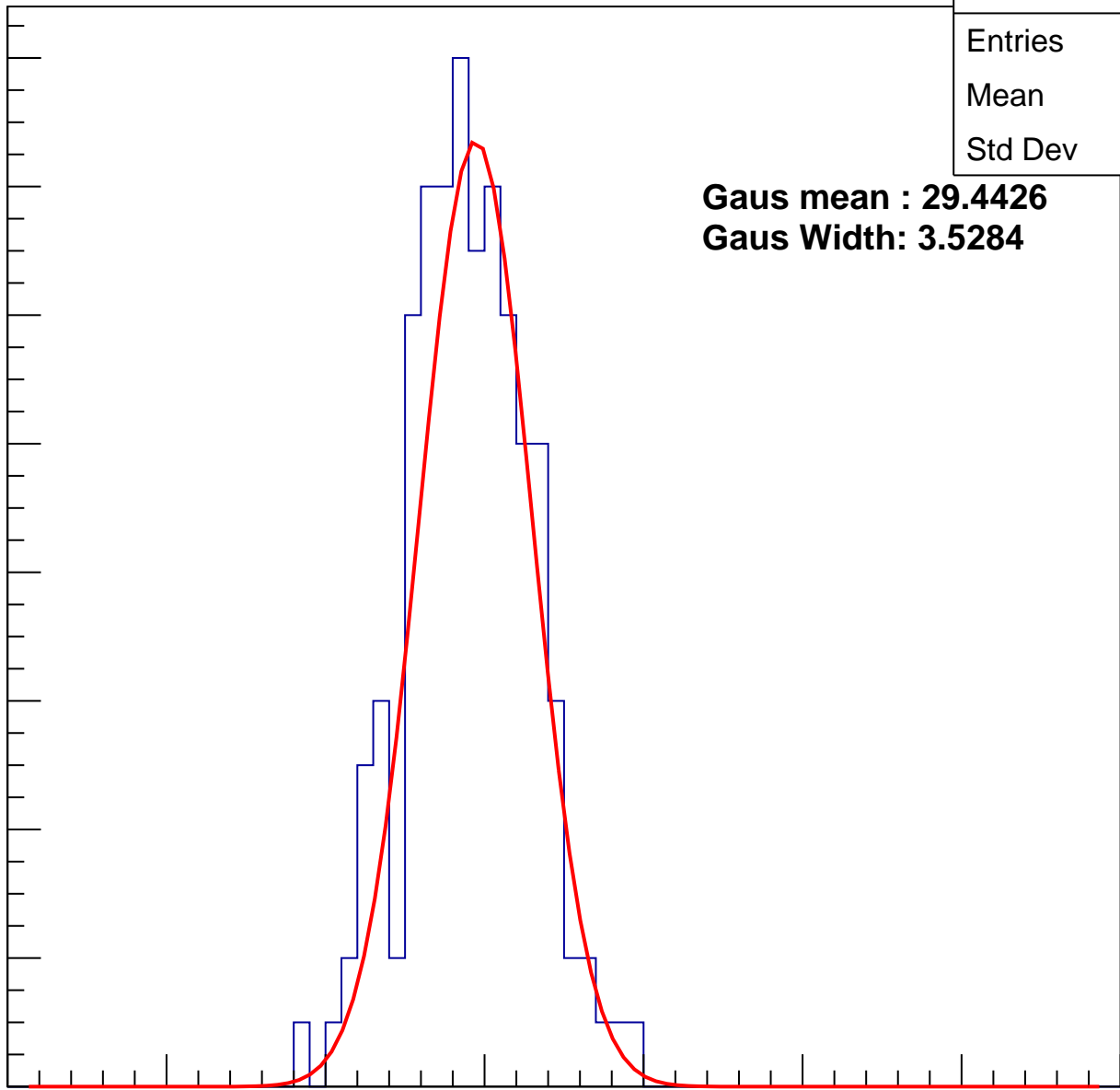
**Gaus Width: 3.5284**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	35.77
Std Dev	3.669

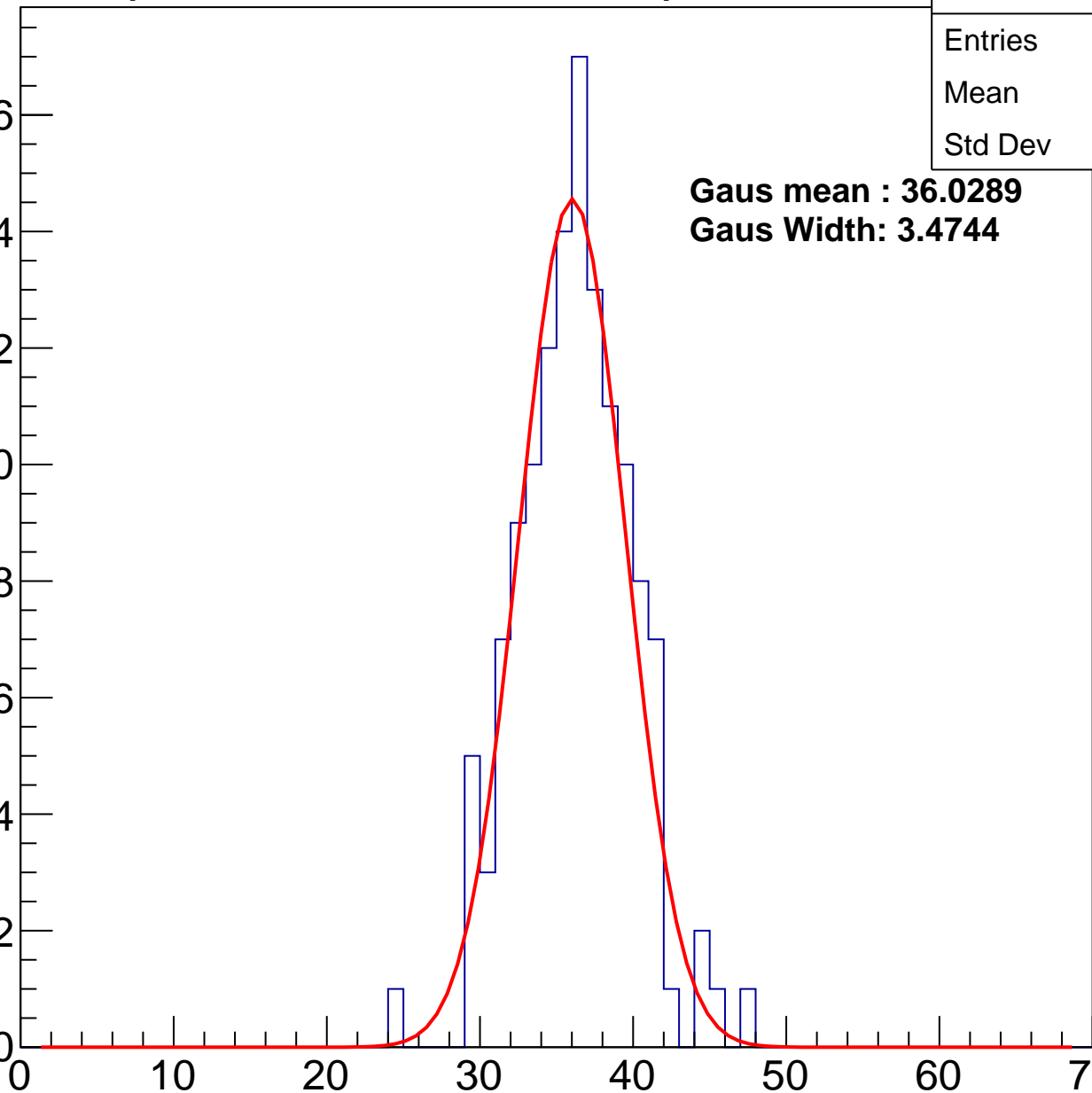
**Gaus mean : 36.0289**

**Gaus Width: 3.4744**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

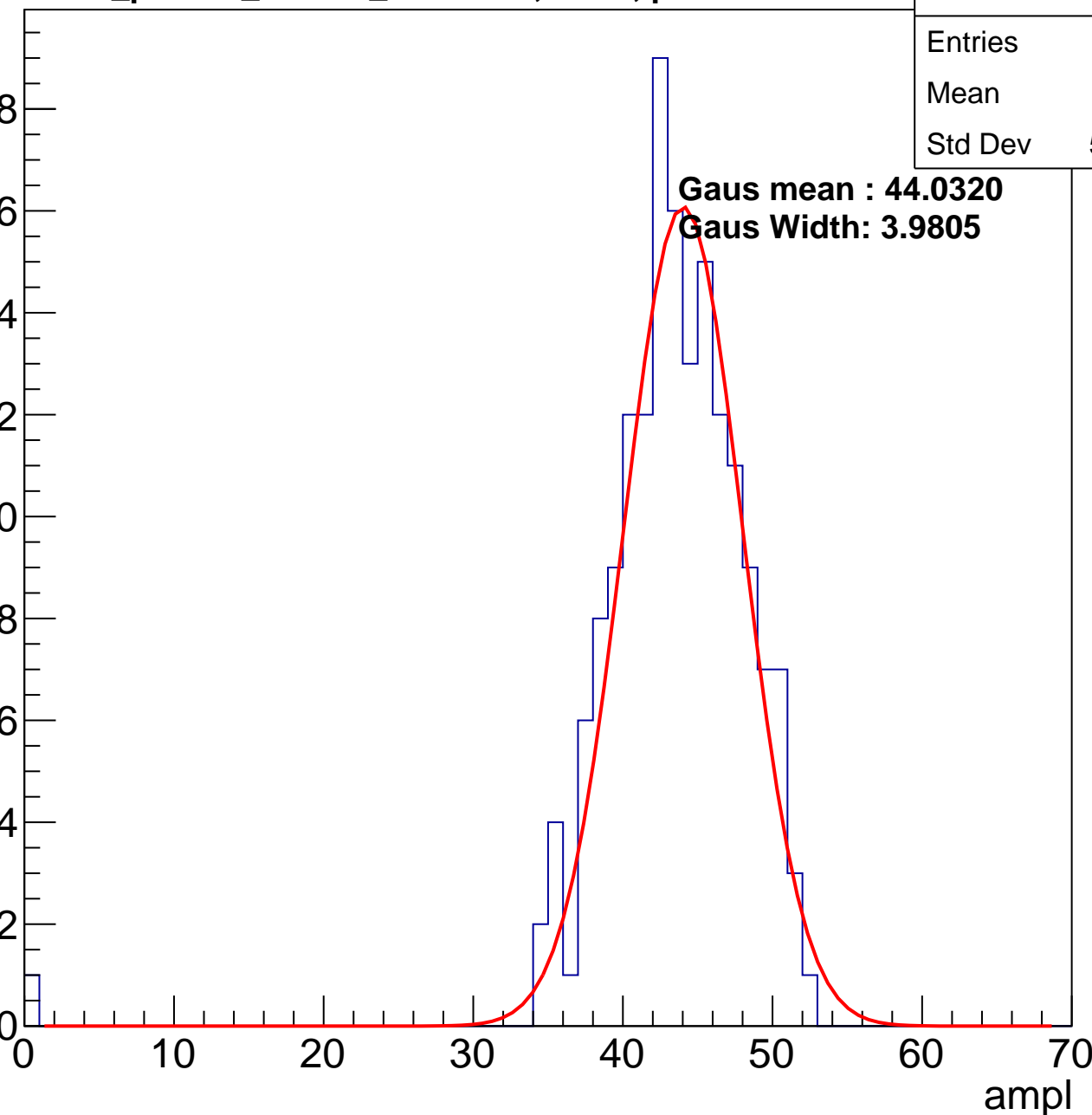
Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	168
Mean	43.01
Std Dev	5.176

**Gaus mean : 44.0320**

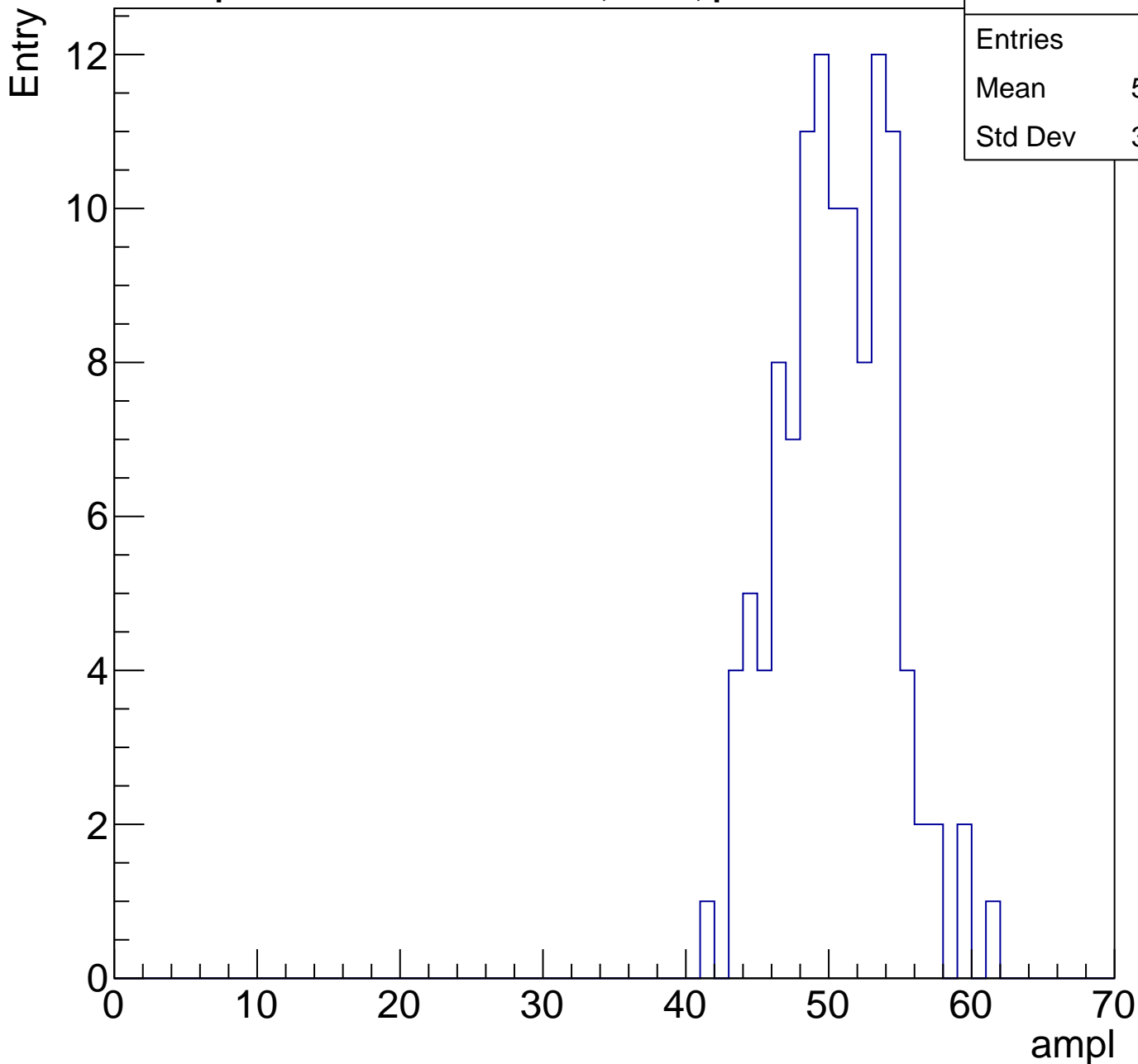
**Gaus Width: 3.9805**



# B0L101S, U26-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	114
Mean	50.06
Std Dev	3.842



# B0L101S, U26-ch86, adc4

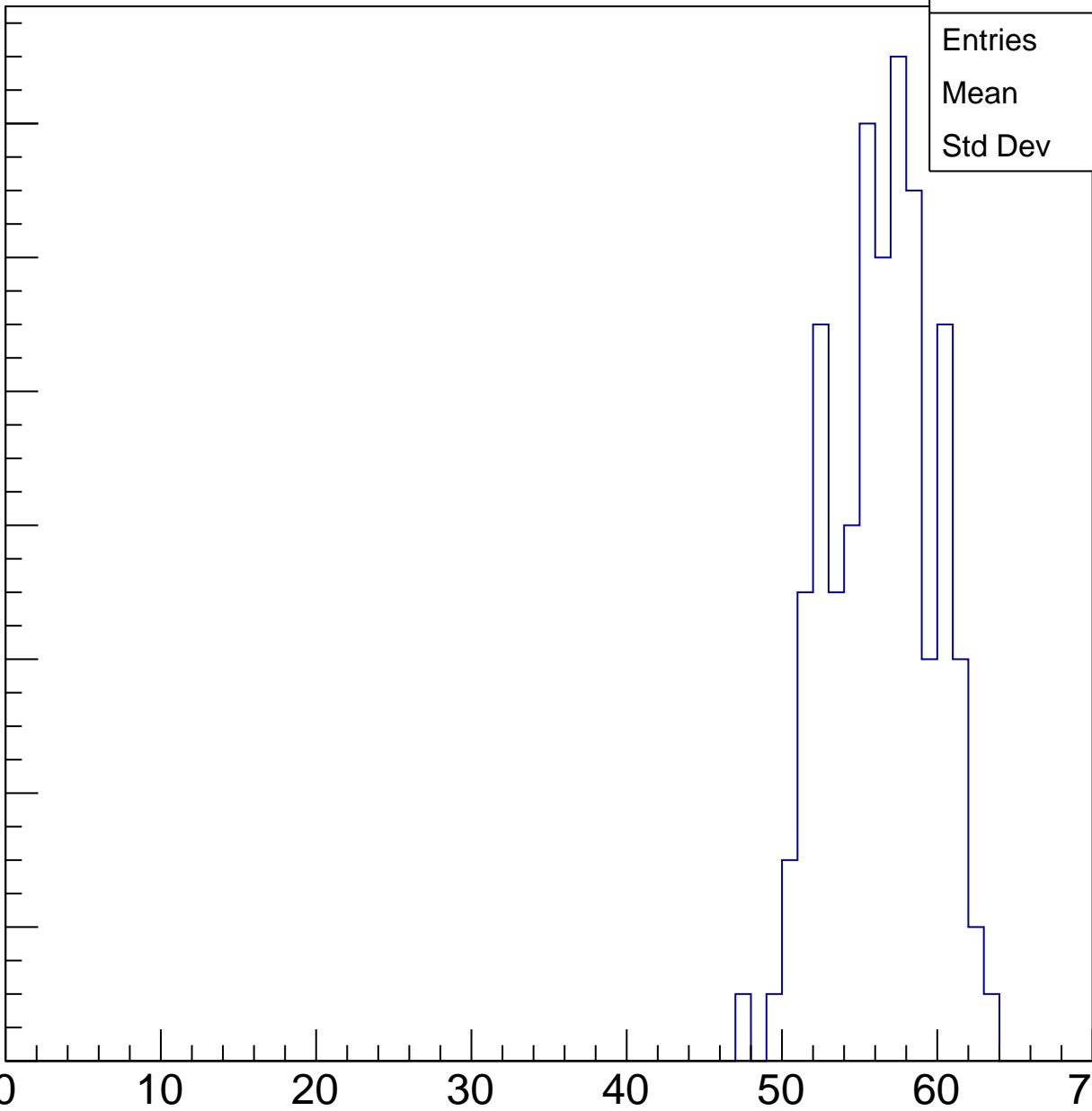
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	55.9
Std Dev	3.274

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch86, adc5

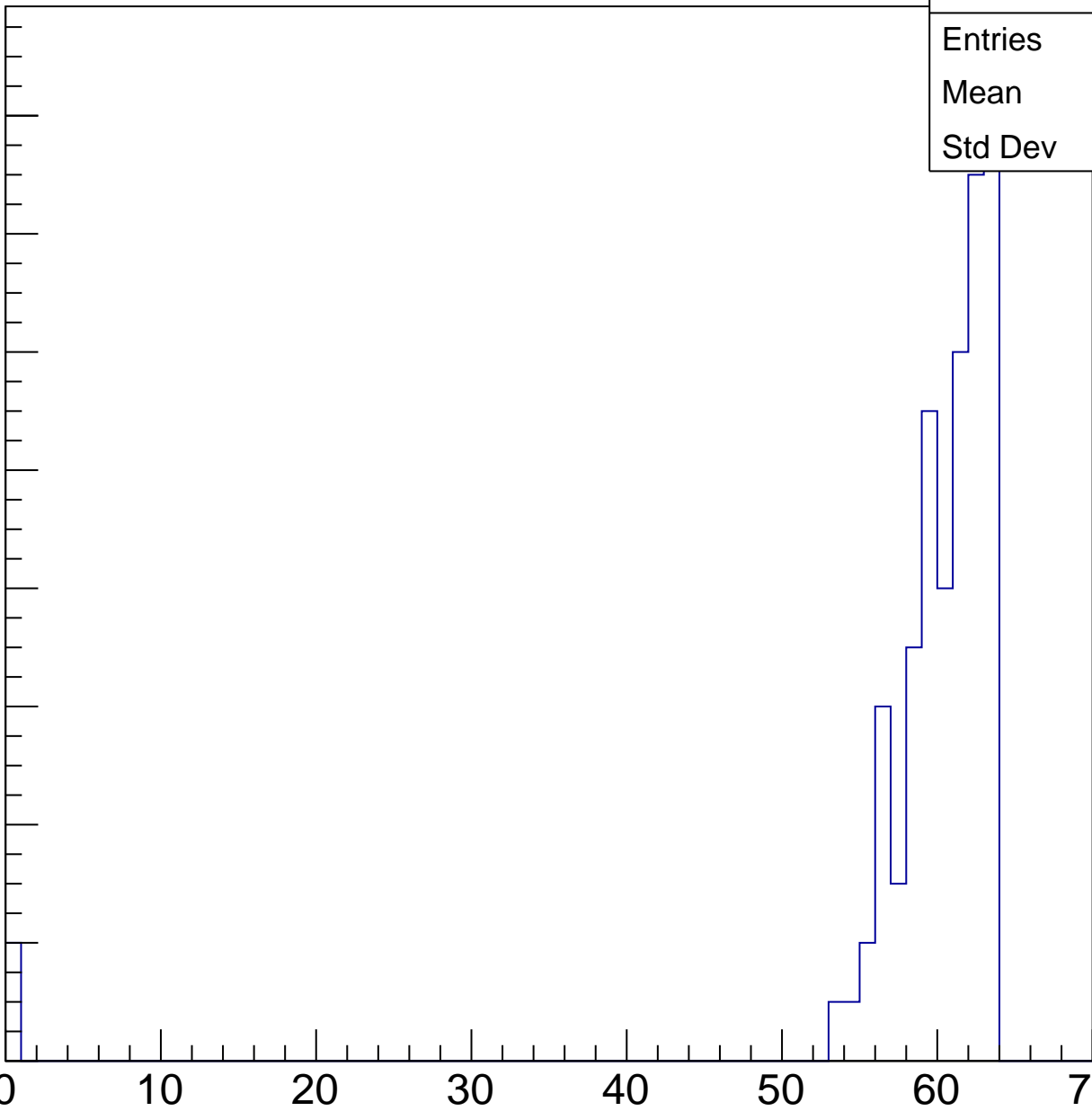
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	58.73
Std Dev	9.446

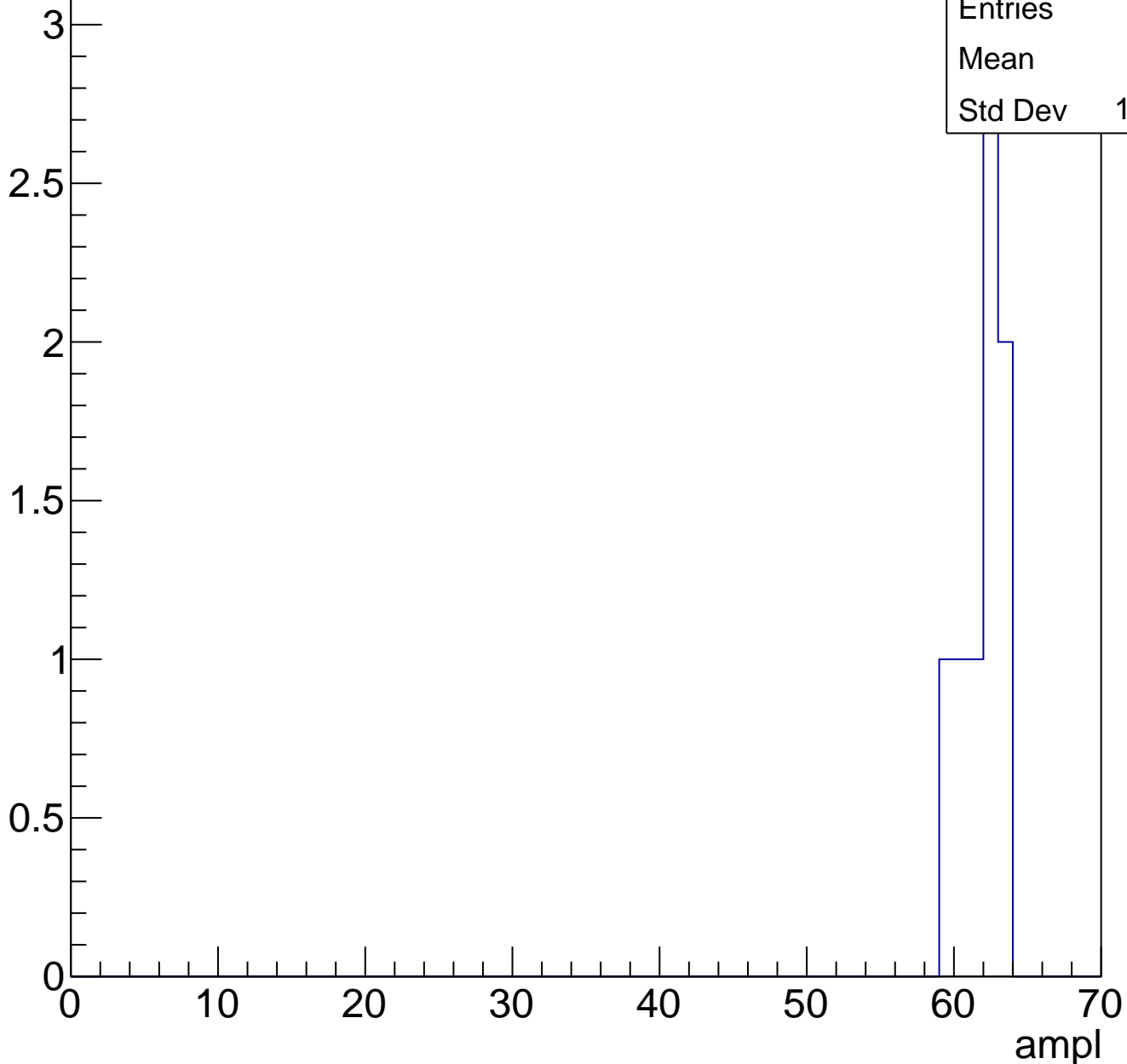
ampl



# B0L101S, U26-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



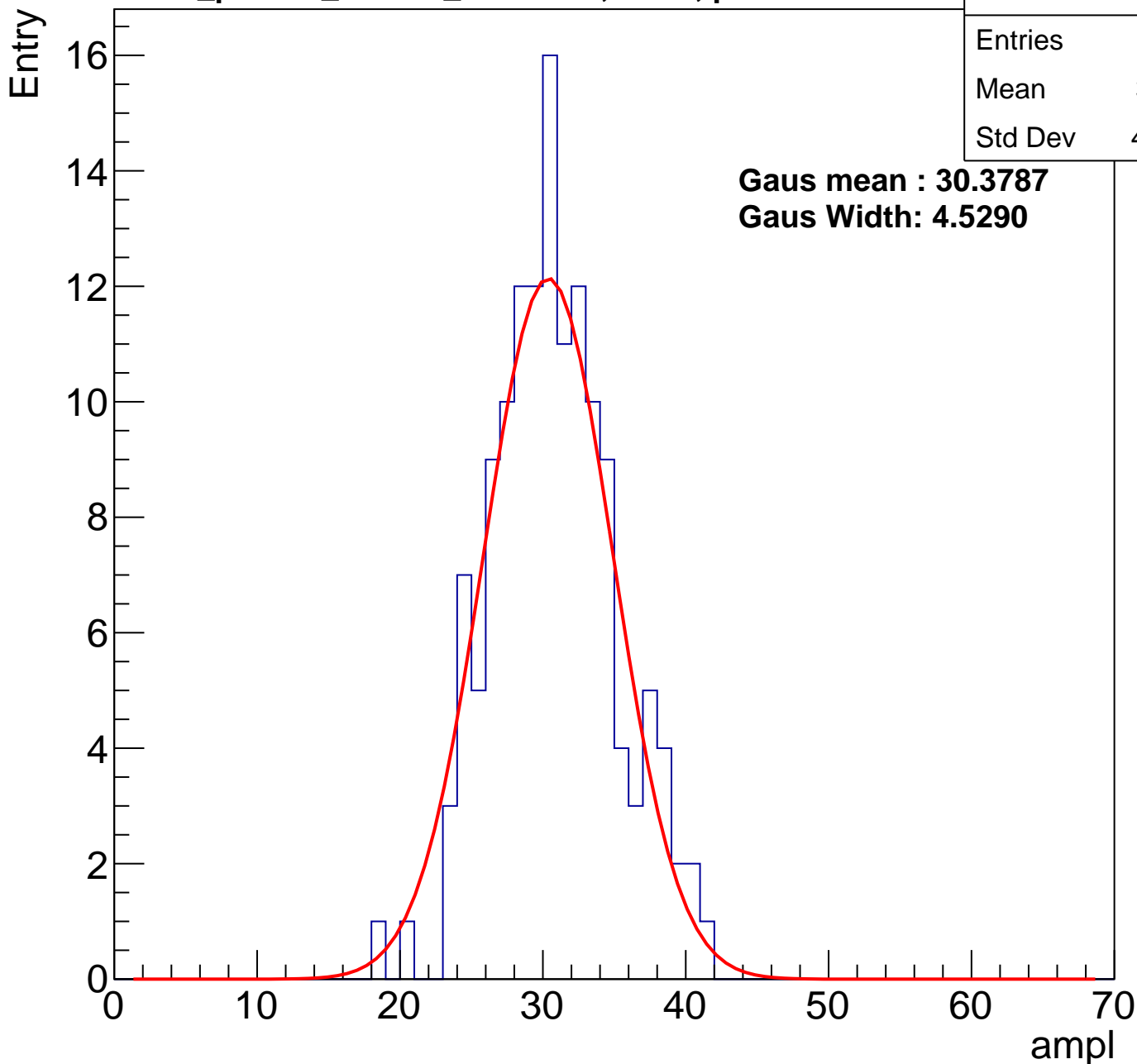
# B0L101S, U26-ch87, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	30.31
Std Dev	4.269

**Gaus mean : 30.3787**

**Gaus Width: 4.5290**



# B0L101S, U26-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	37.17
Std Dev	3.532

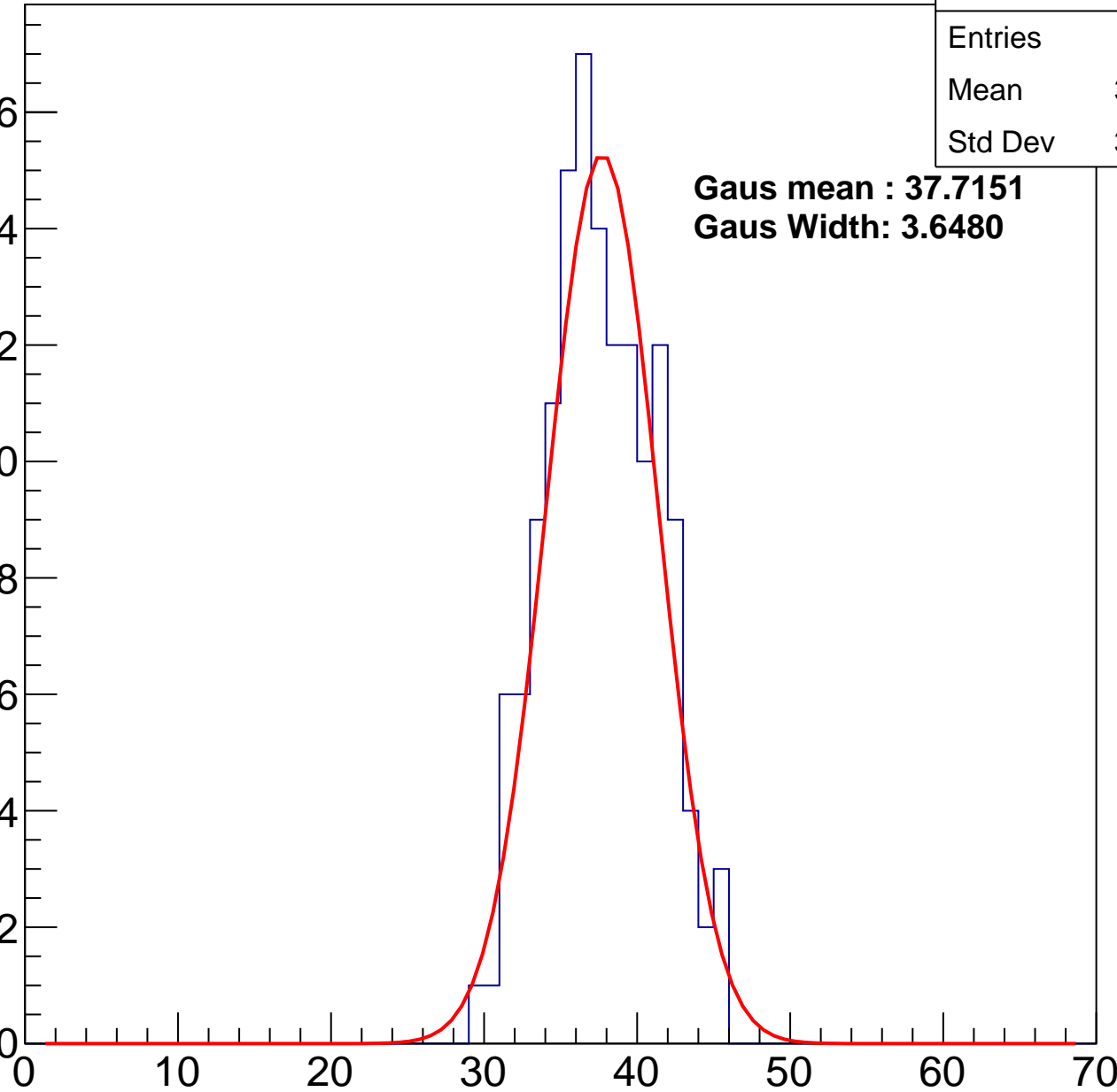
**Gaus mean : 37.7151**

**Gaus Width: 3.6480**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch87, adc2

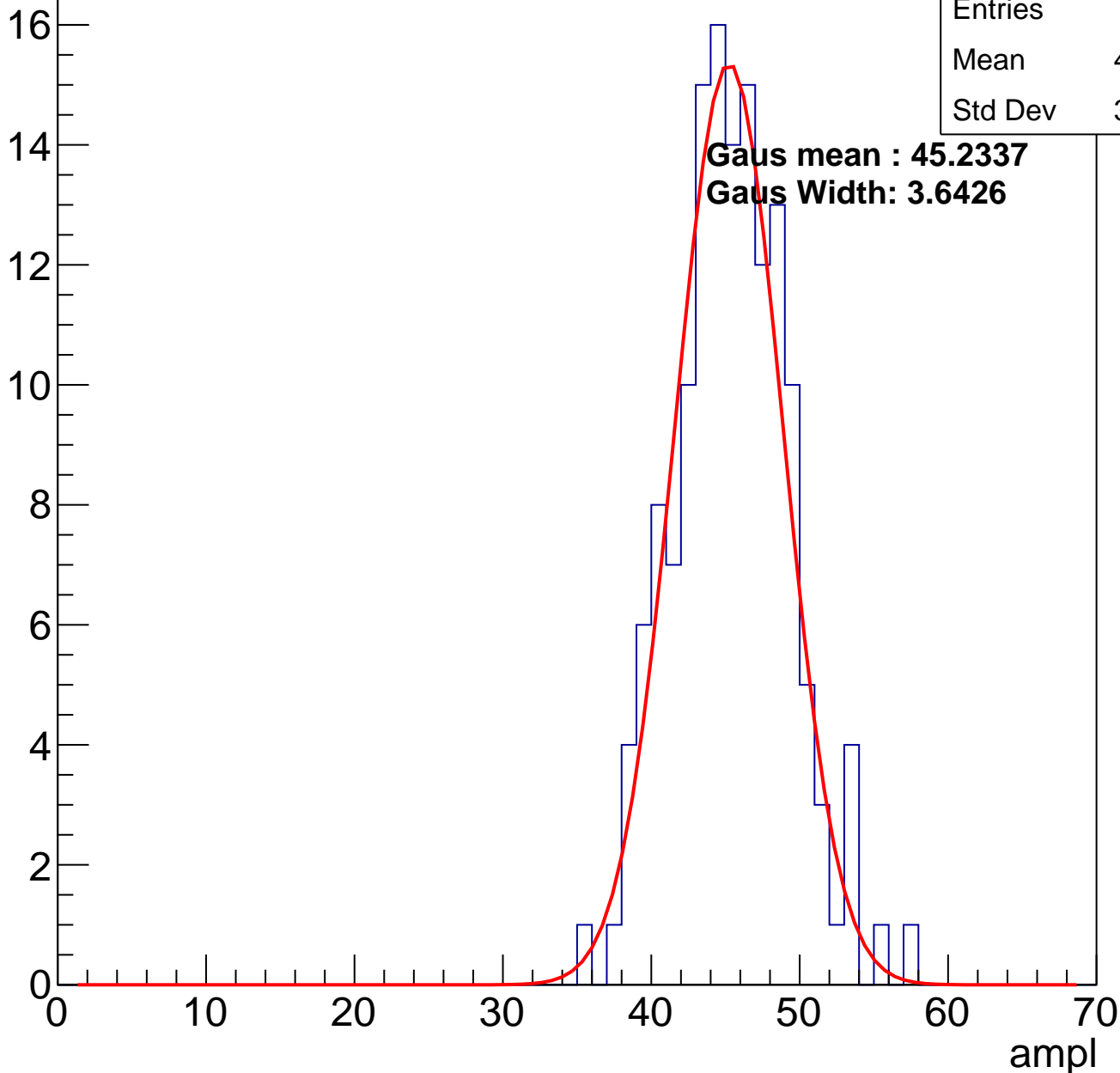
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	44.97
Std Dev	3.847

**Gaus mean : 45.2337**

**Gaus Width: 3.6426**

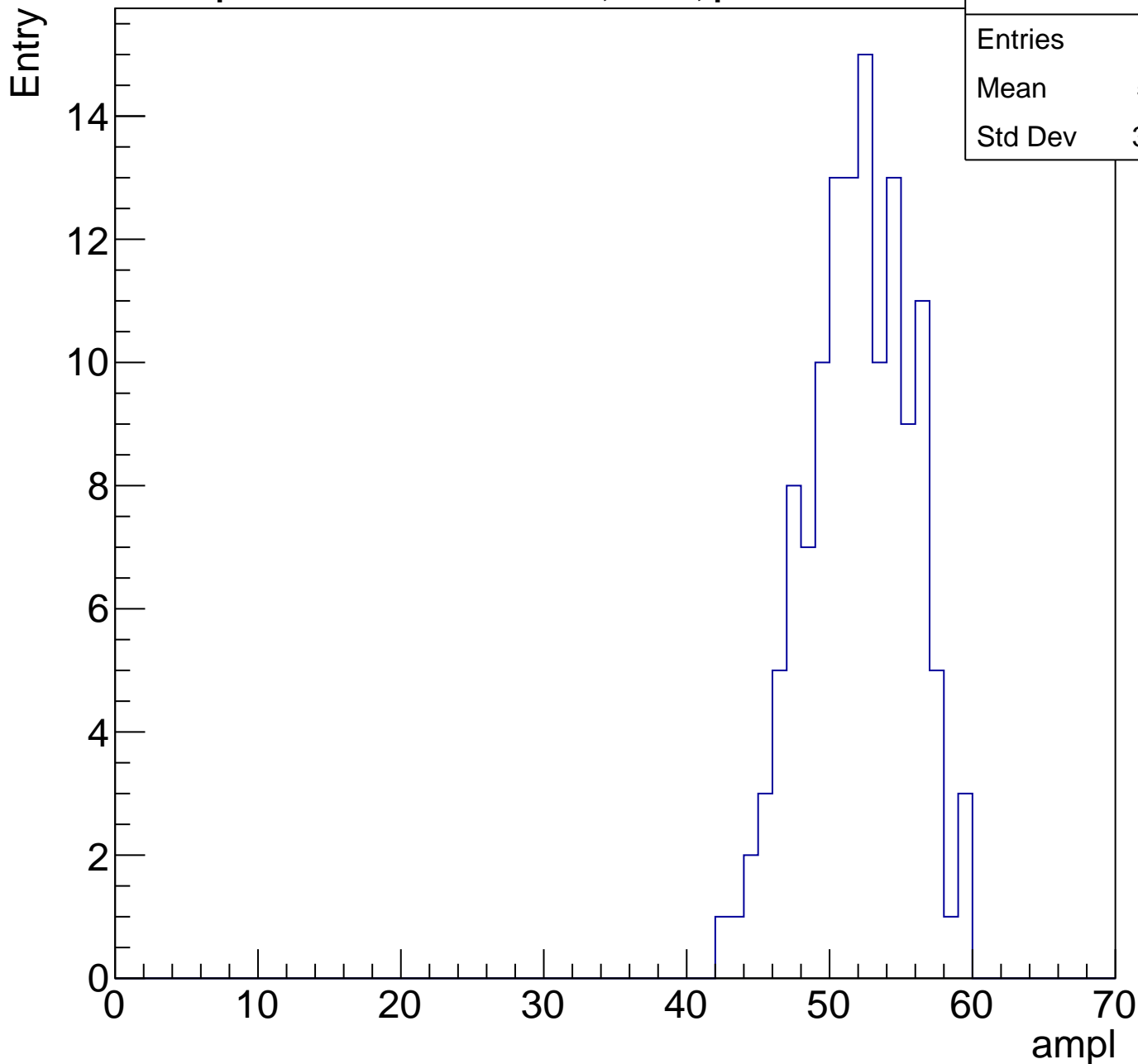
Entry



# B0L101S, U26-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	51.51
Std Dev	3.615



# B0L101S, U26-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

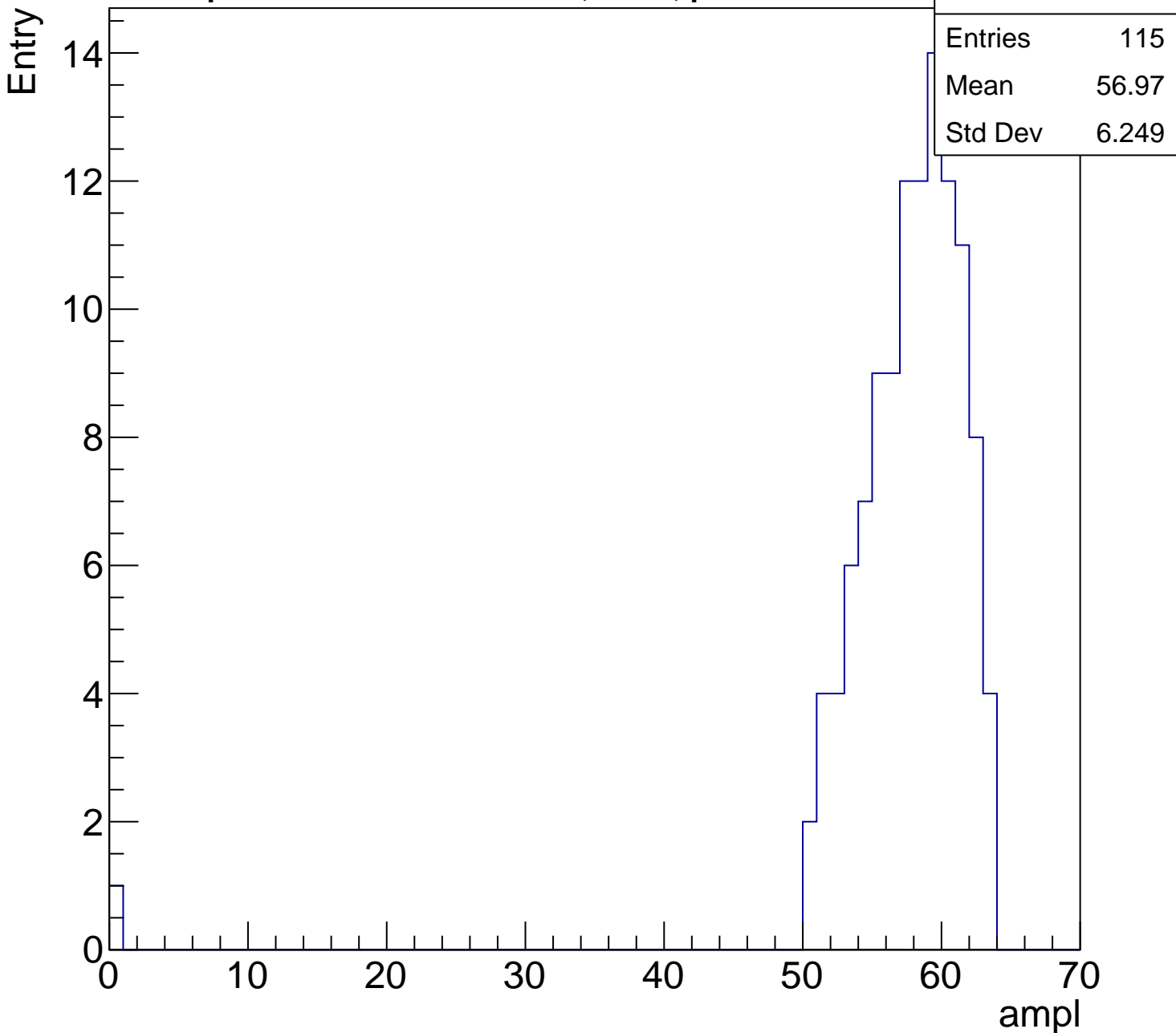
Entries	115
Mean	56.97
Std Dev	6.249

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch87, adc5

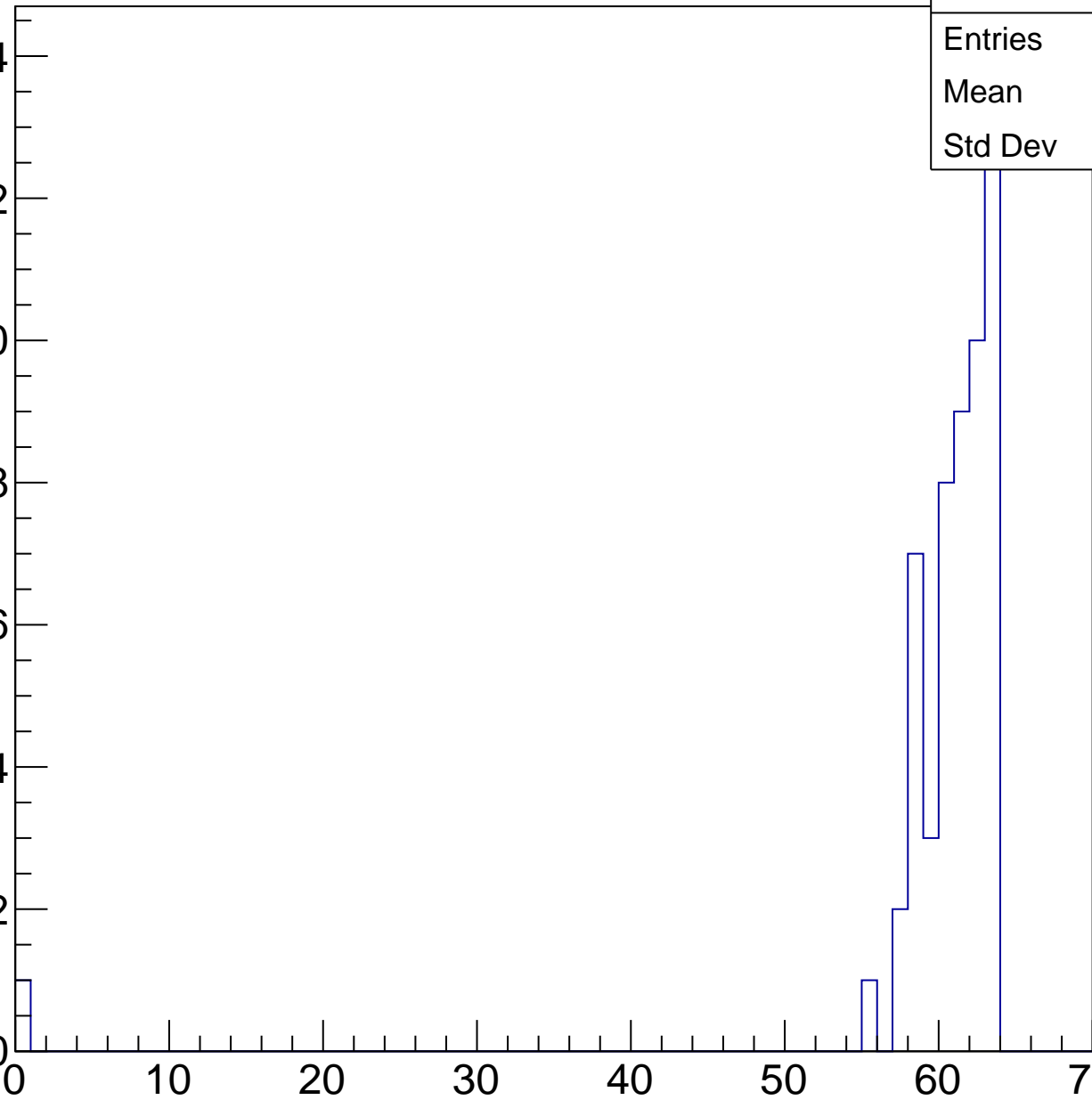
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	55
Mean	59.69
Std Dev	8.36

ampl



# B0L101S, U26-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch88, adc0

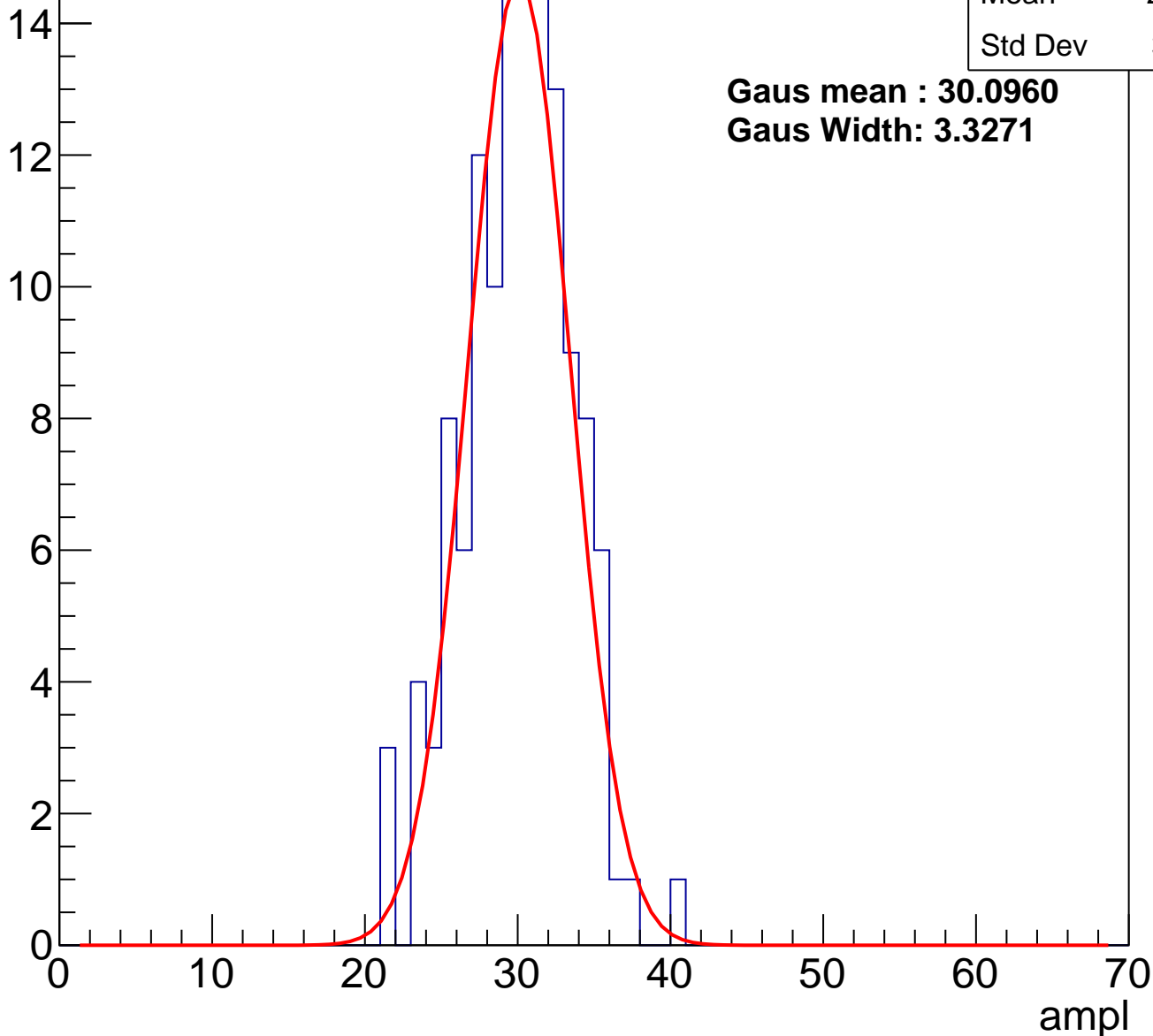
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	130
Mean	29.58
Std Dev	3.501

**Gaus mean : 30.0960**

**Gaus Width: 3.3271**

Entry



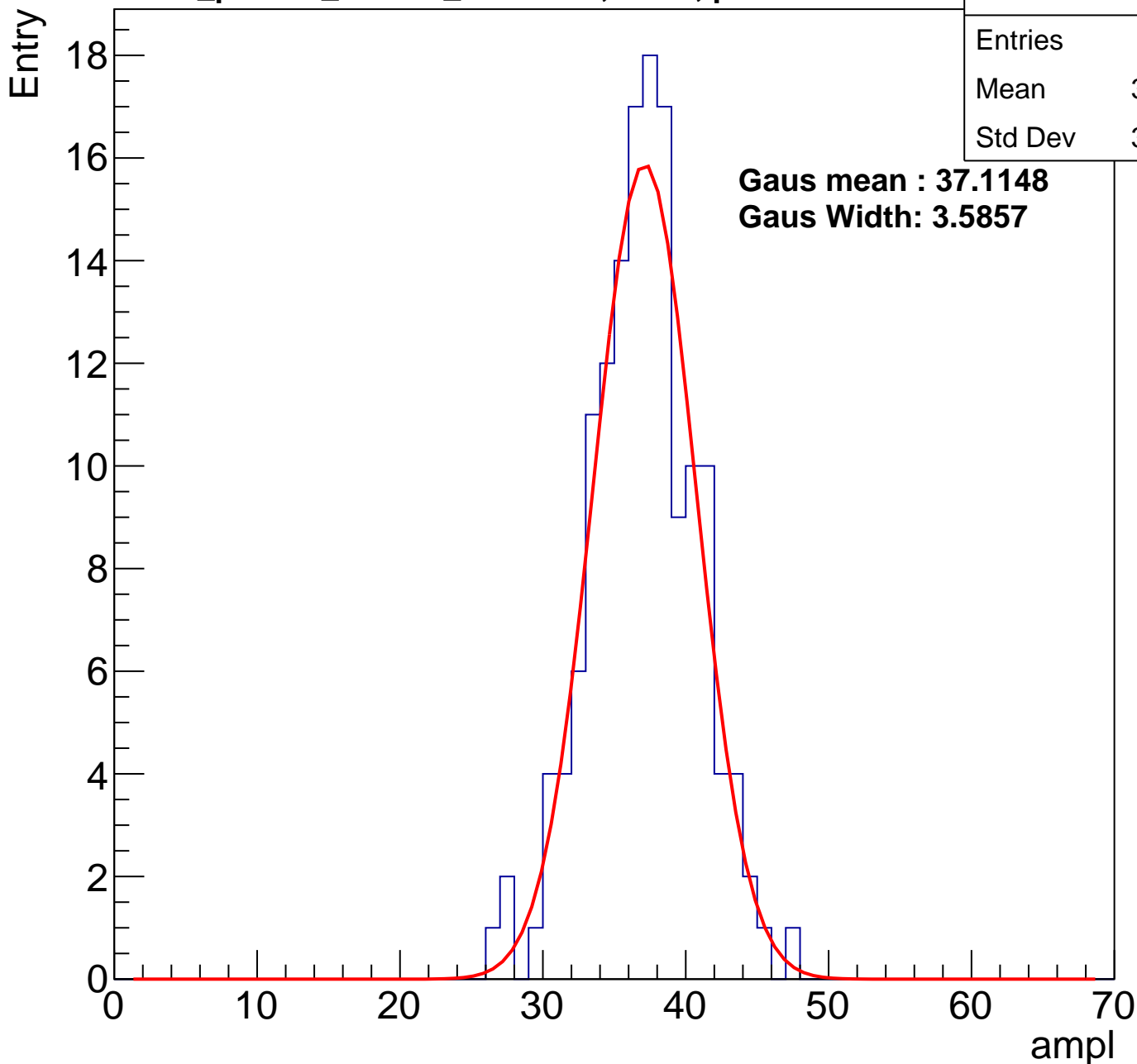
# B0L101S, U26-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	36.56
Std Dev	3.687

**Gaus mean : 37.1148**

**Gaus Width: 3.5857**



# B0L101S, U26-ch88, adc2

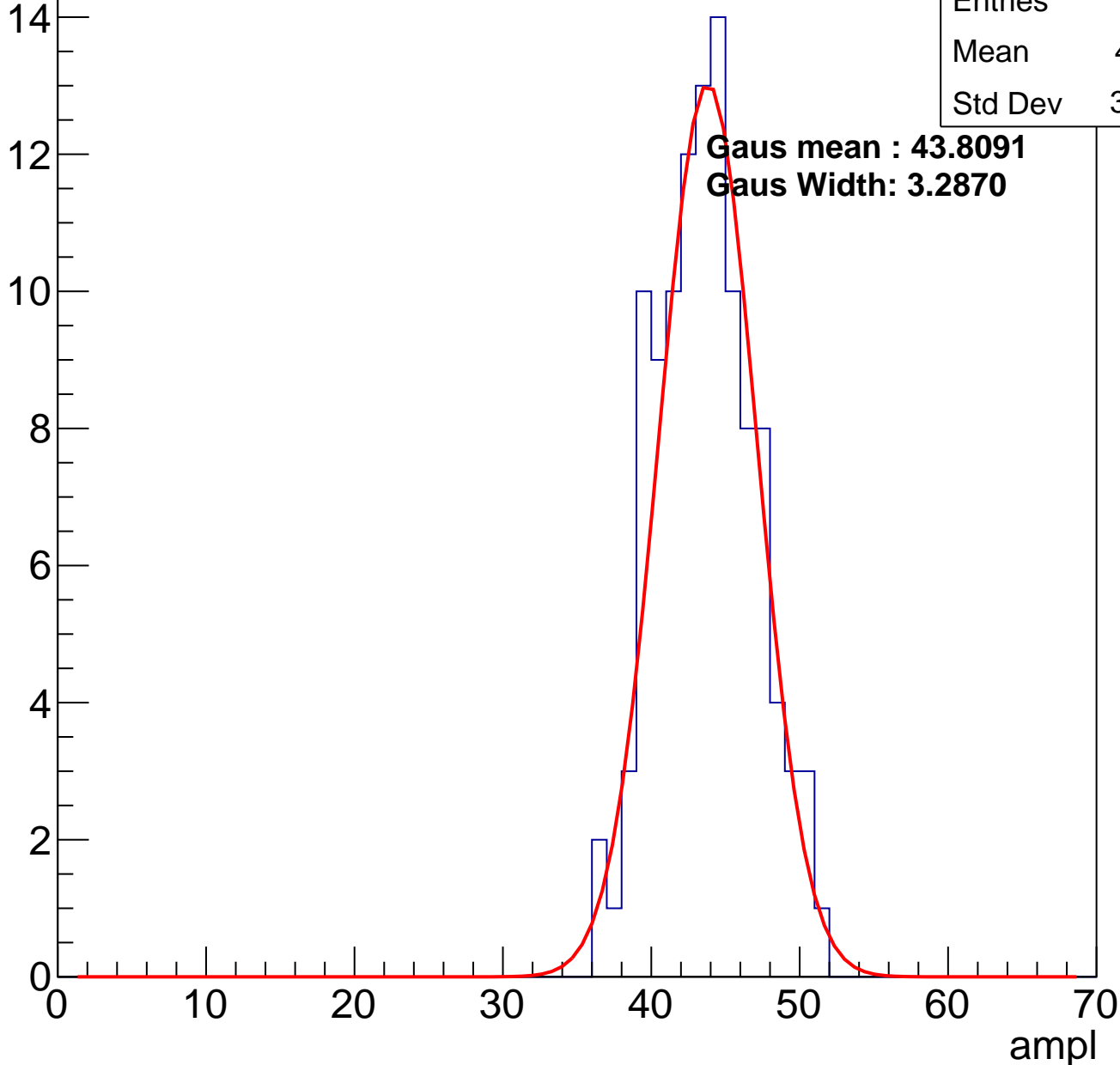
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	111
Mean	43.21
Std Dev	3.247

**Gaus mean : 43.8091**

**Gaus Width: 3.2870**

Entry

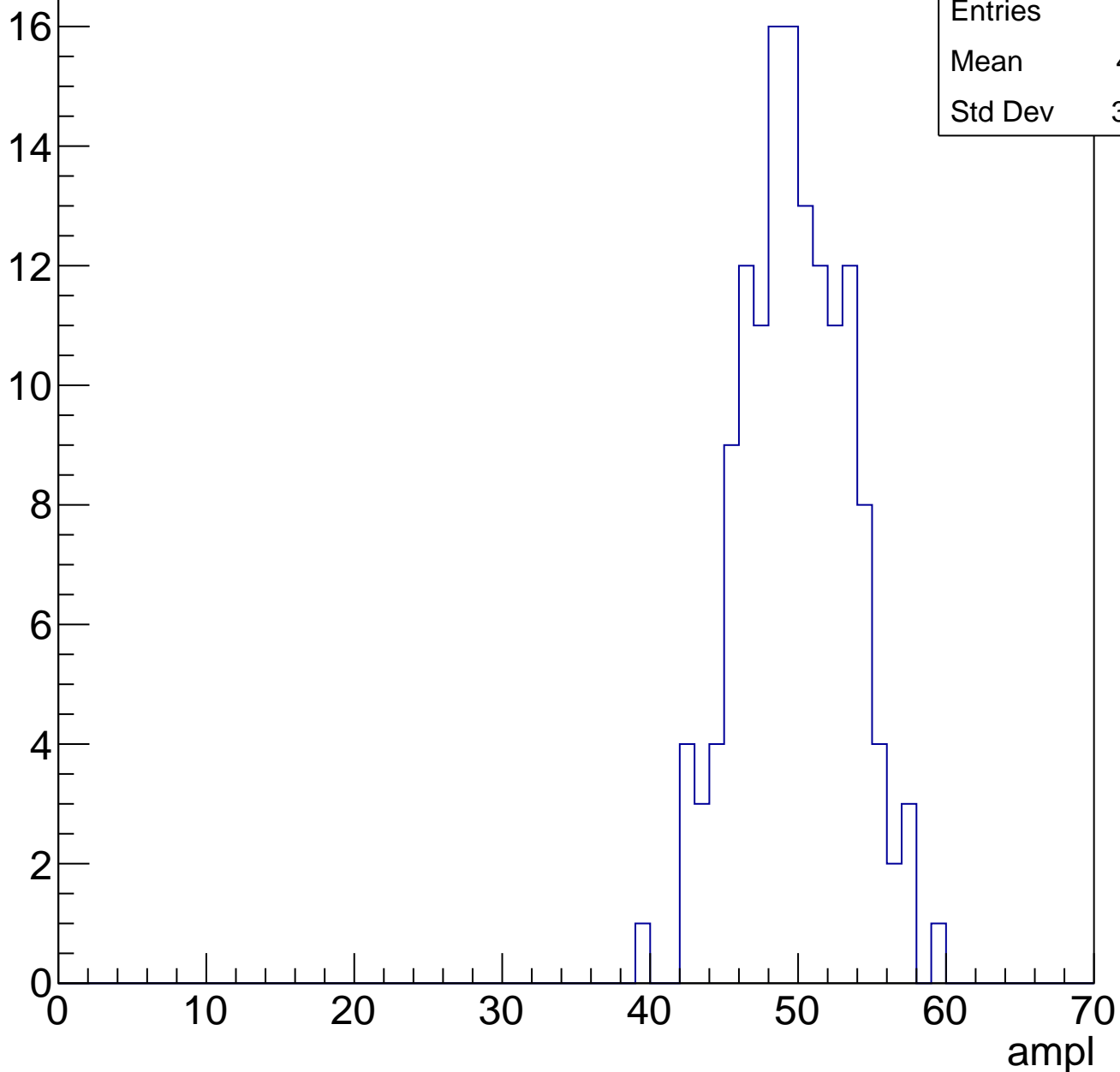


# B0L101S, U26-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	142
Mean	49.31
Std Dev	3.647

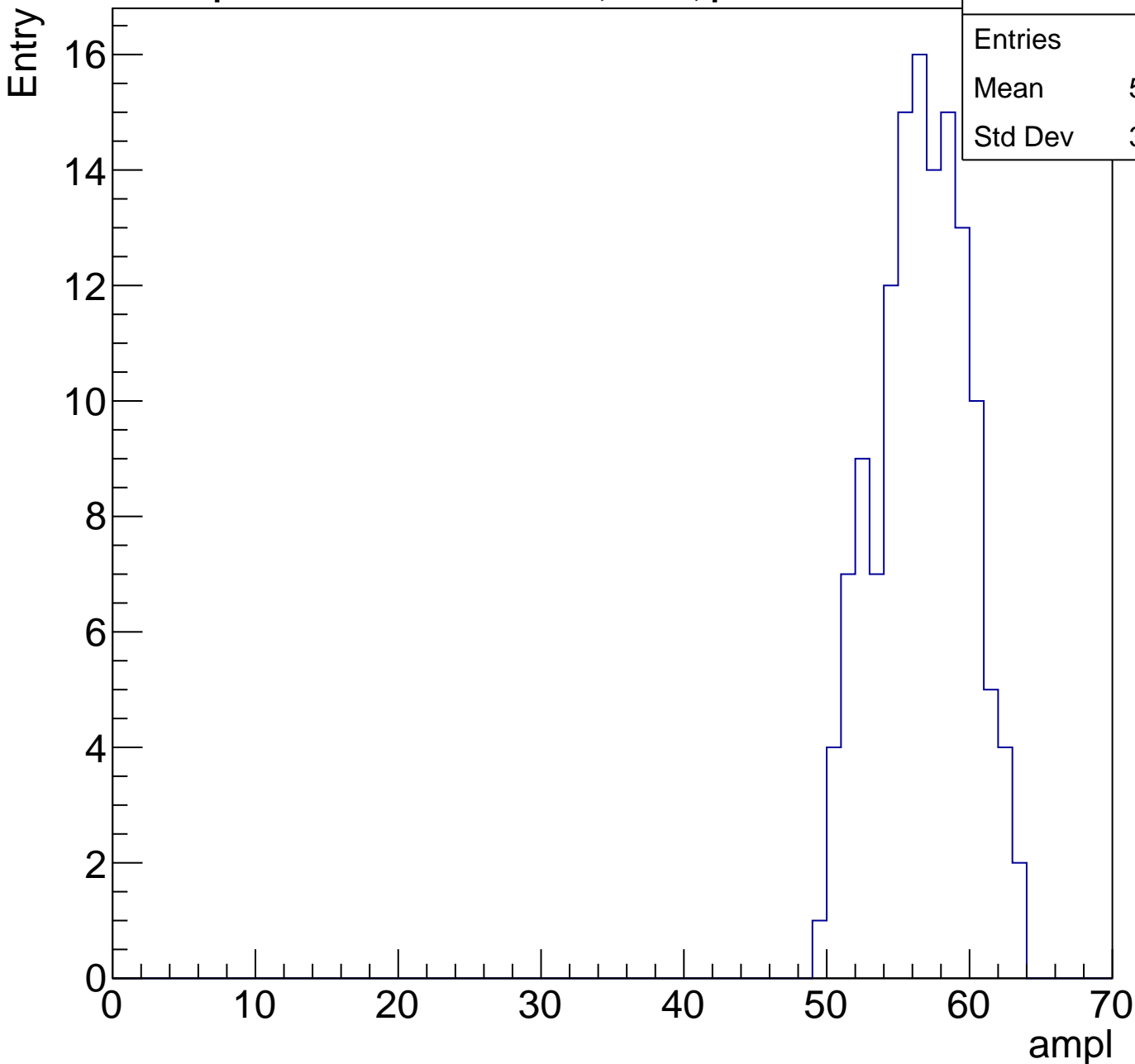
Entry



# B0L101S, U26-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	56.18
Std Dev	3.176



# B0L101S, U26-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	66
Mean	59.56
Std Dev	7.705

ampl

0

10

20

30

40

50

60

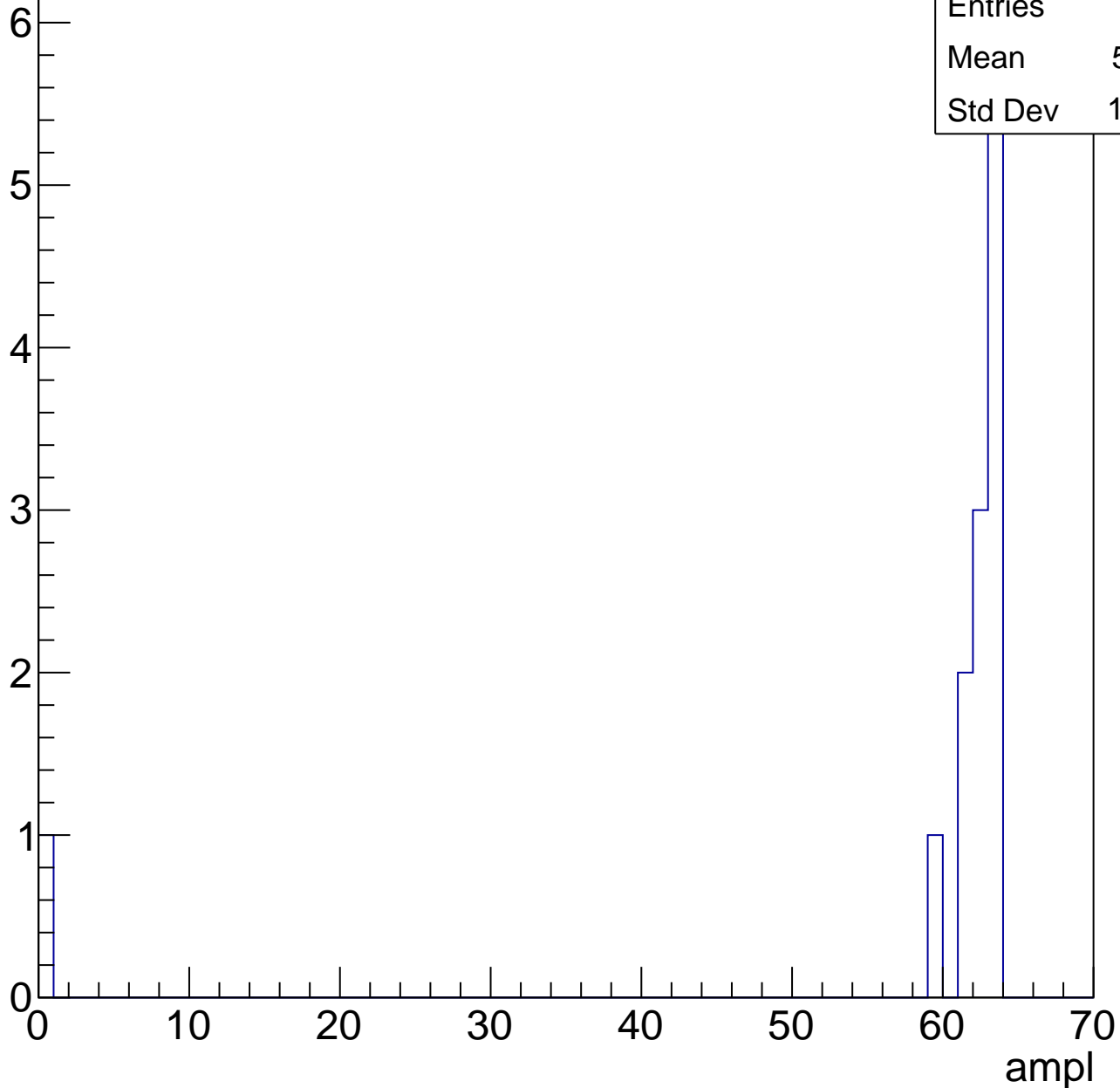
70

# B0L101S, U26-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.31
Std Dev	16.58





# B0L101S, U26-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	29.91
Std Dev	5.619

**Gaus mean : 30.7424**

**Gaus Width: 3.6734**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

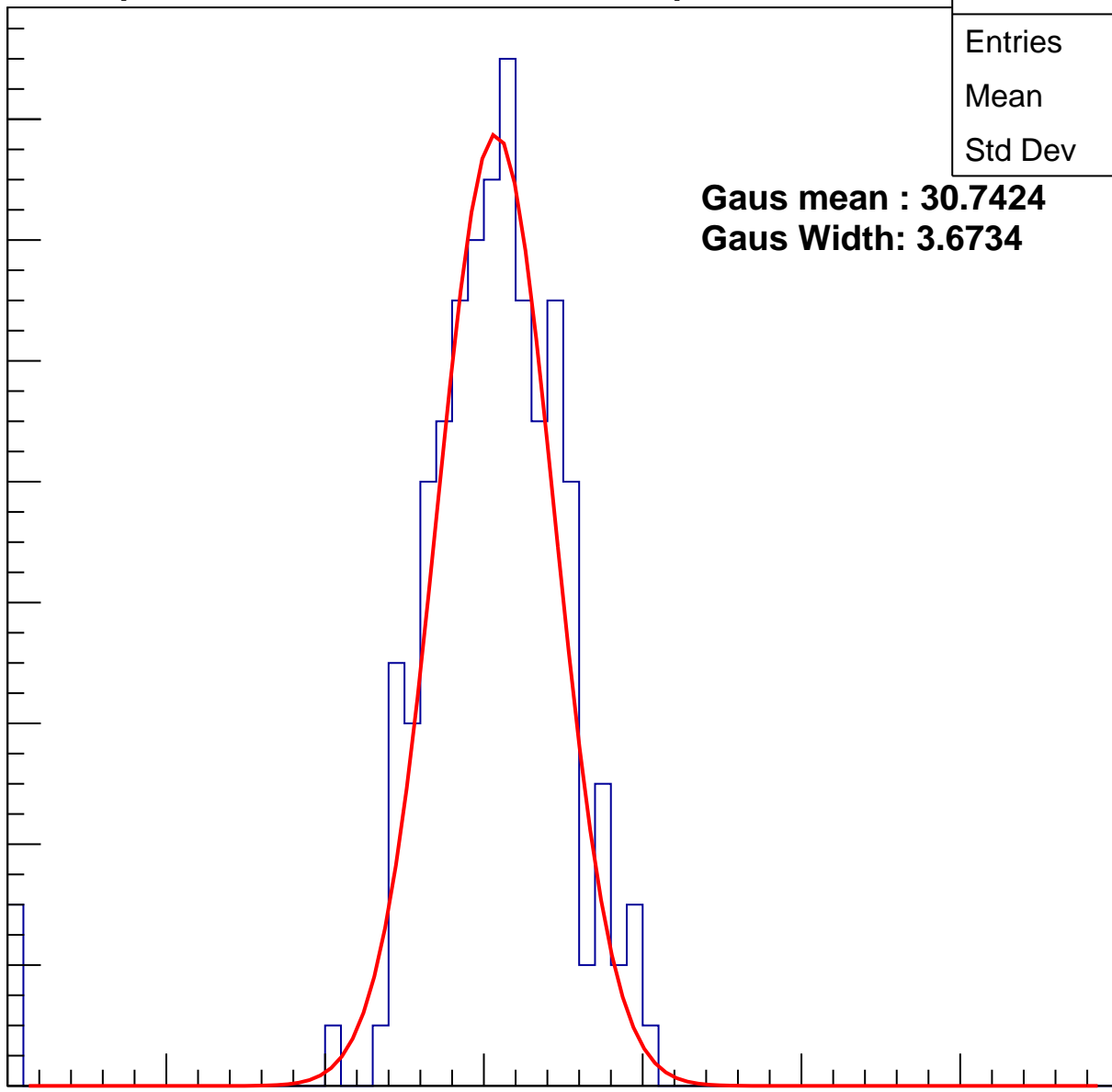
40

50

60

70

ampl



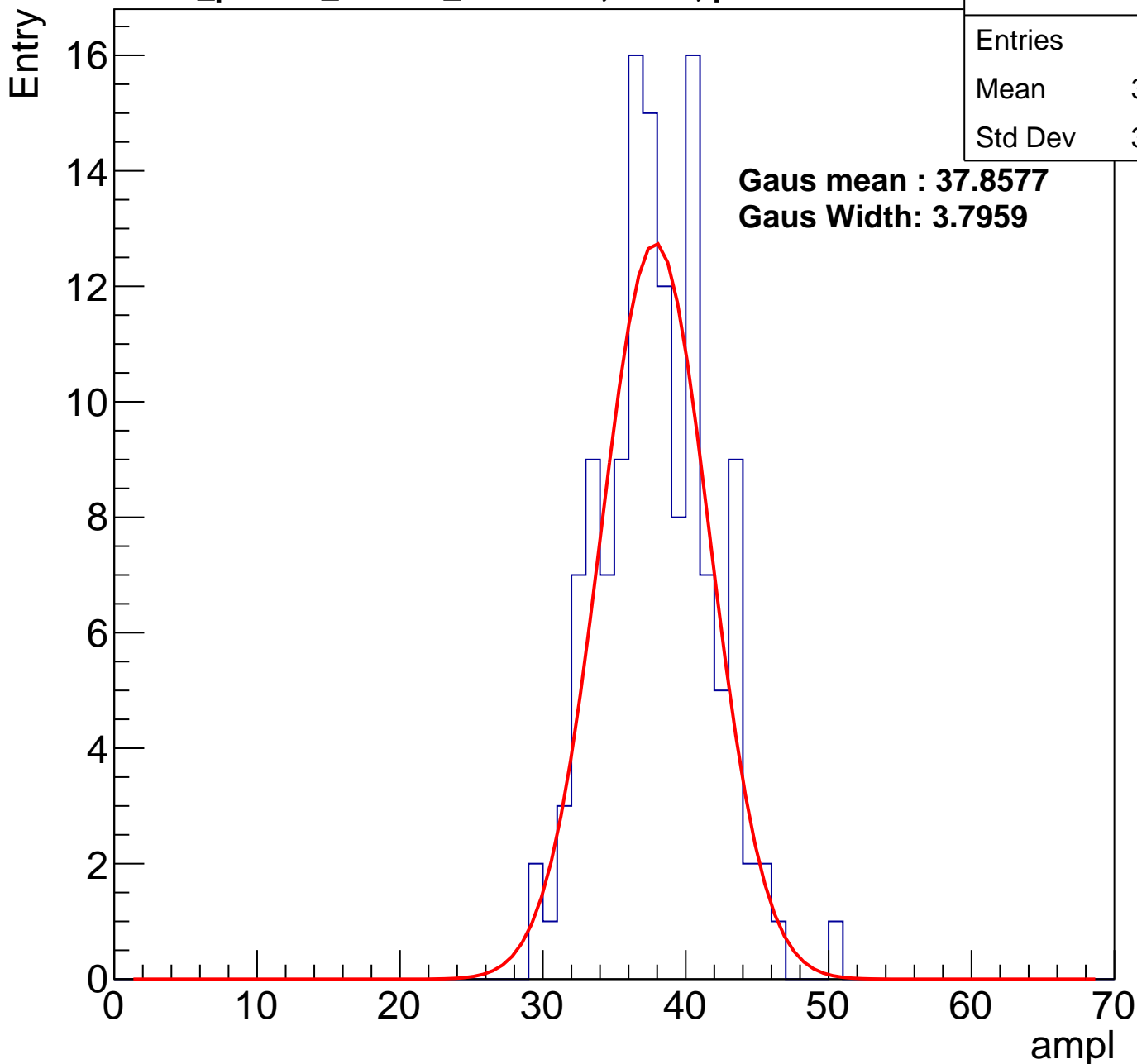
# B0L101S, U26-ch89, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	37.52
Std Dev	3.803

**Gaus mean : 37.8577**

**Gaus Width: 3.7959**



# B0L101S, U26-ch89, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	43.88
Std Dev	3.422

**Gaus mean : 44.6617**

**Gaus Width: 3.6670**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

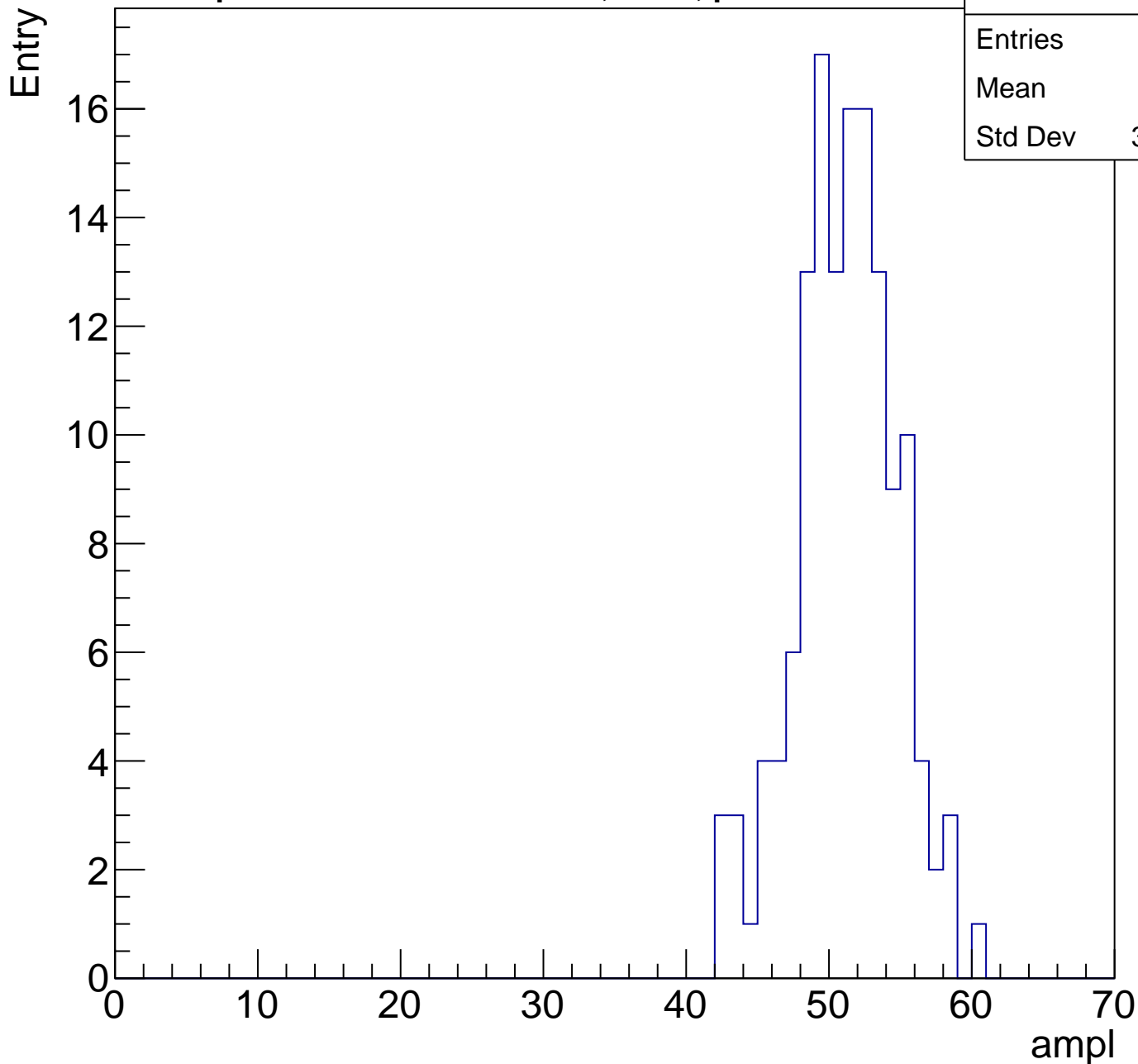
ampl

70

# B0L101S, U26-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	138
Mean	50.7
Std Dev	3.529



# B0L101S, U26-ch89, adc4

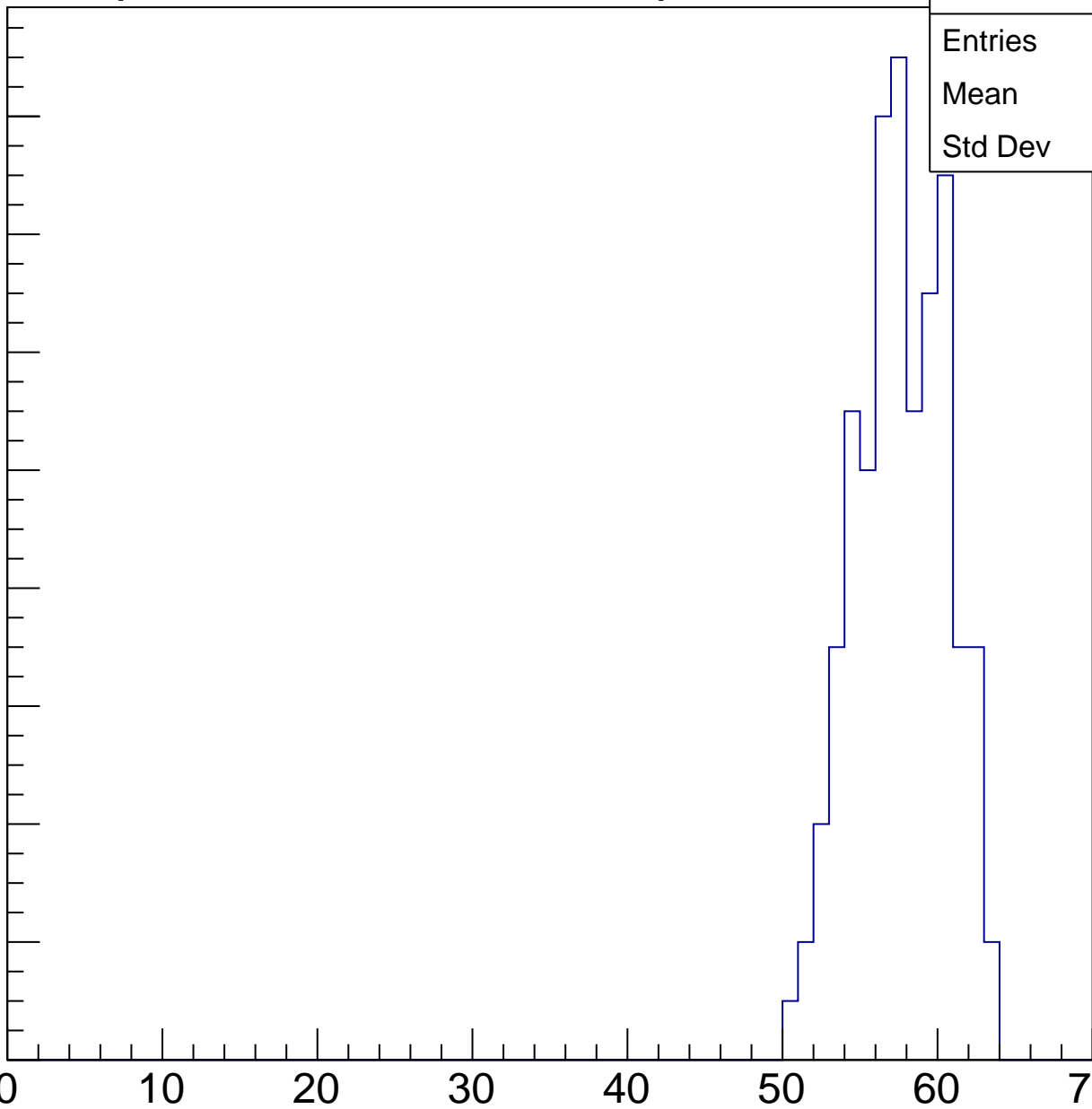
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	57.17
Std Dev	2.91

ampl



# B0L101S, U26-ch89, adc5

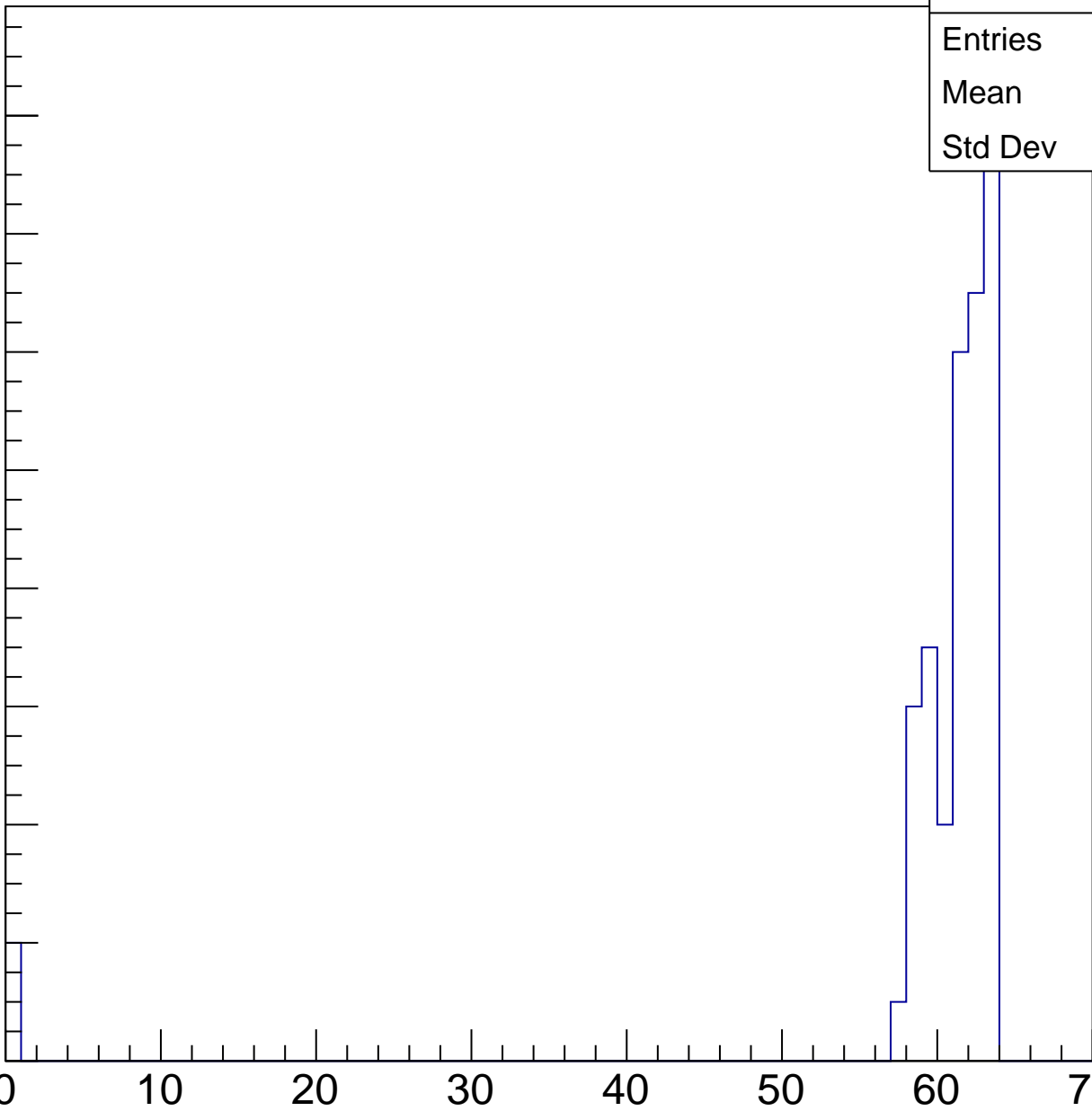
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	62
Mean	59.15
Std Dev	10.93

ampl



# B0L101S, U26-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	29.07
Std Dev	3.735

**Gaus mean : 29.3648**

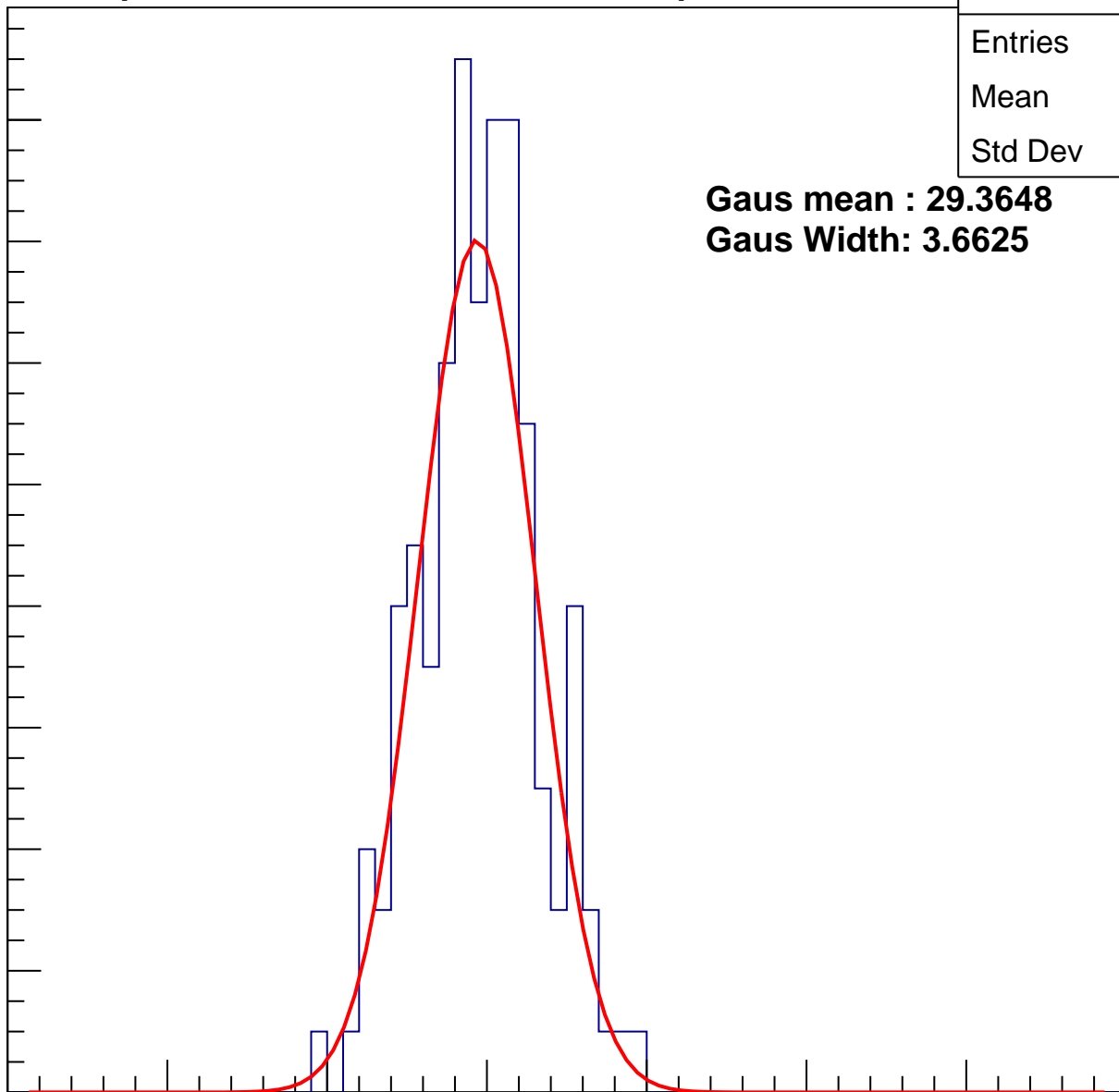
**Gaus Width: 3.6625**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



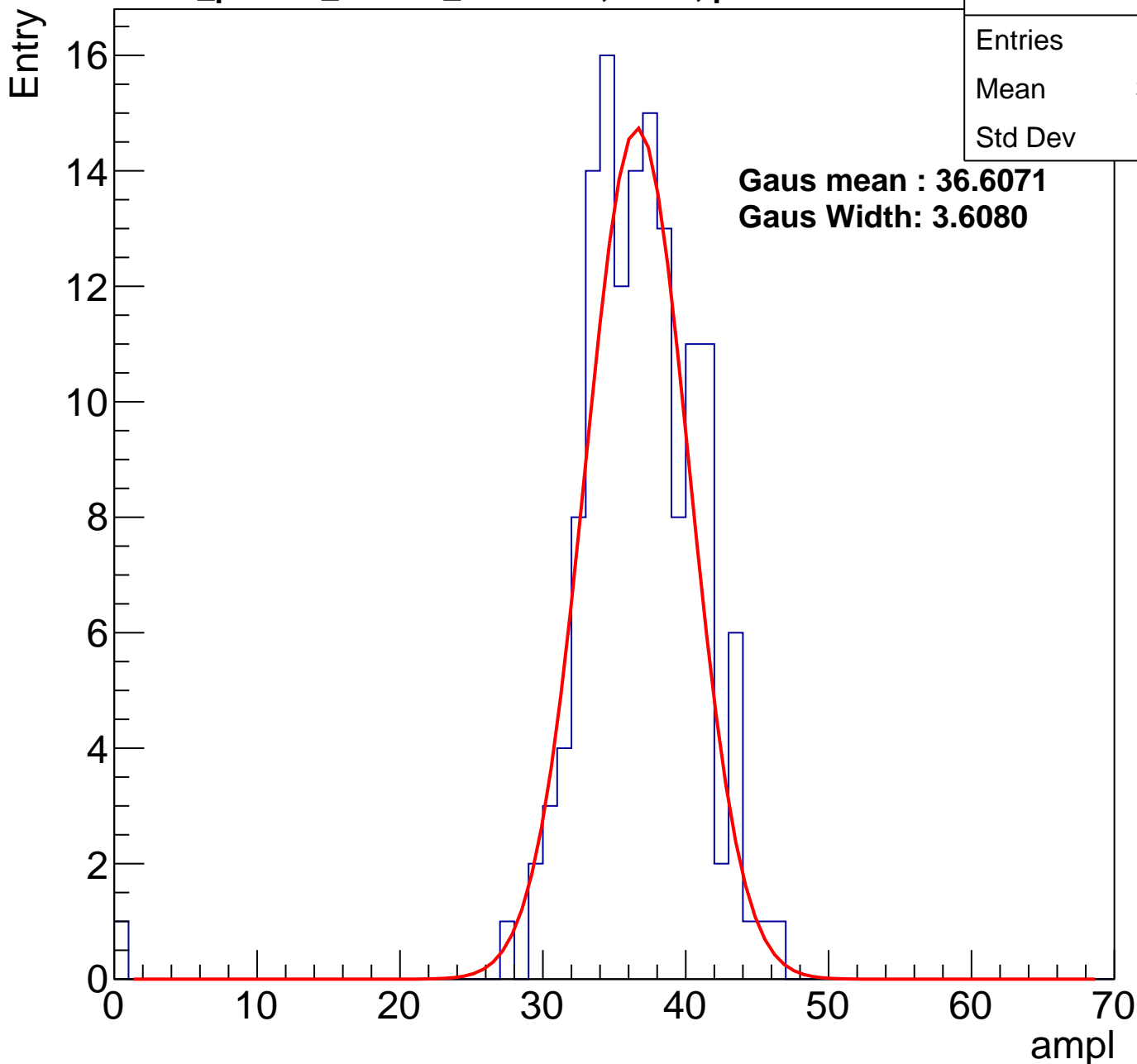
# B0L101S, U26-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	144
Mean	36.21
Std Dev	4.72

**Gaus mean : 36.6071**

**Gaus Width: 3.6080**



# B0L101S, U26-ch90, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

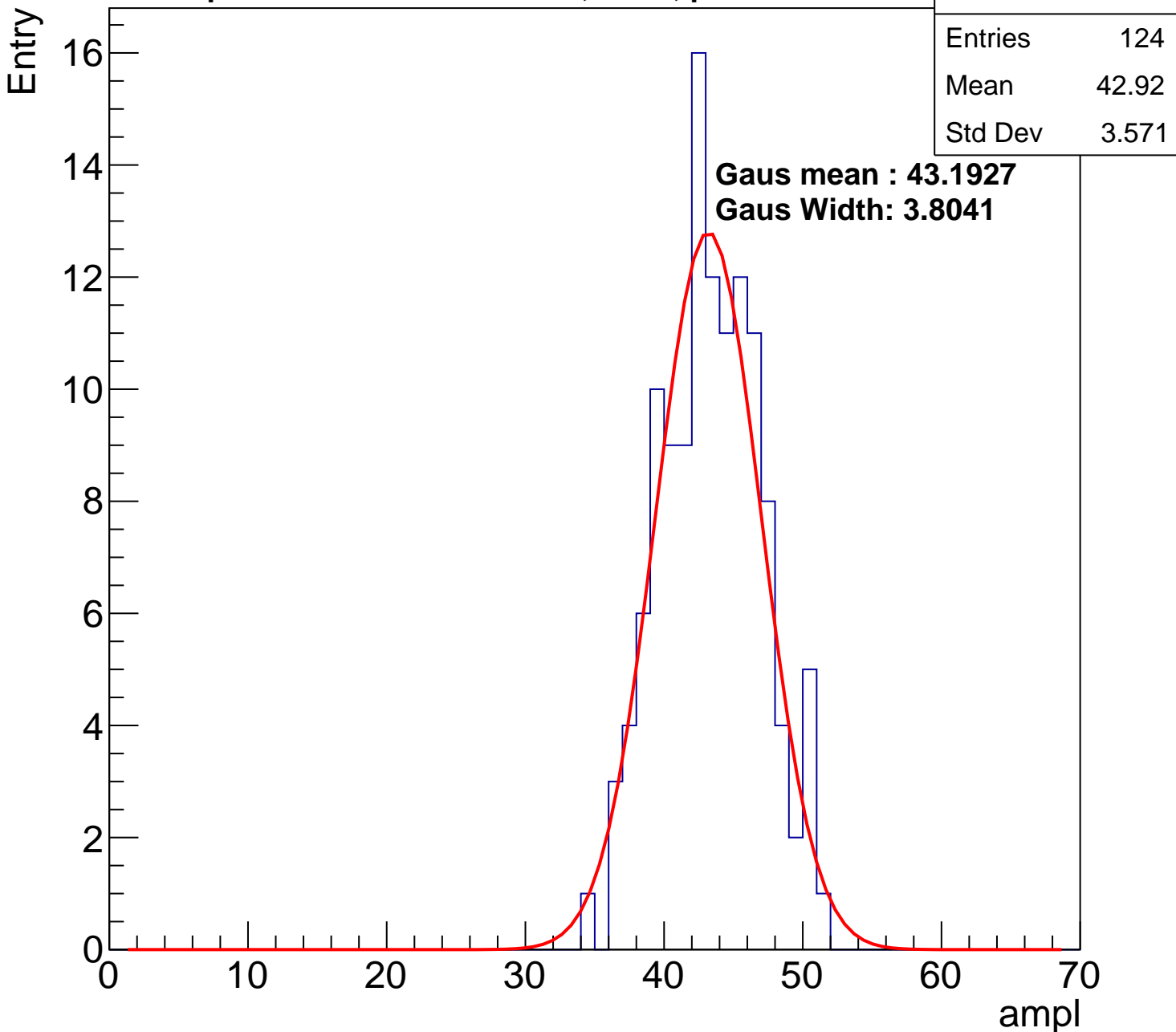
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	124
Mean	42.92
Std Dev	3.571

**Gaus mean : 43.1927**

**Gaus Width: 3.8041**

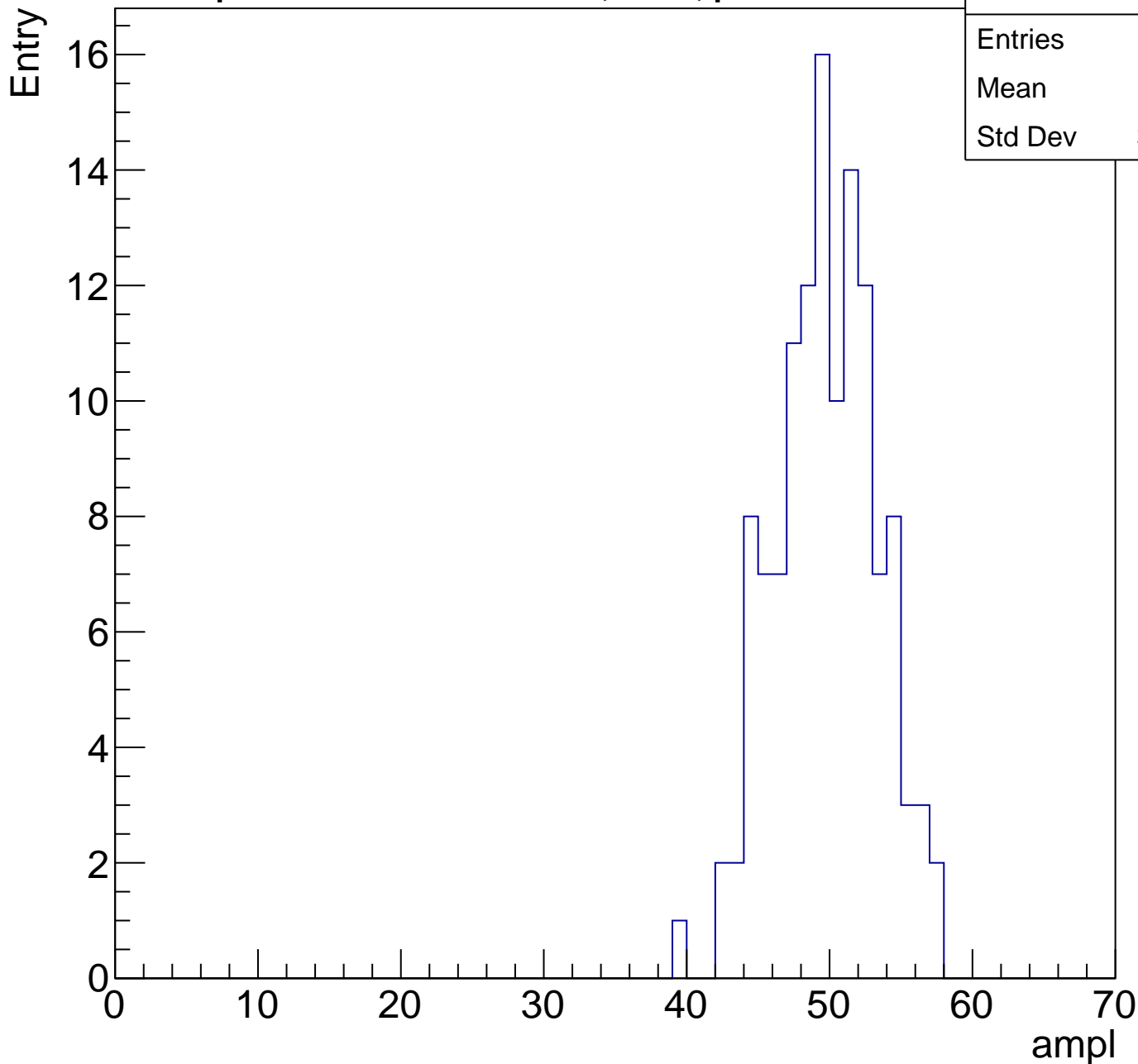
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U26-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	125
Mean	49.3
Std Dev	3.531



# B0L101S, U26-ch90, adc4

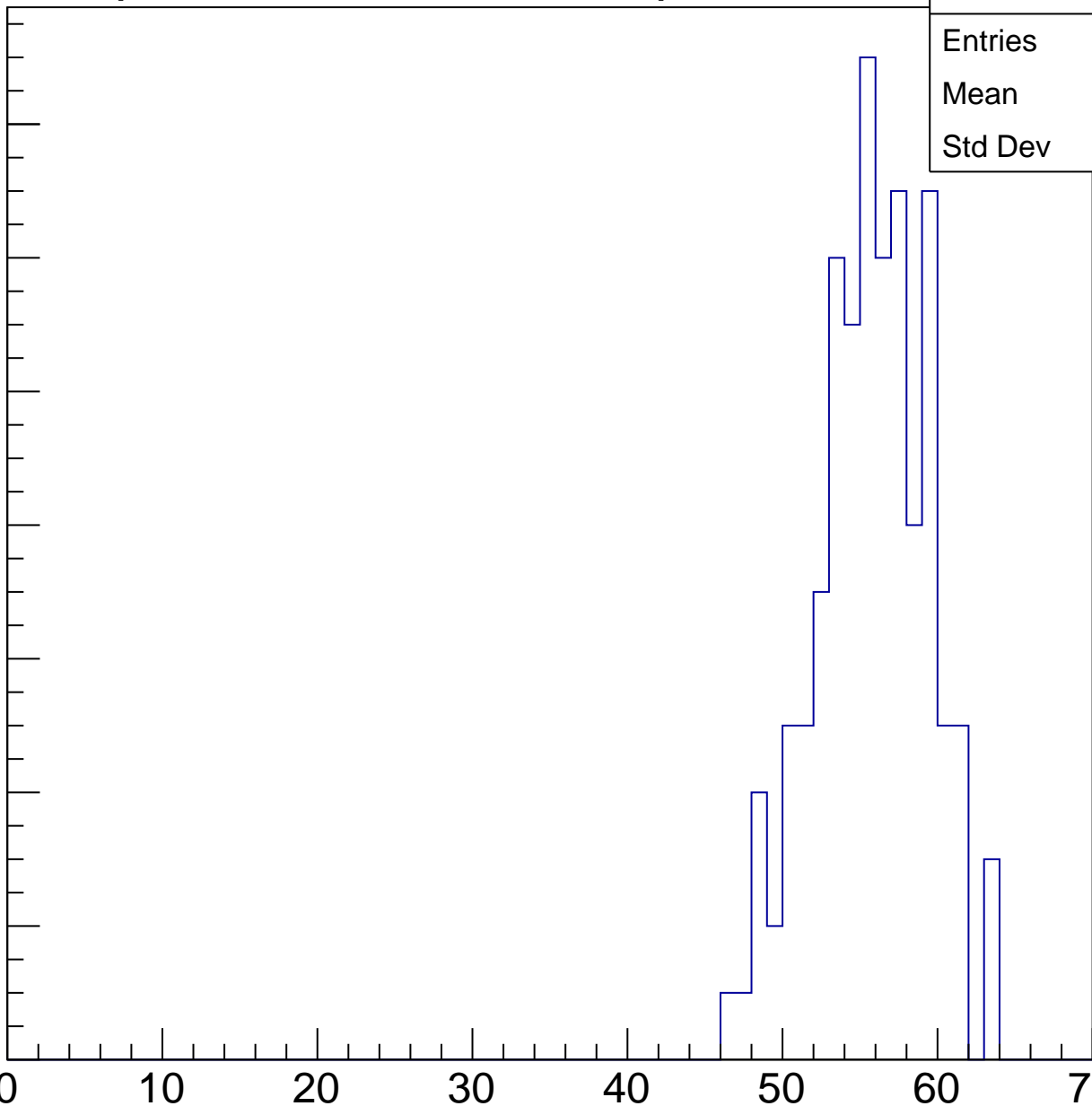
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	122
Mean	55.29
Std Dev	3.588

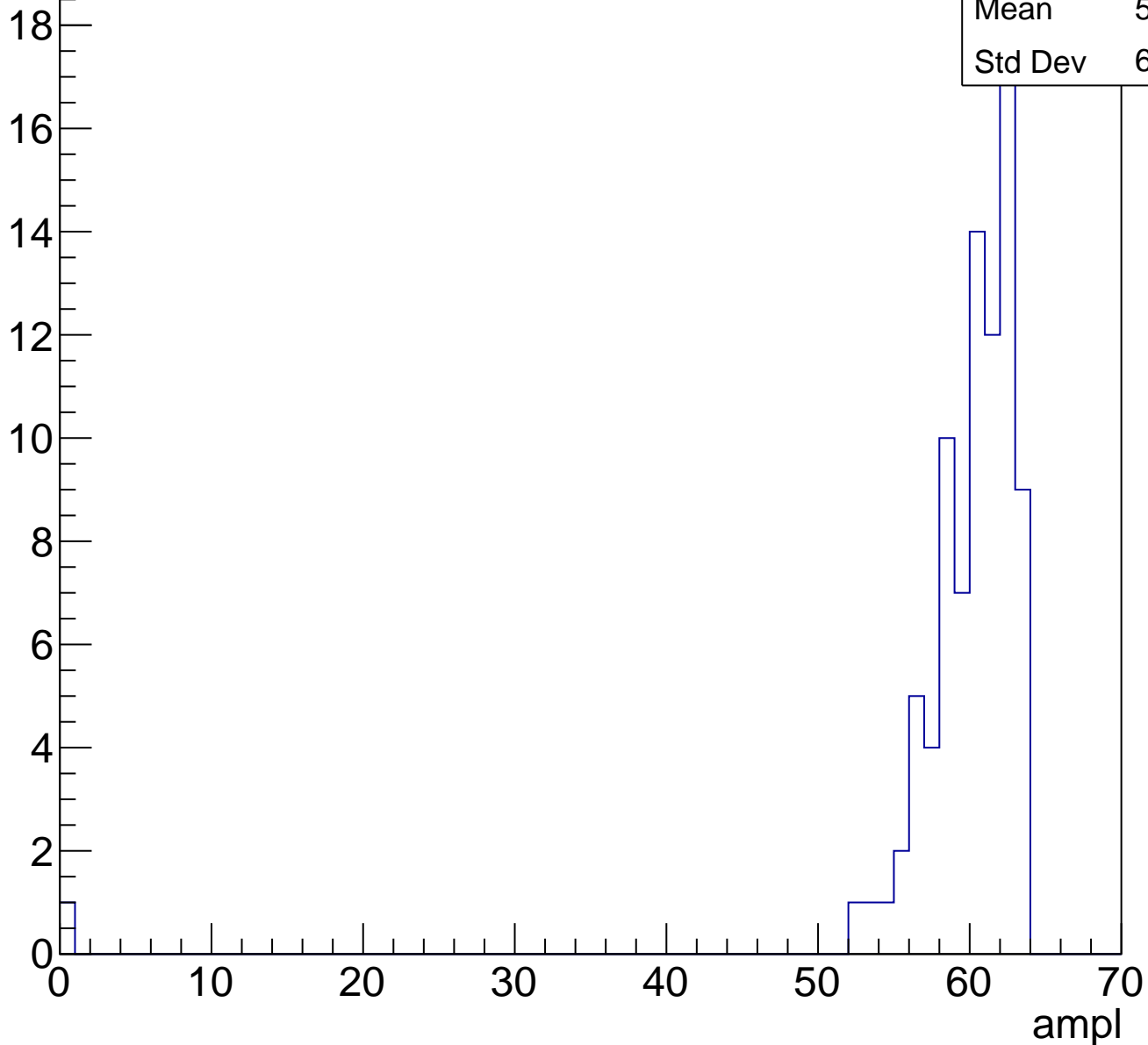
ampl



# B0L101S, U26-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

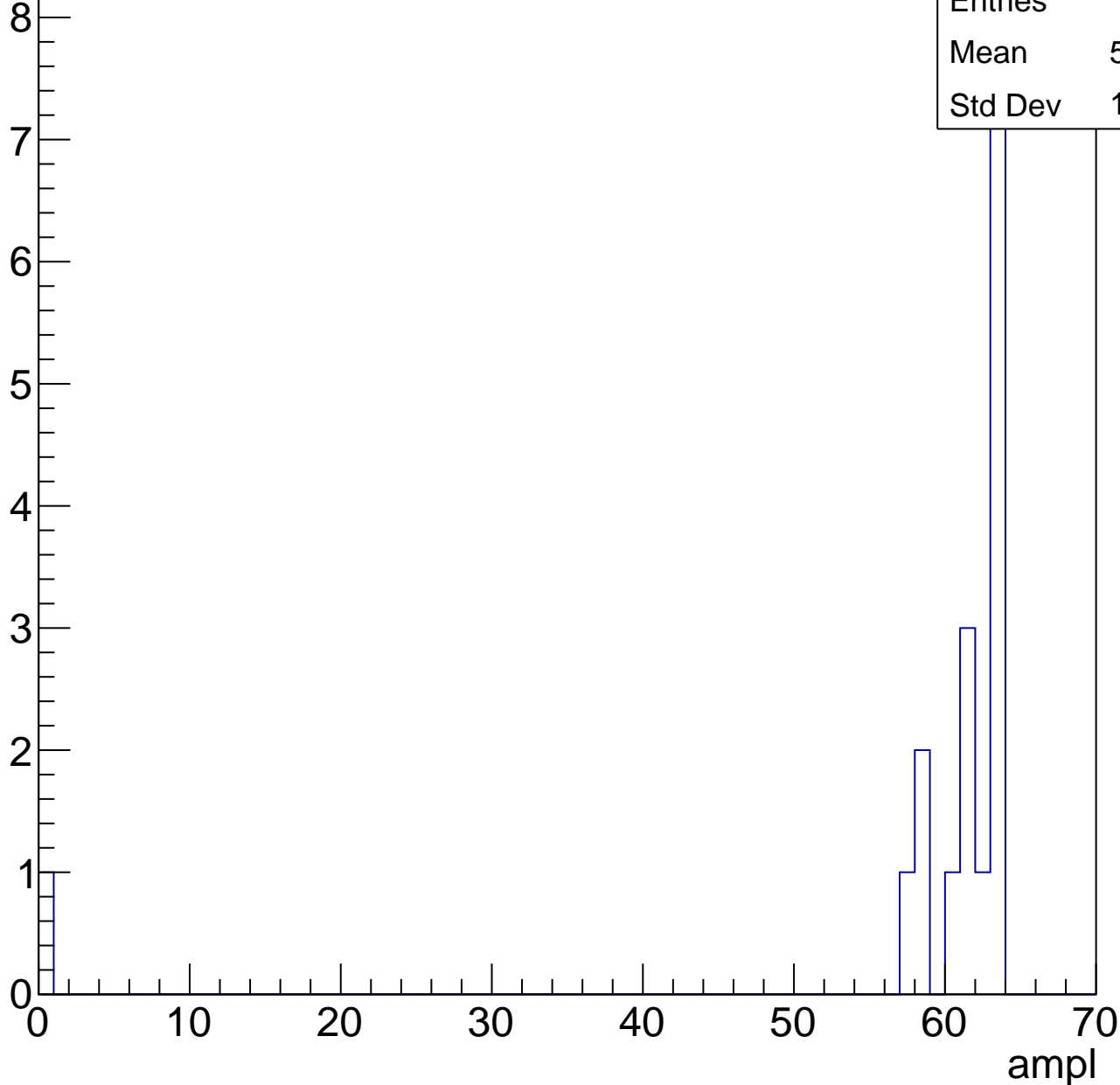


# B0L101S, U26-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	57.76
Std Dev	14.57

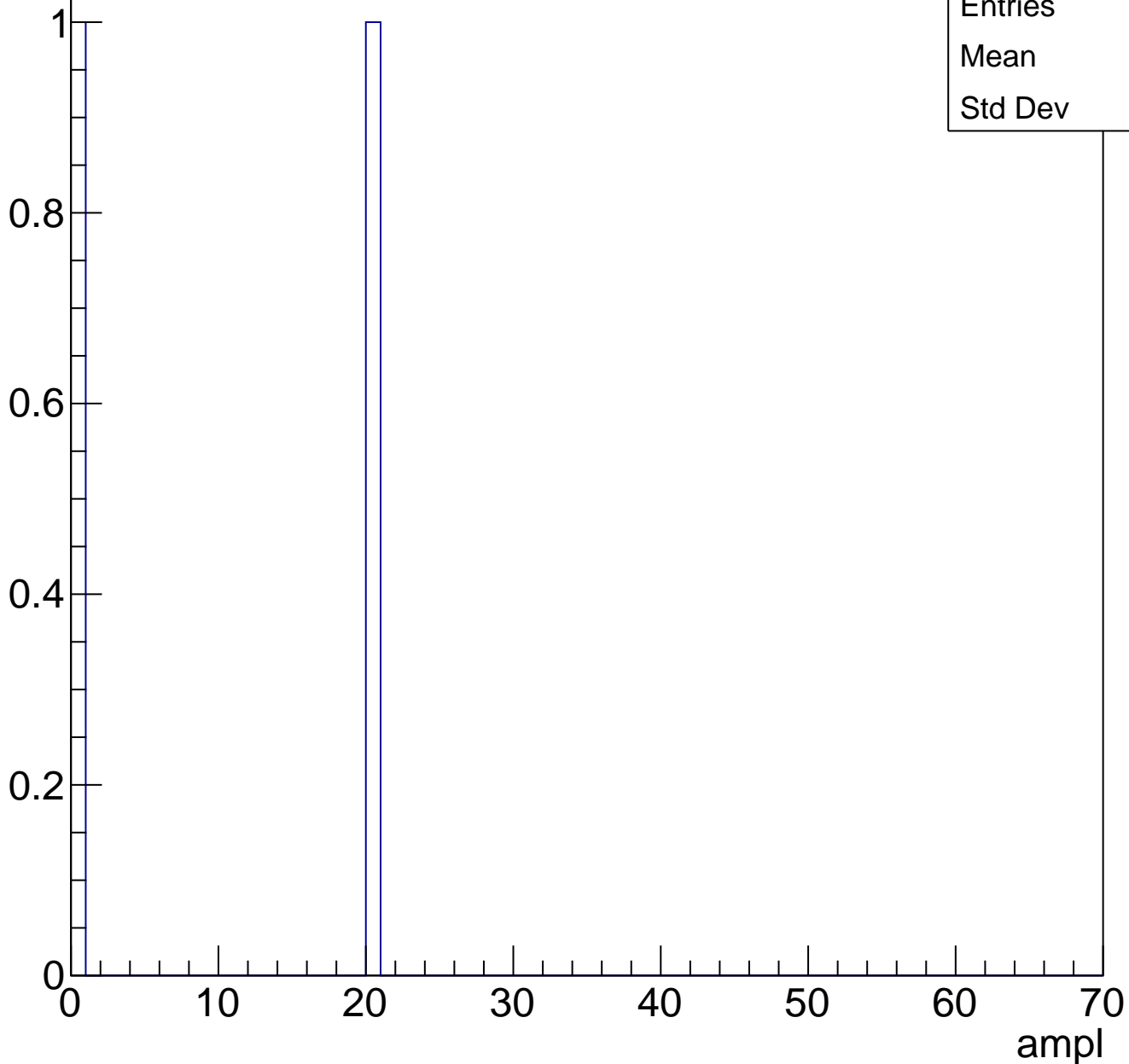




# B0L101S, U26-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

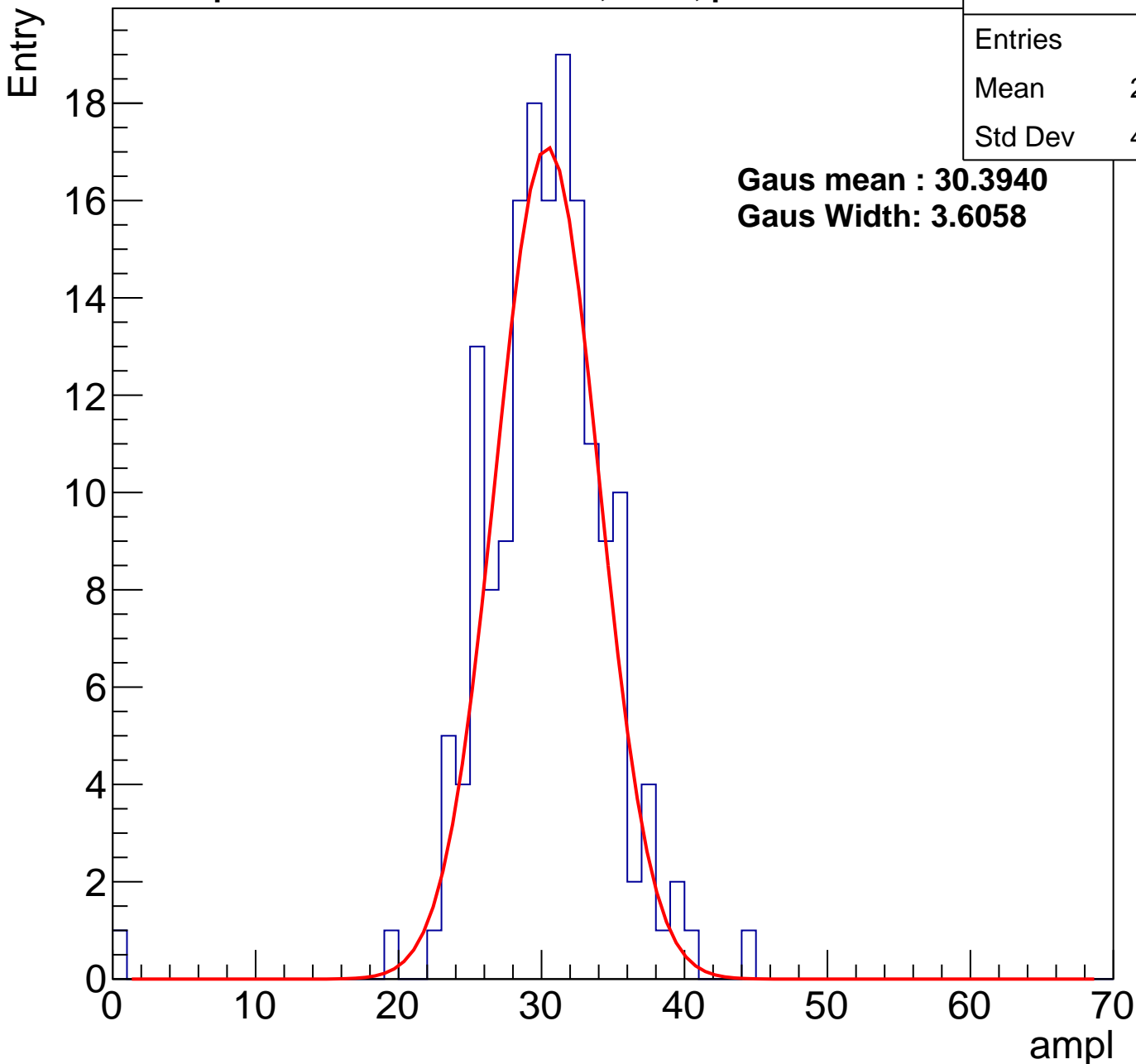


# B0L101S, U26-ch91, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	168
Mean	29.87
Std Dev	4.534

**Gaus mean : 30.3940**  
**Gaus Width: 3.6058**



# B0L101S, U26-ch91, adc1

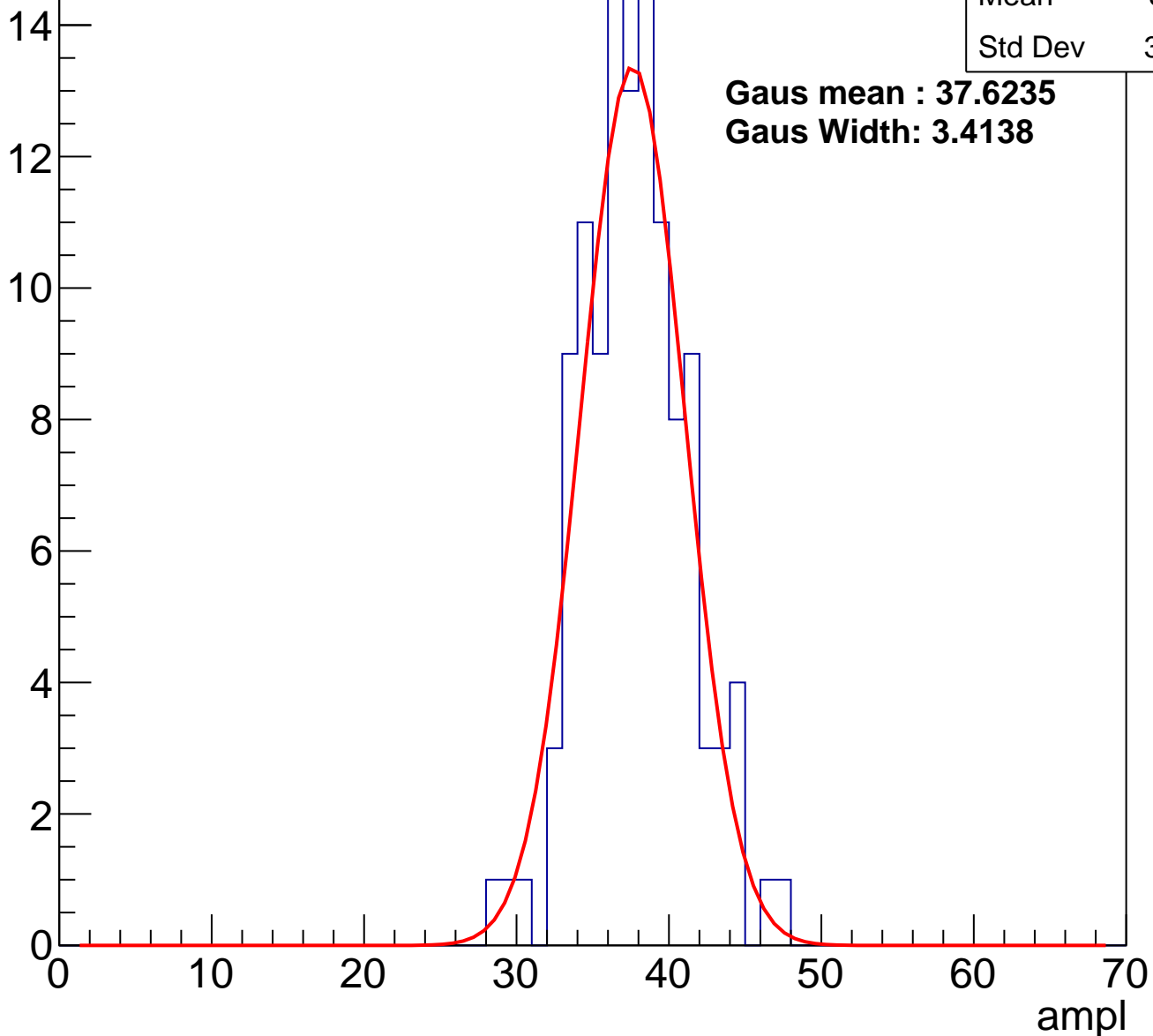
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	37.31
Std Dev	3.428

**Gaus mean : 37.6235**

**Gaus Width: 3.4138**

Entry



# B0L101S, U26-ch91, adc2

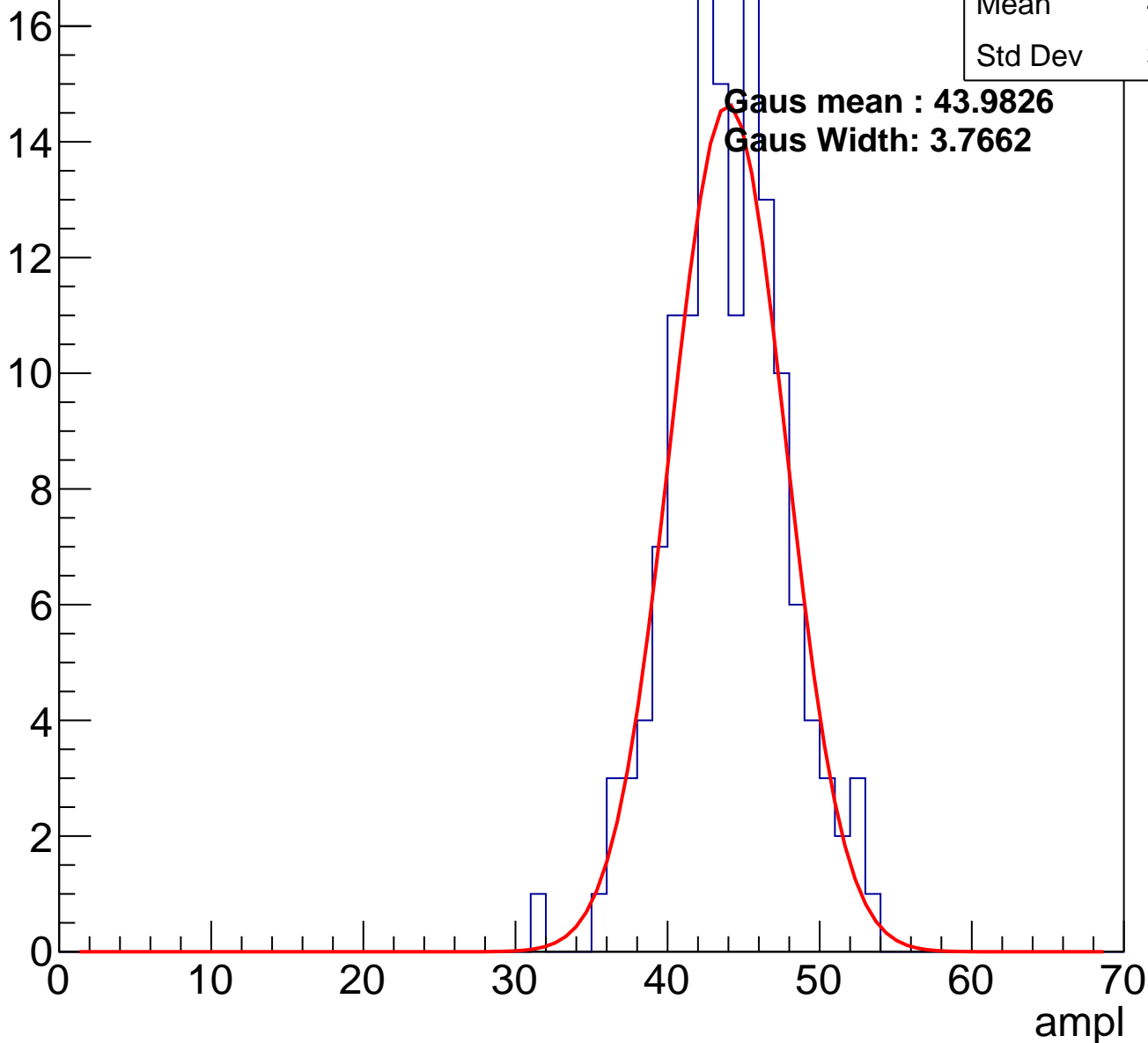
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	143
Mean	43.51
Std Dev	3.801

**Gaus mean : 43.9826**

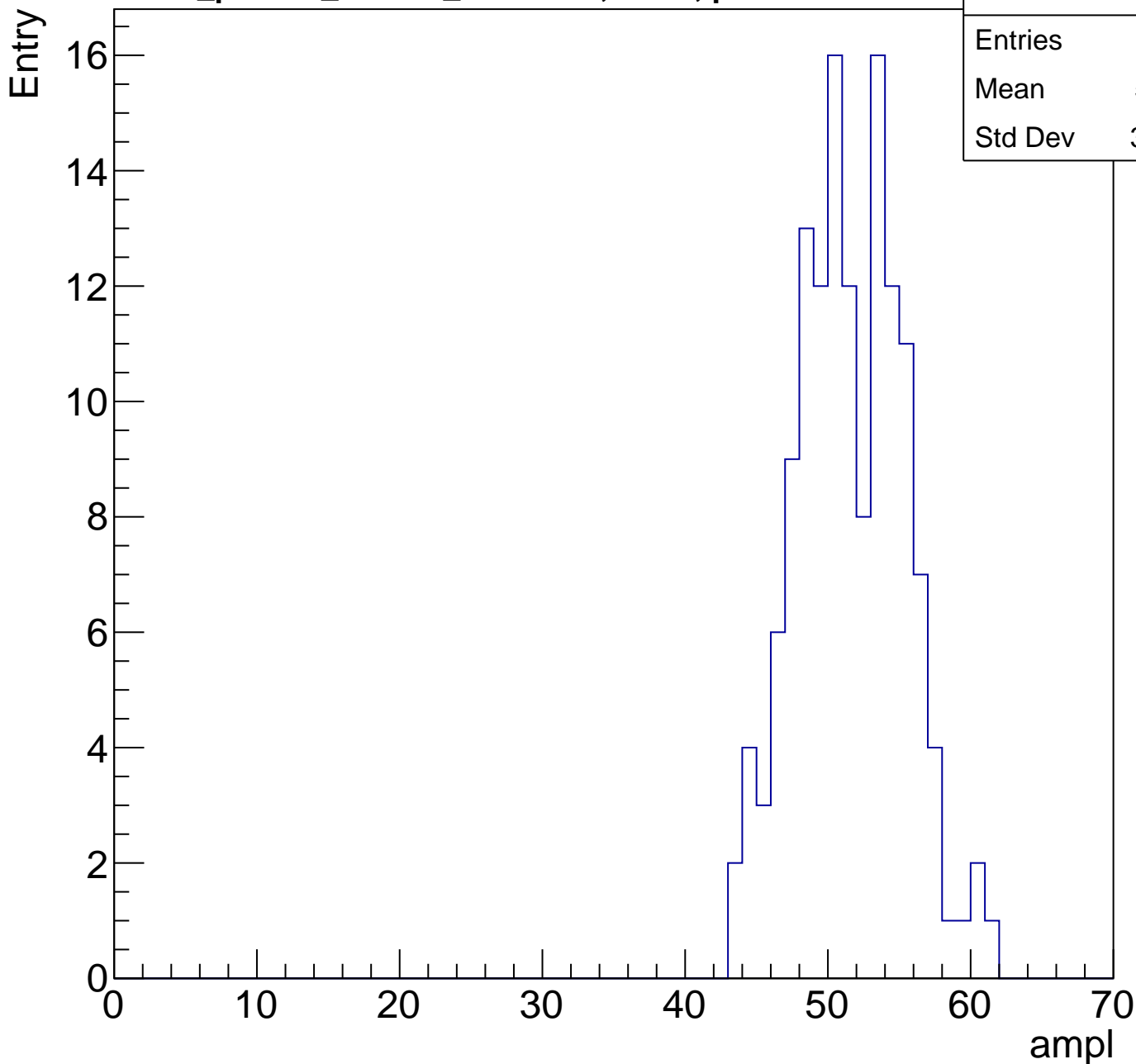
**Gaus Width: 3.7662**



# B0L101S, U26-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

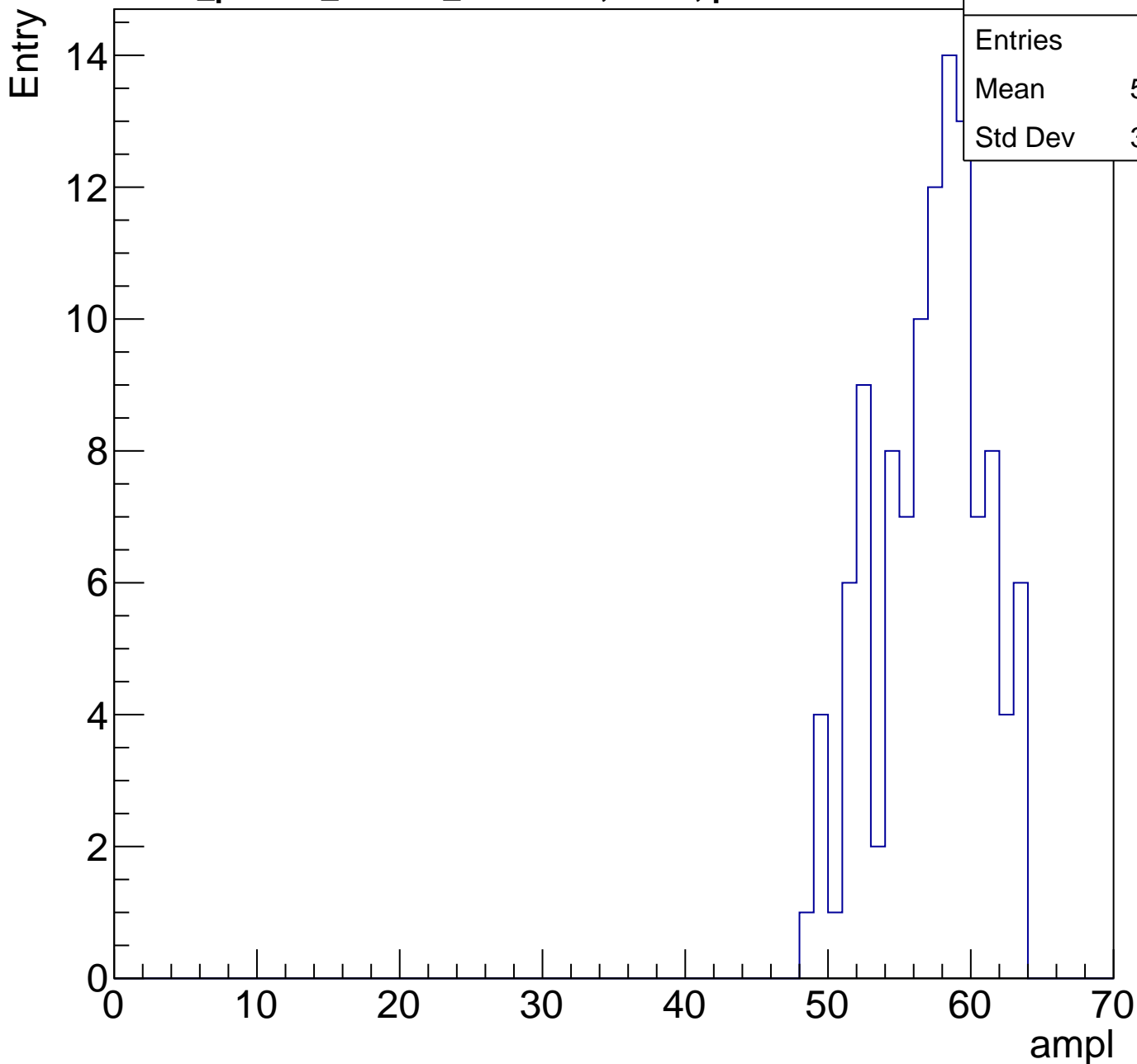
Entries	140
Mean	51.11
Std Dev	3.737



# B0L101S, U26-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	56.68
Std Dev	3.697



# B0L101S, U26-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

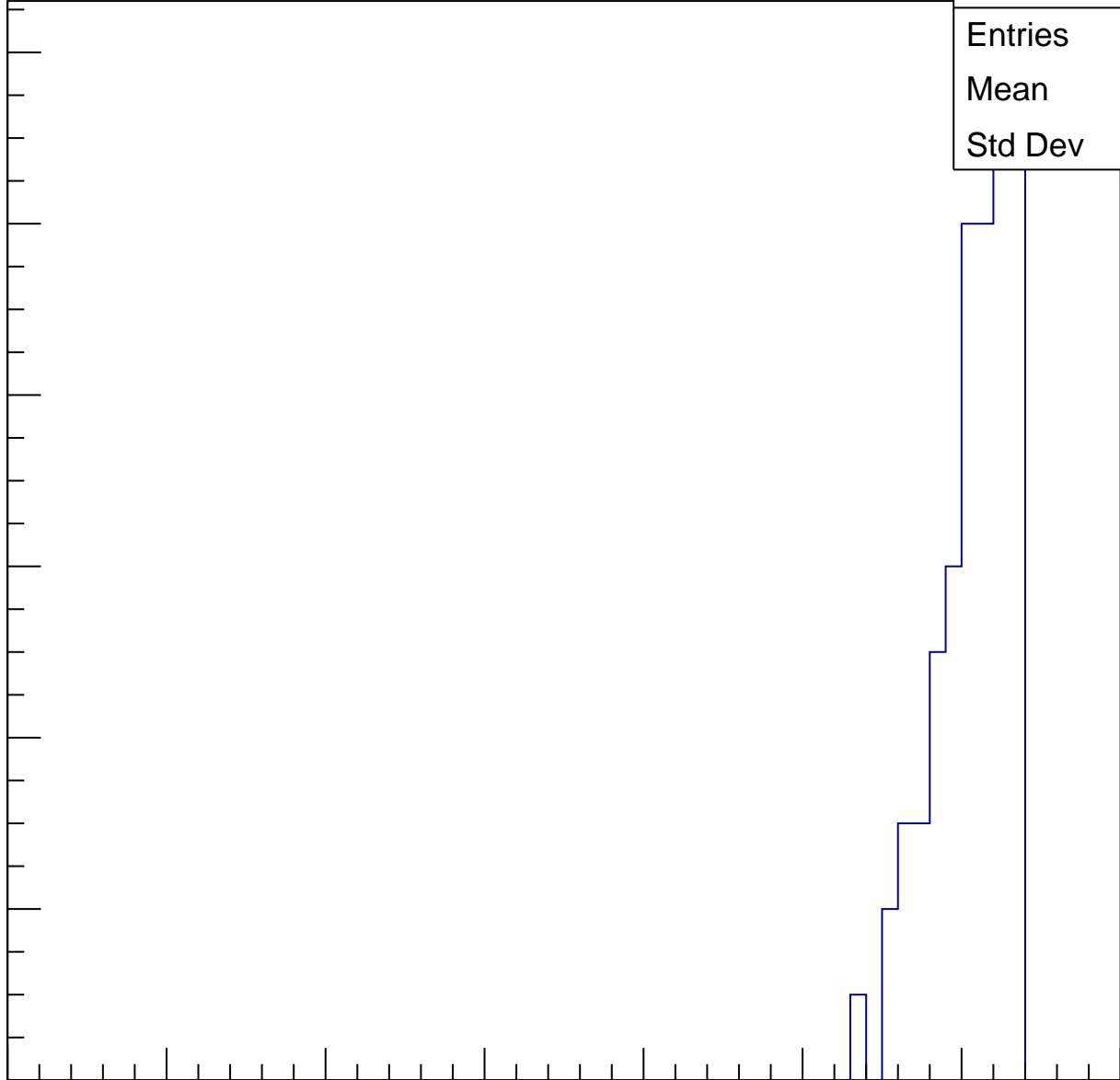
2

0

Entries	63
Mean	60.21
Std Dev	2.372

ampl

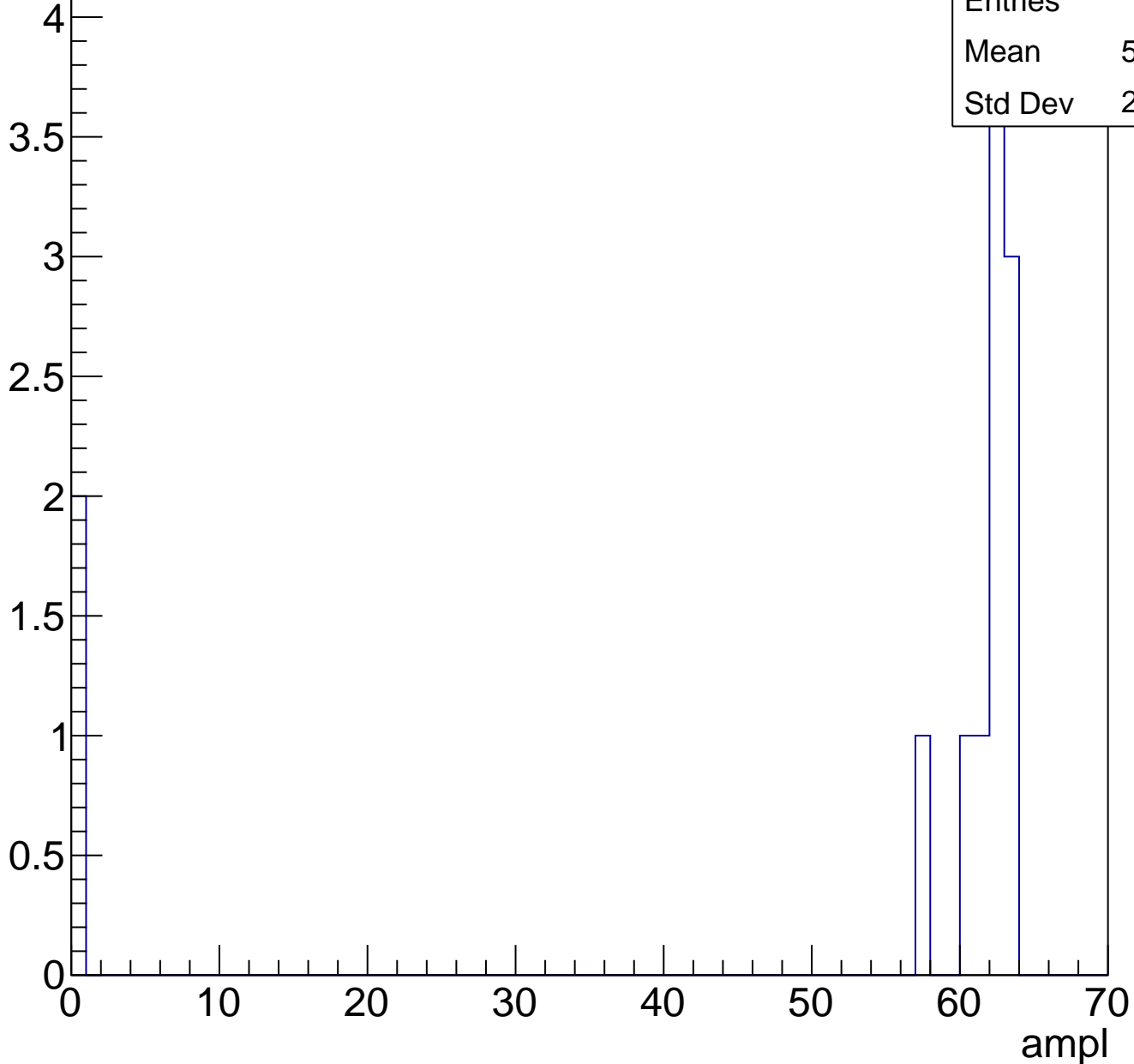
0 10 20 30 40 50 60 70



# B0L101S, U26-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

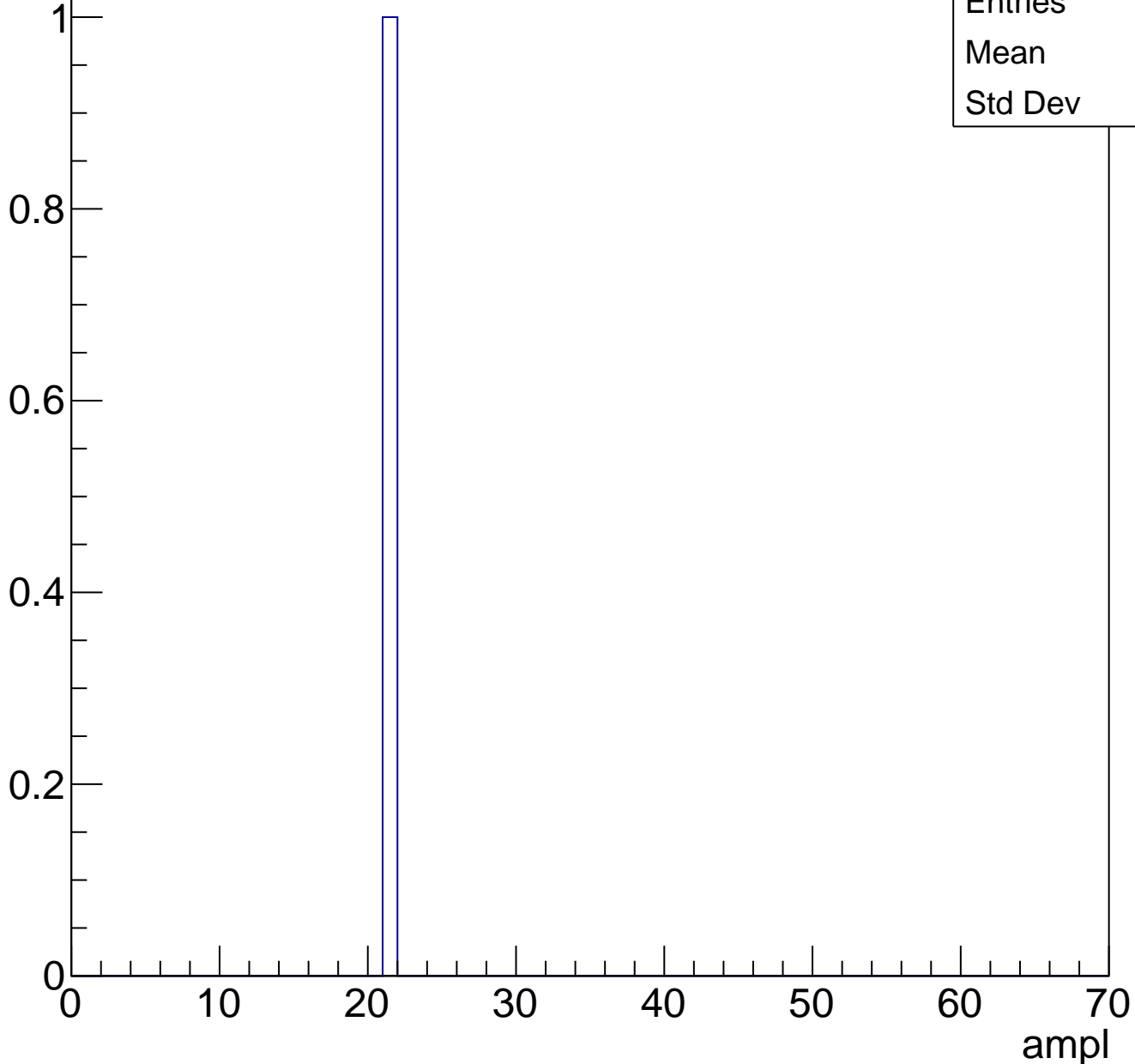




# B0L101S, U26-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

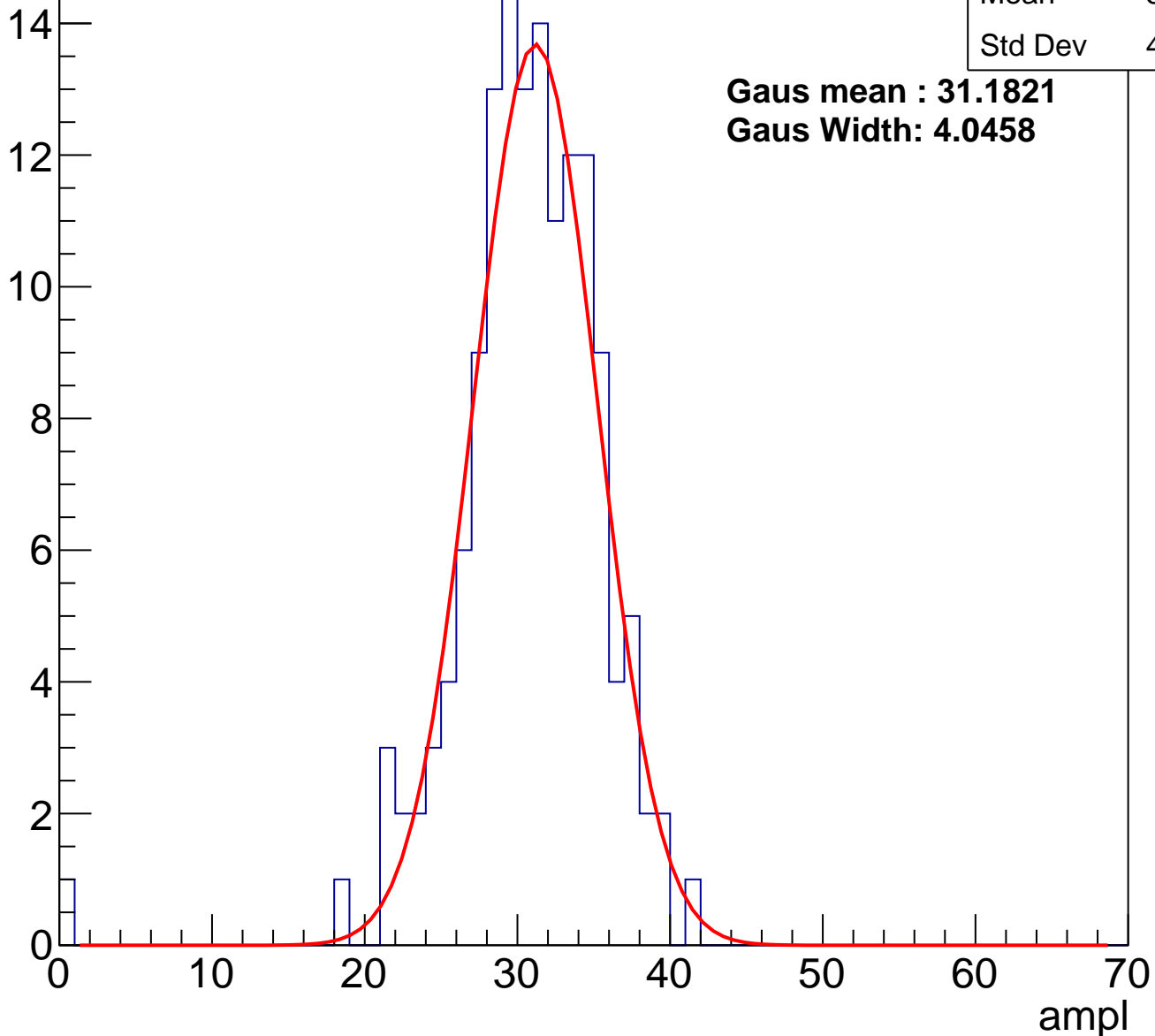
Entry



# B0L101S, U26-ch92, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	37.56
Std Dev	3.822

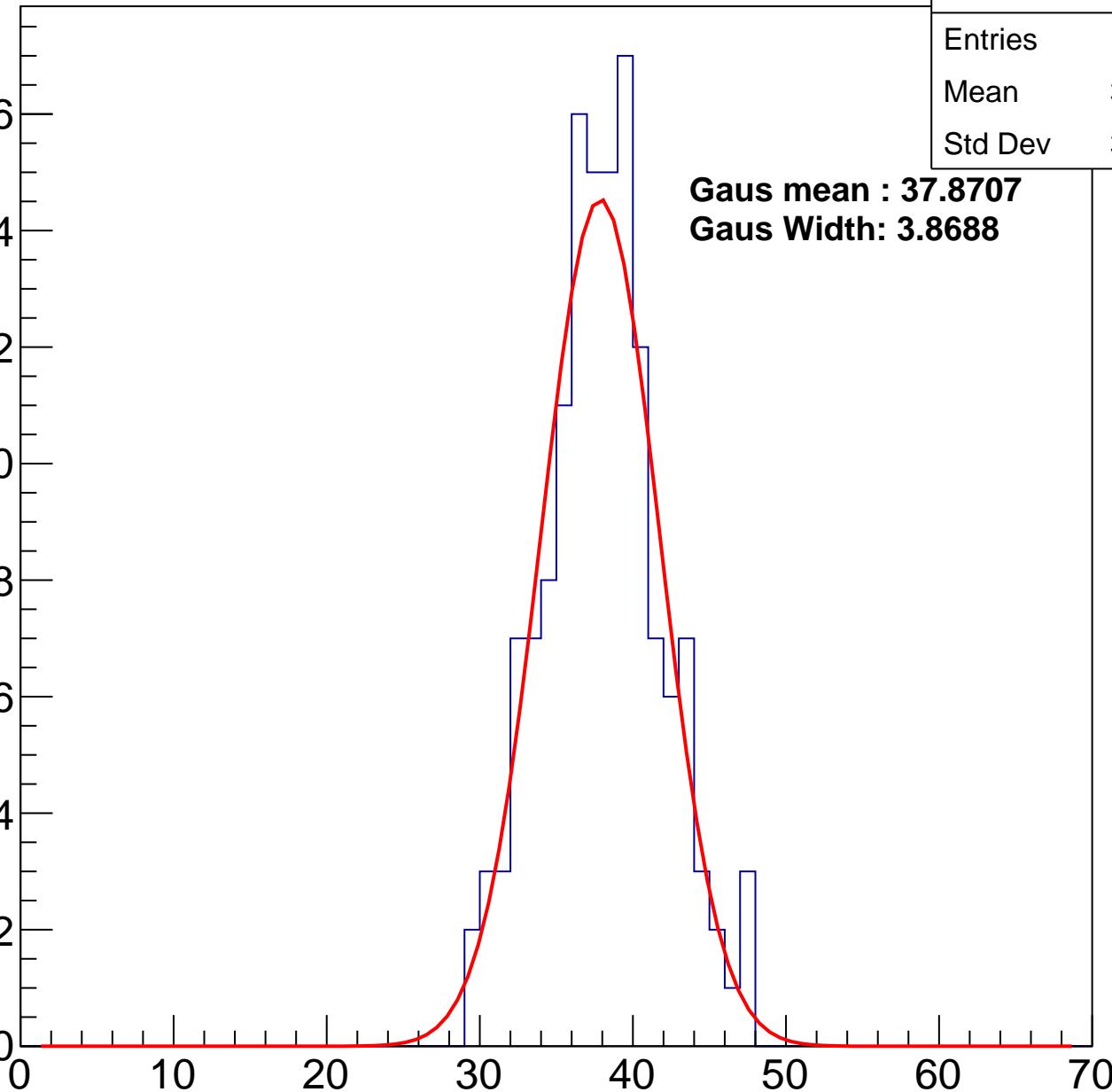
**Gaus mean : 37.8707**

**Gaus Width: 3.8688**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch92, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

108

Mean

44.39

Std Dev

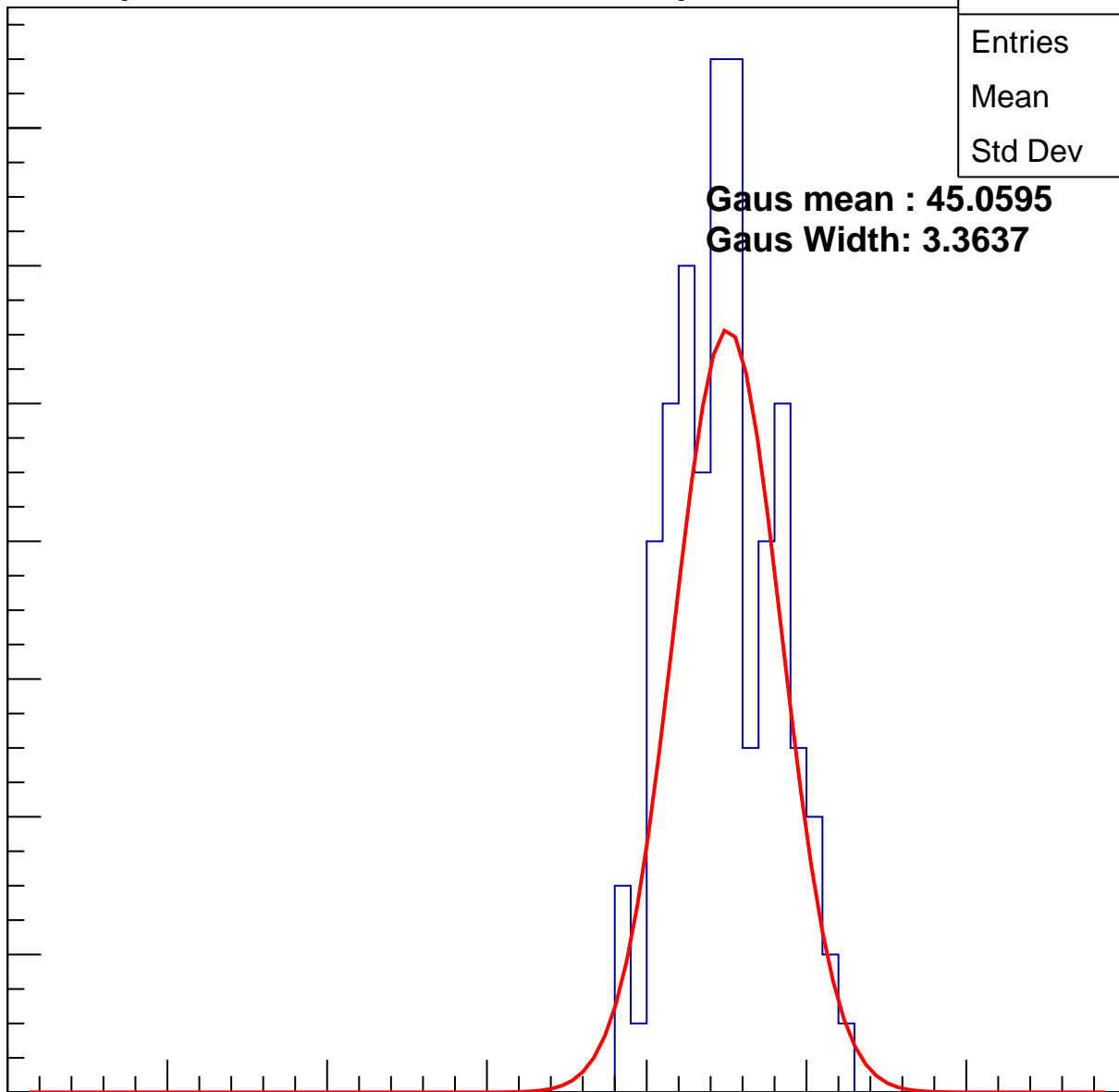
3.176

**Gaus mean : 45.0595**

**Gaus Width: 3.3637**

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch92, adc3

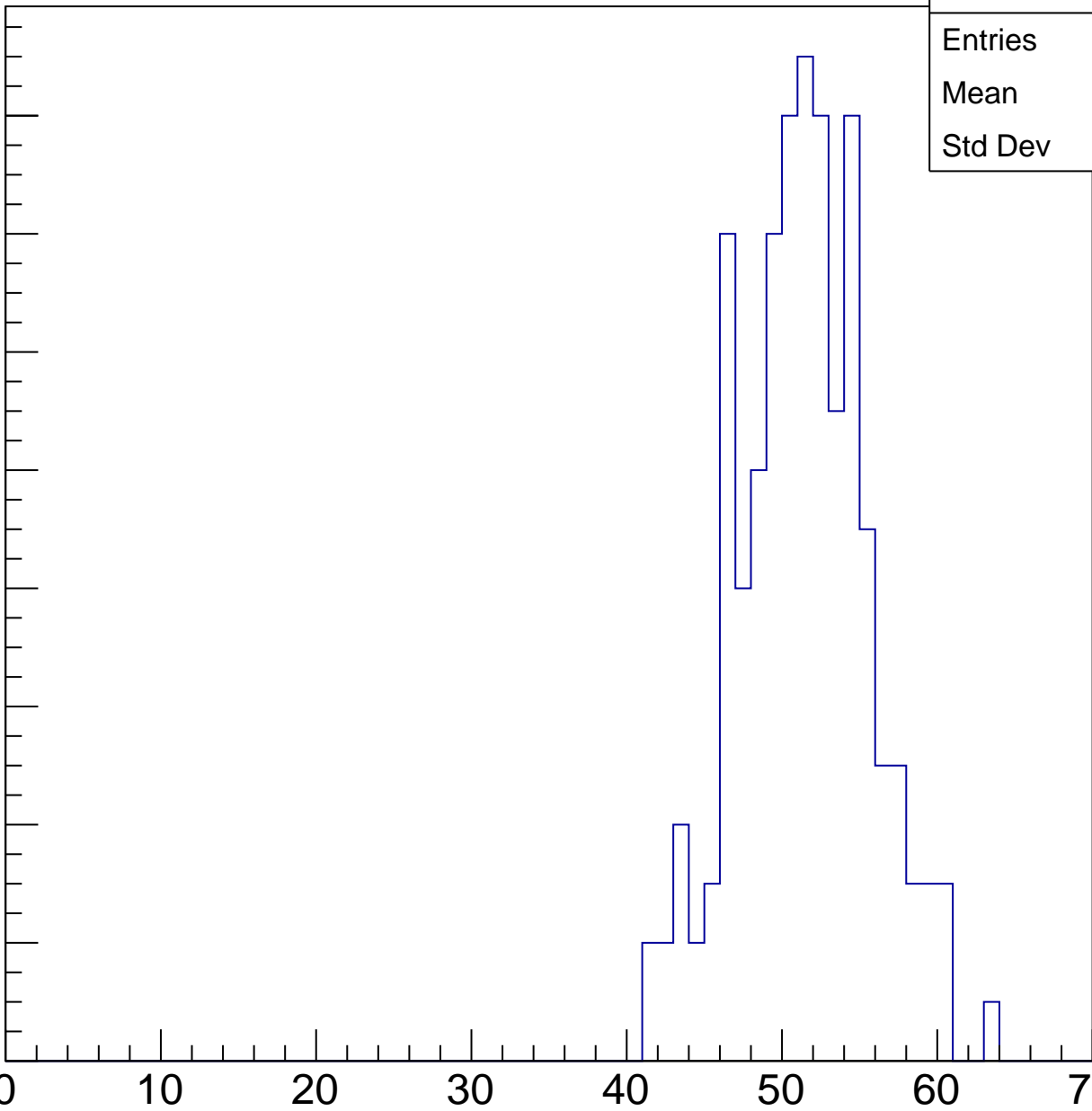
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	50.9
Std Dev	4.162

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

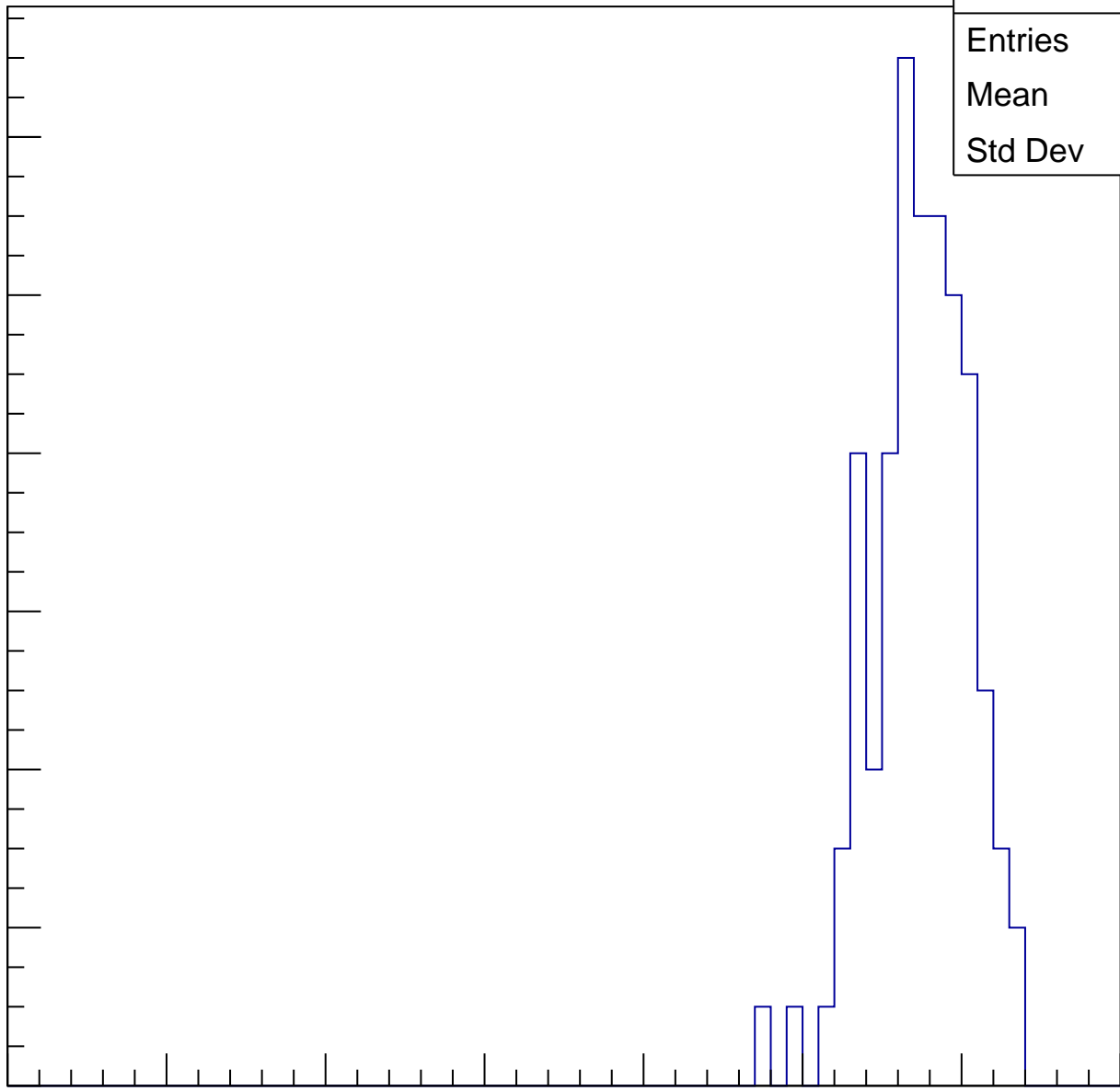
2

0

Entries	90
Mean	56.92
Std Dev	3.06

ampl

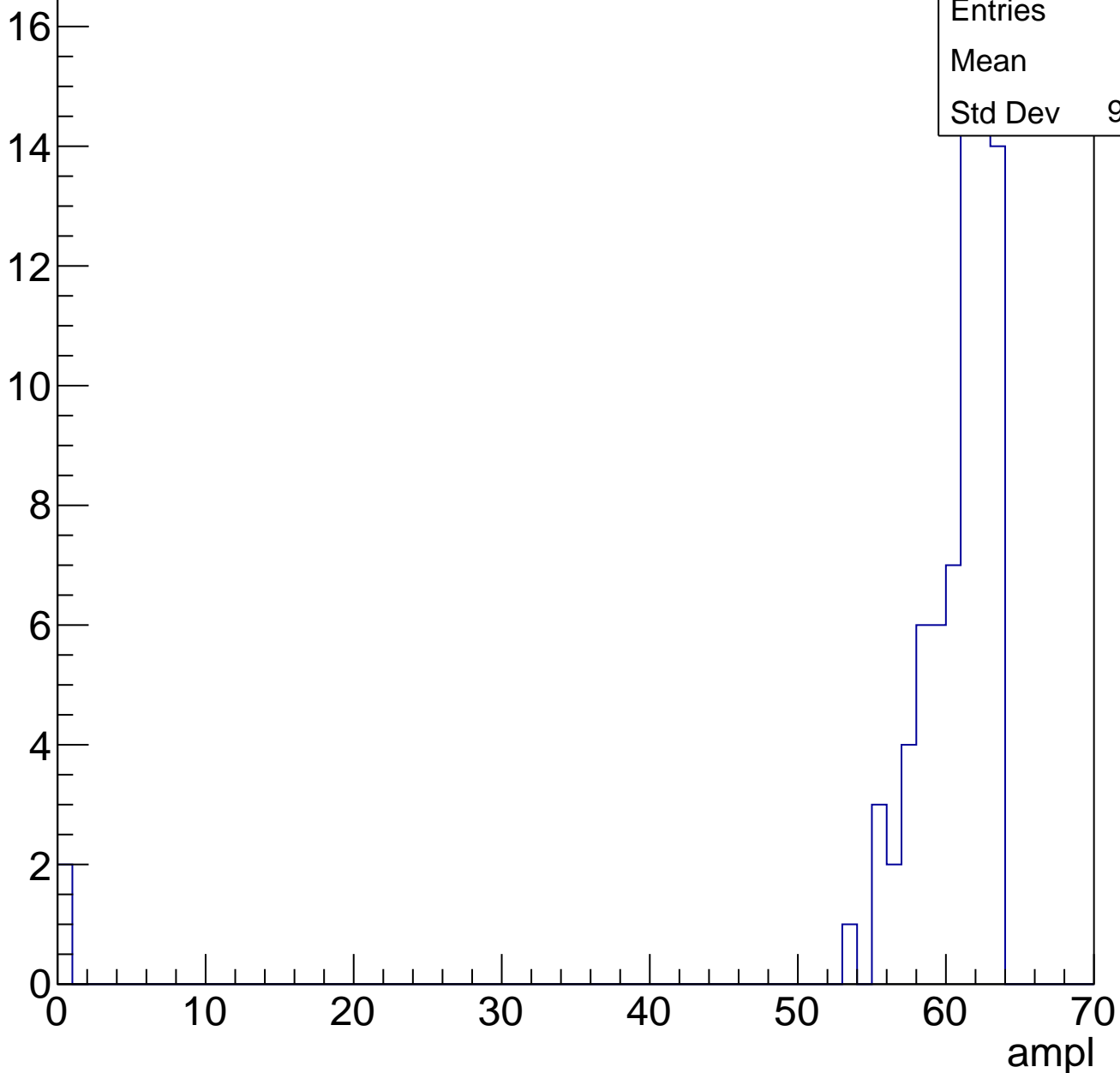
0 10 20 30 40 50 60 70



# B0L101S, U26-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

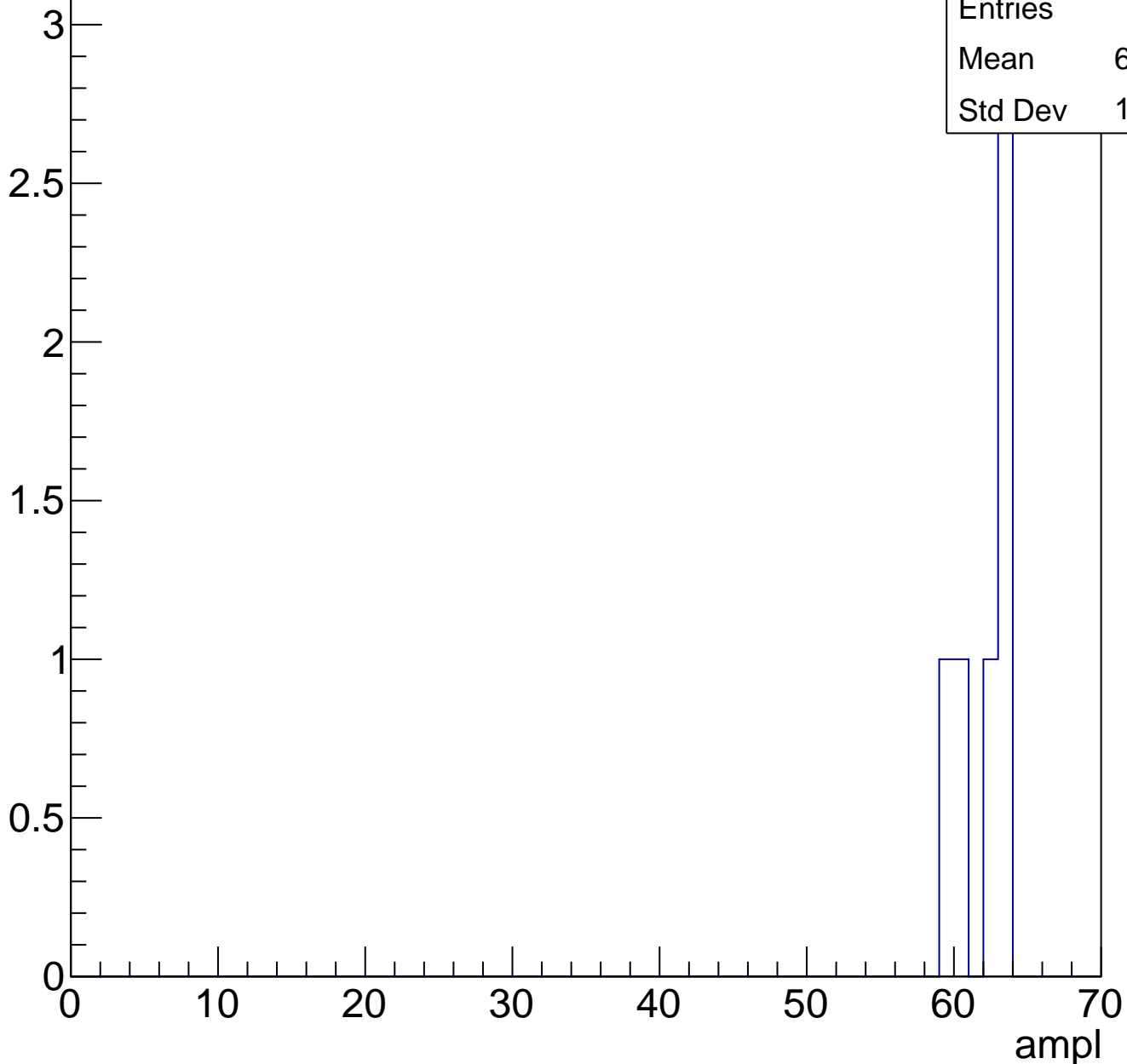
Entry



# B0L101S, U26-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



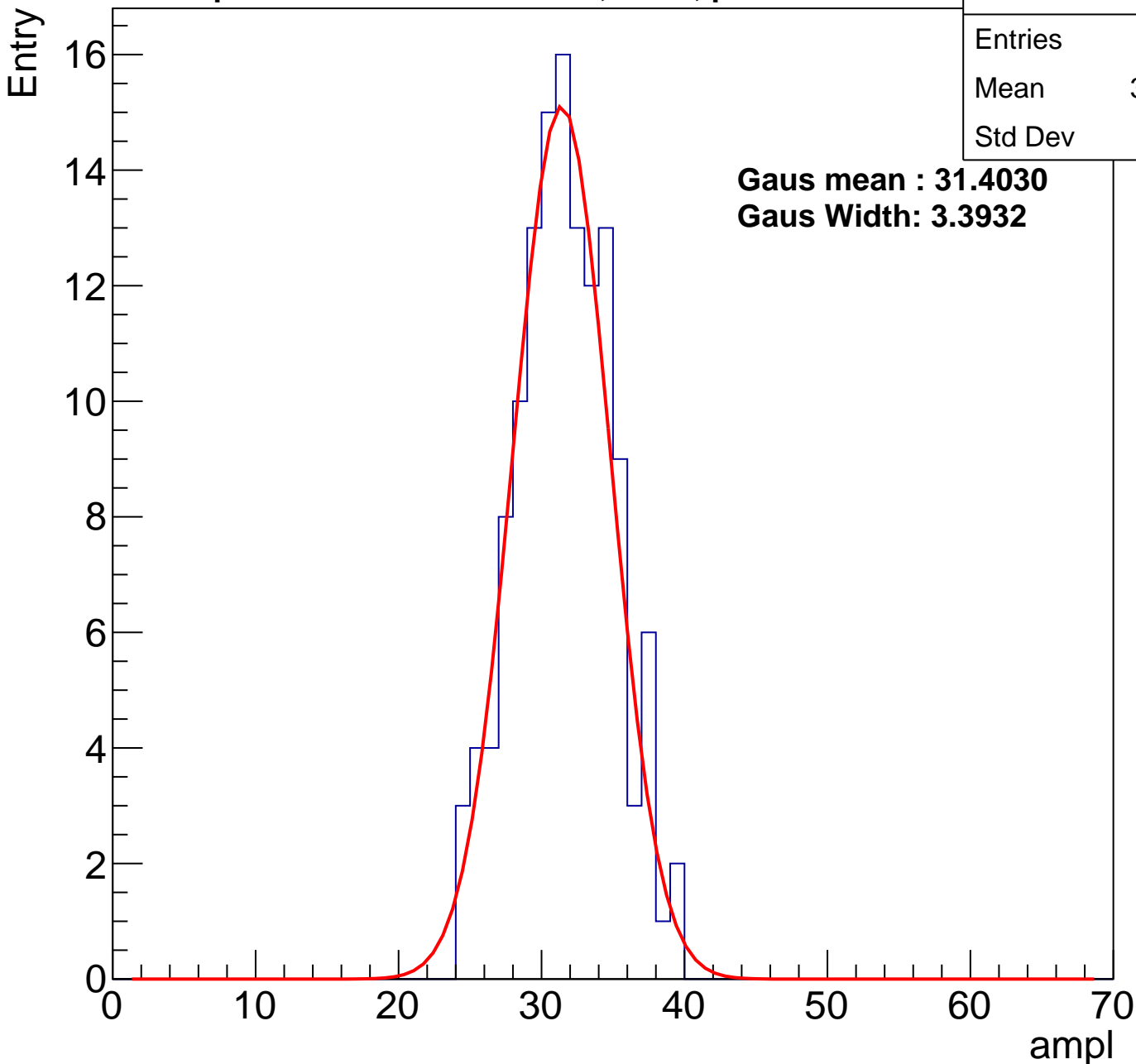
# B0L101S, U26-ch93, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	31.14
Std Dev	3.33

**Gaus mean : 31.4030**

**Gaus Width: 3.3932**



# B0L101S, U26-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	38.06
Std Dev	3.548

**Gaus mean : 38.5627**

**Gaus Width: 3.7336**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

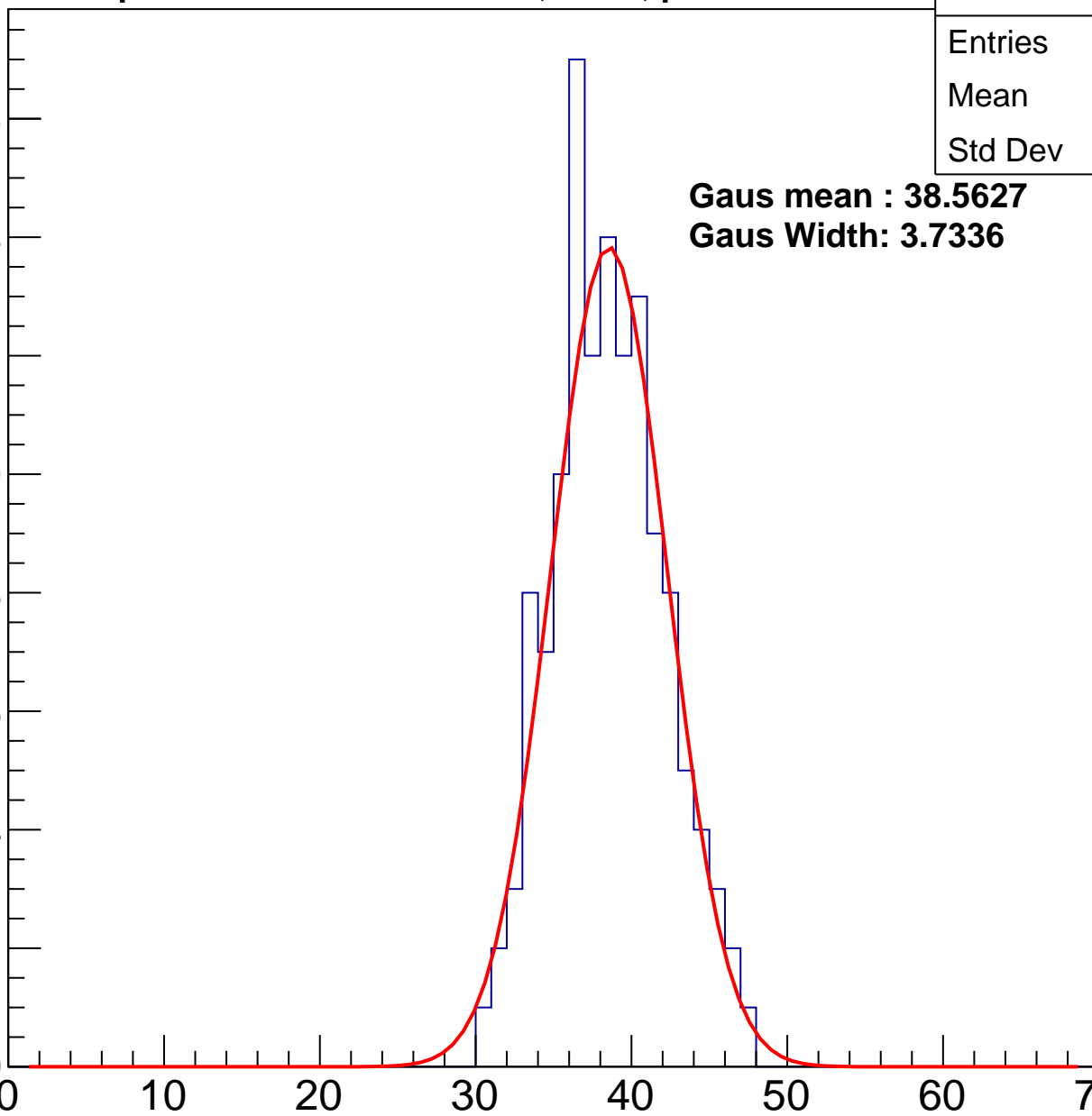
40

50

60

70

ampl



# B0L101S, U26-ch93, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

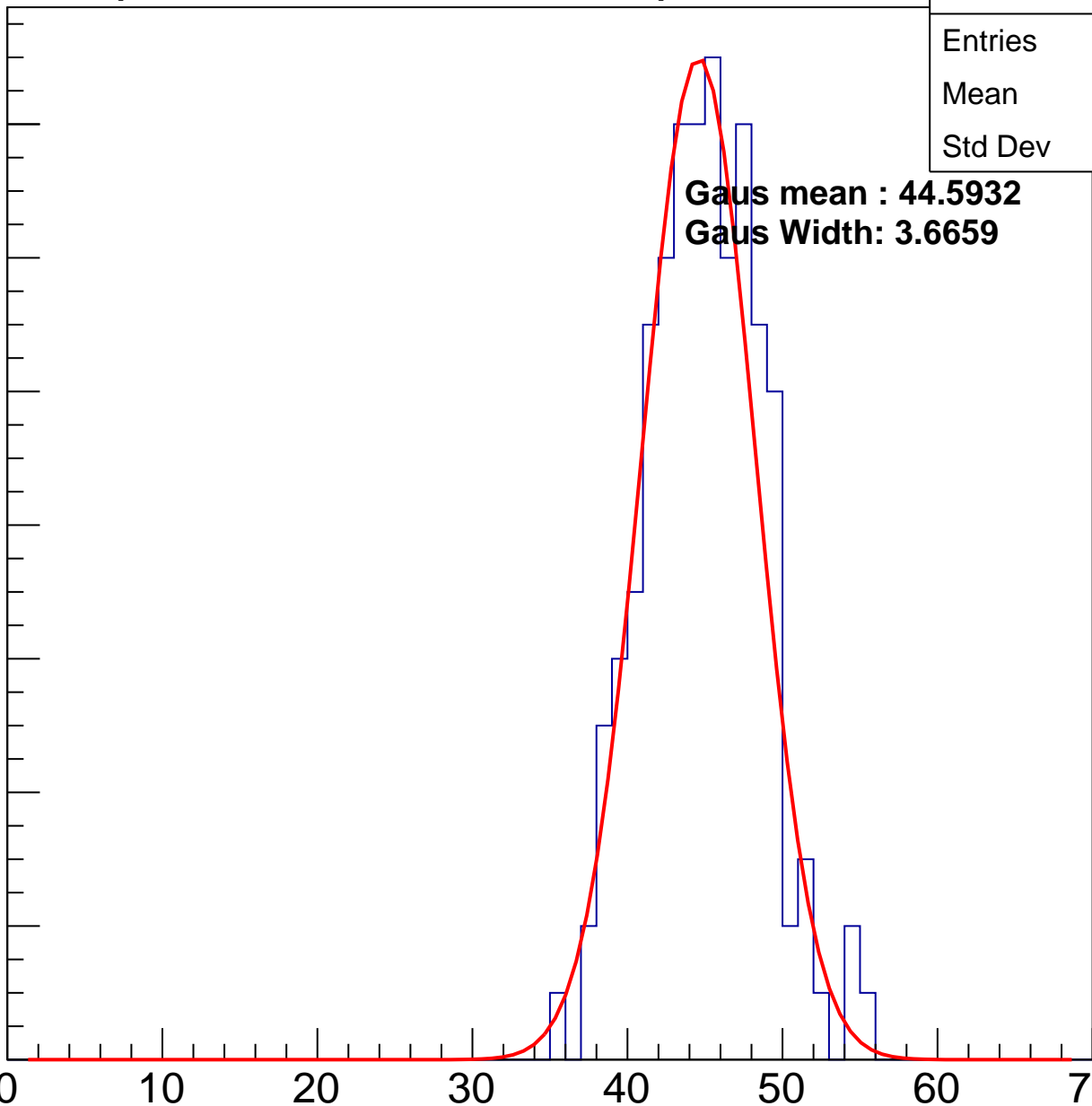
14  
12  
10  
8  
6  
4  
2  
0

Entries	143
Mean	44.45
Std Dev	3.713

**Gaus mean : 44.5932**

**Gaus Width: 3.6659**

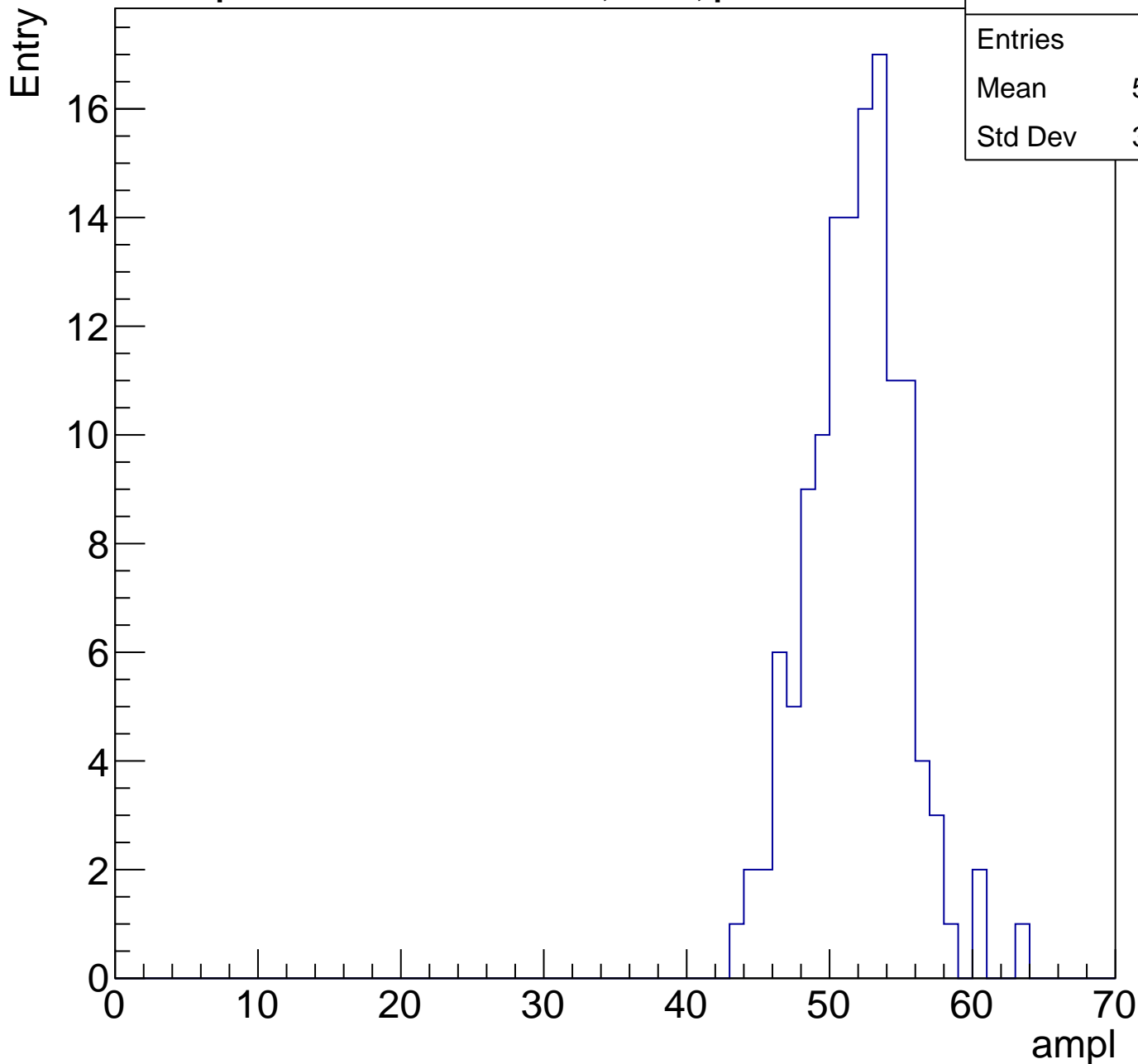
0 10 20 30 40 50 60 70  
ampl



# B0L101S, U26-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

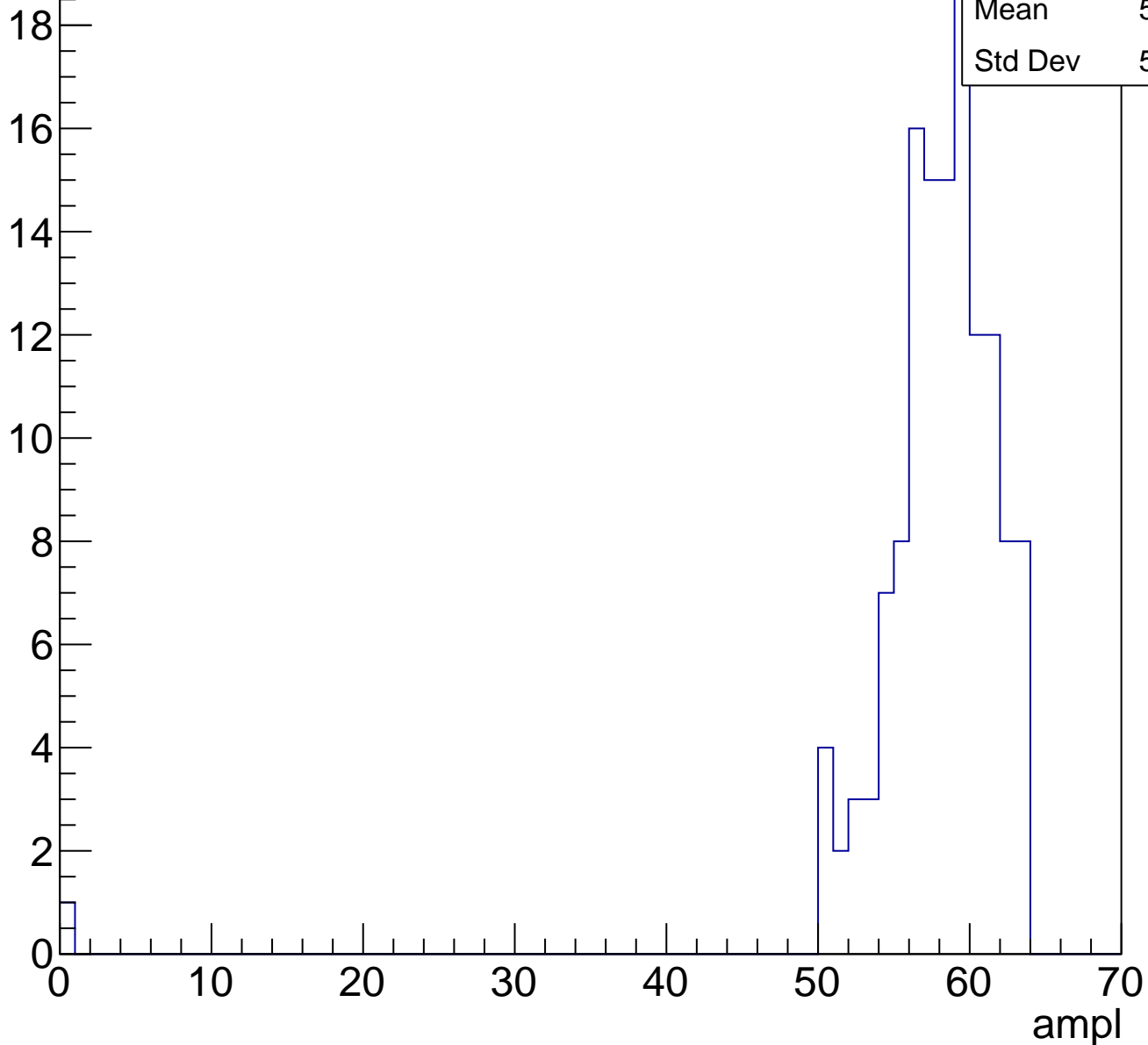
Entries	129
Mean	51.44
Std Dev	3.405



# B0L101S, U26-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	133
Mean	57.36
Std Dev	5.889

# B0L101S, U26-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	43
Mean	59.4
Std Dev	9.445

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

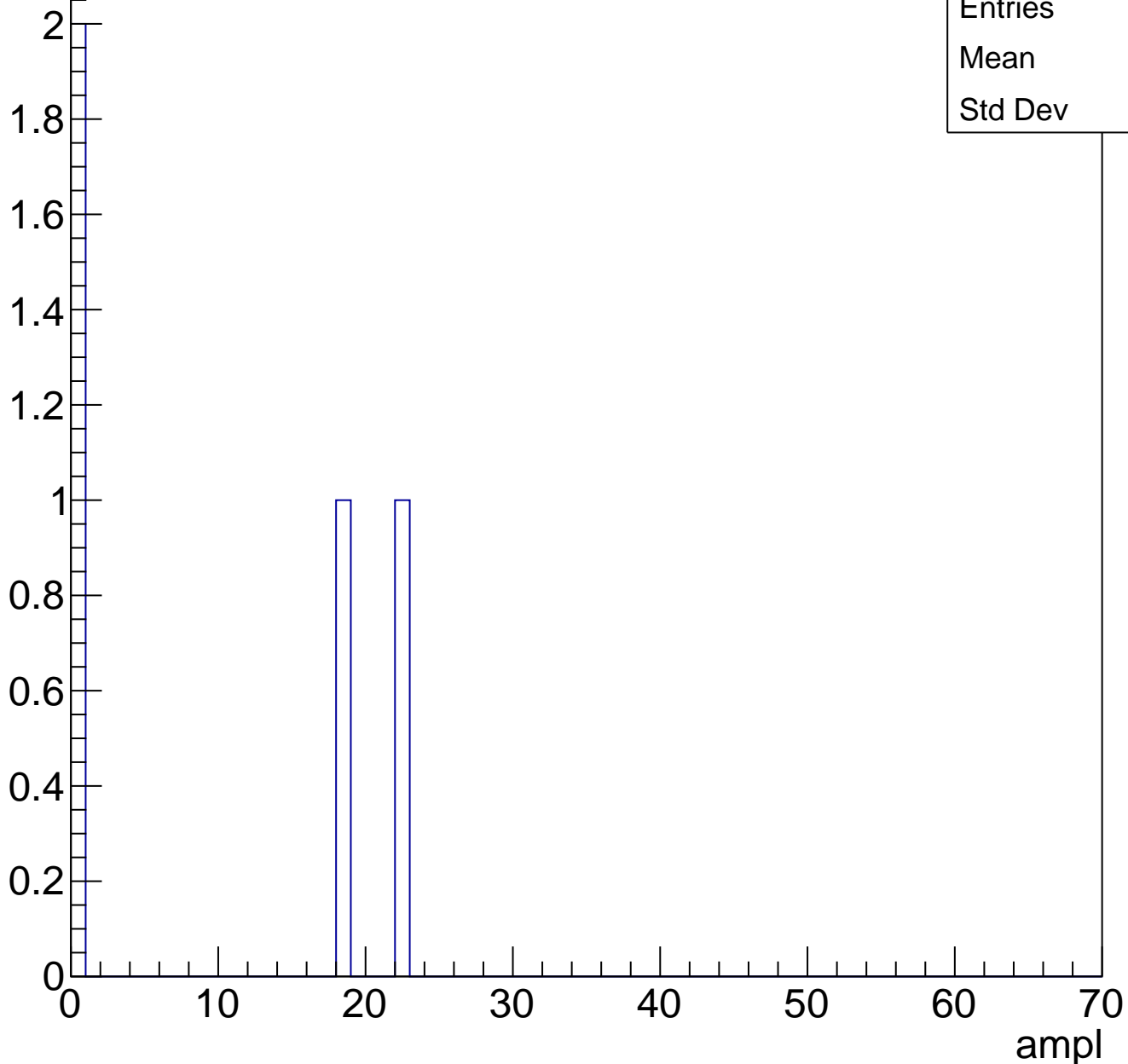




# B0L101S, U26-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	4
Mean	10
Std Dev	10.1

# B0L101S, U26-ch94, adc0

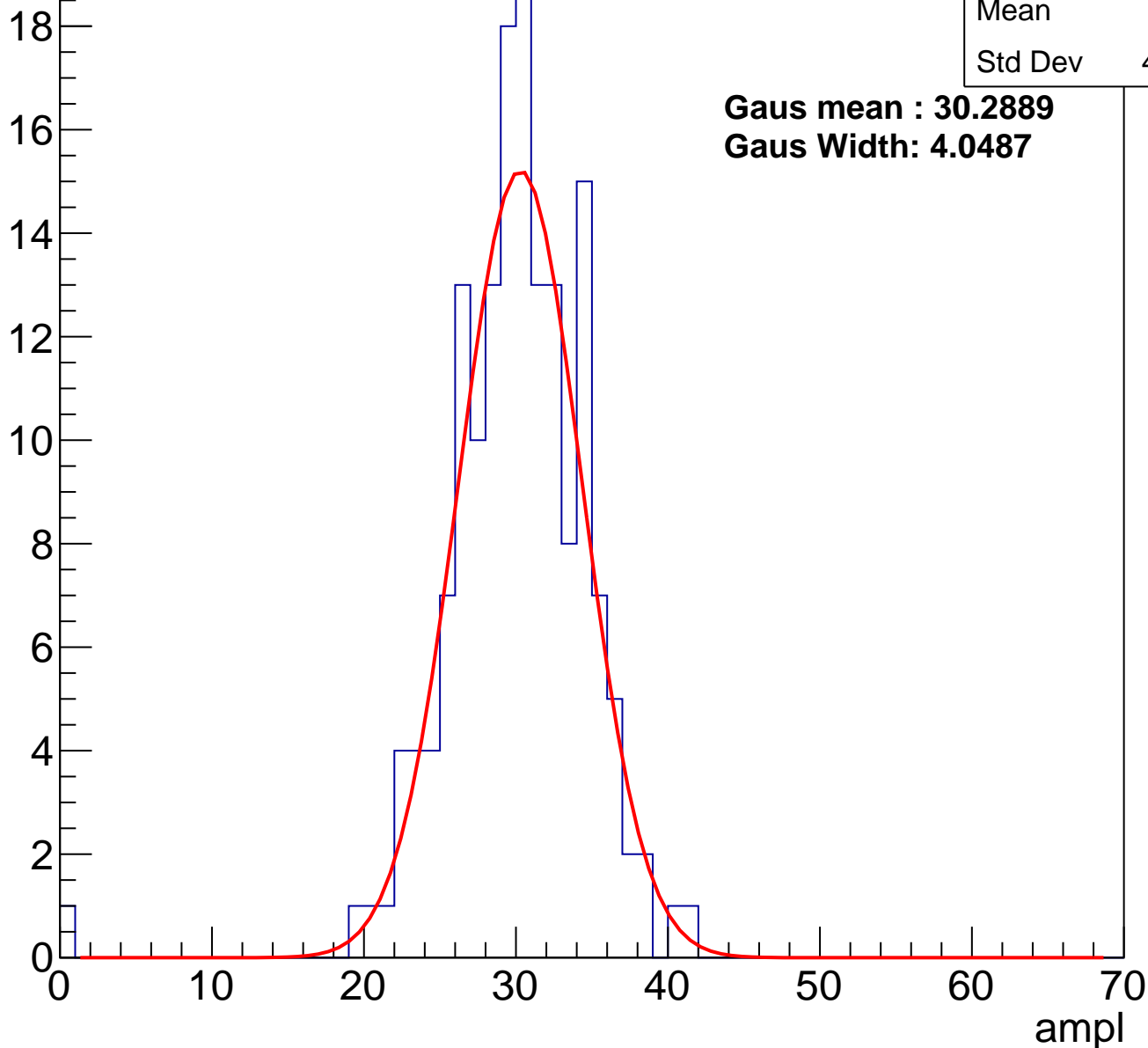
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	163
Mean	29.6
Std Dev	4.629

**Gaus mean : 30.2889**

**Gaus Width: 4.0487**

Entry



# B0L101S, U26-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	36.61
Std Dev	3.527

**Gaus mean : 37.2605**

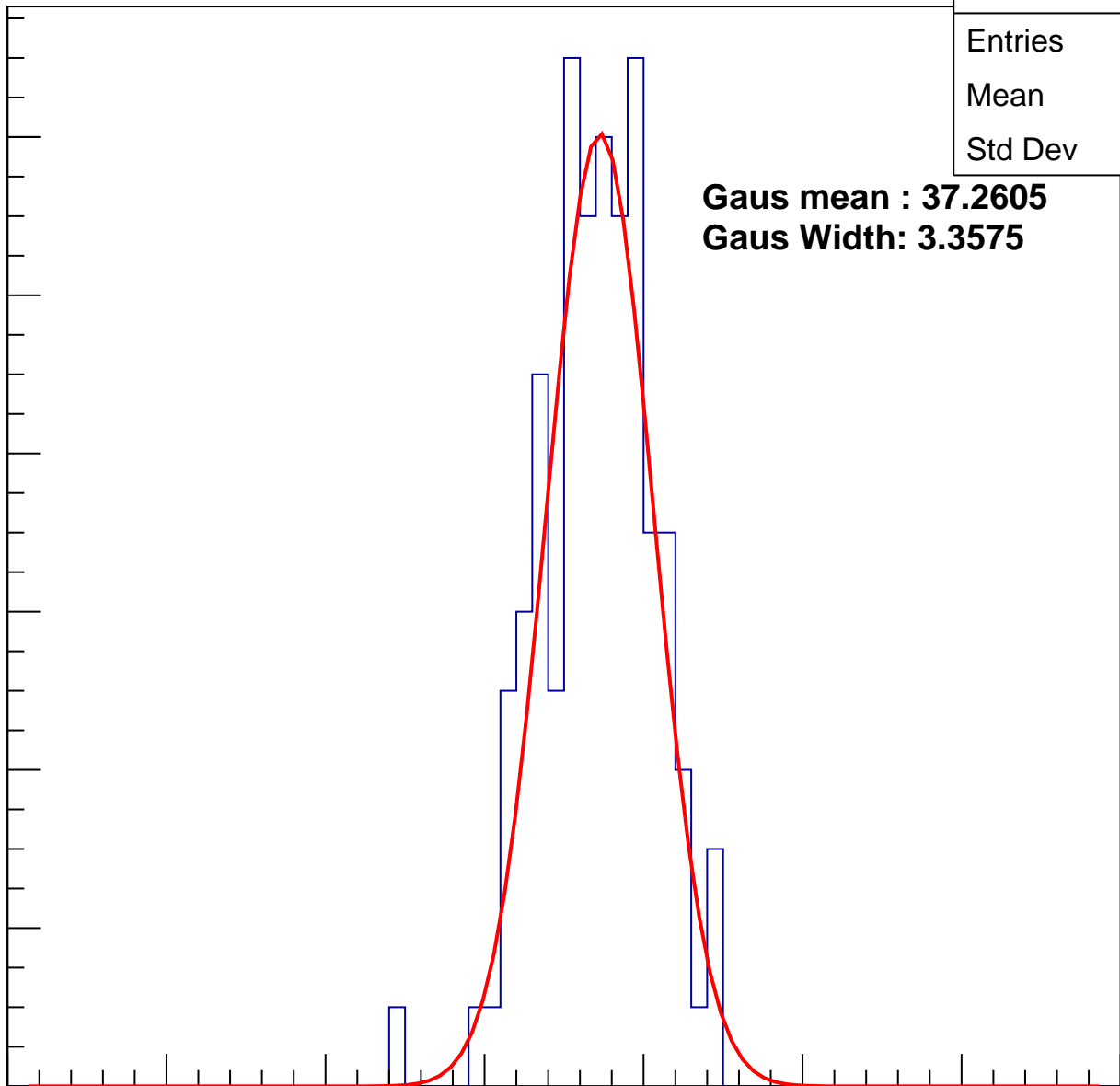
**Gaus Width: 3.3575**

Entry

12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch94, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	143
Mean	42.83
Std Dev	3.82

**Gaus mean : 43.2091**

**Gaus Width: 4.1348**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

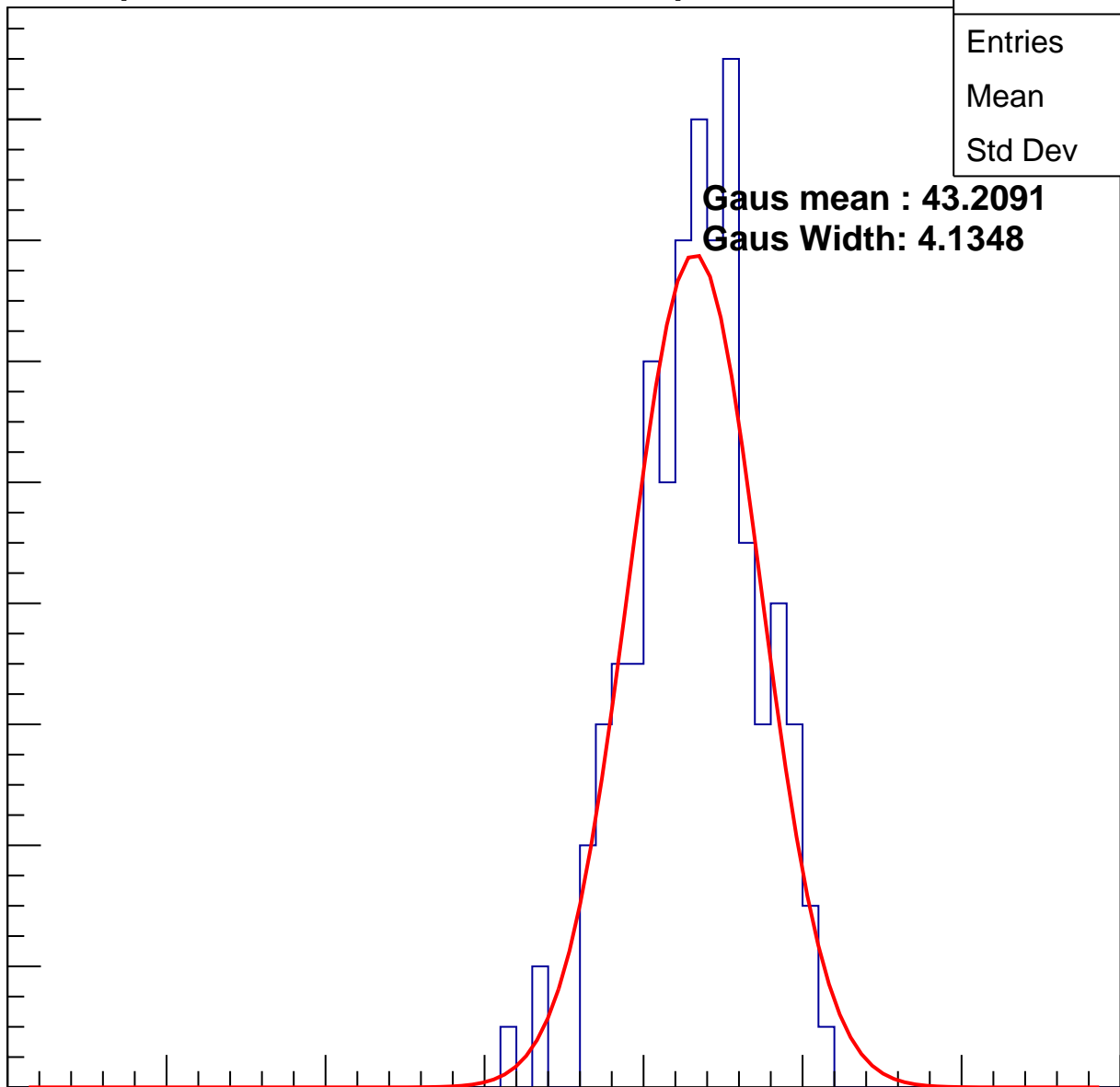
40

50

60

70

ampl



# B0L101S, U26-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

49.91

Std Dev

3.578

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch94, adc4

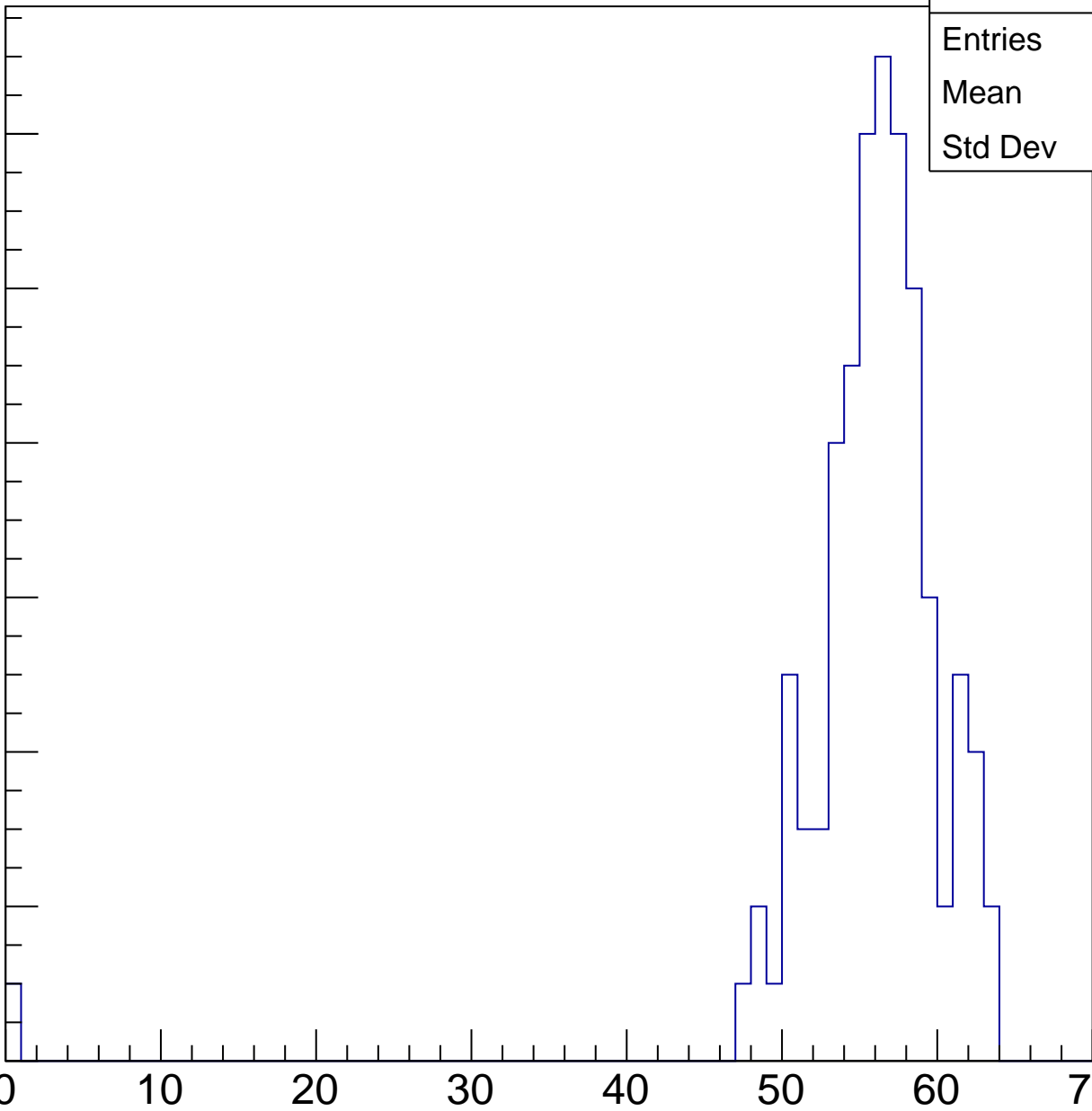
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	99
Mean	55.21
Std Dev	6.56

Entry

12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch94, adc5

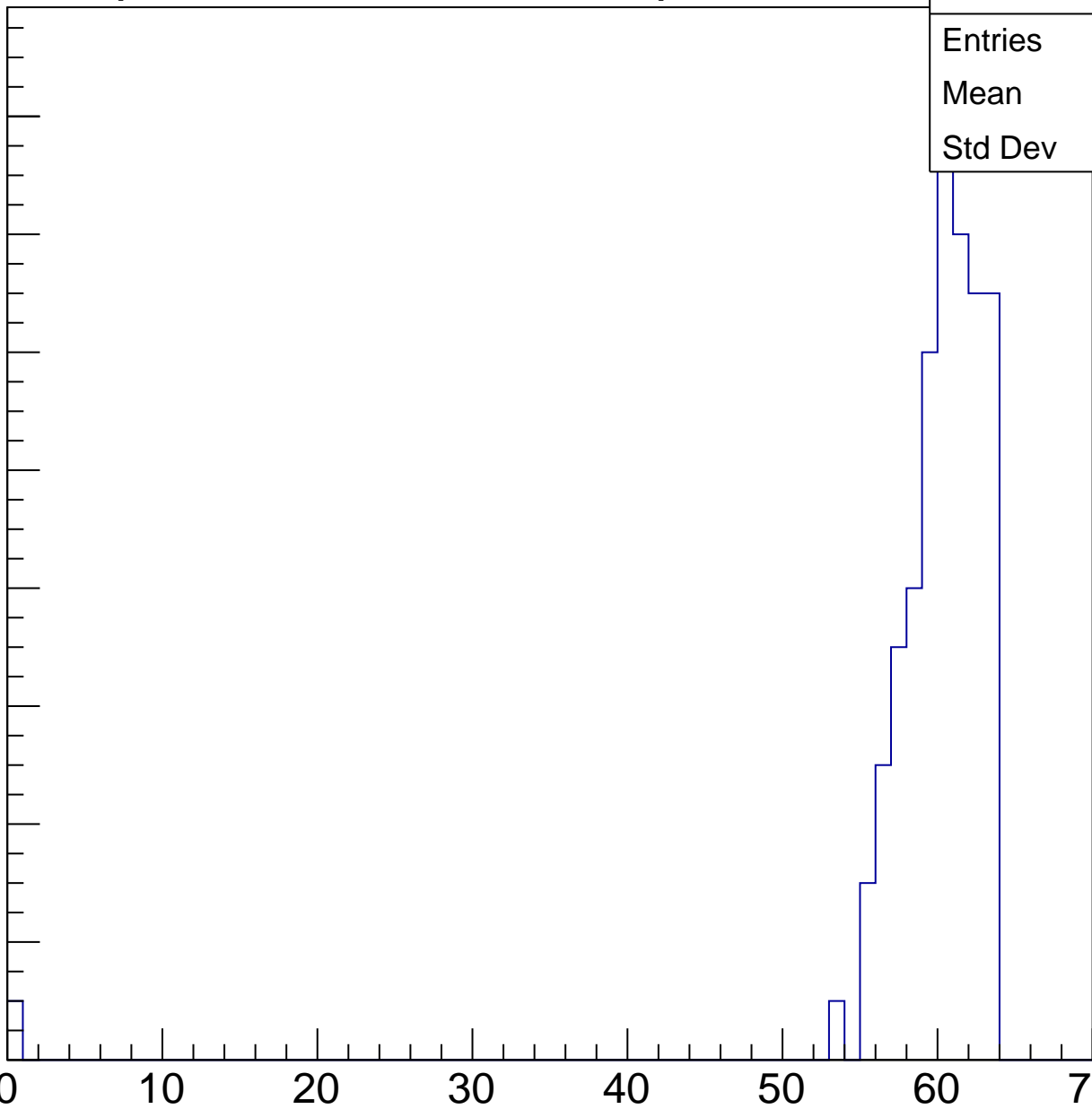
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	94
Mean	59.23
Std Dev	6.555

ampl

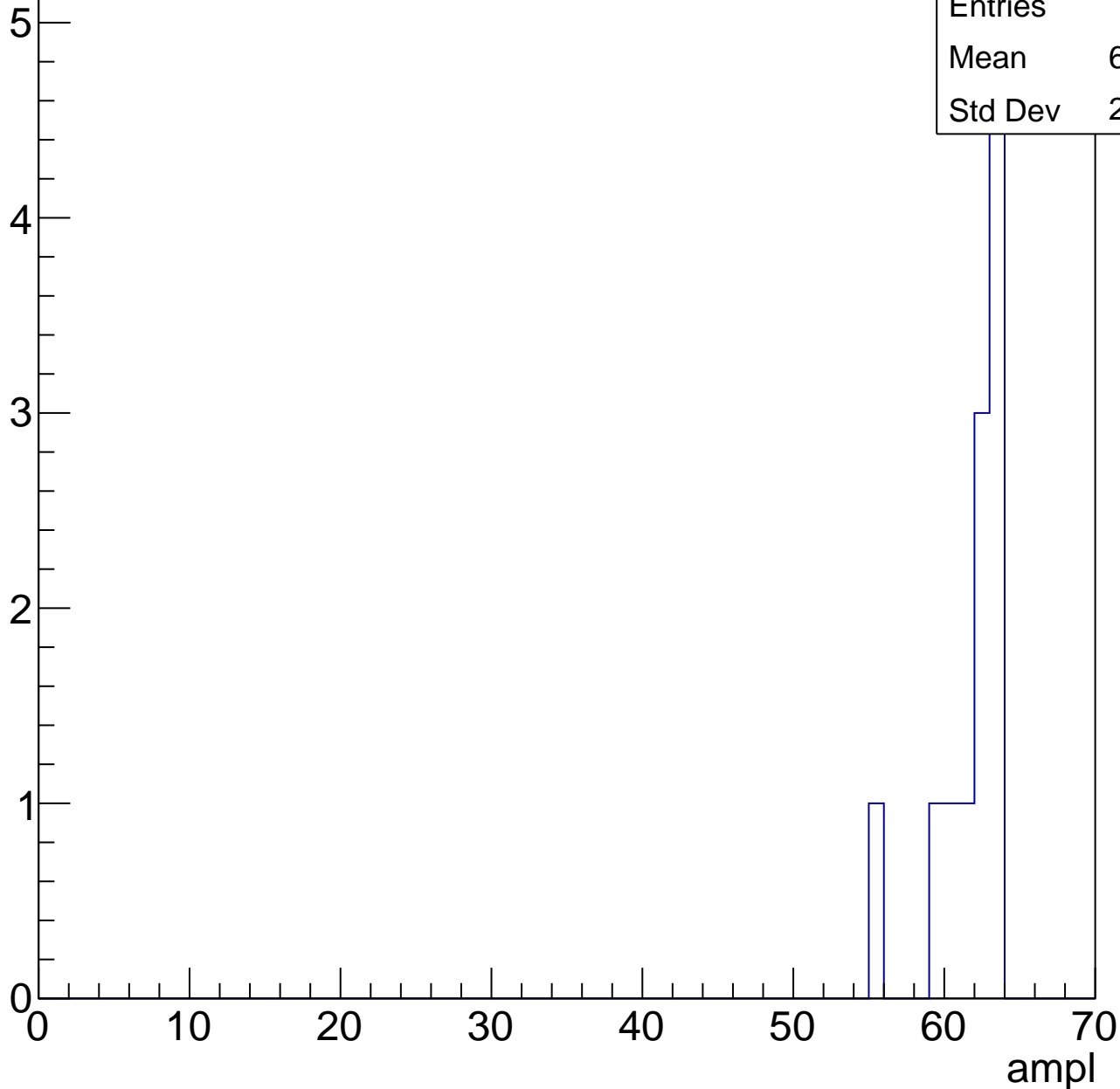


# B0L101S, U26-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.33
Std Dev	2.285





# B0L101S, U26-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch95, adc0

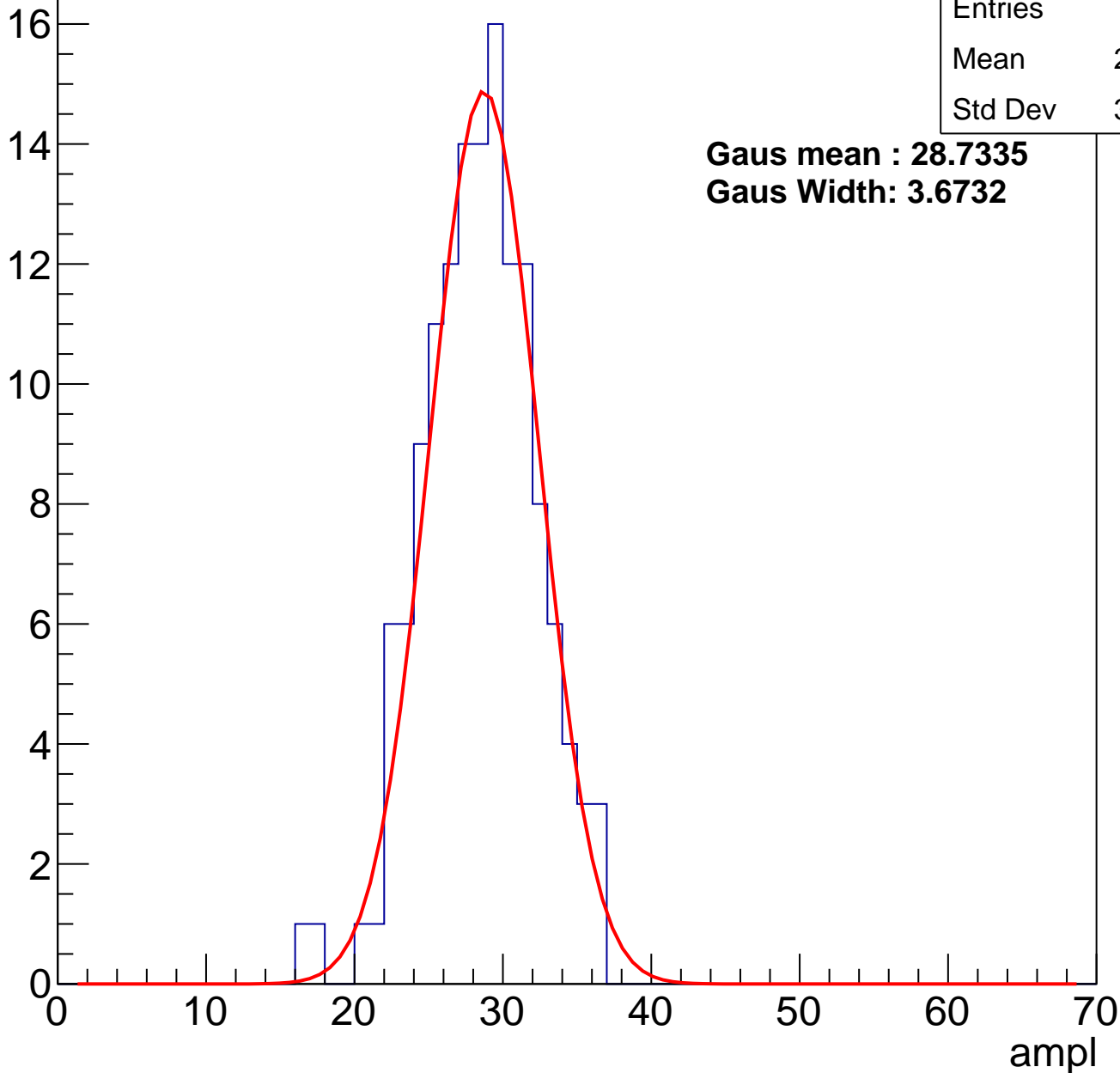
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	140
Mean	27.97
Std Dev	3.762

**Gaus mean : 28.7335**

**Gaus Width: 3.6732**

Entry



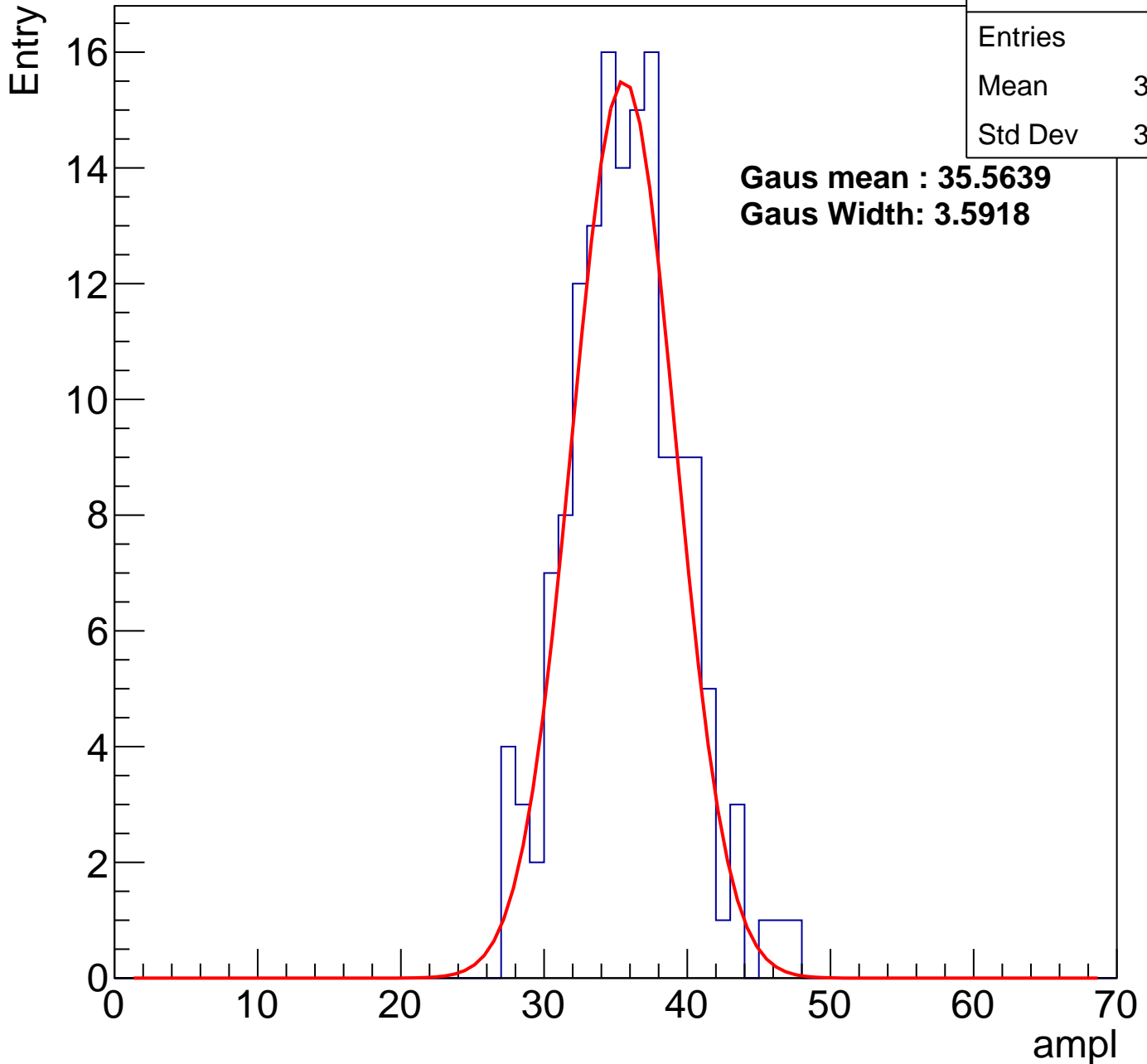
# B0L101S, U26-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	35.26
Std Dev	3.894

**Gaus mean : 35.5639**

**Gaus Width: 3.5918**



# B0L101S, U26-ch95, adc2

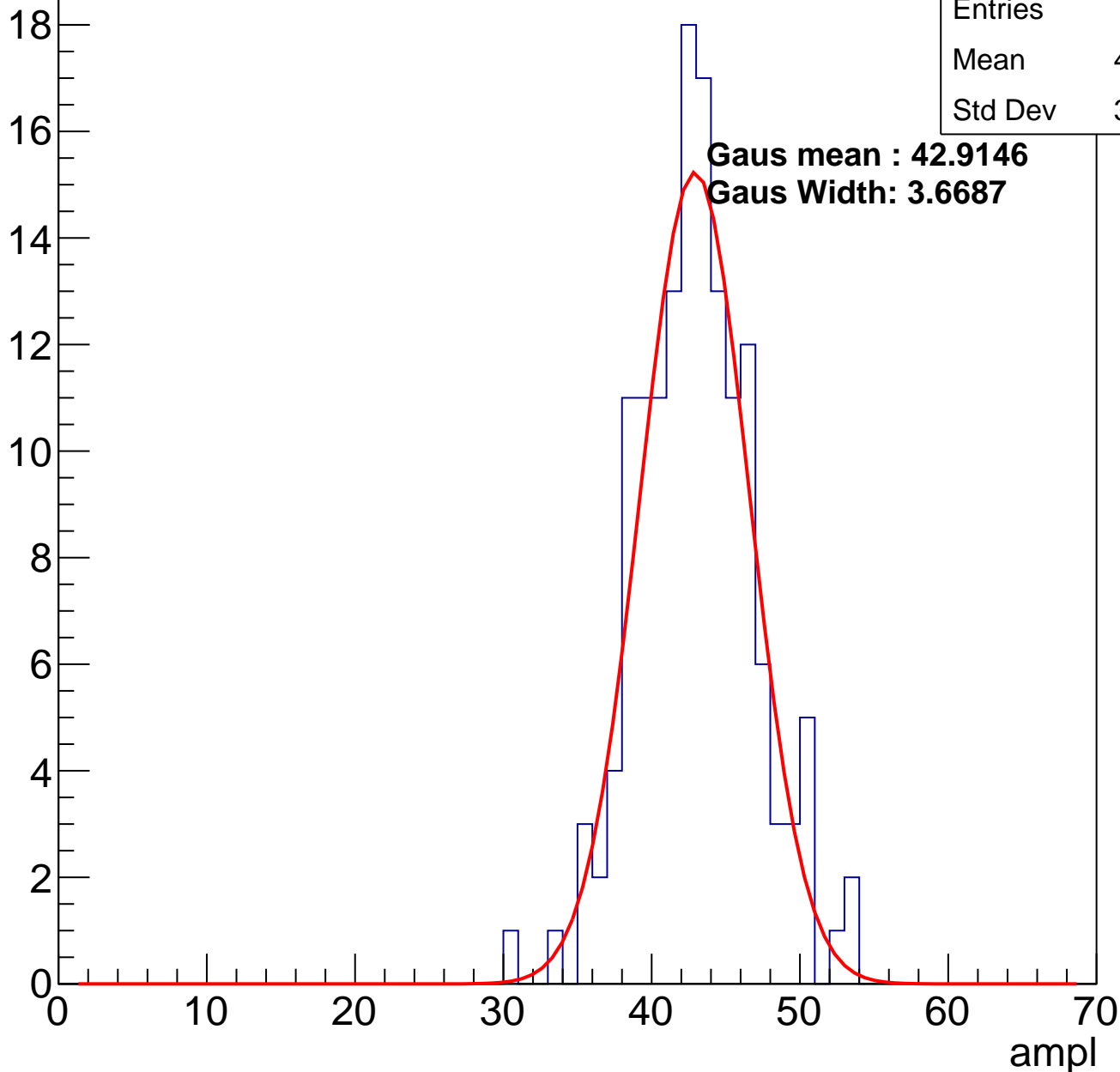
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	42.53
Std Dev	3.908

**Gaus mean : 42.9146**

**Gaus Width: 3.6687**

Entry



# B0L101S, U26-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

144

Mean

49.75

Std Dev

3.463

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	55.83
Std Dev	3.06

Entry

12

10

8

6

4

2

0

0

10

20

30

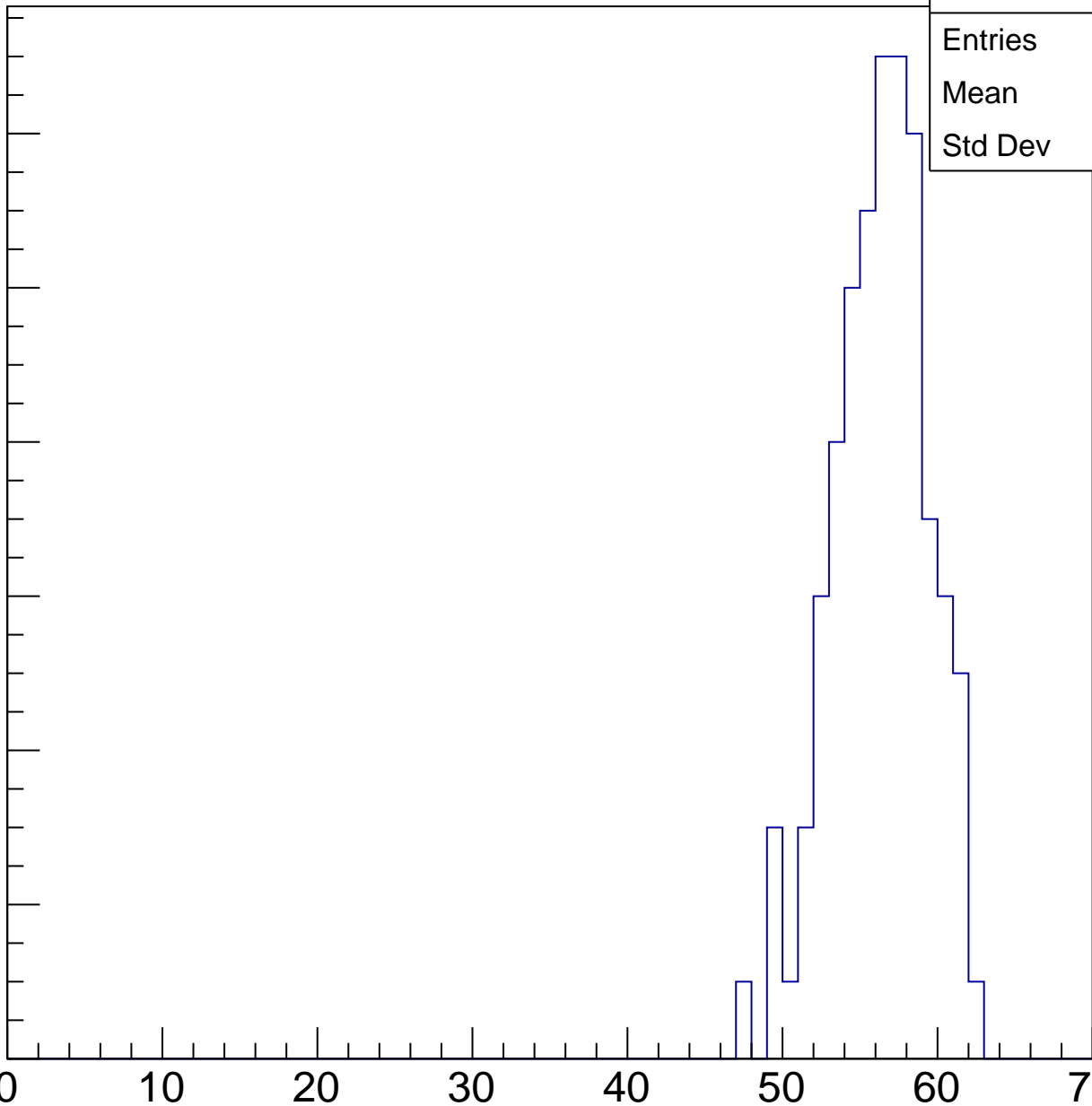
40

50

60

70

ampl



# B0L101S, U26-ch95, adc5

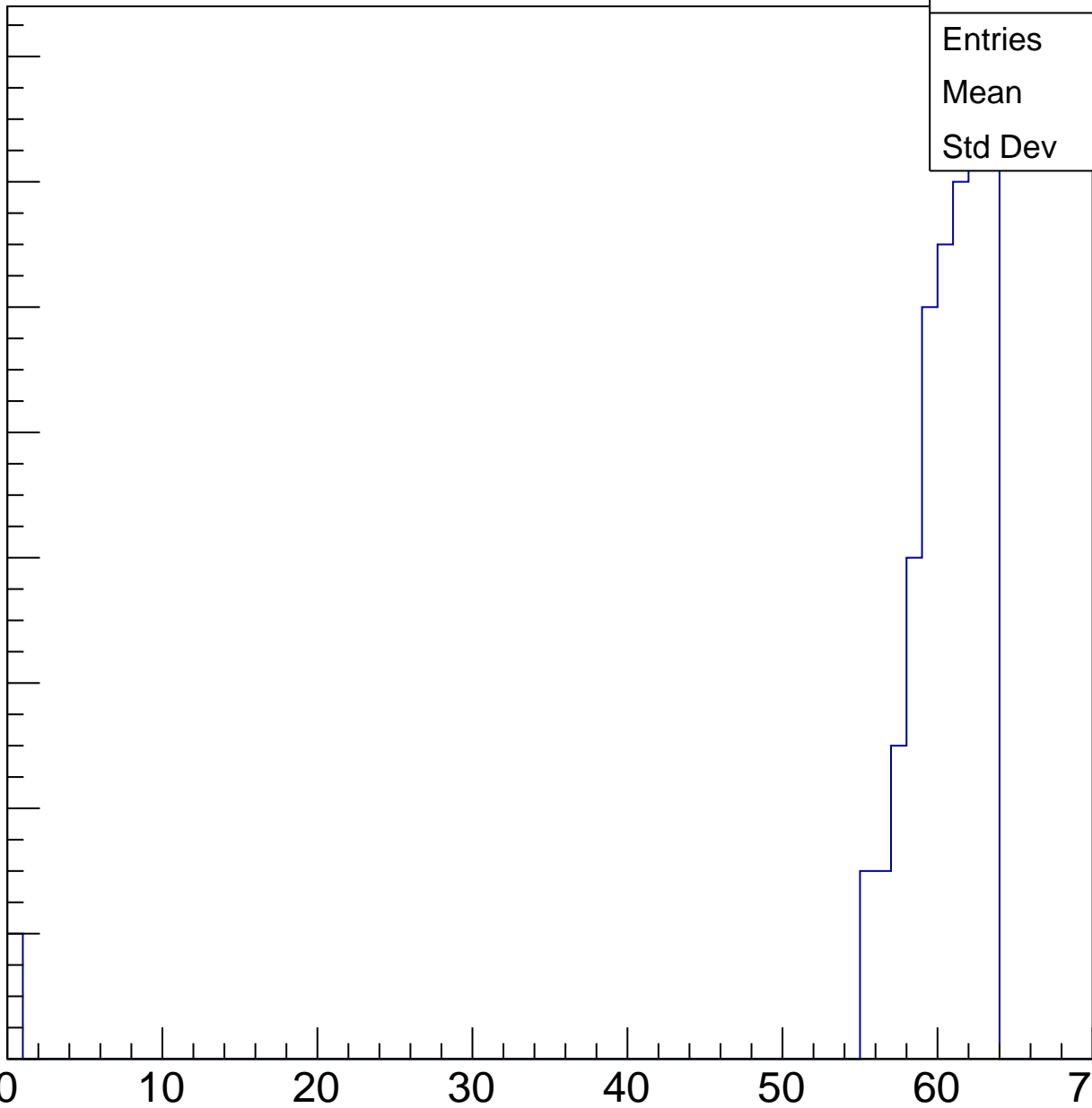
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	92
Mean	58.96
Std Dev	9.049

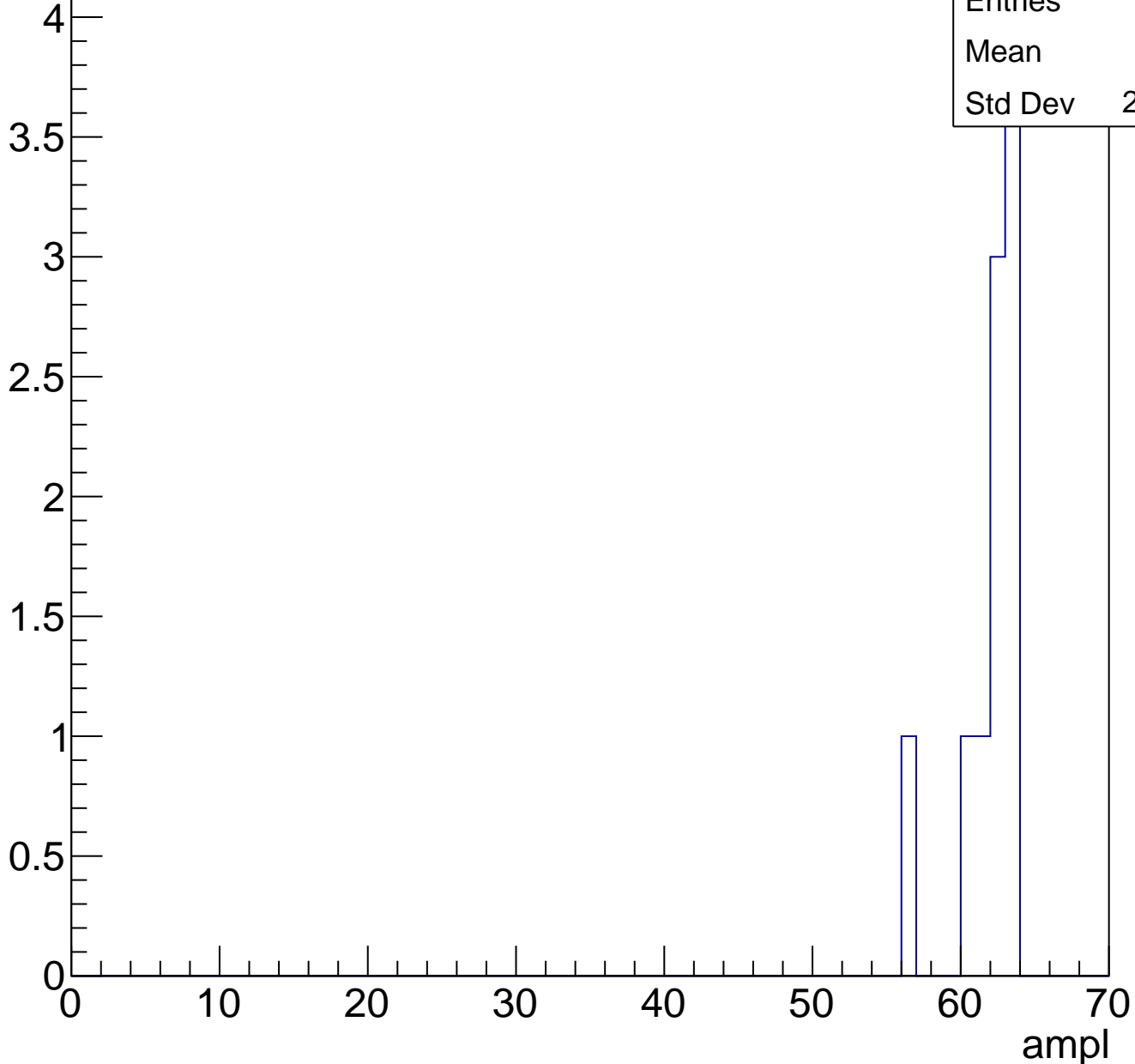
ampl



# B0L101S, U26-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



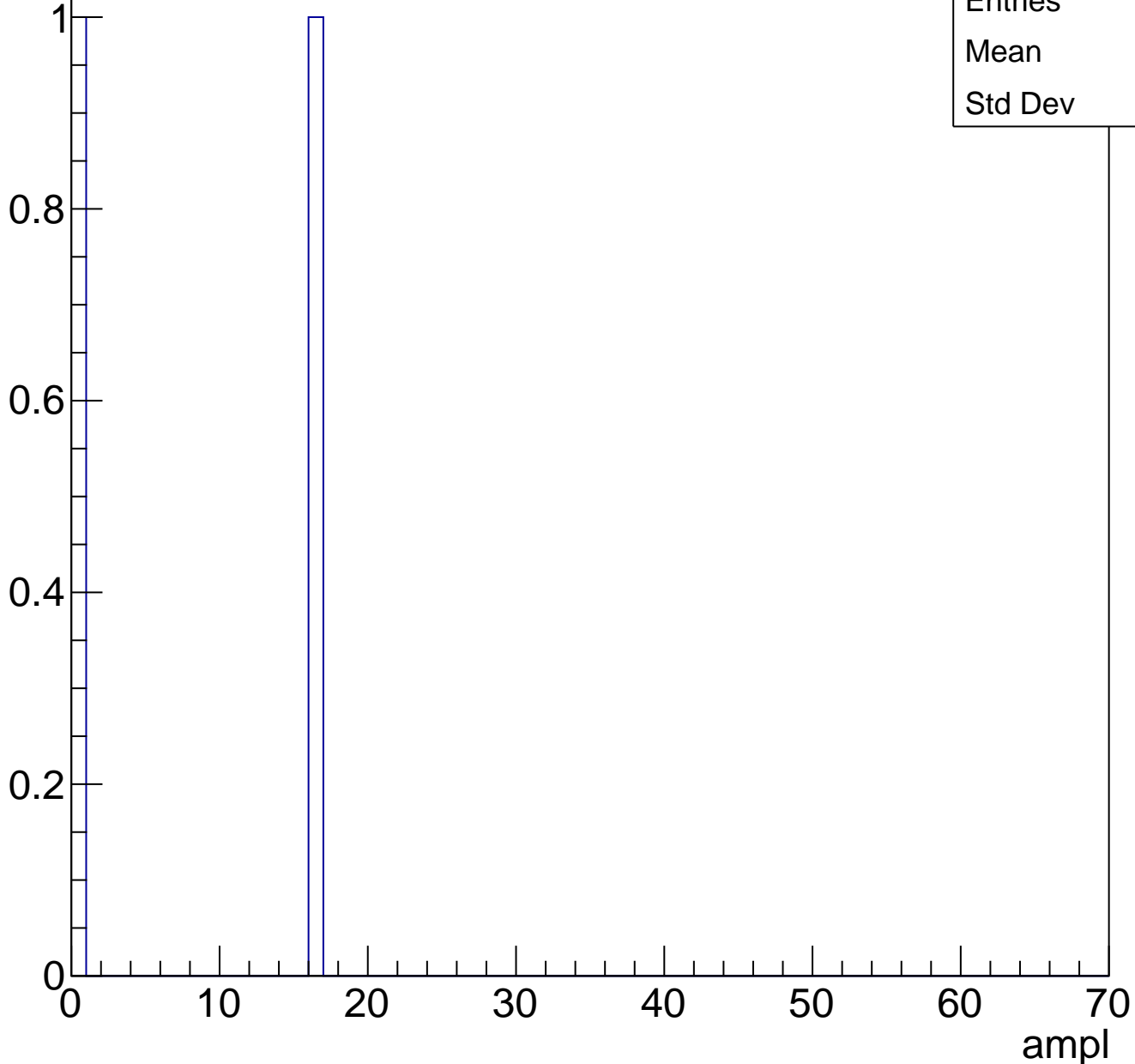
Entries	10
Mean	61.5
Std Dev	2.062



# B0L101S, U26-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	8
Std Dev	8

# B0L101S, U26-ch96, adc0

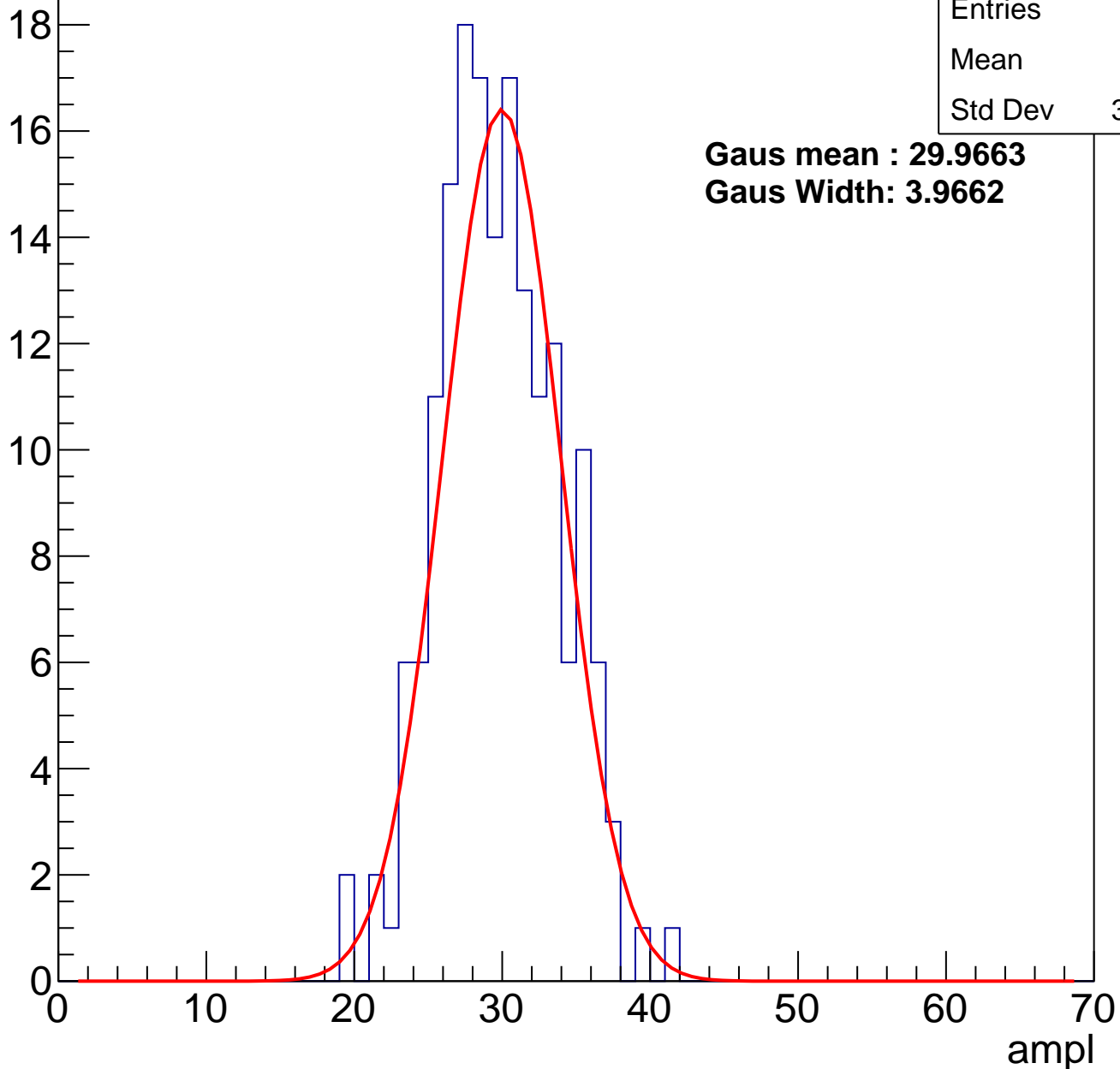
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	172
Mean	29.3
Std Dev	3.982

**Gaus mean : 29.9663**

**Gaus Width: 3.9662**

Entry



# B0L101S, U26-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	112
Mean	36.48
Std Dev	3.754

**Gaus mean : 37.2046**

**Gaus Width: 3.7785**

Entry

12

10

8

6

4

2

0

0

10

20

30

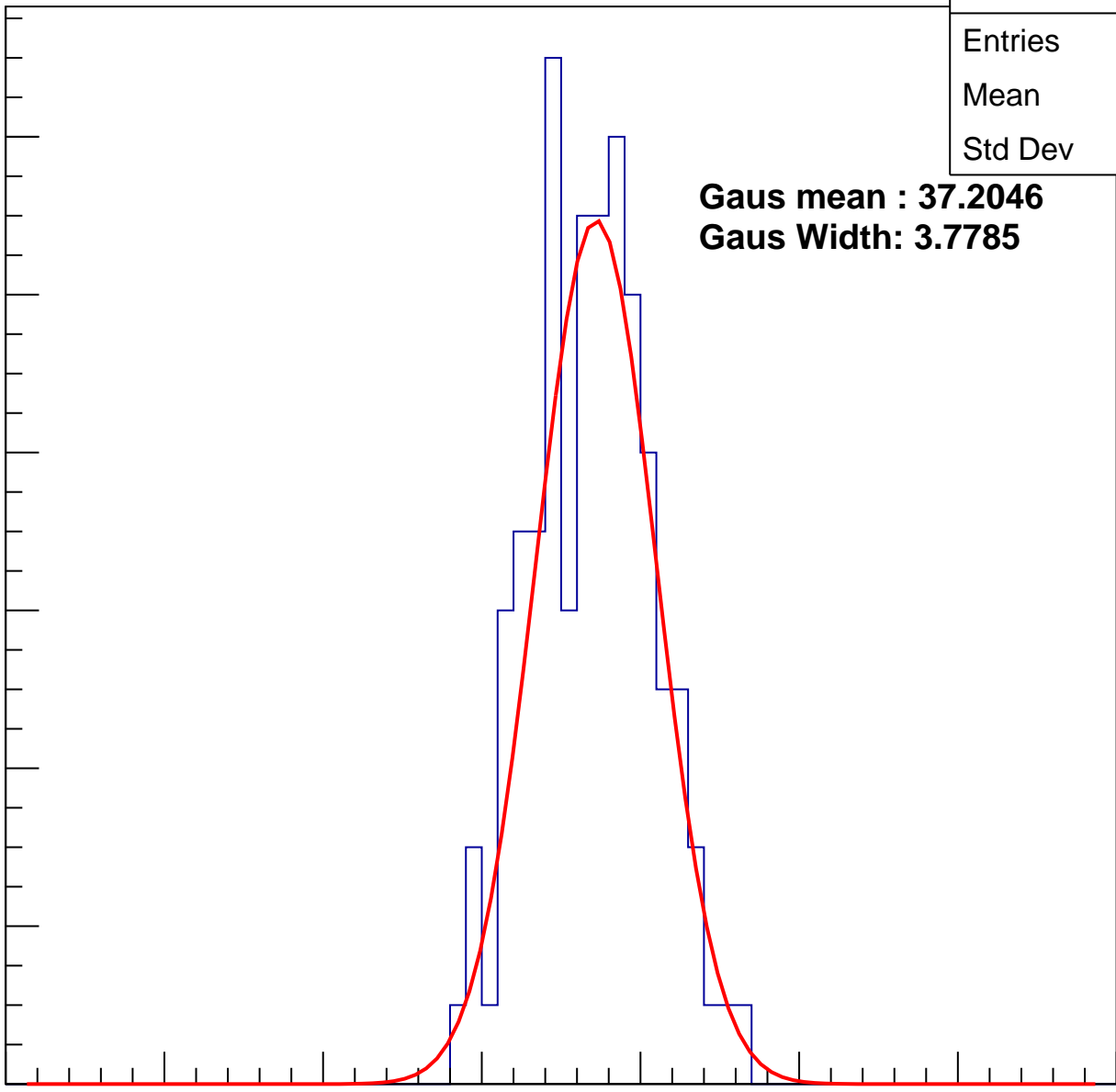
40

50

60

70

ampl



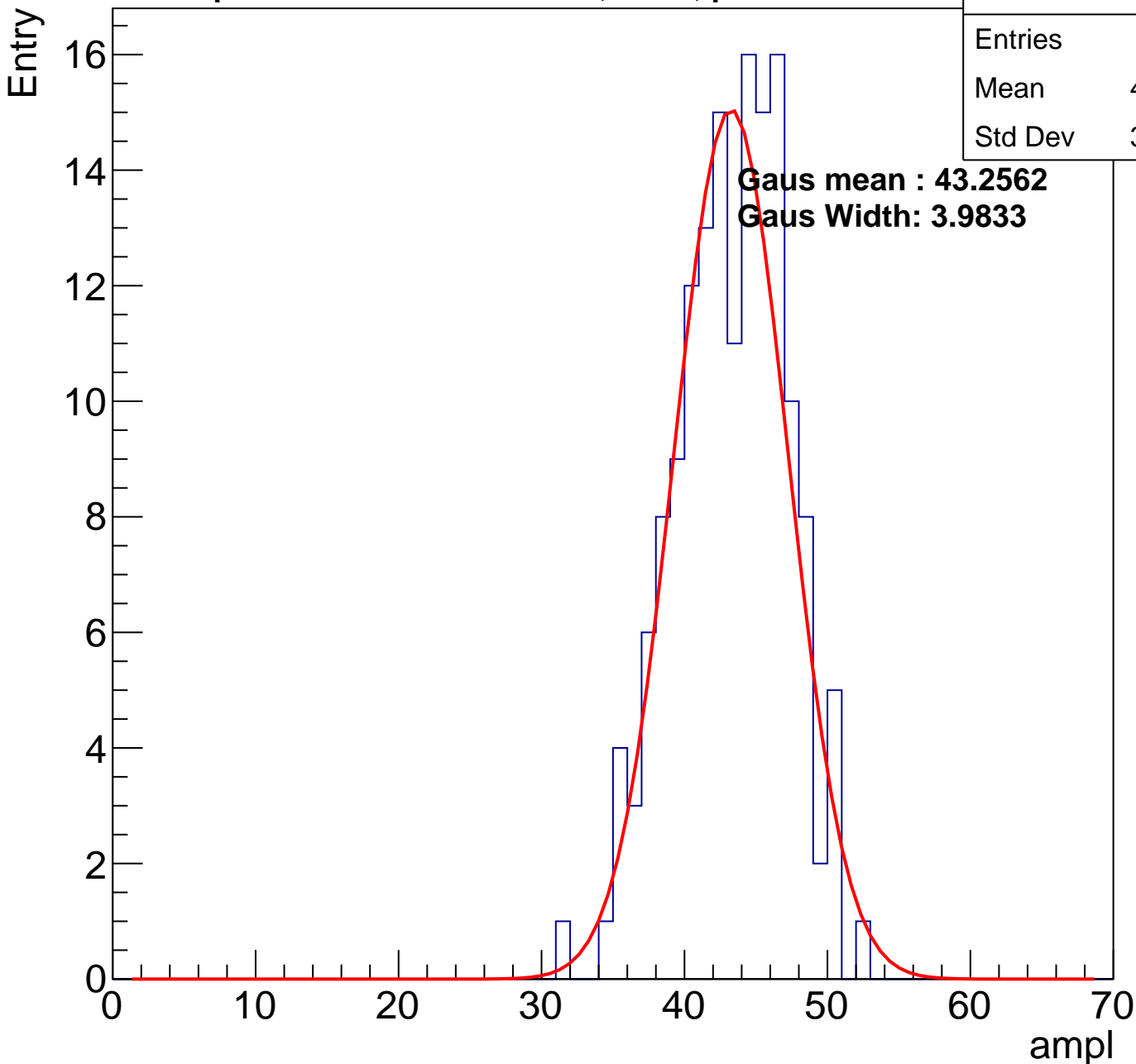
# B0L101S, U26-ch96, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	42.79
Std Dev	3.868

**Gaus mean : 43.2562**

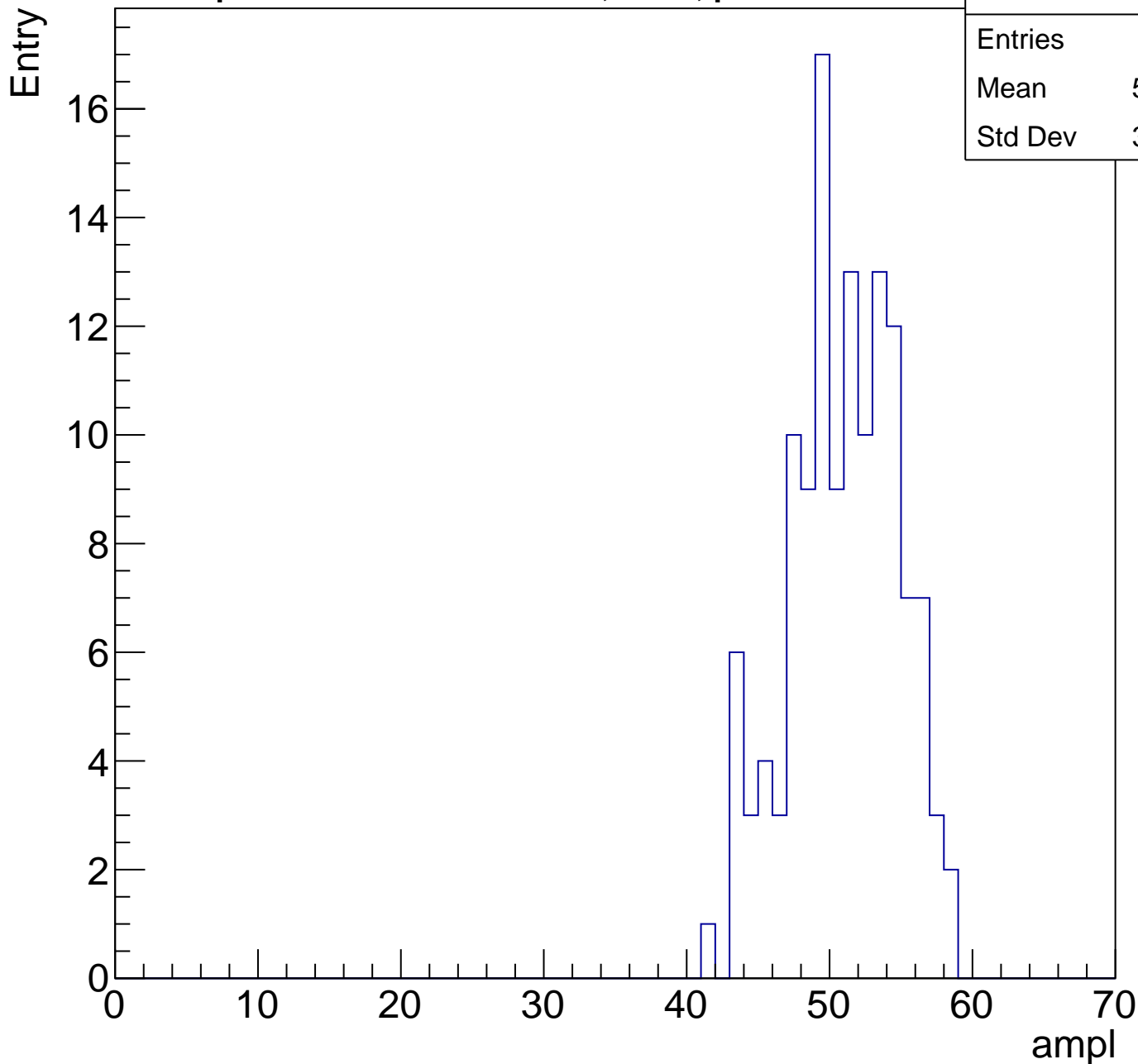
**Gaus Width: 3.9833**



# B0L101S, U26-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

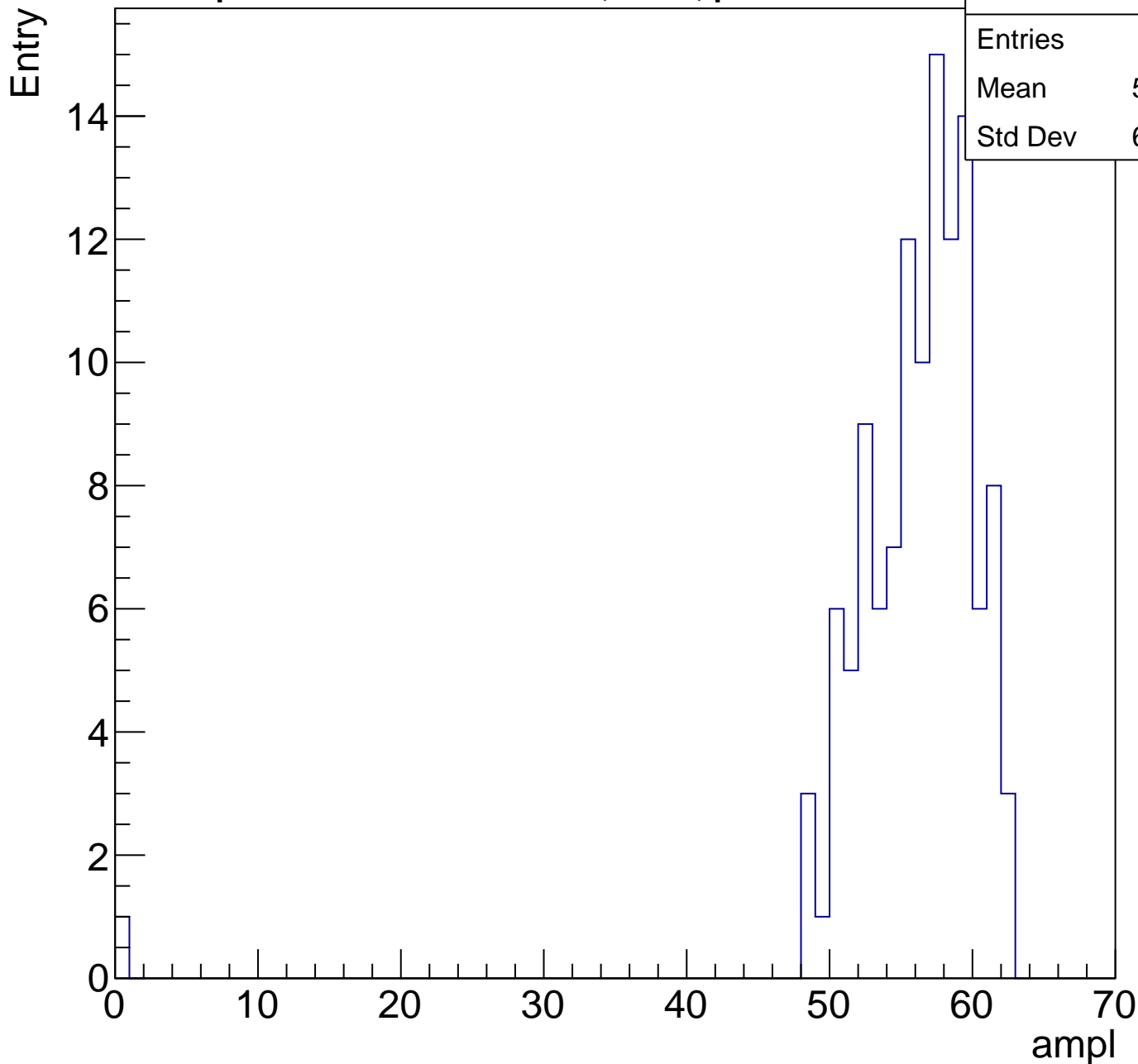
Entries	129
Mean	50.53
Std Dev	3.738



# B0L101S, U26-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	55.45
Std Dev	6.184



# B0L101S, U26-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

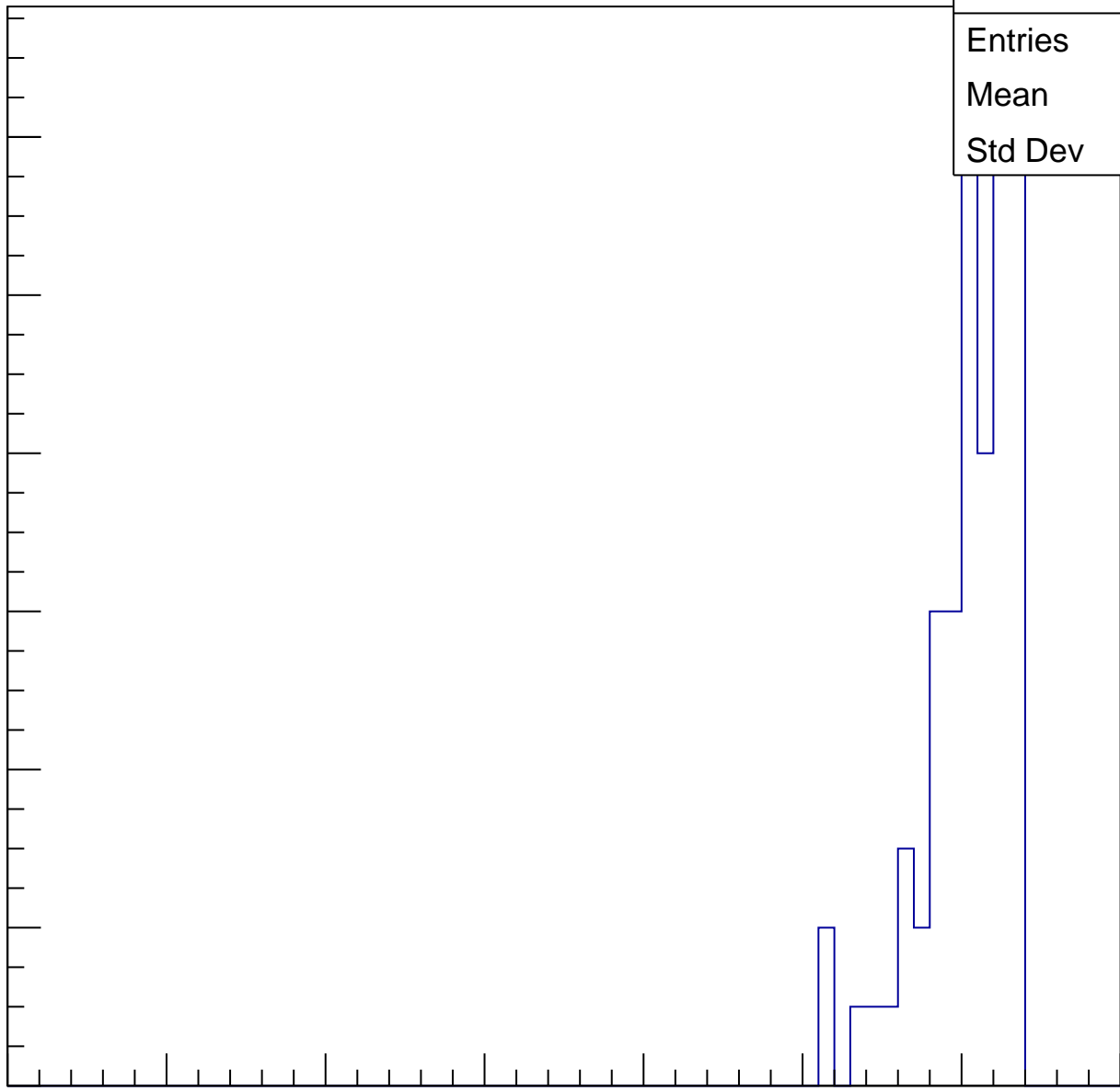
2

0

Entries	69
Mean	60.01
Std Dev	2.81

ampl

0 10 20 30 40 50 60 70

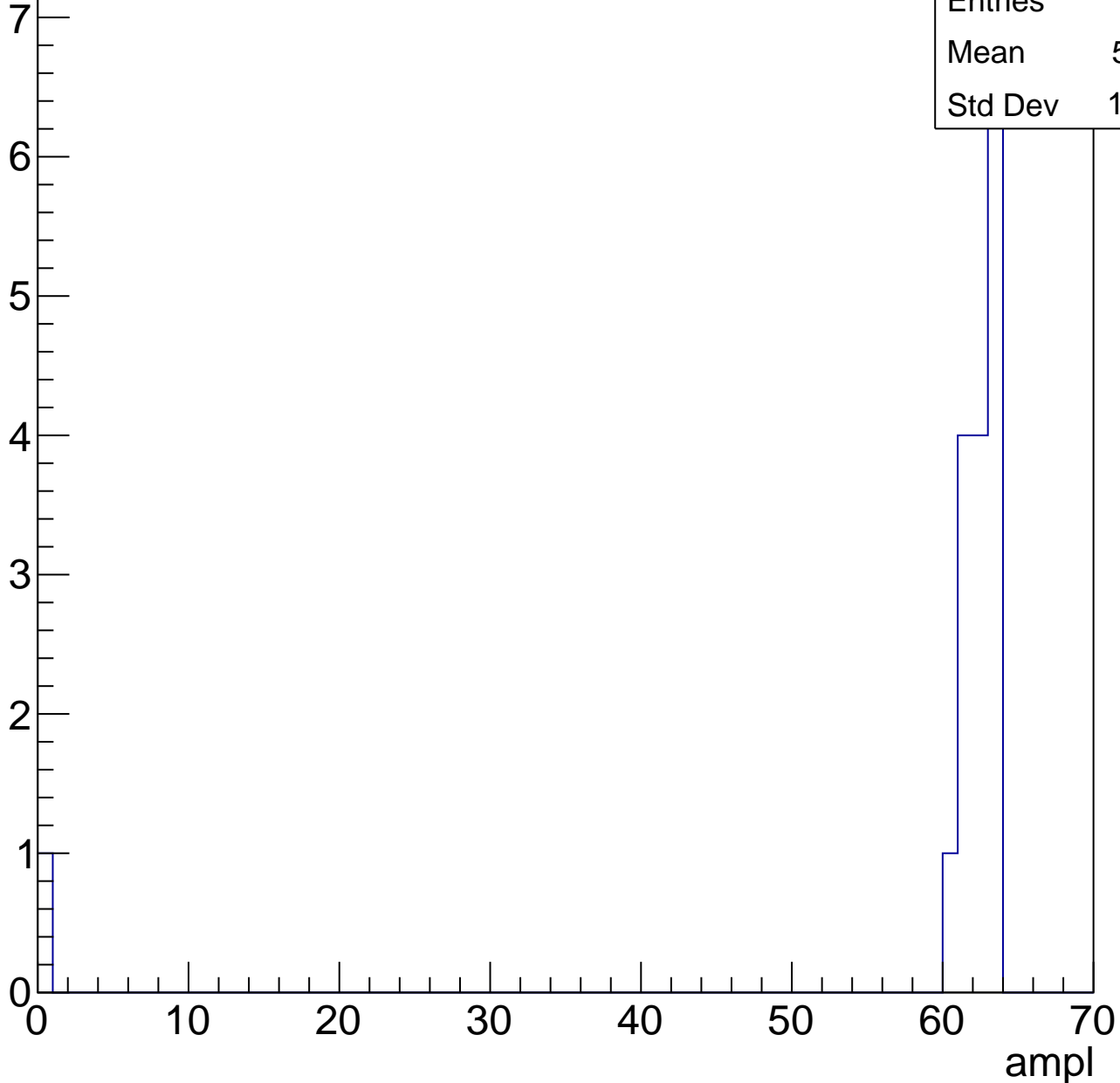


# B0L101S, U26-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.41
Std Dev	14.63





# B0L101S, U26-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L101S, U26-ch97, adc0

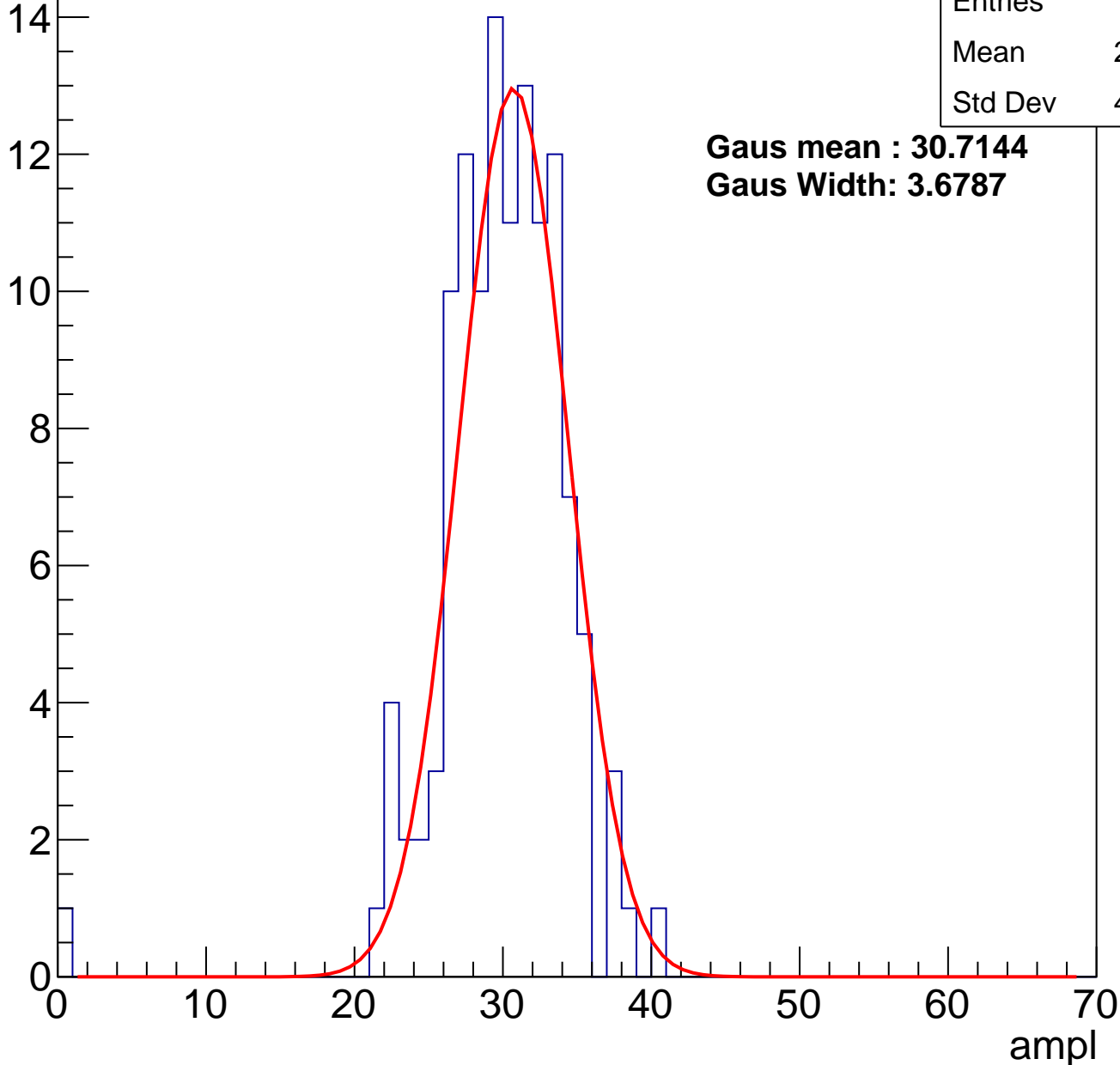
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	29.52
Std Dev	4.498

**Gaus mean : 30.7144**

**Gaus Width: 3.6787**

Entry



# B0L101S, U26-ch97, adc1

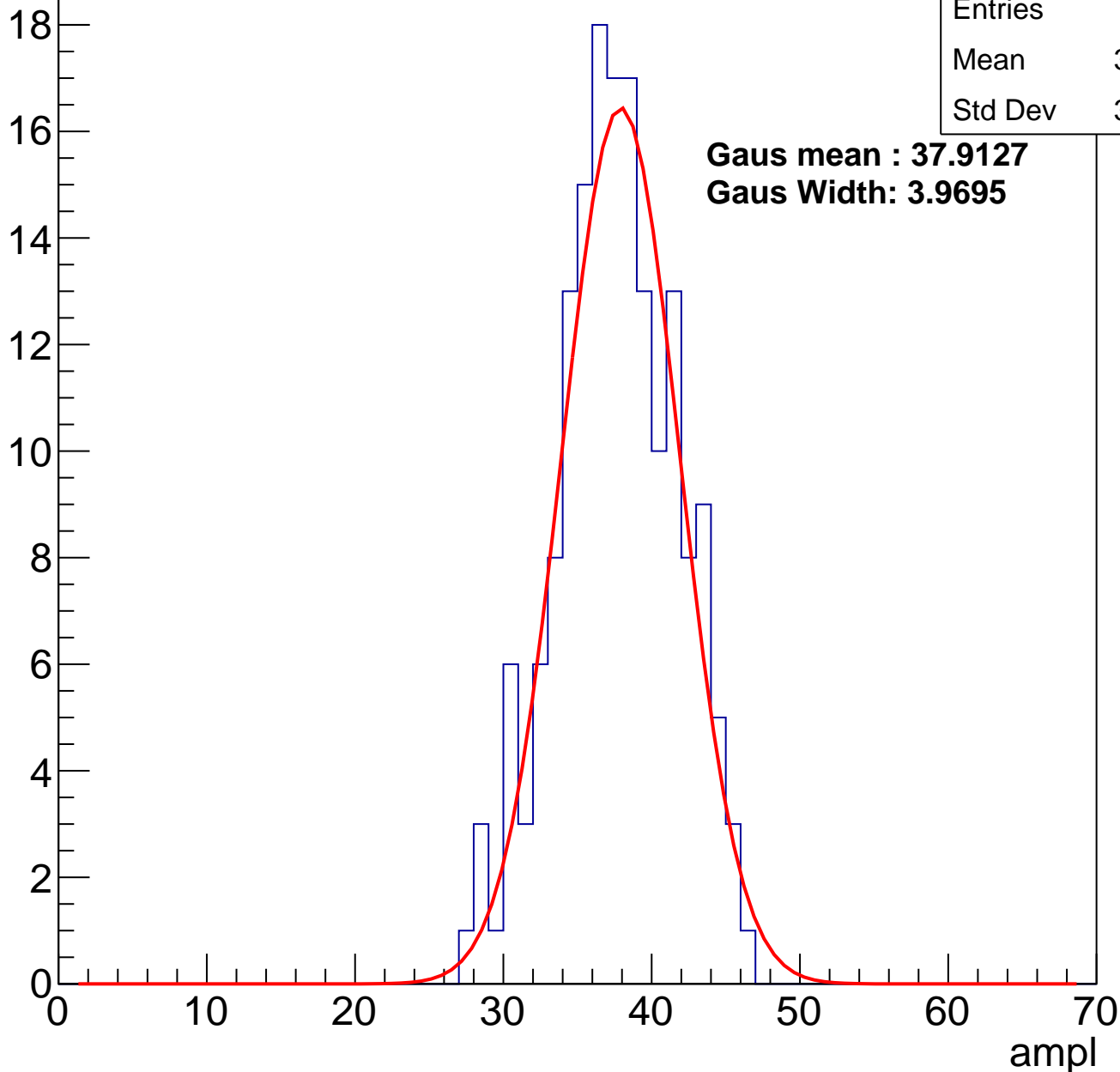
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	170
Mean	37.19
Std Dev	3.974

**Gaus mean : 37.9127**

**Gaus Width: 3.9695**

Entry



# B0L101S, U26-ch97, adc2

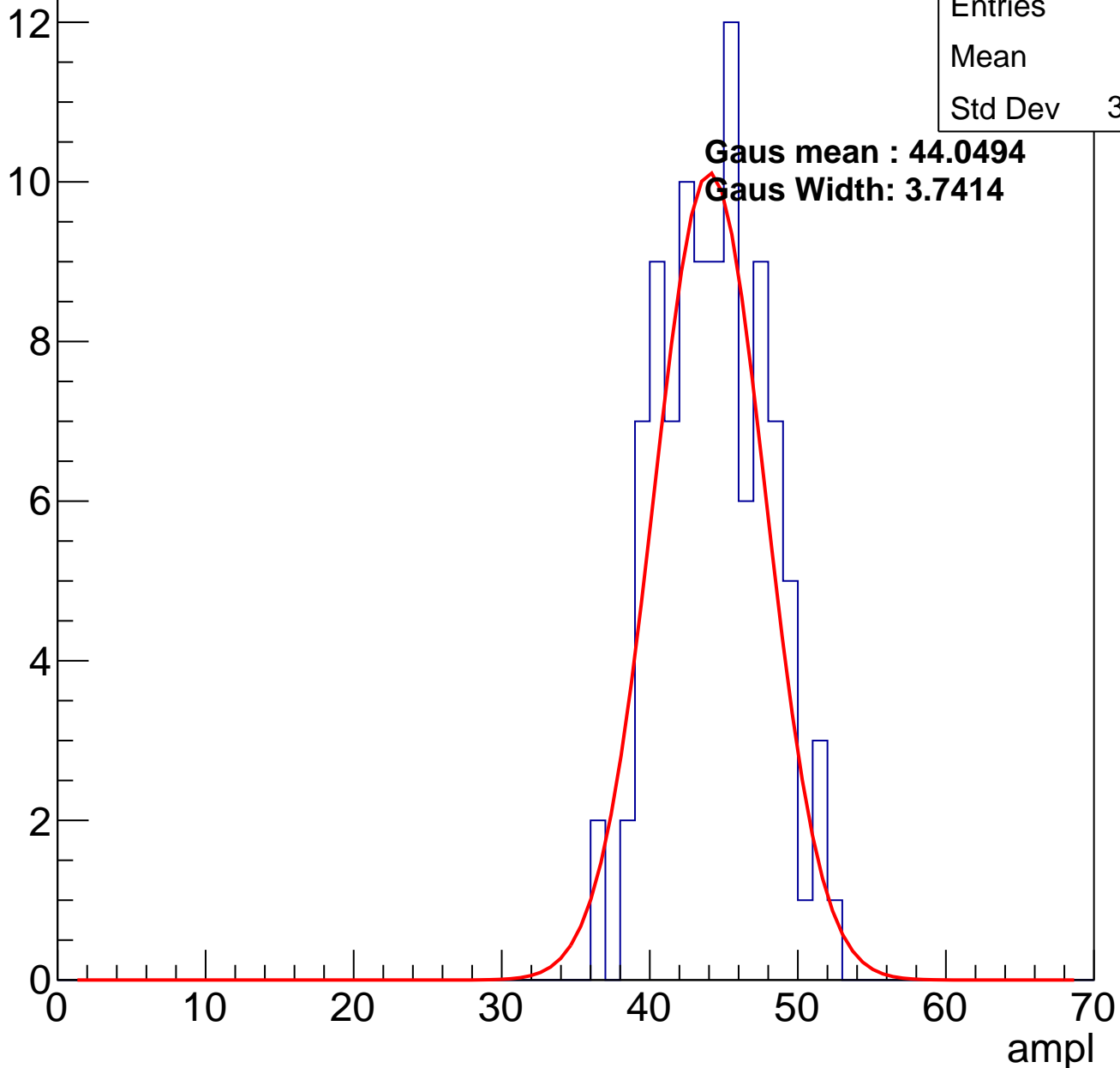
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	99
Mean	43.9
Std Dev	3.526

**Gaus mean : 44.0494**

**Gaus Width: 3.7414**

Entry



# B0L101S, U26-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

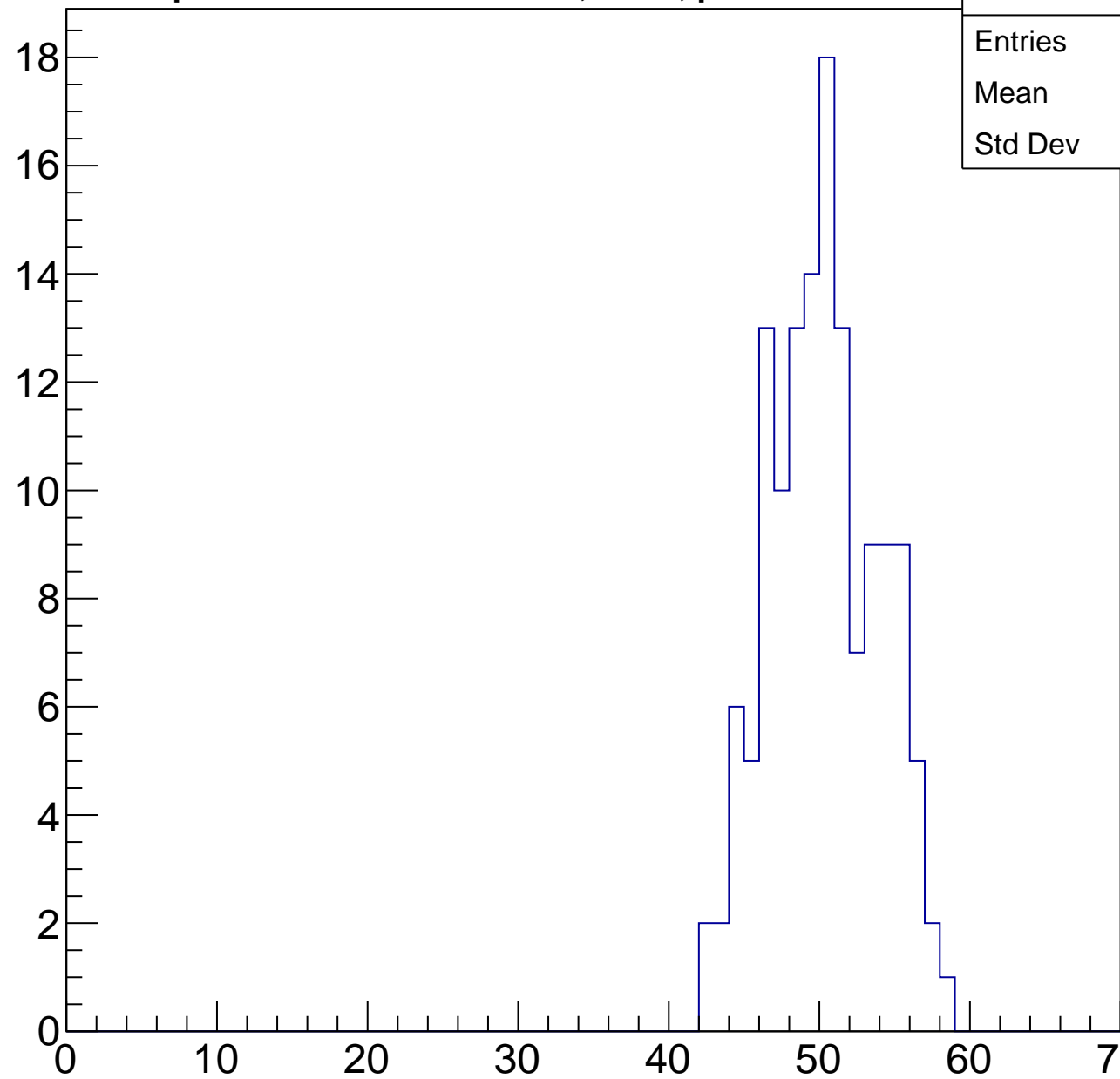
Entries	138
Mean	49.81
Std Dev	3.57

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

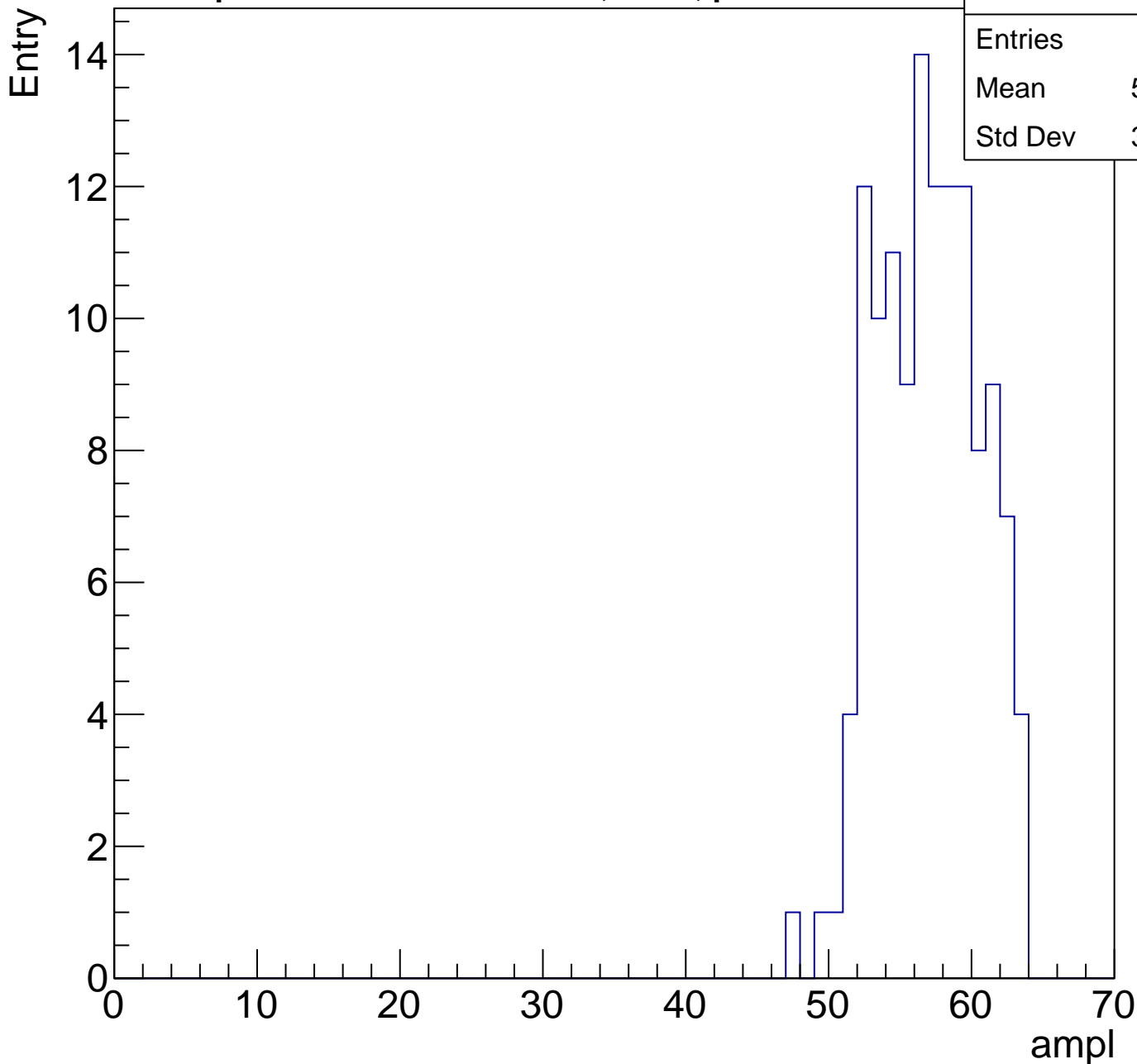
ampl



# B0L101S, U26-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	127
Mean	56.54
Std Dev	3.475



# B0L101S, U26-ch97, adc5

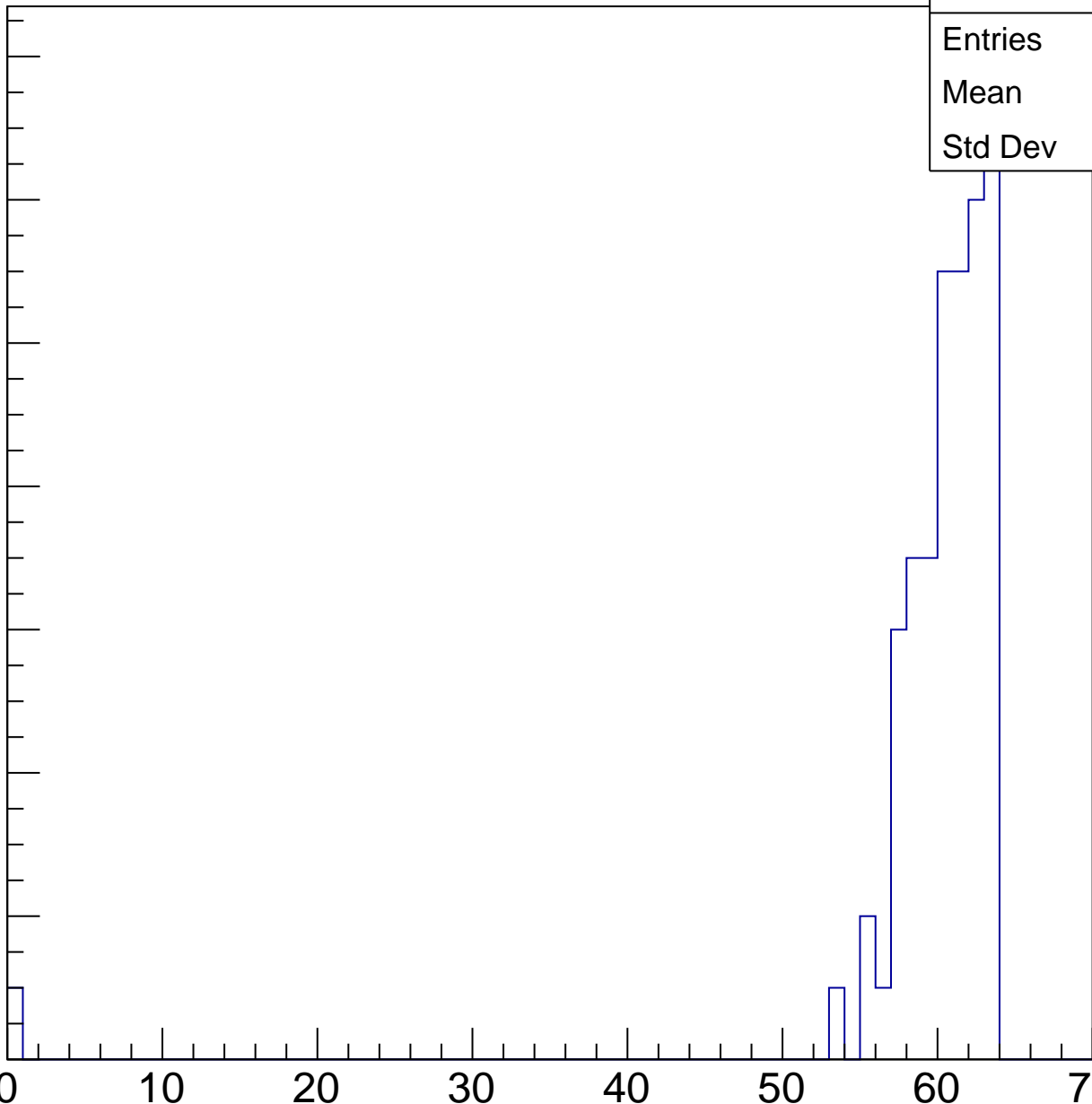
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	59.41
Std Dev	7.369

ampl



# B0L101S, U26-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

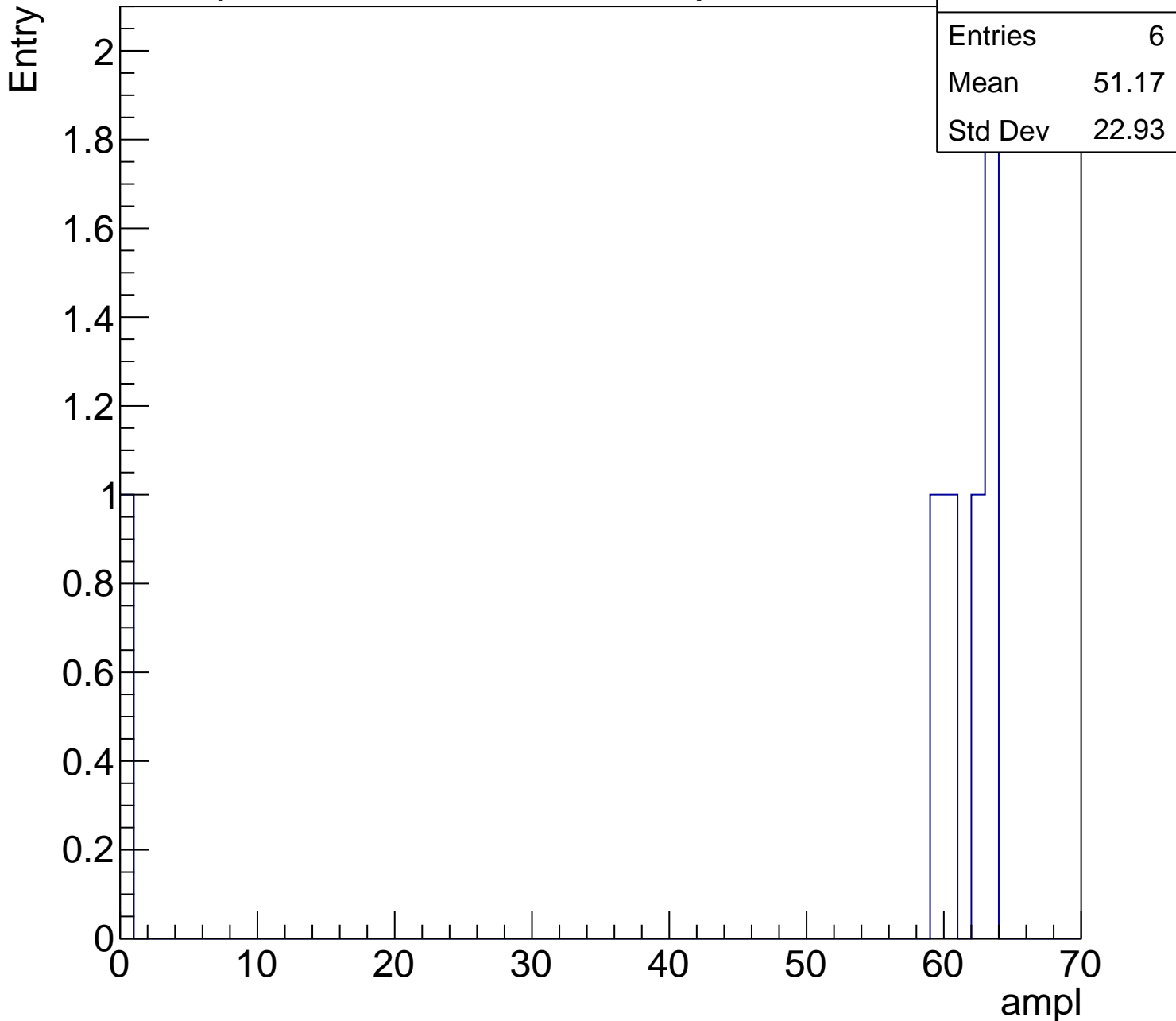
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.17
Std Dev	22.93

0 10 20 30 40 50 60 70

ampl





# B0L101S, U26-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	182
Mean	31.73
Std Dev	4.283

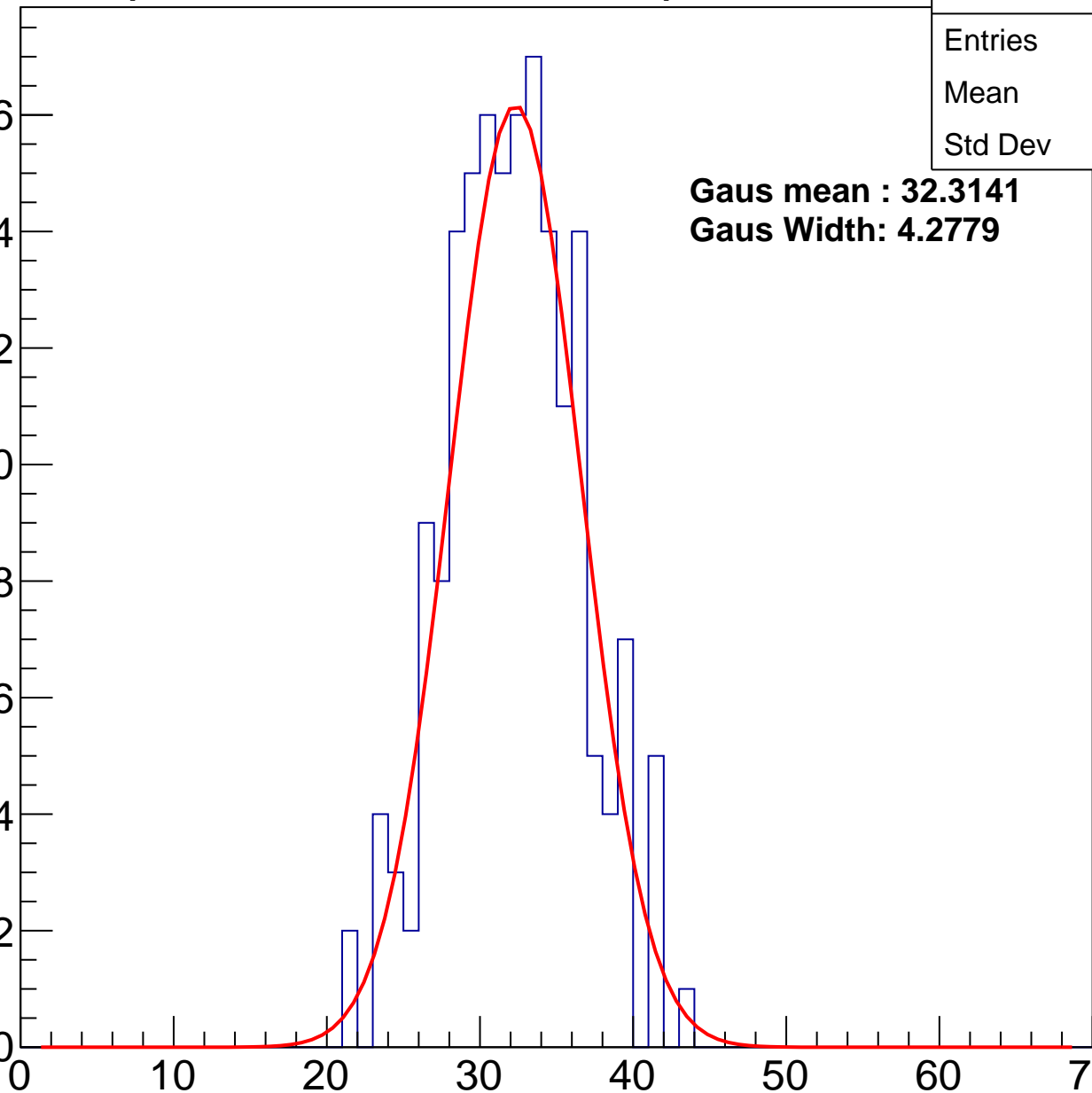
**Gaus mean : 32.3141**

**Gaus Width: 4.2779**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	95
Mean	37.93
Std Dev	3.831

**Gaus mean : 39.0037**

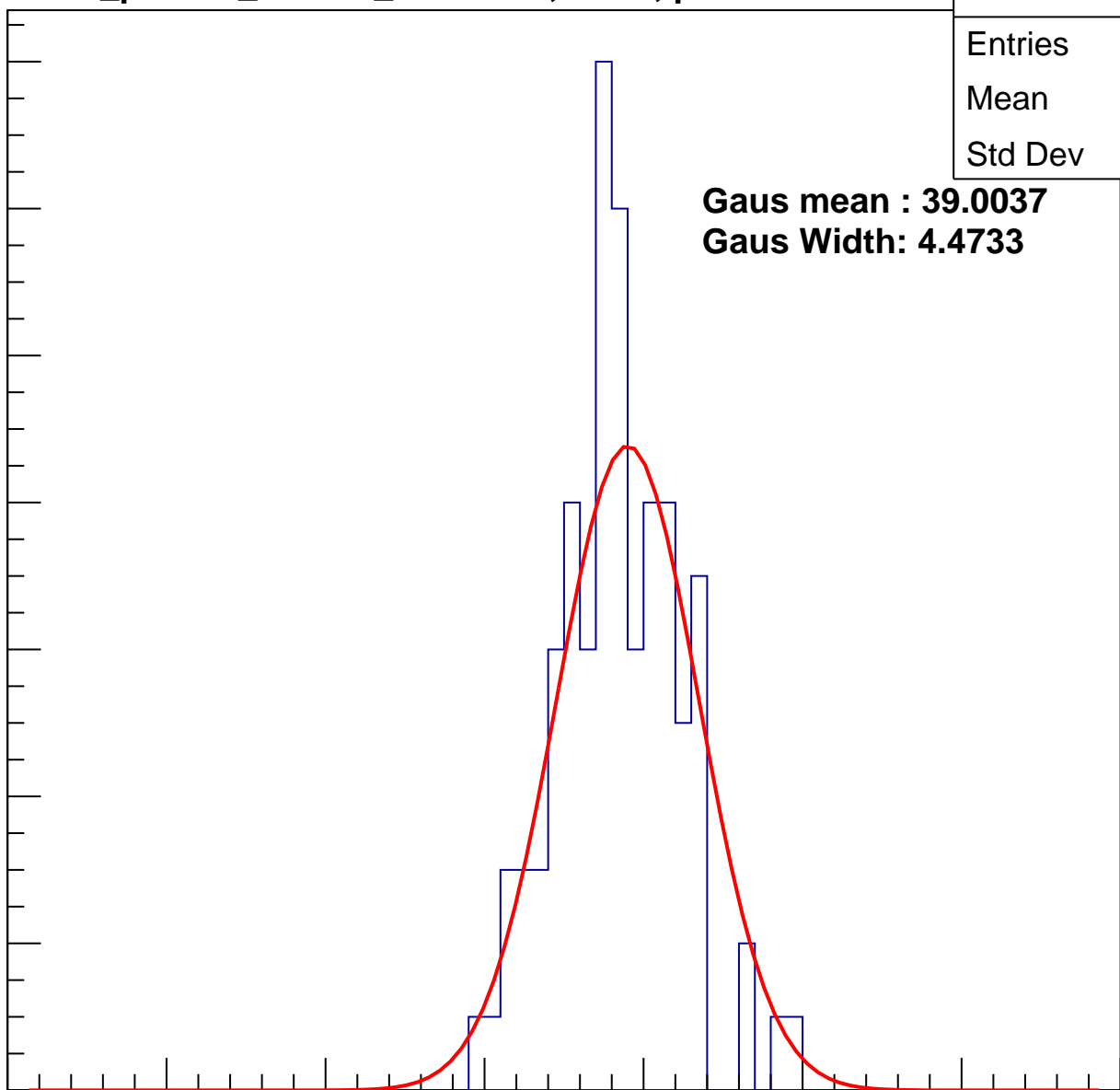
**Gaus Width: 4.4733**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch98, adc2

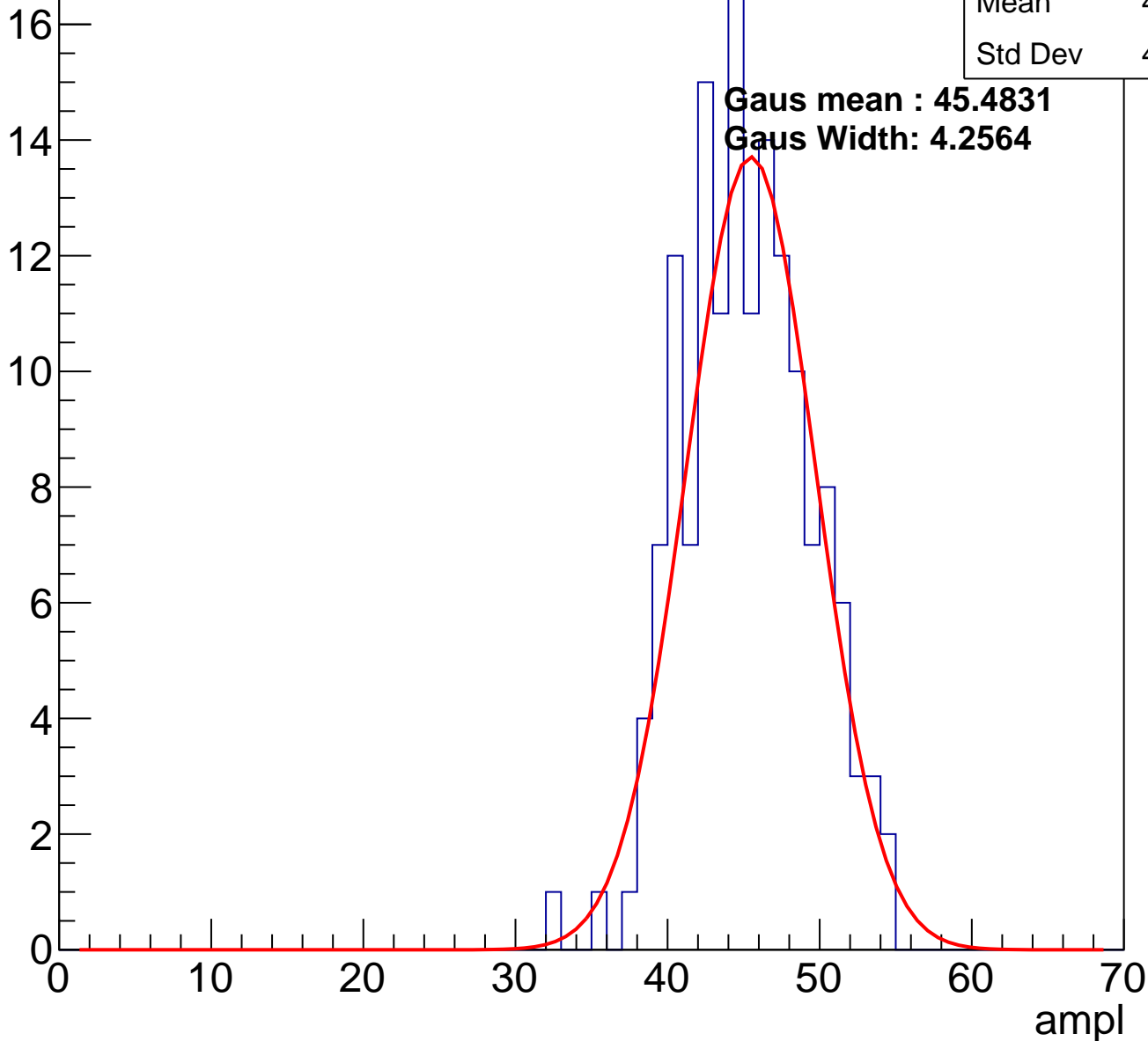
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	152
Mean	44.75
Std Dev	4.104

**Gaus mean : 45.4831**

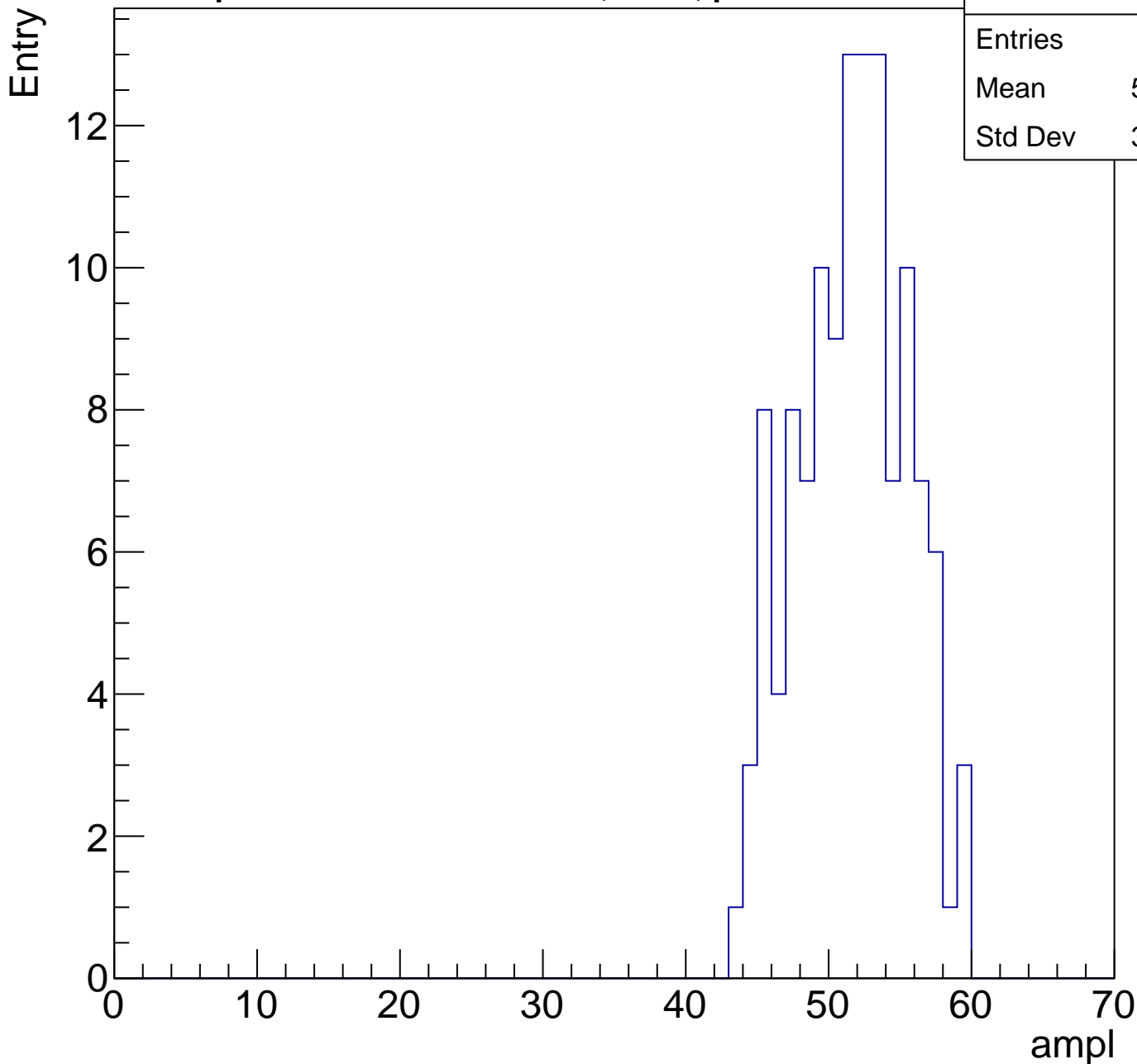
**Gaus Width: 4.2564**



# B0L101S, U26-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	51.19
Std Dev	3.777



# B0L101S, U26-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	92
Mean	56.39
Std Dev	3.65

Entry

12

10

8

6

4

2

0

0

10

20

30

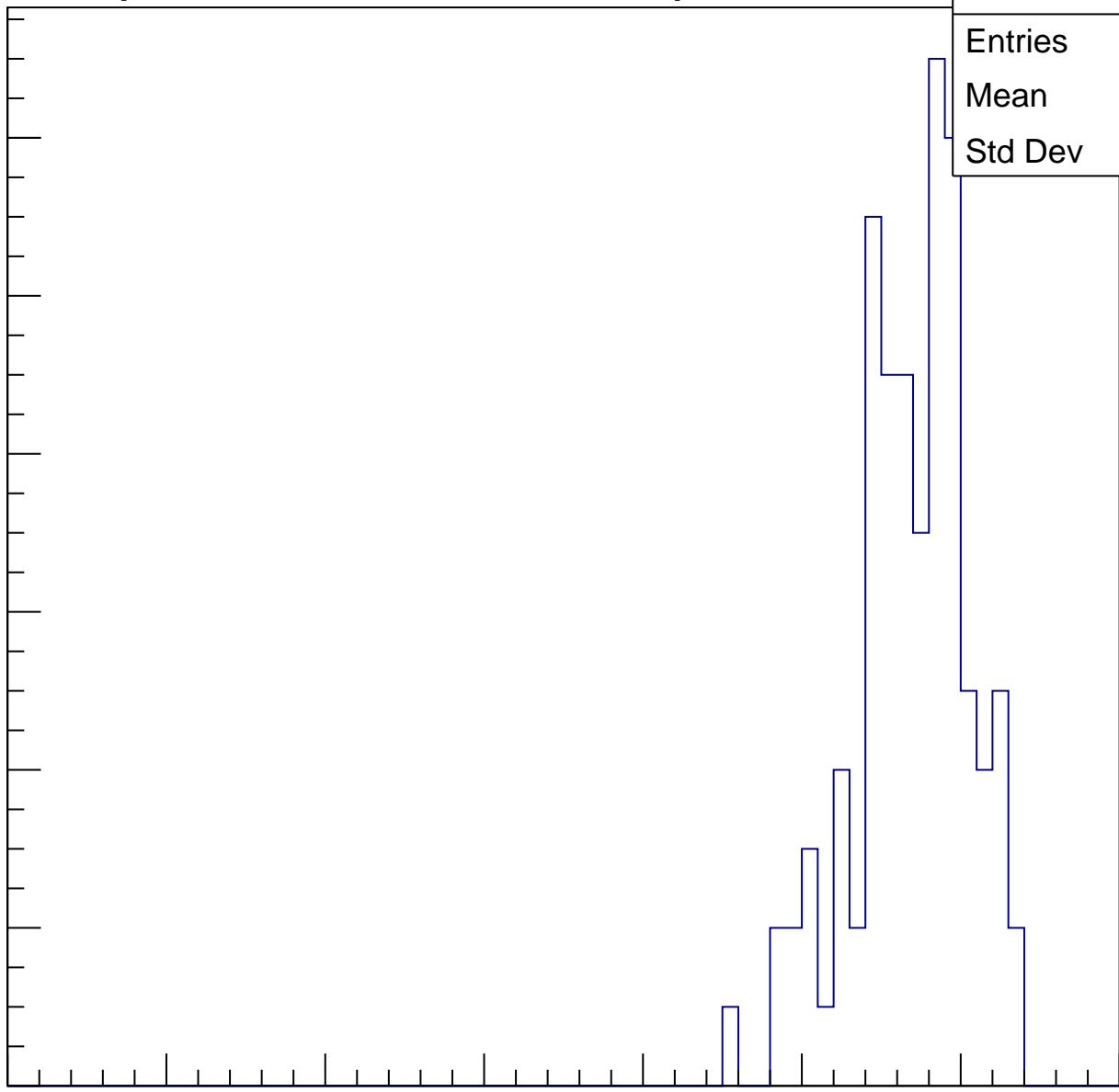
40

50

60

70

ampl



# B0L101S, U26-ch98, adc5

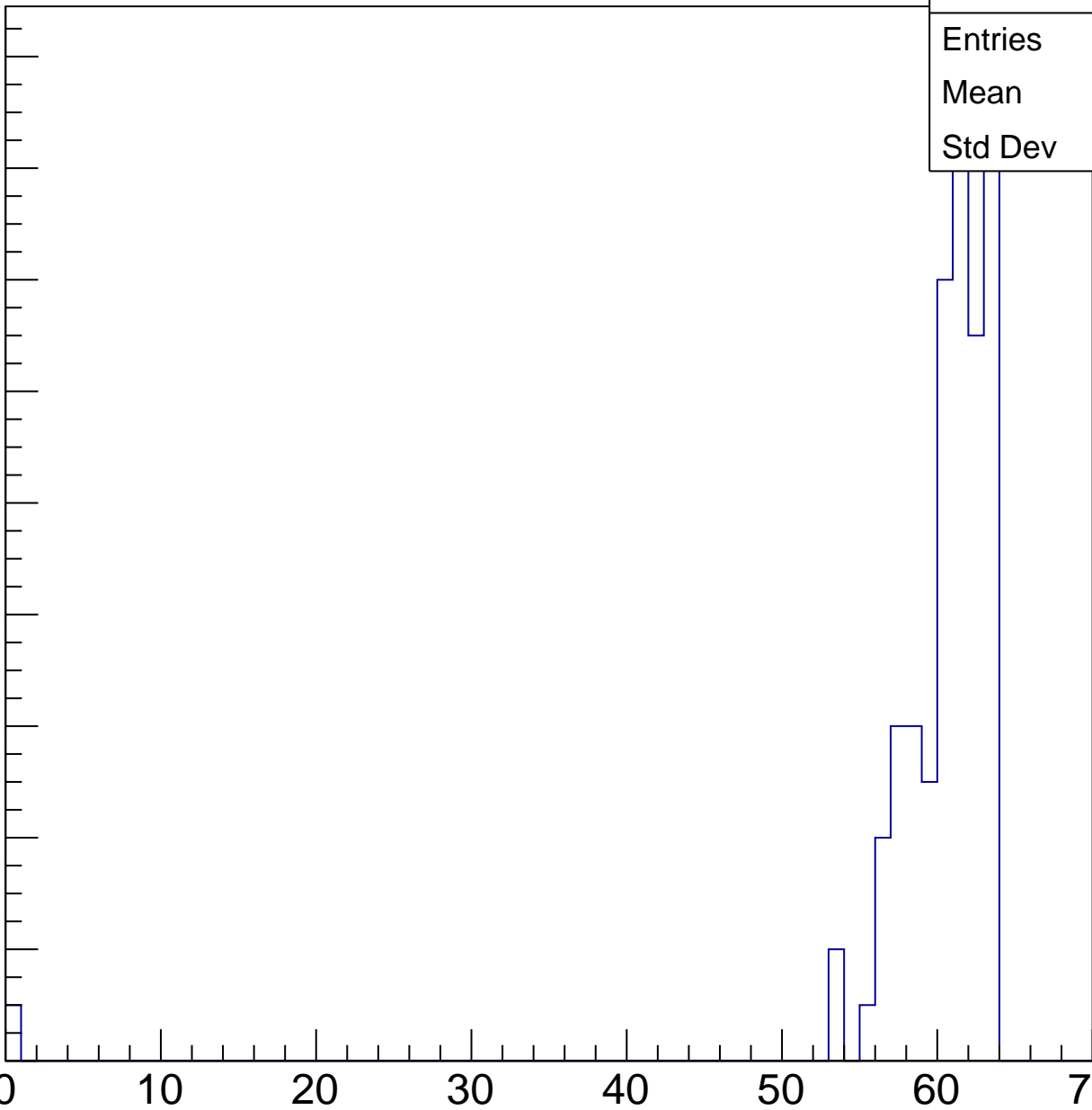
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	86
Mean	59.6
Std Dev	6.893

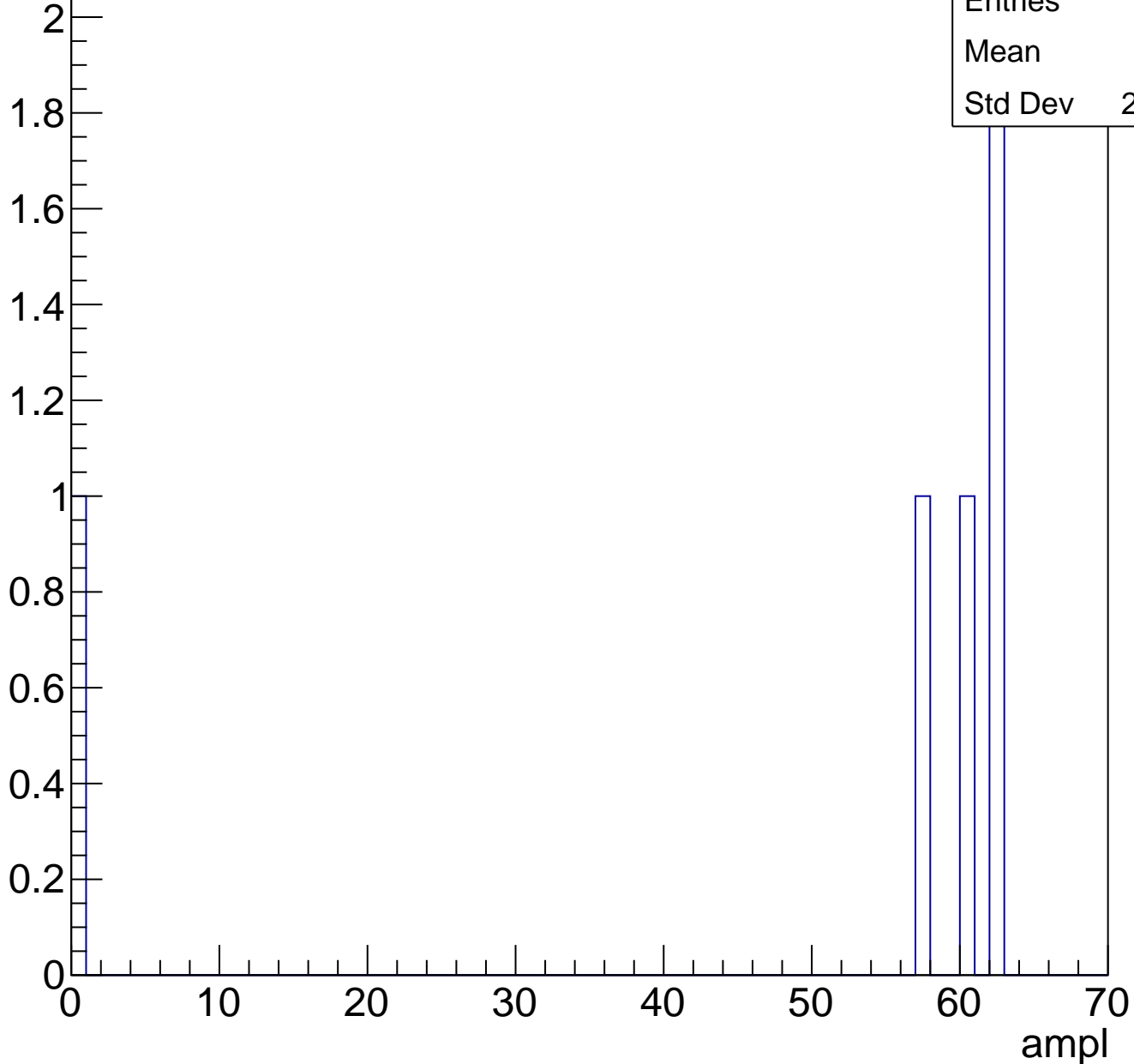
ampl



# B0L101S, U26-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	5
Mean	48.2
Std Dev	24.17



# B0L101S, U26-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch99, adc0

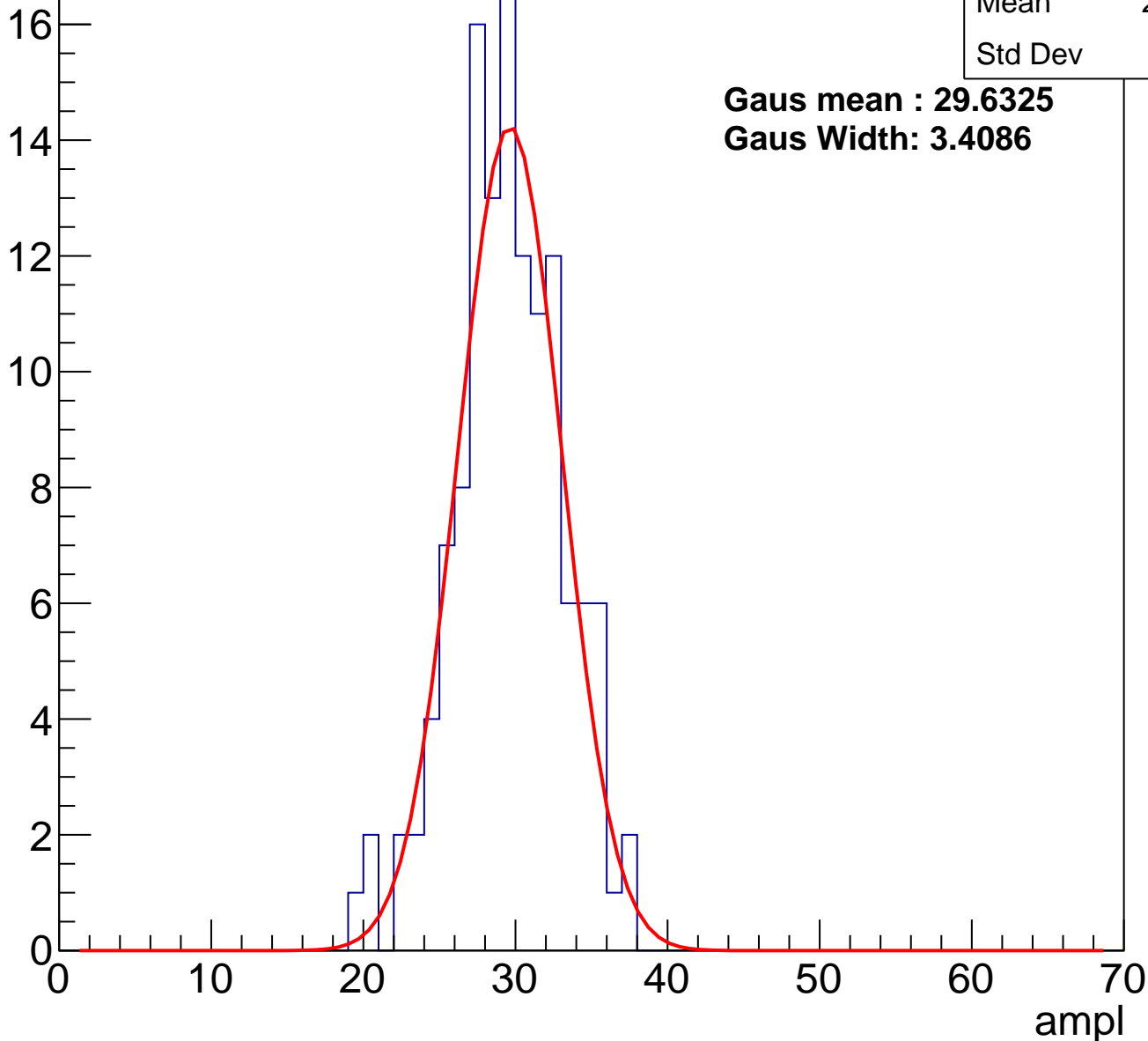
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	128
Mean	29.09
Std Dev	3.53

**Gaus mean : 29.6325**

**Gaus Width: 3.4086**



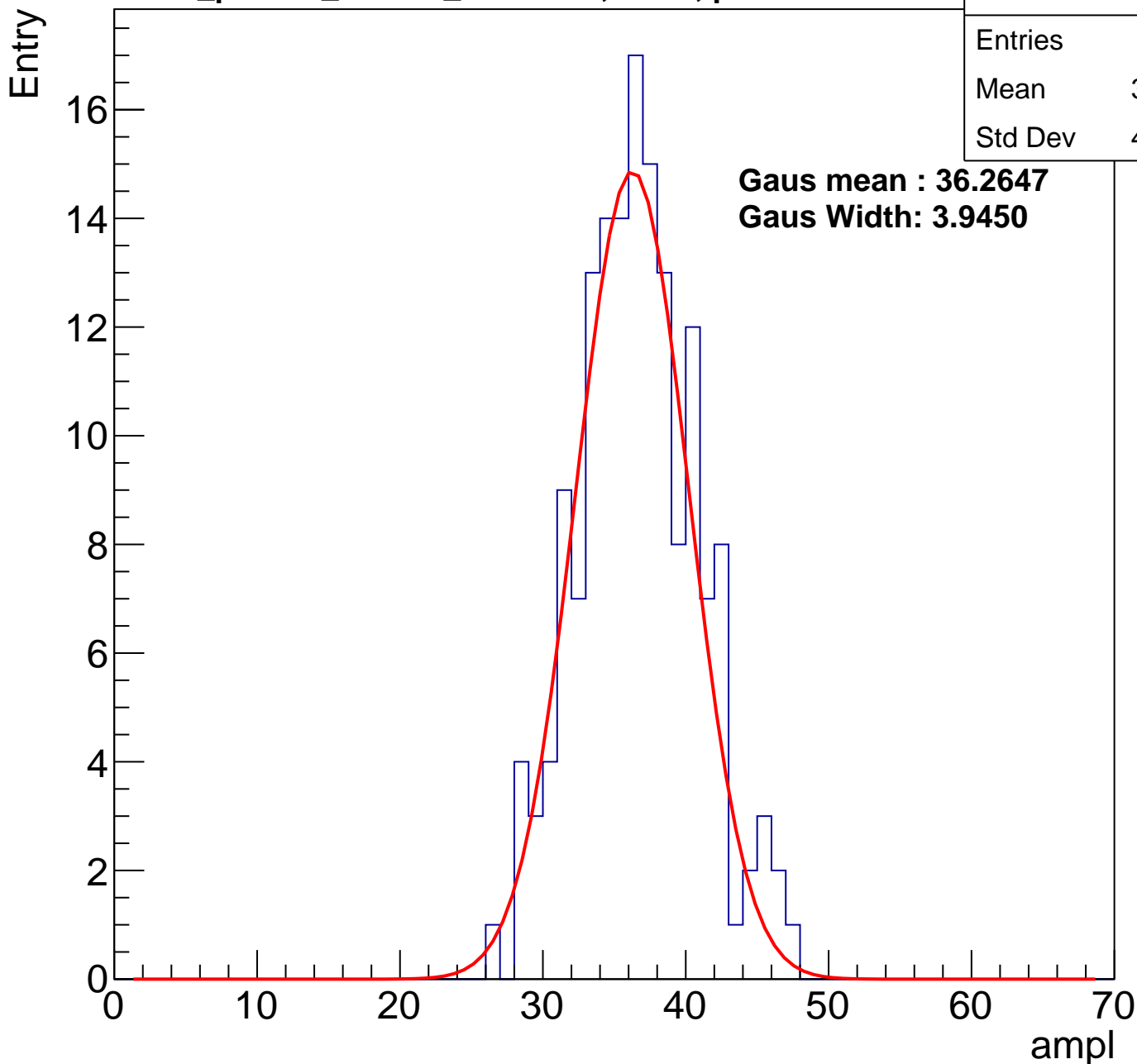
# B0L101S, U26-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	36.23
Std Dev	4.132

**Gaus mean : 36.2647**

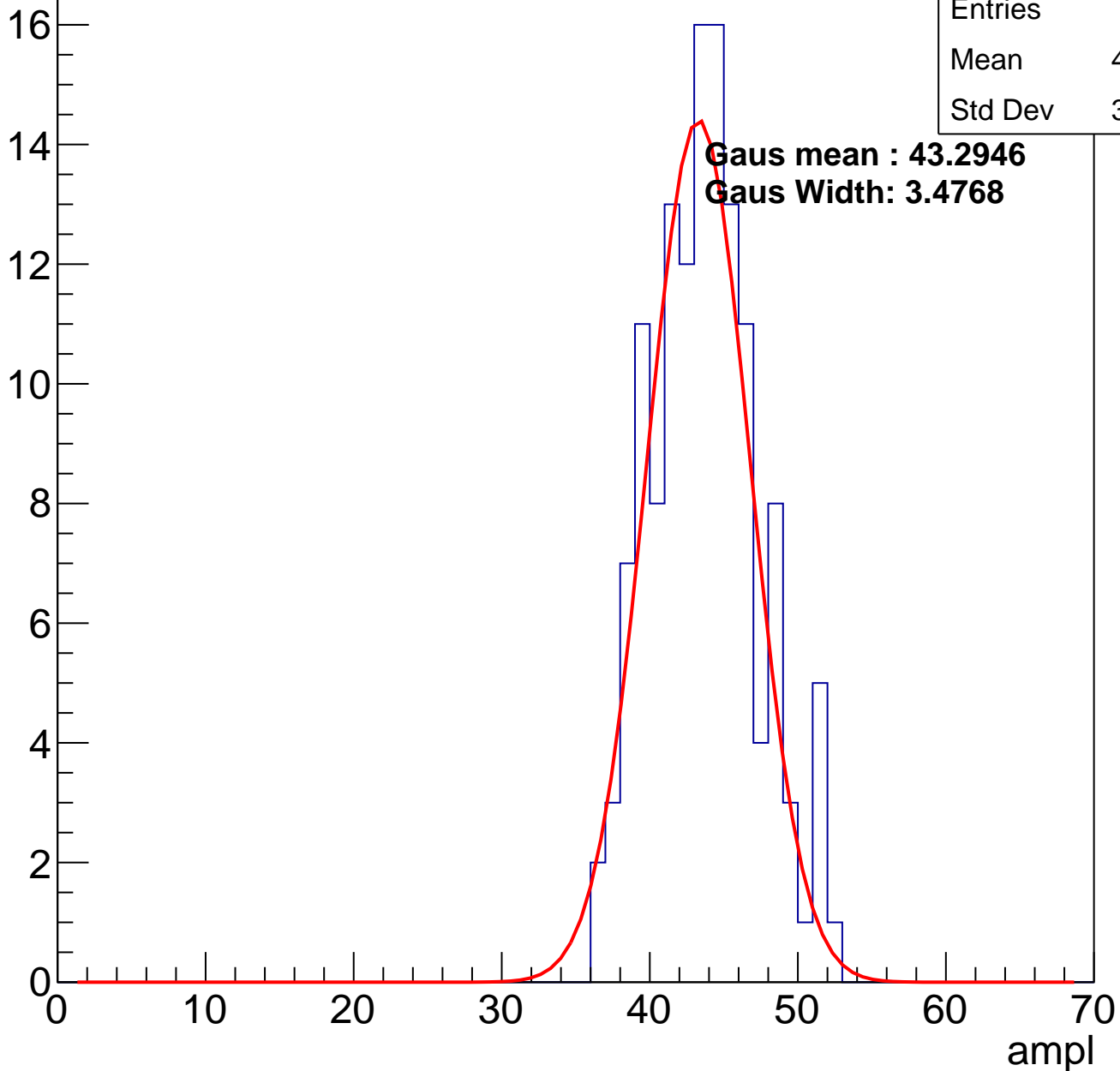
**Gaus Width: 3.9450**



# B0L101S, U26-ch99, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

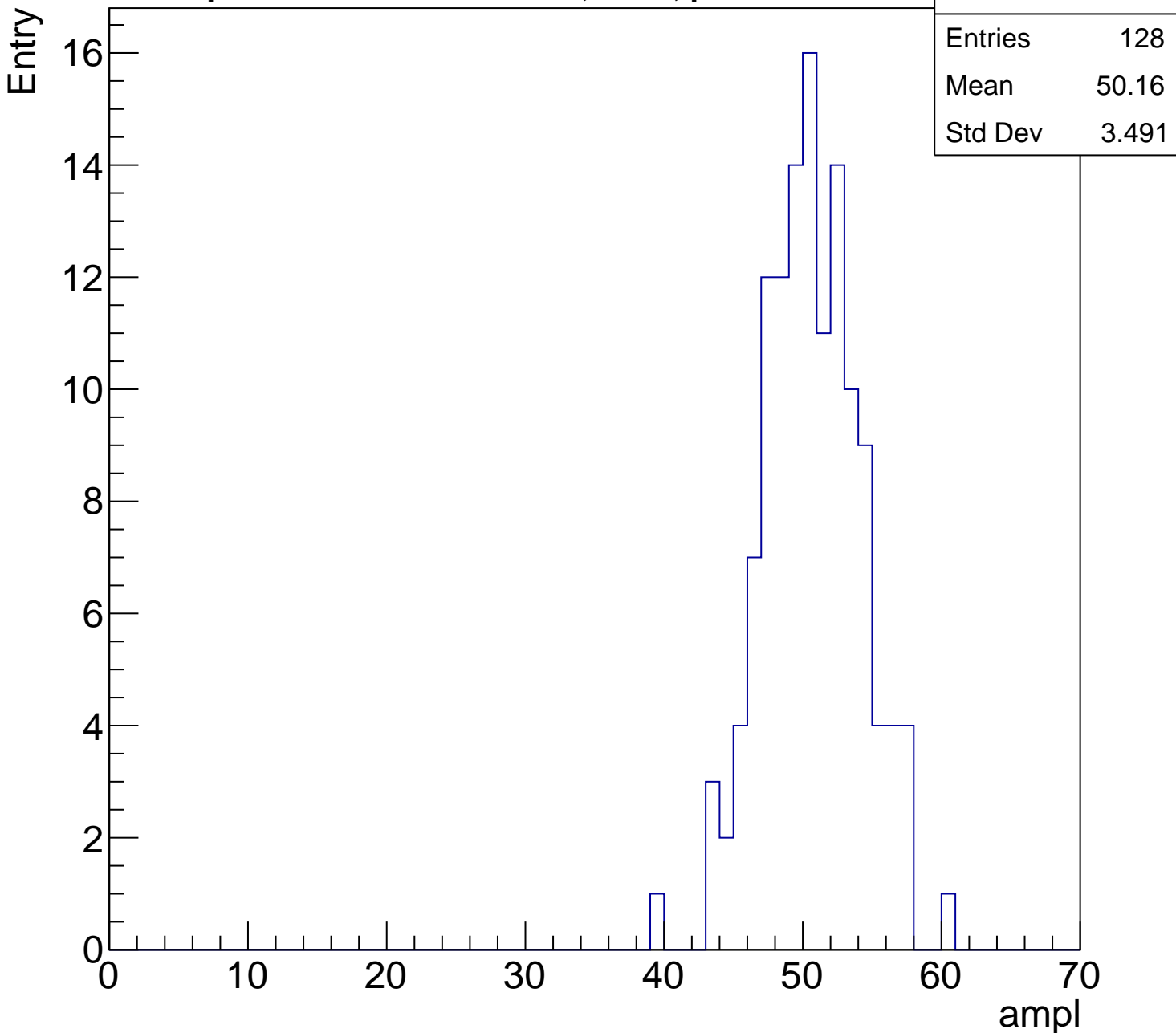
Entries	128
Mean	50.16
Std Dev	3.491

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

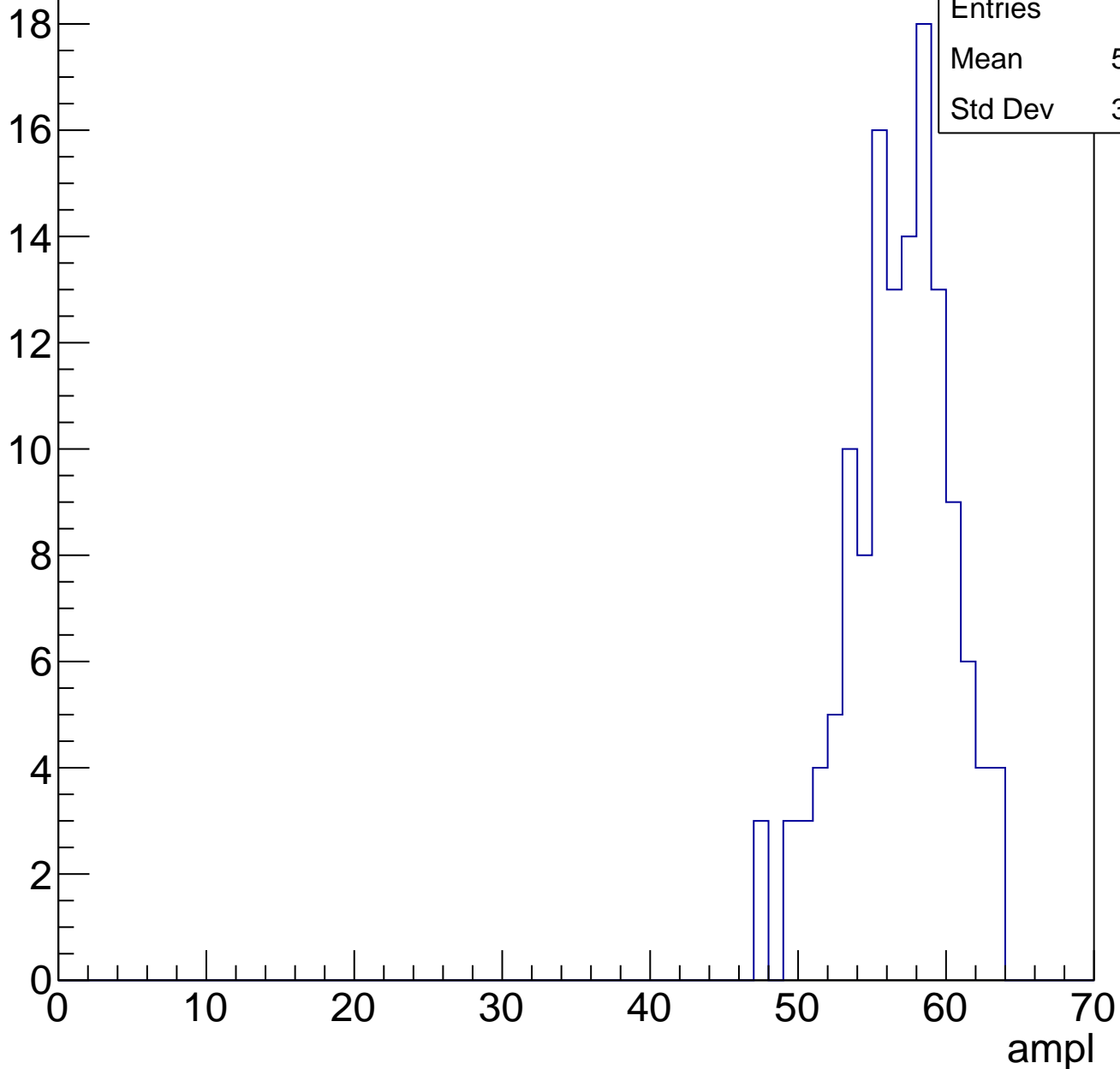


# B0L101S, U26-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	56.29
Std Dev	3.513

Entry



# B0L101S, U26-ch99, adc5

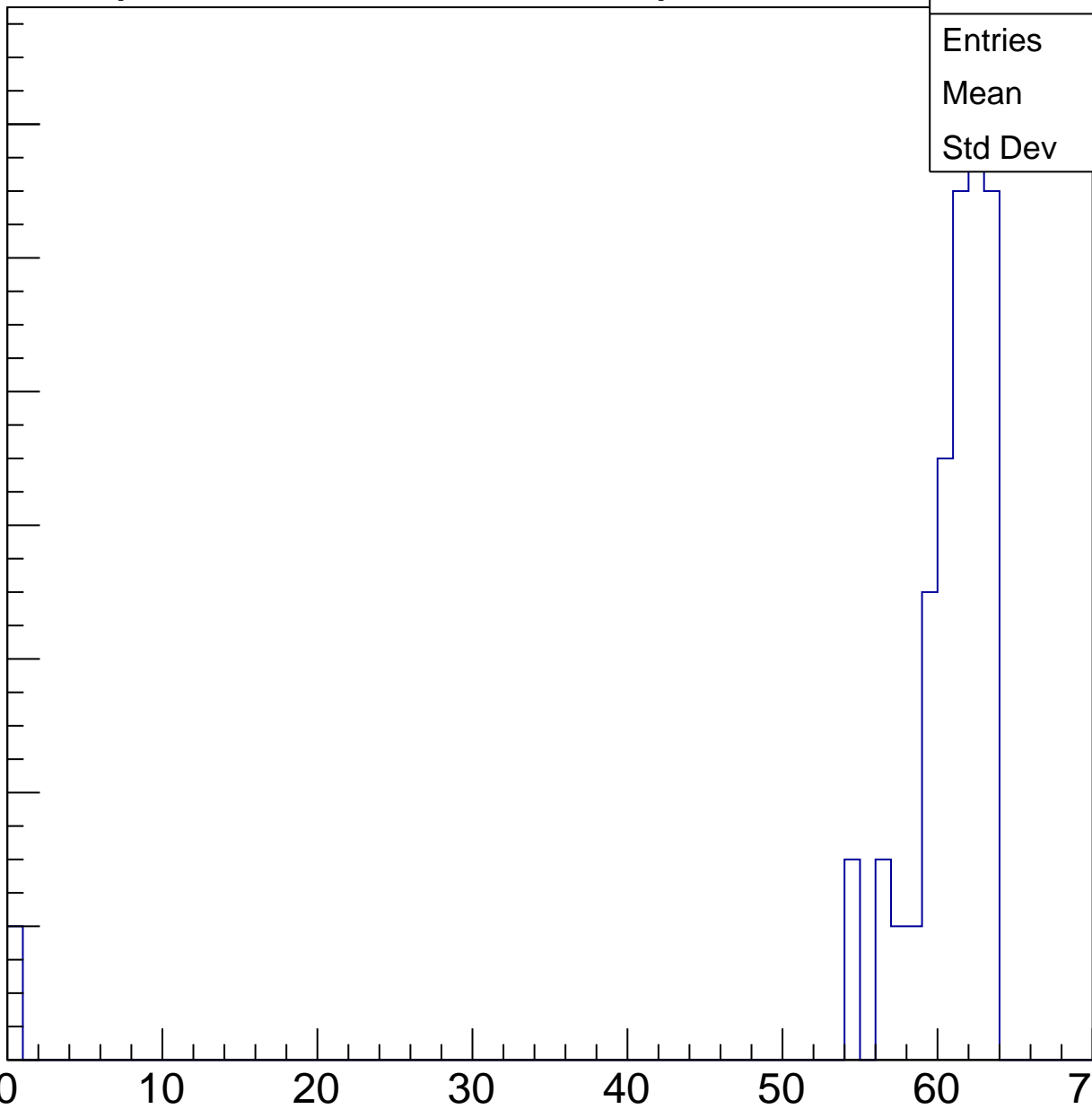
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	69
Mean	58.77
Std Dev	10.41

ampl



# B0L101S, U26-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch100, adc0

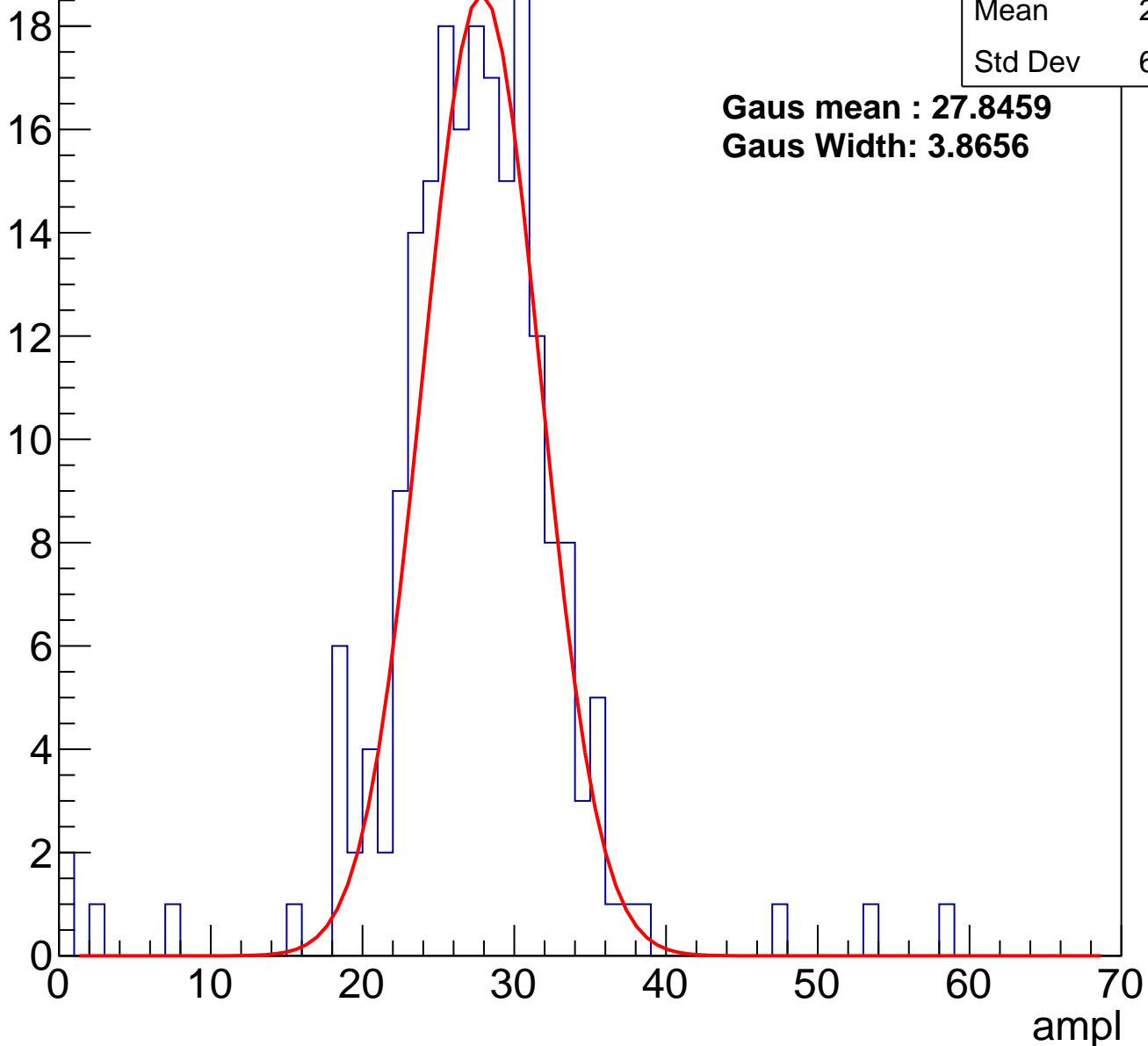
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	202
Mean	26.95
Std Dev	6.259

**Gaus mean : 27.8459**

**Gaus Width: 3.8656**

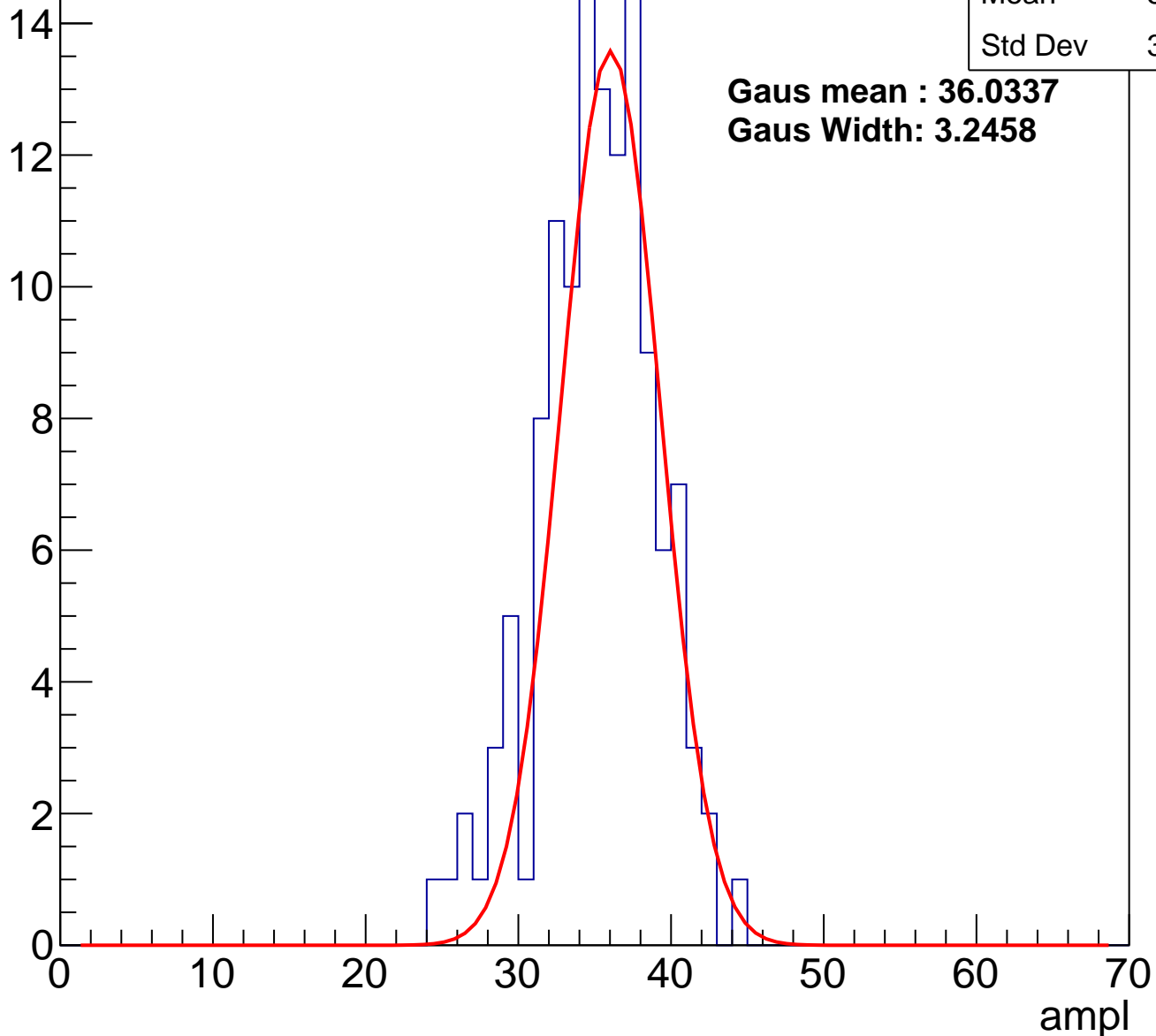
Entry



# B0L101S, U26-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch100, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

114

Mean

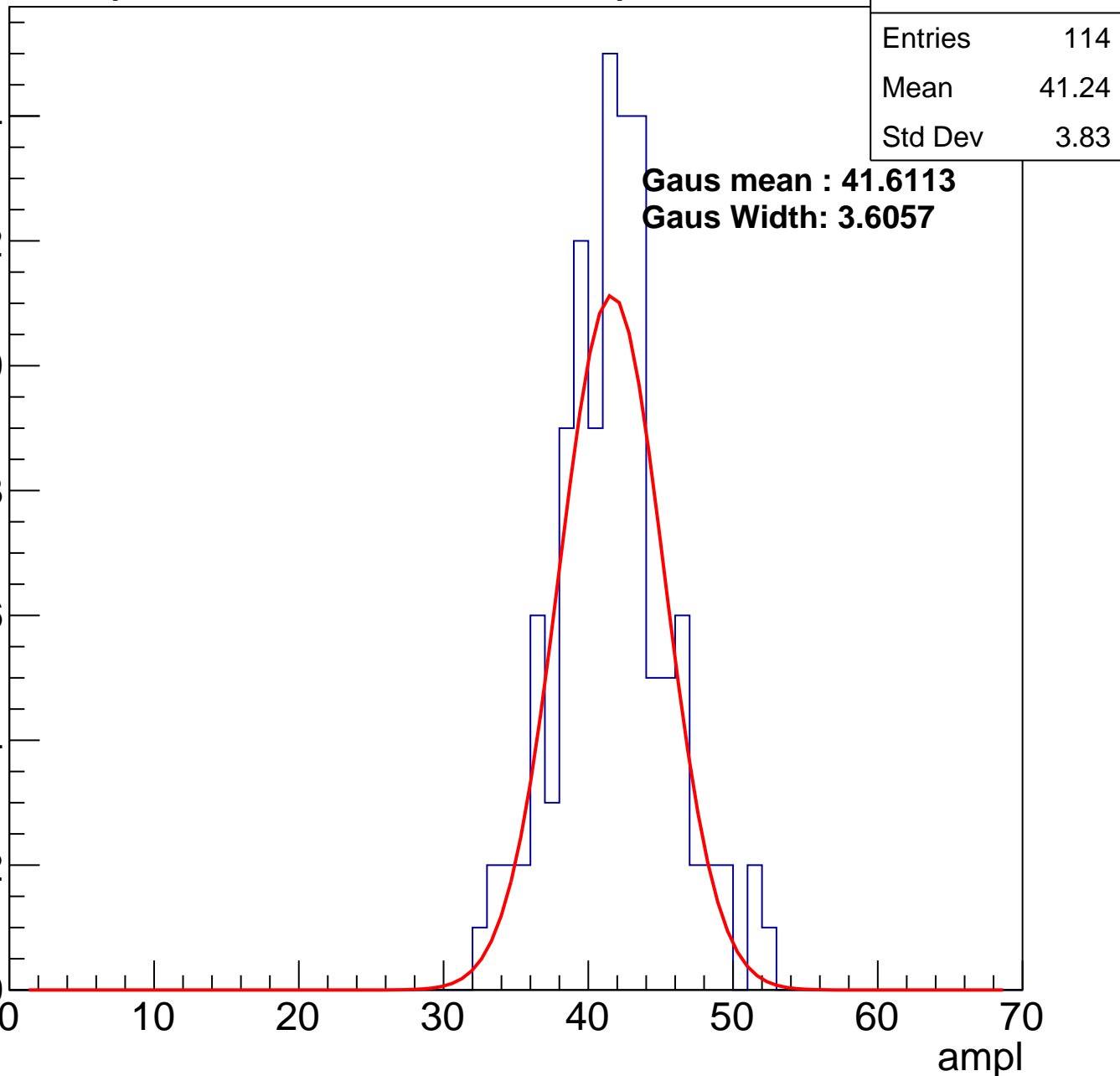
41.24

Std Dev

3.83

**Gaus mean : 41.6113**

**Gaus Width: 3.6057**

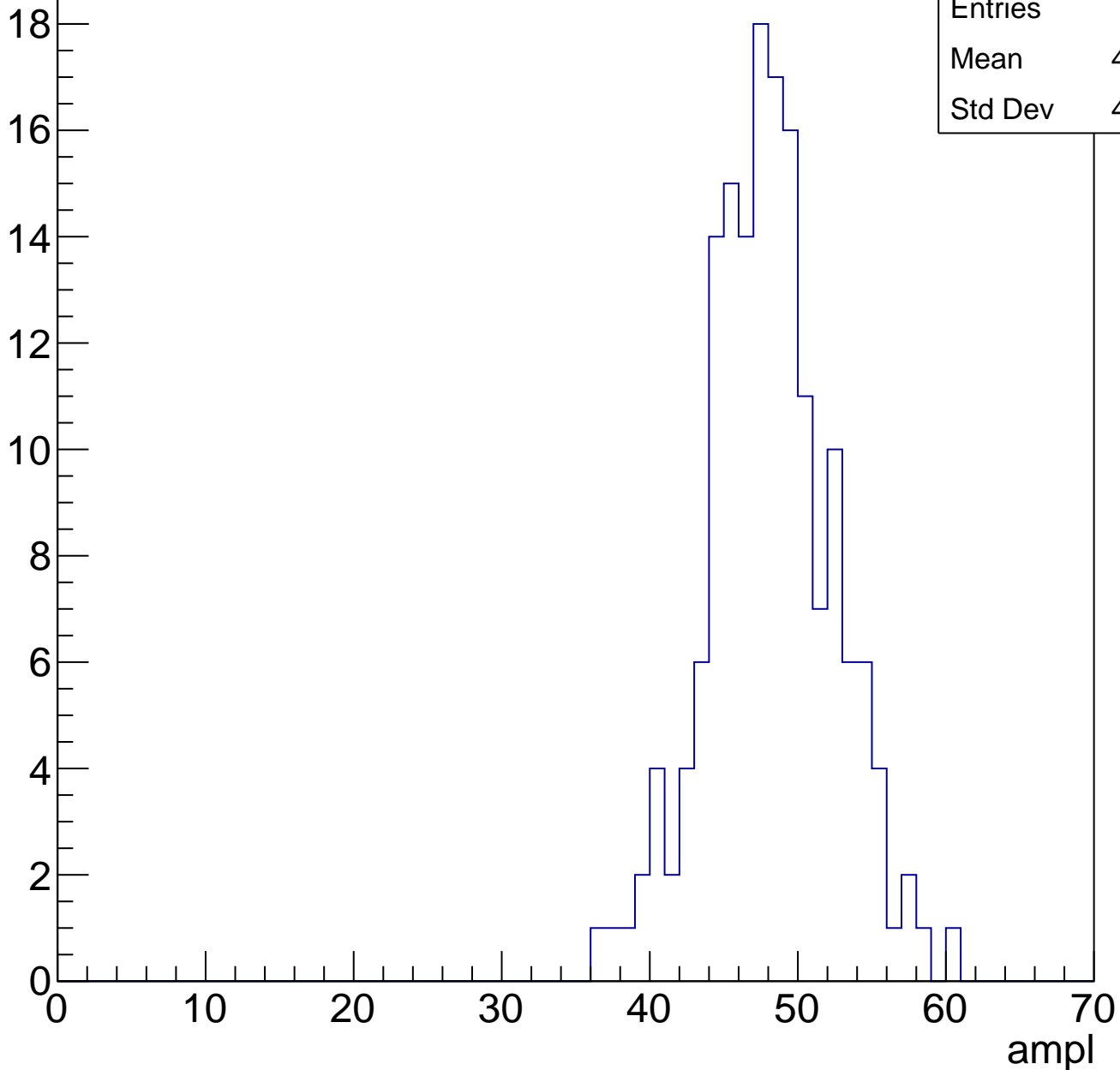


# B0L101S, U26-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	164
Mean	47.65
Std Dev	4.207

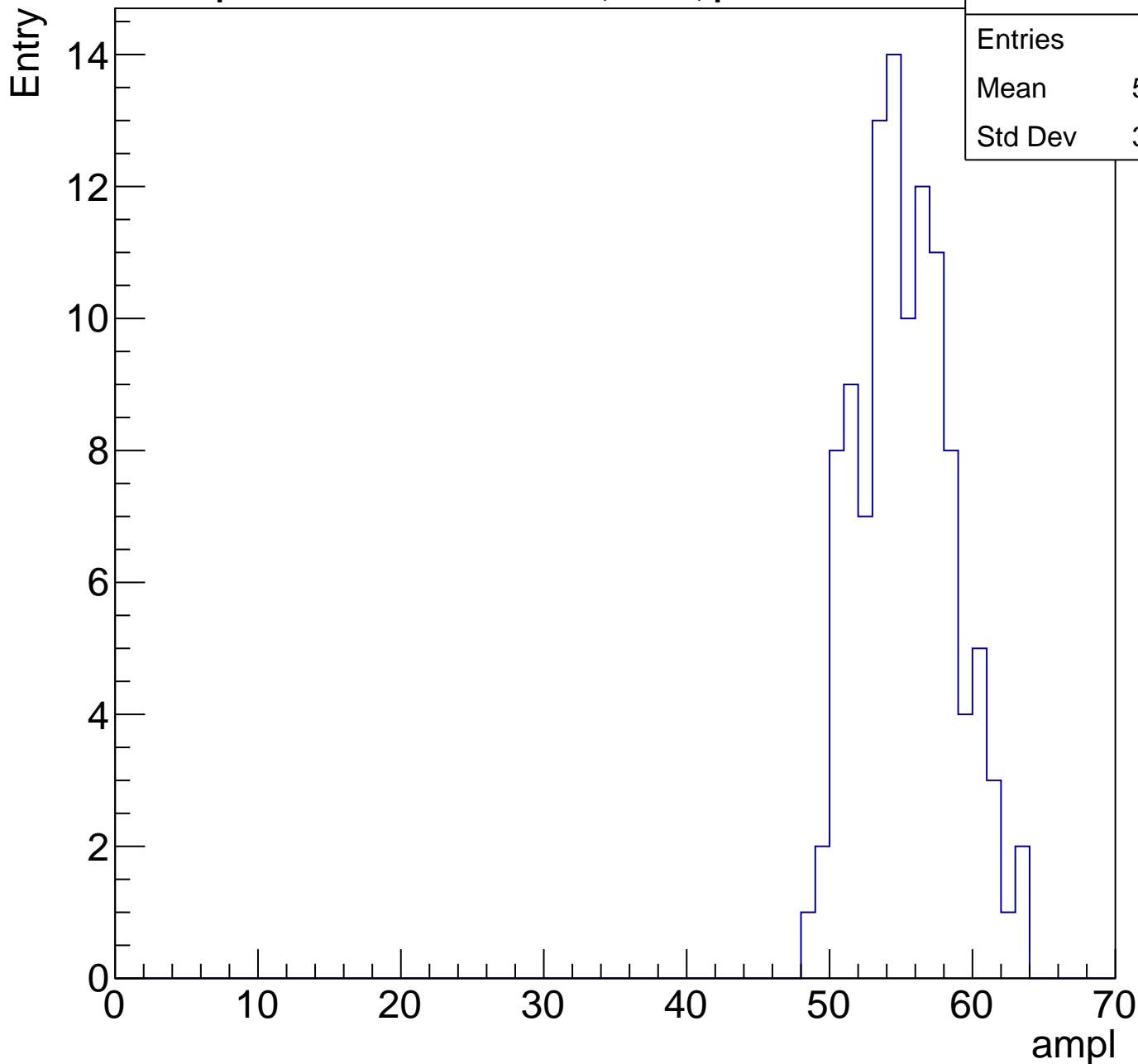
Entry



# B0L101S, U26-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	54.85
Std Dev	3.294



# B0L101S, U26-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

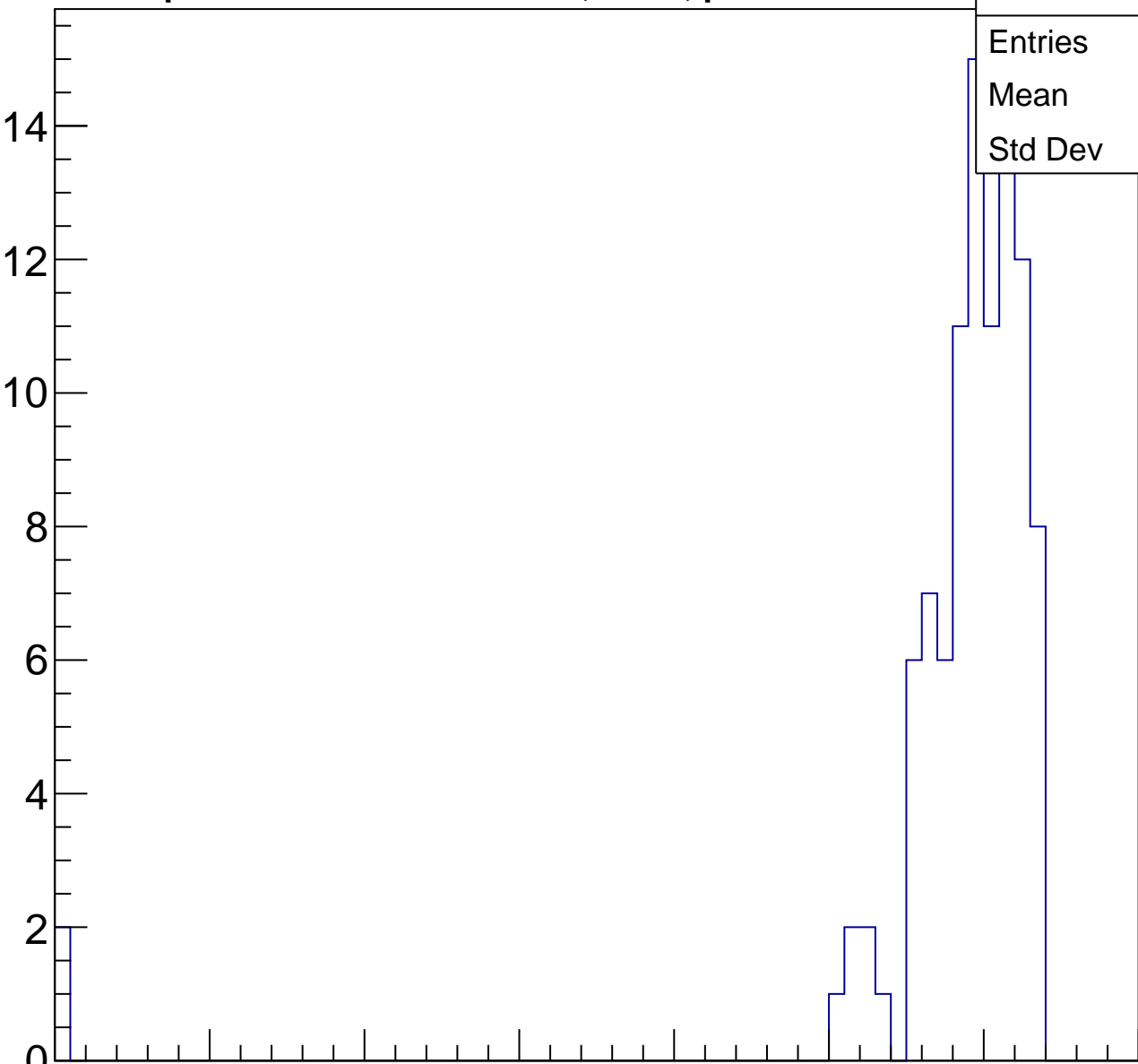
Entries	99
Mean	57.77
Std Dev	8.794

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

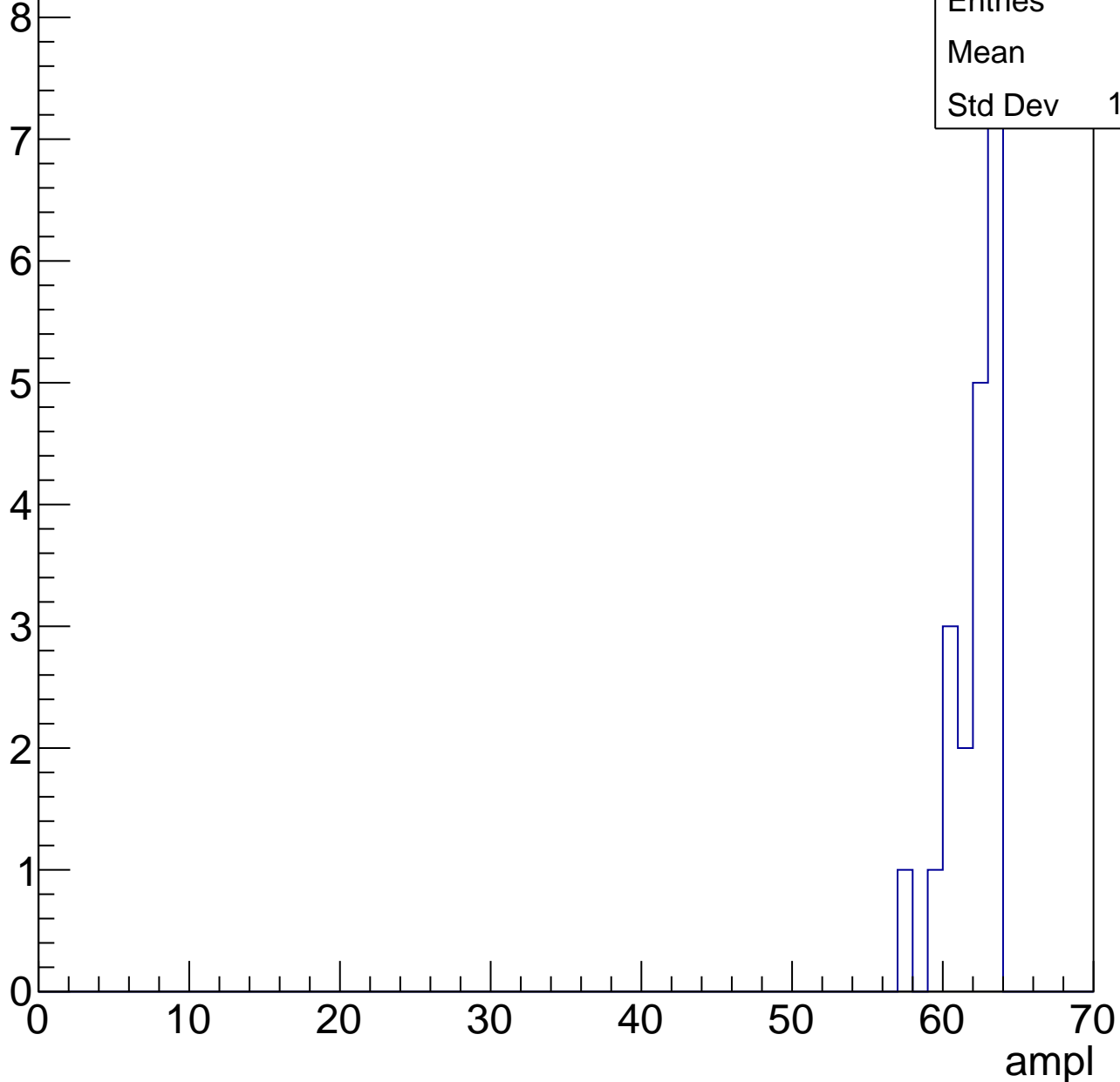


# B0L101S, U26-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.6
Std Dev	1.625

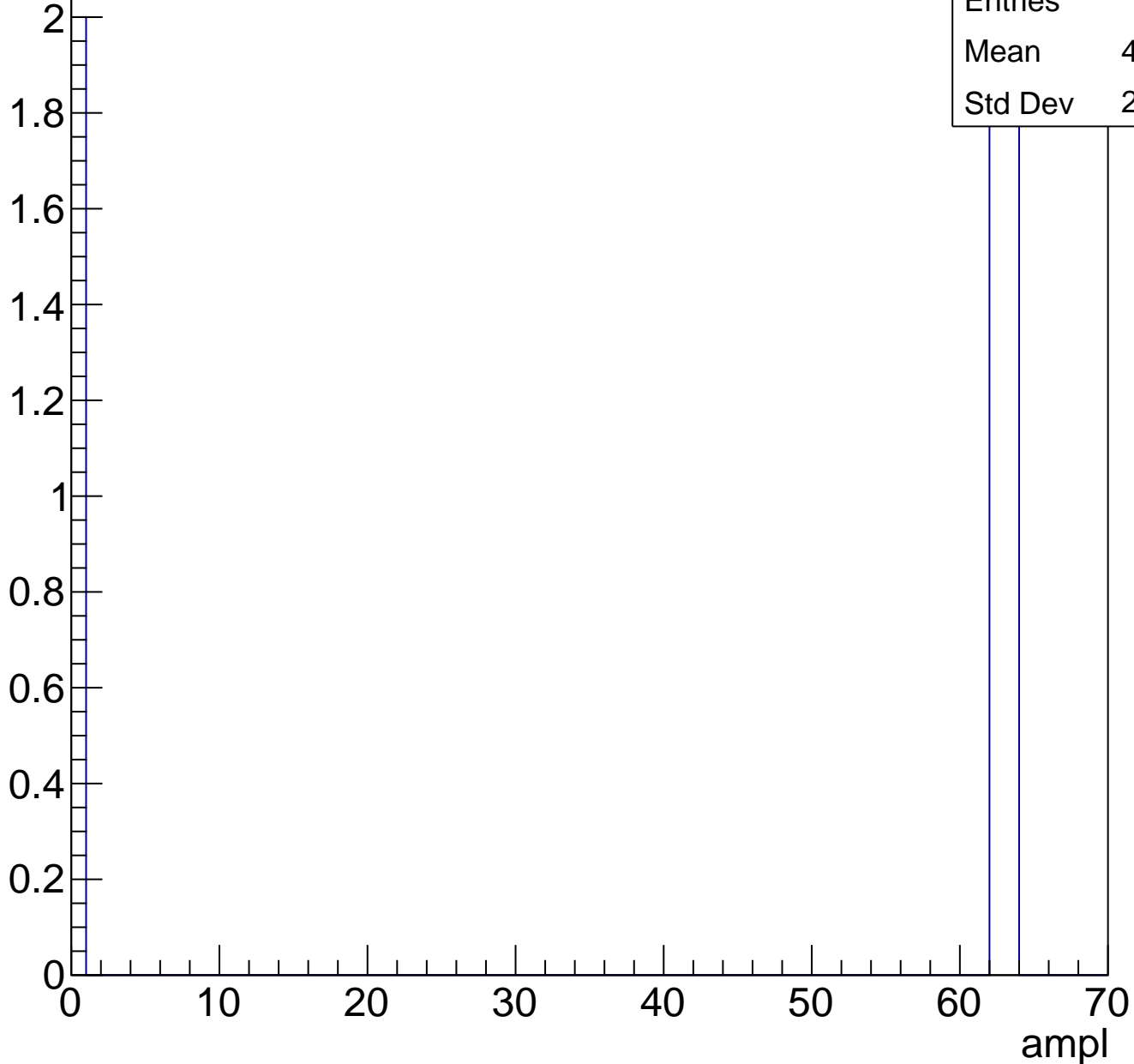




# B0L101S, U26-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	155
Mean	29.63
Std Dev	3.751

**Gaus mean : 30.2906**

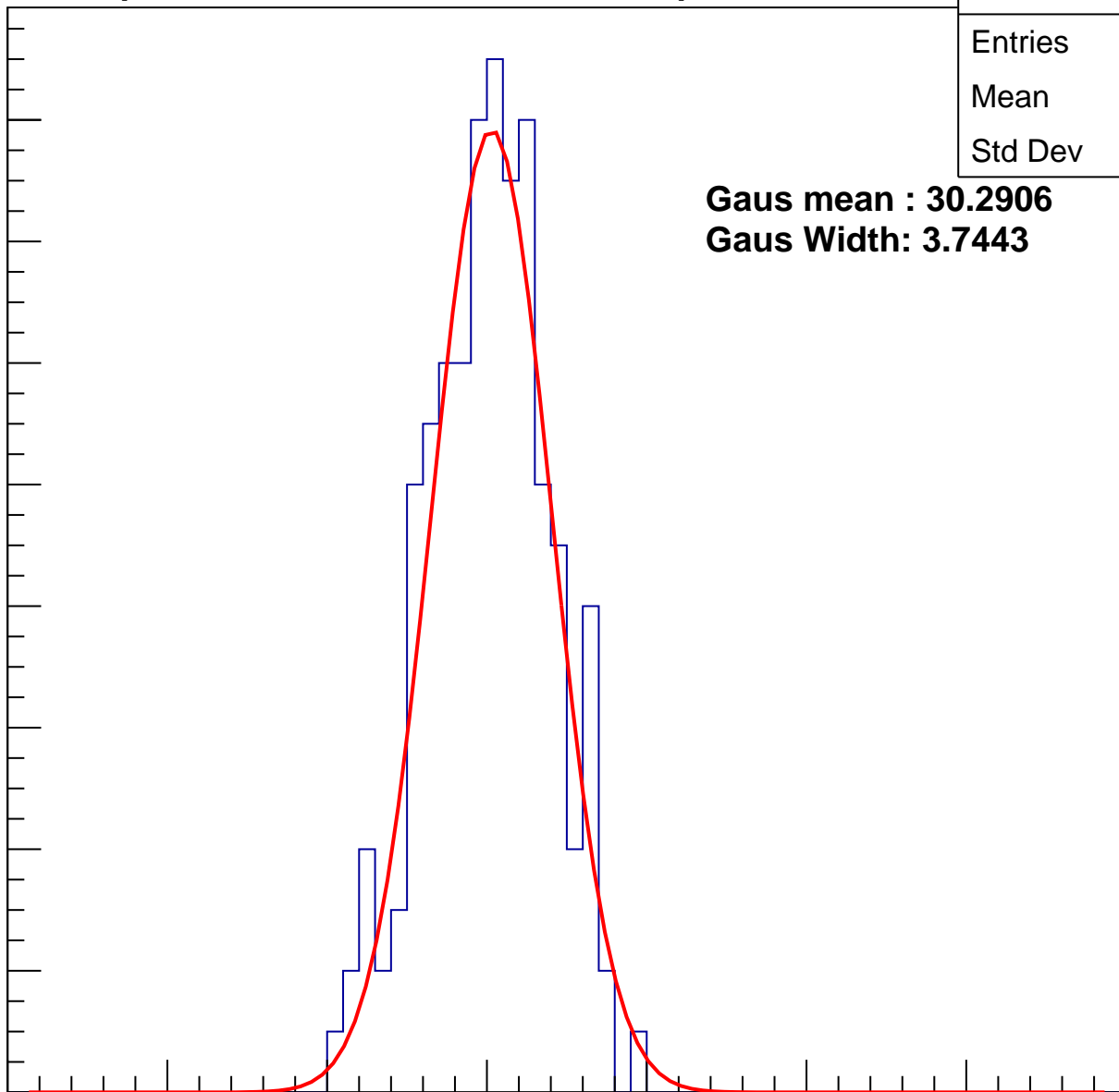
**Gaus Width: 3.7443**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



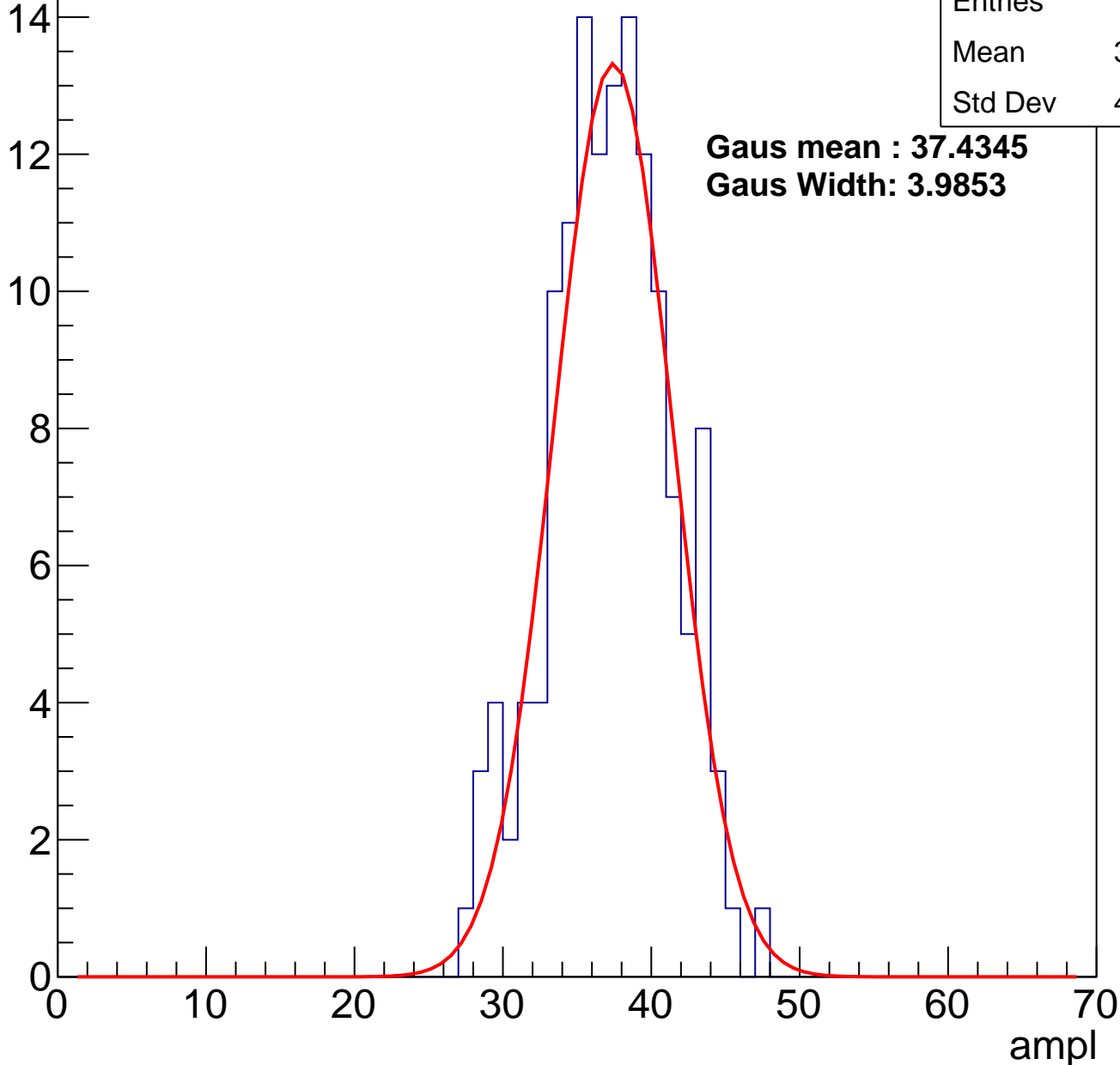
# B0L101S, U26-ch101, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	36.77
Std Dev	4.022

**Gaus mean : 37.4345**  
**Gaus Width: 3.9853**

Entry



# B0L101S, U26-ch101, adc2

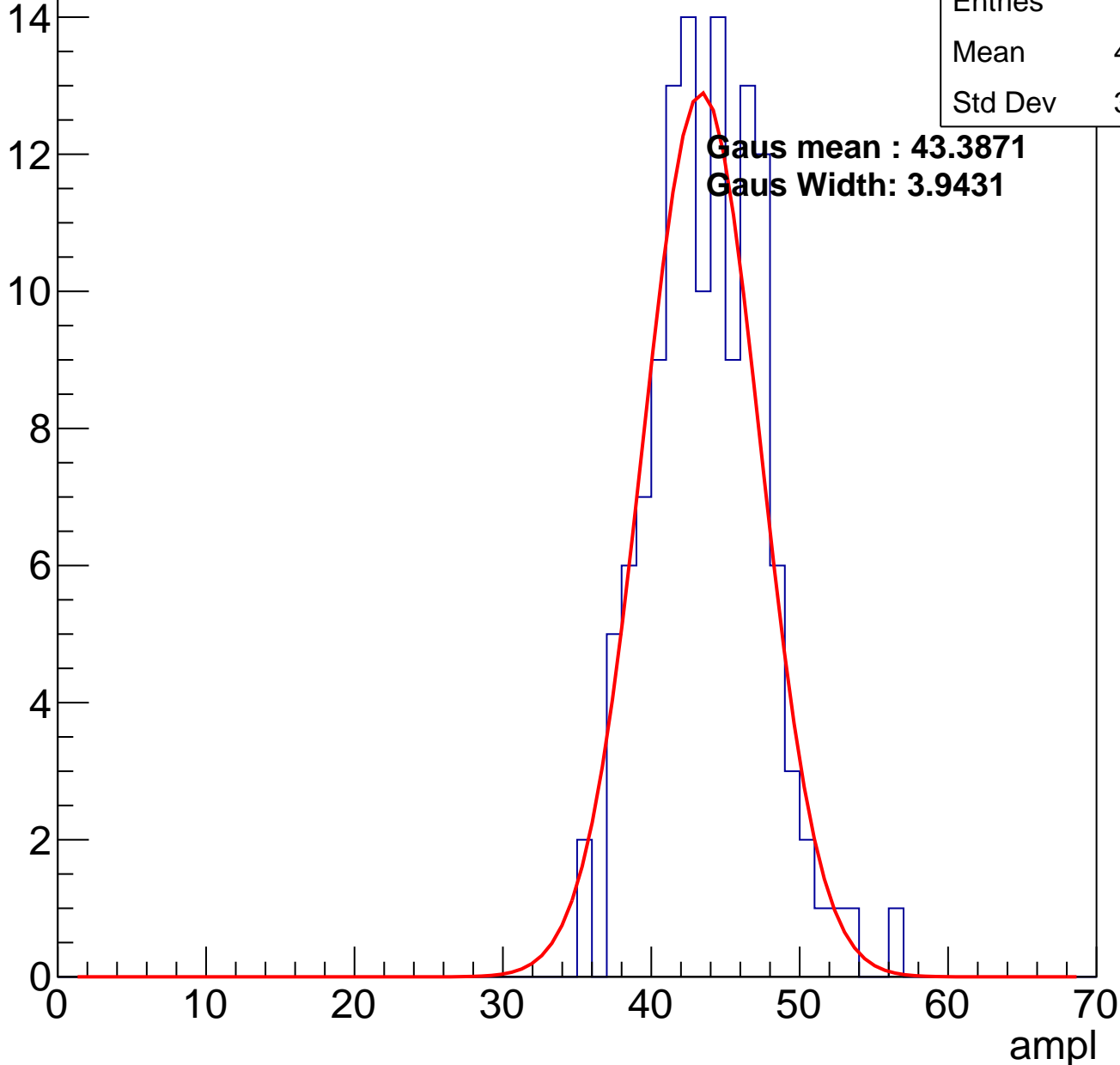
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	129
Mean	43.39
Std Dev	3.763

**Gaus mean : 43.3871**

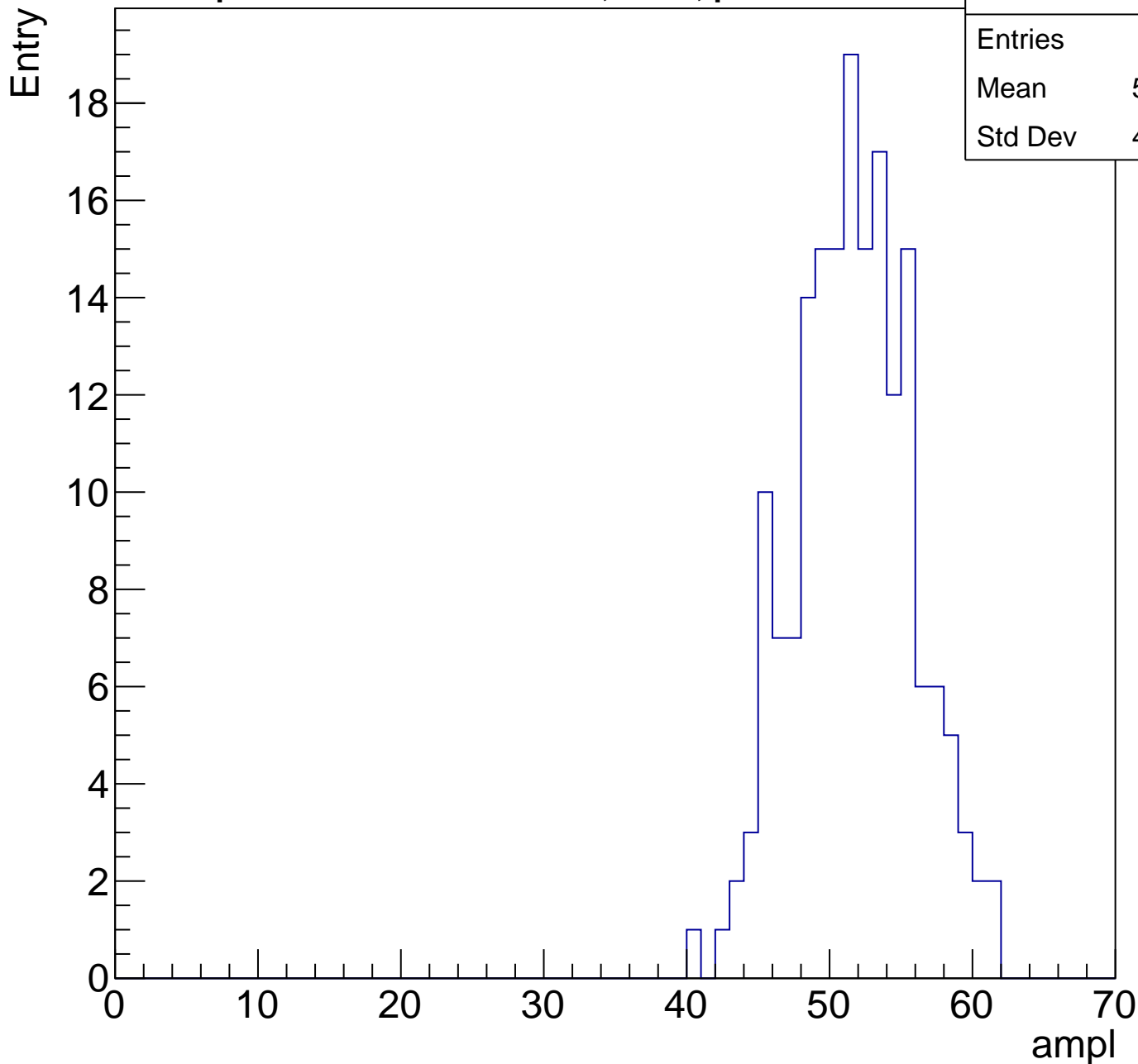
**Gaus Width: 3.9431**



# B0L101S, U26-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	177
Mean	51.23
Std Dev	4.056



# B0L101S, U26-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	106
Mean	57.08
Std Dev	6.472

Entry

12

10

8

6

4

2

0

0

10

20

30

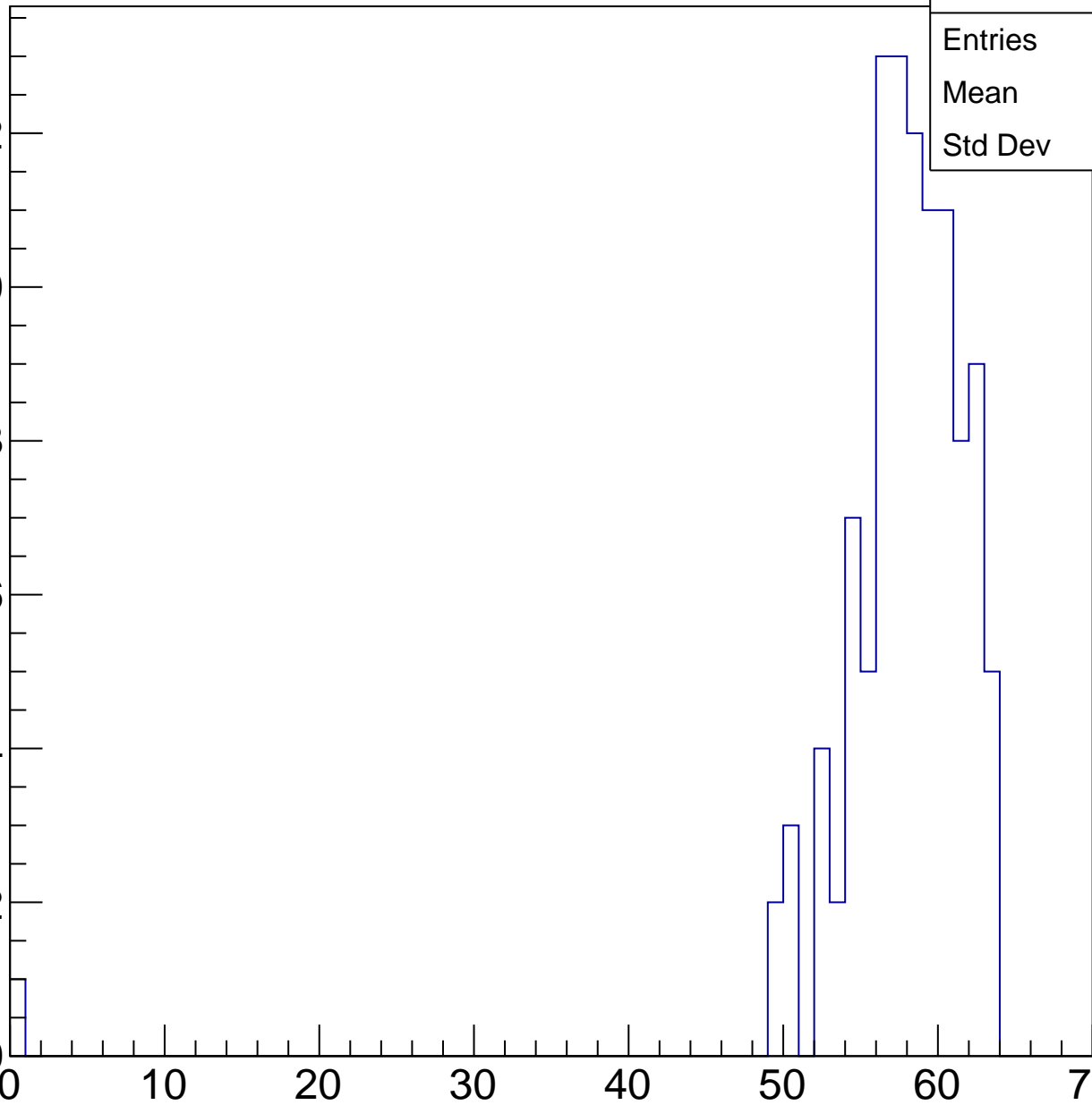
40

50

60

70

ampl



# B0L101S, U26-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

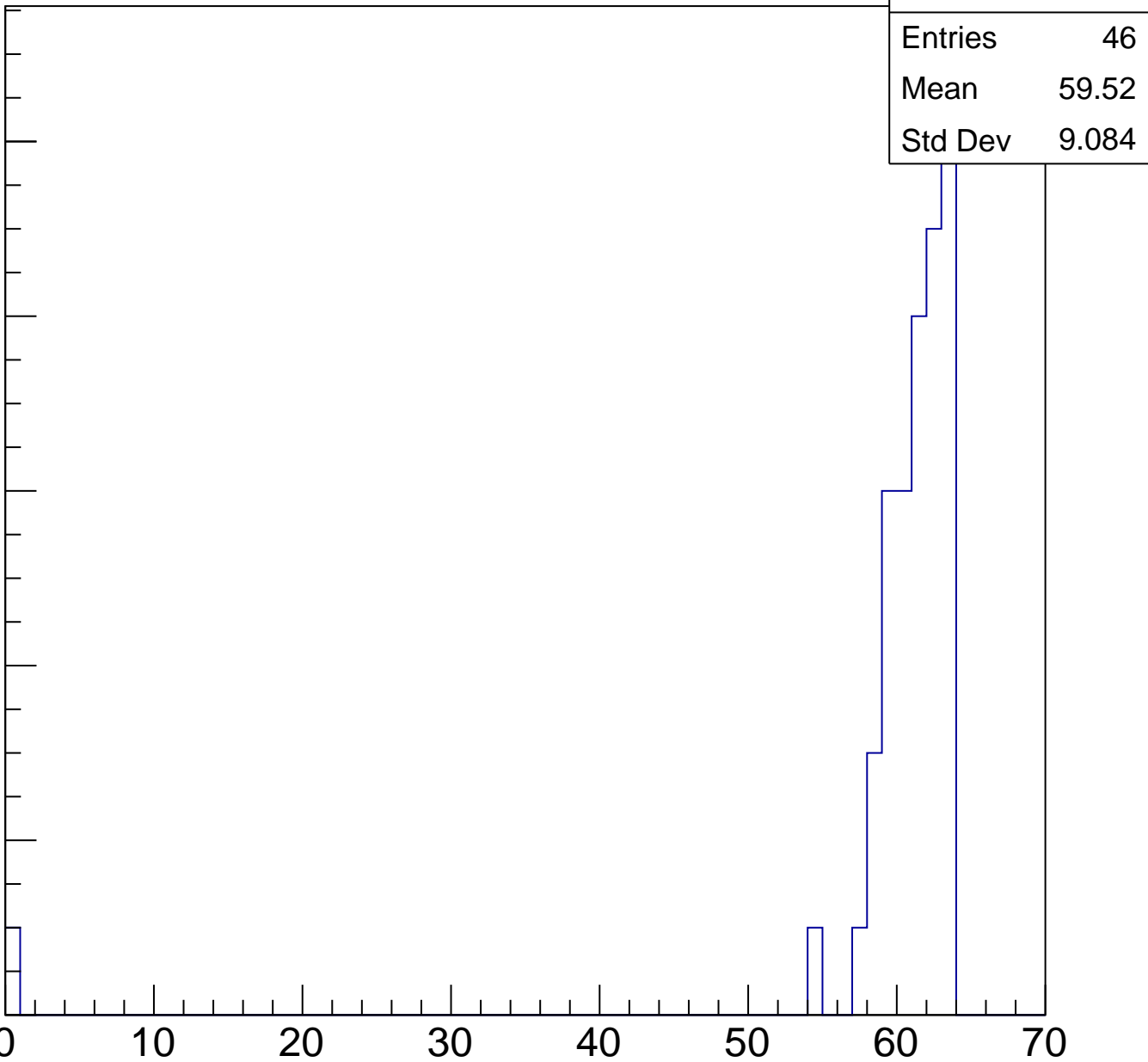
0

Entries	46
---------	----

Mean	59.52
------	-------

Std Dev	9.084
---------	-------

ampl



# B0L101S, U26-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	62
Std Dev	1.054

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L101S, U26-ch102, adc0

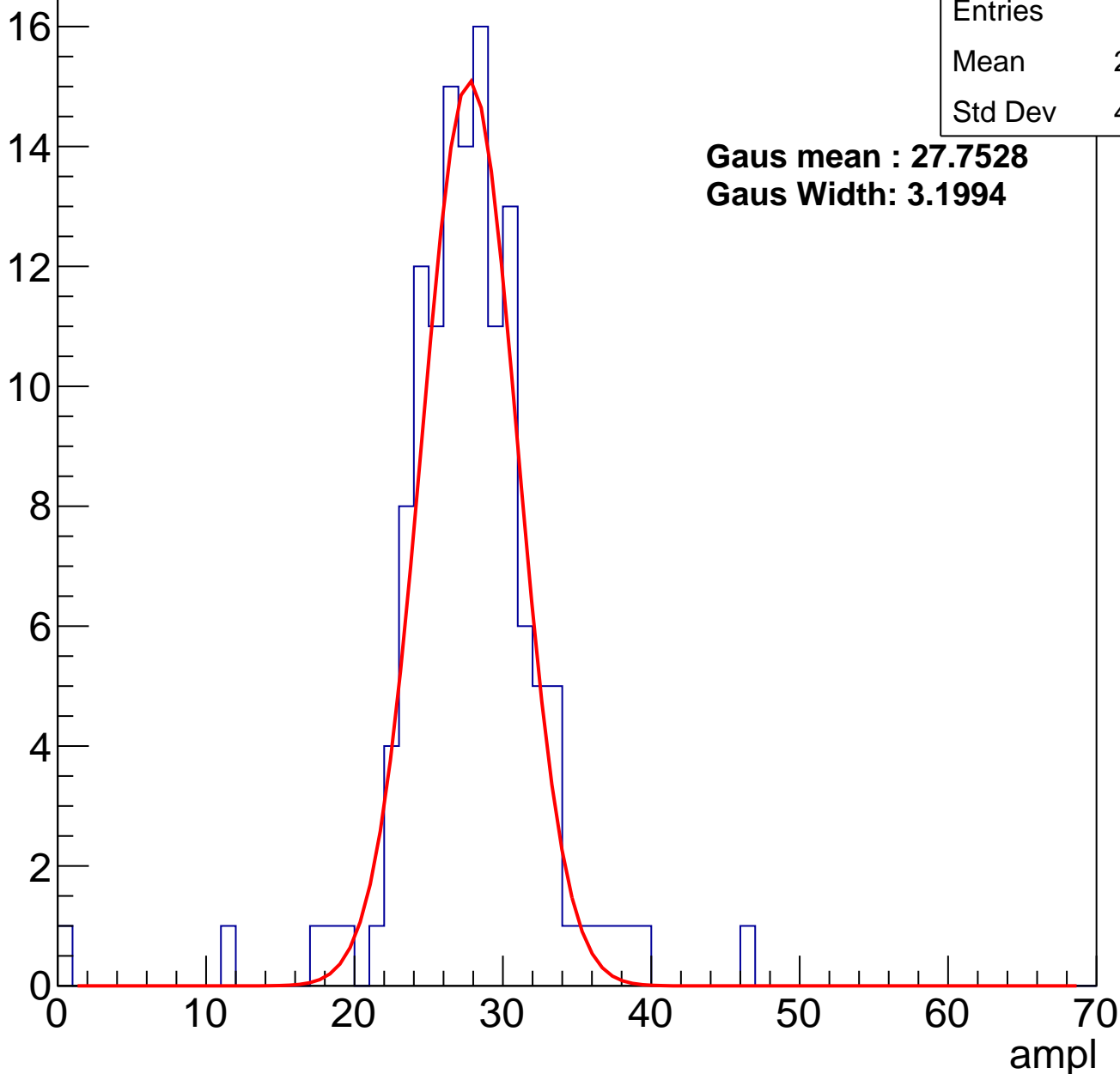
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	27.23
Std Dev	4.868

**Gaus mean : 27.7528**

**Gaus Width: 3.1994**

Entry



# B0L101S, U26-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

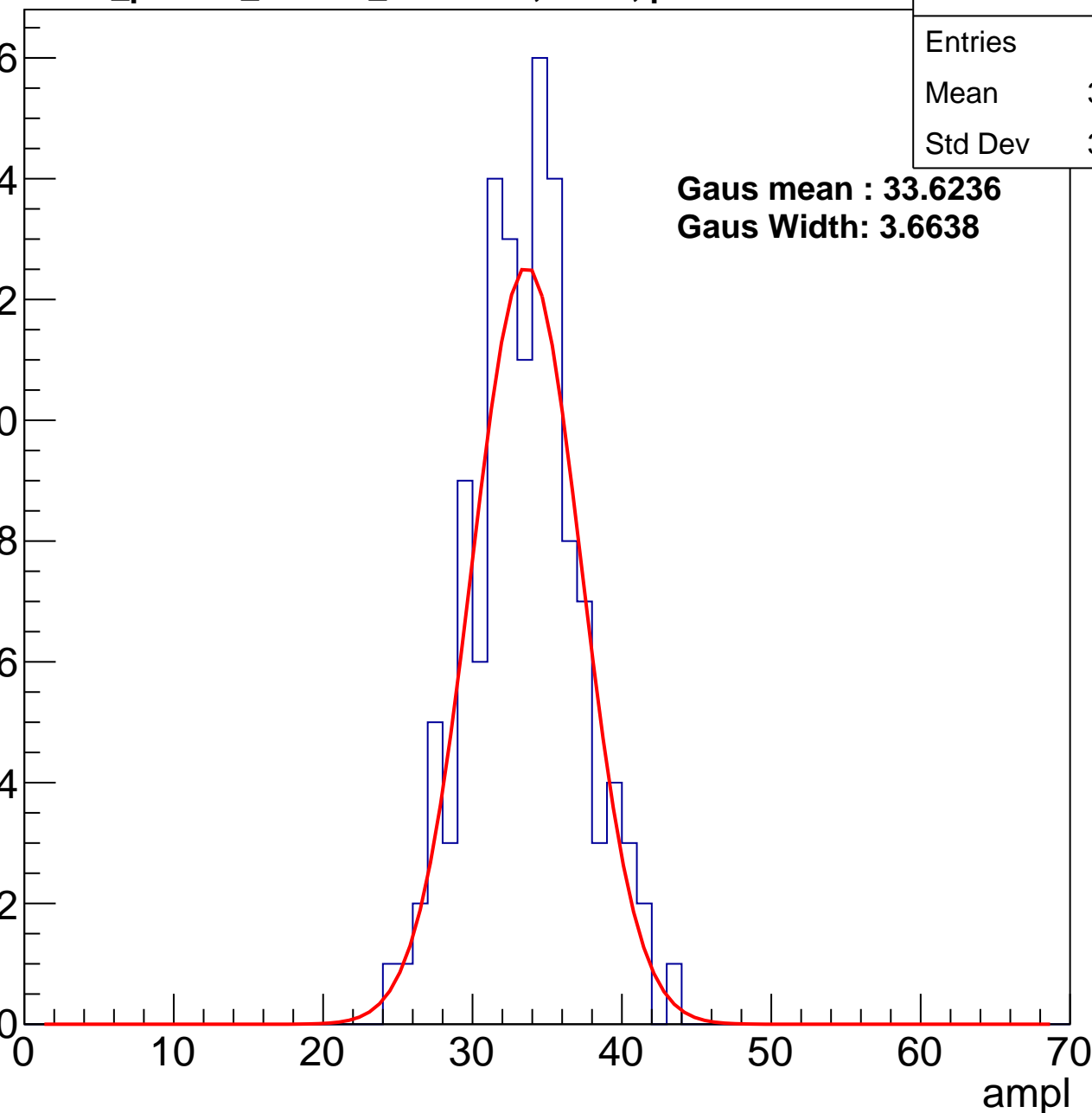
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	33.09
Std Dev	3.622

**Gaus mean : 33.6236**

**Gaus Width: 3.6638**



# B0L101S, U26-ch102, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

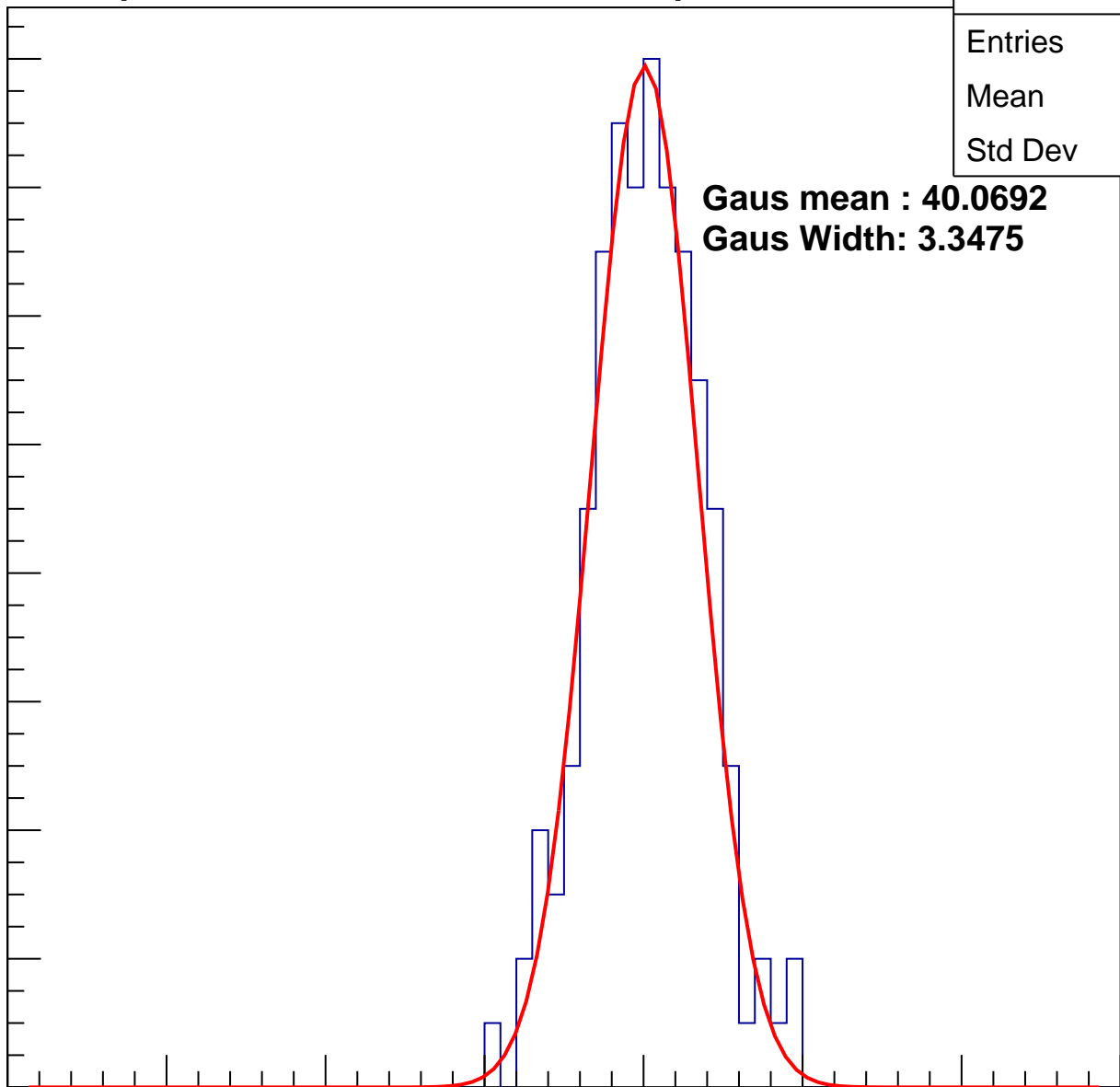
Entries	140
Mean	39.74
Std Dev	3.562

**Gaus mean : 40.0692**

**Gaus Width: 3.3475**

0 10 20 30 40 50 60 70

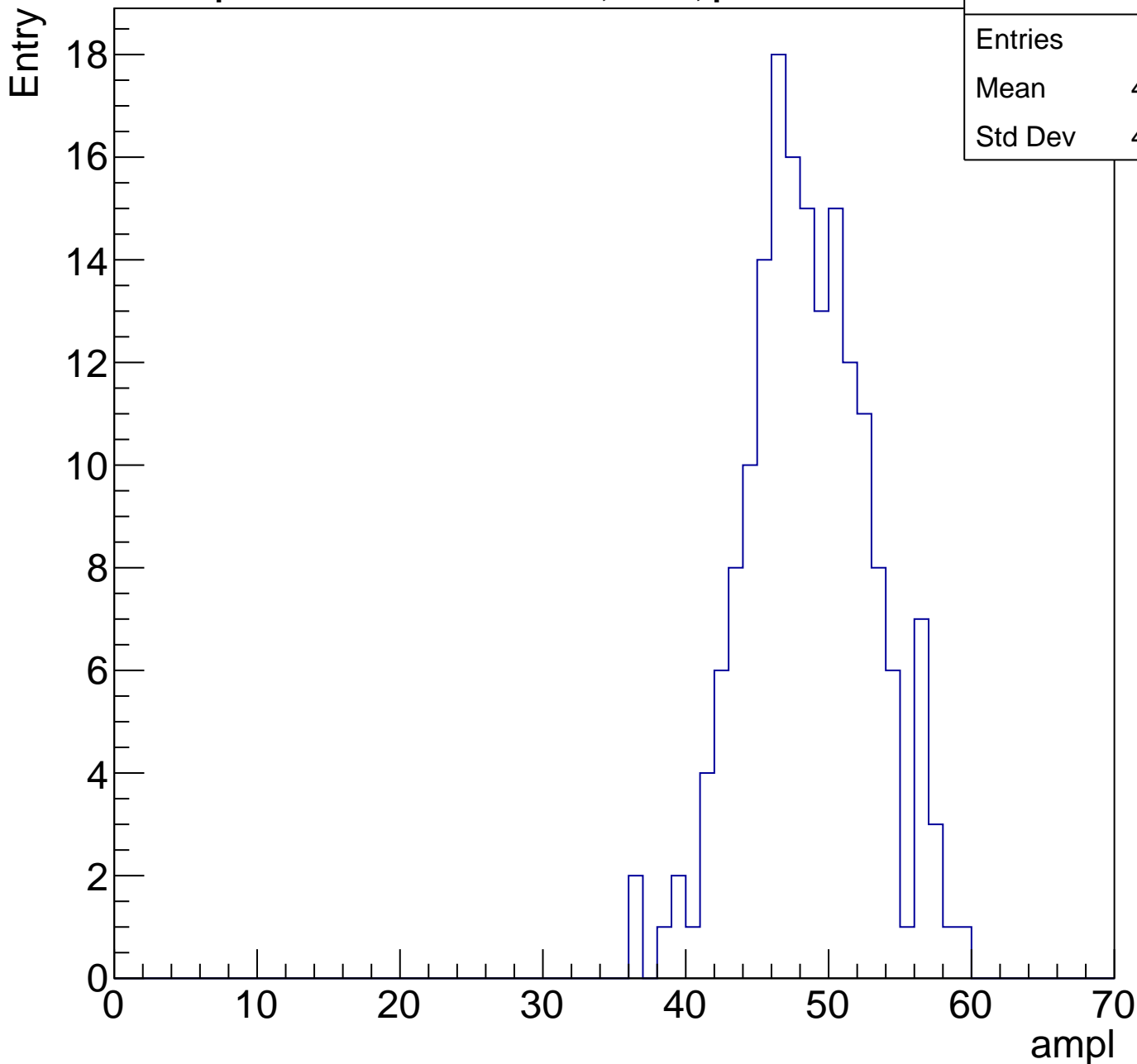
ampl



# B0L101S, U26-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	175
Mean	48.07
Std Dev	4.364



# B0L101S, U26-ch102, adc4

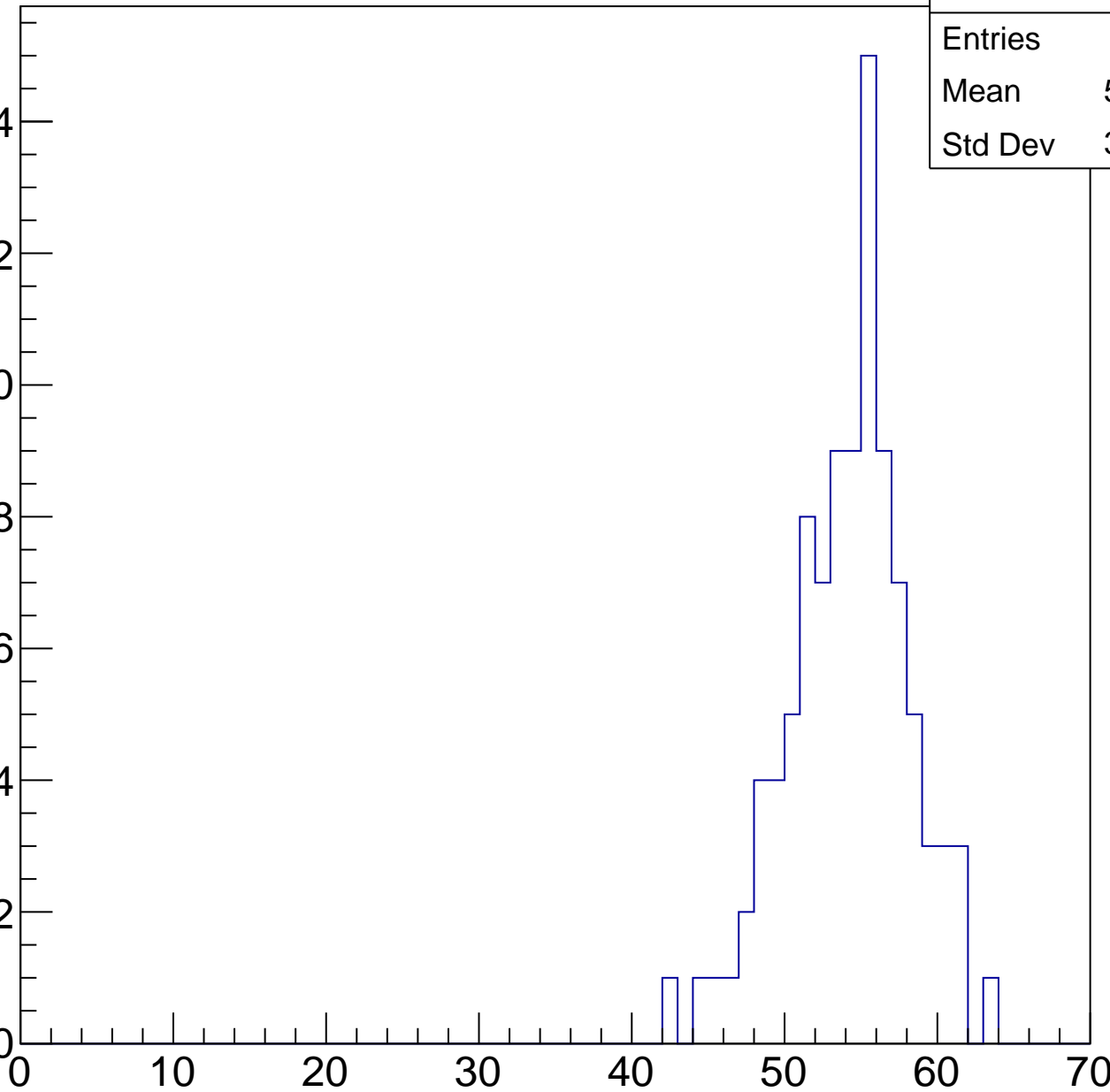
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	98
Mean	53.72
Std Dev	3.935

ampl



# B0L101S, U26-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

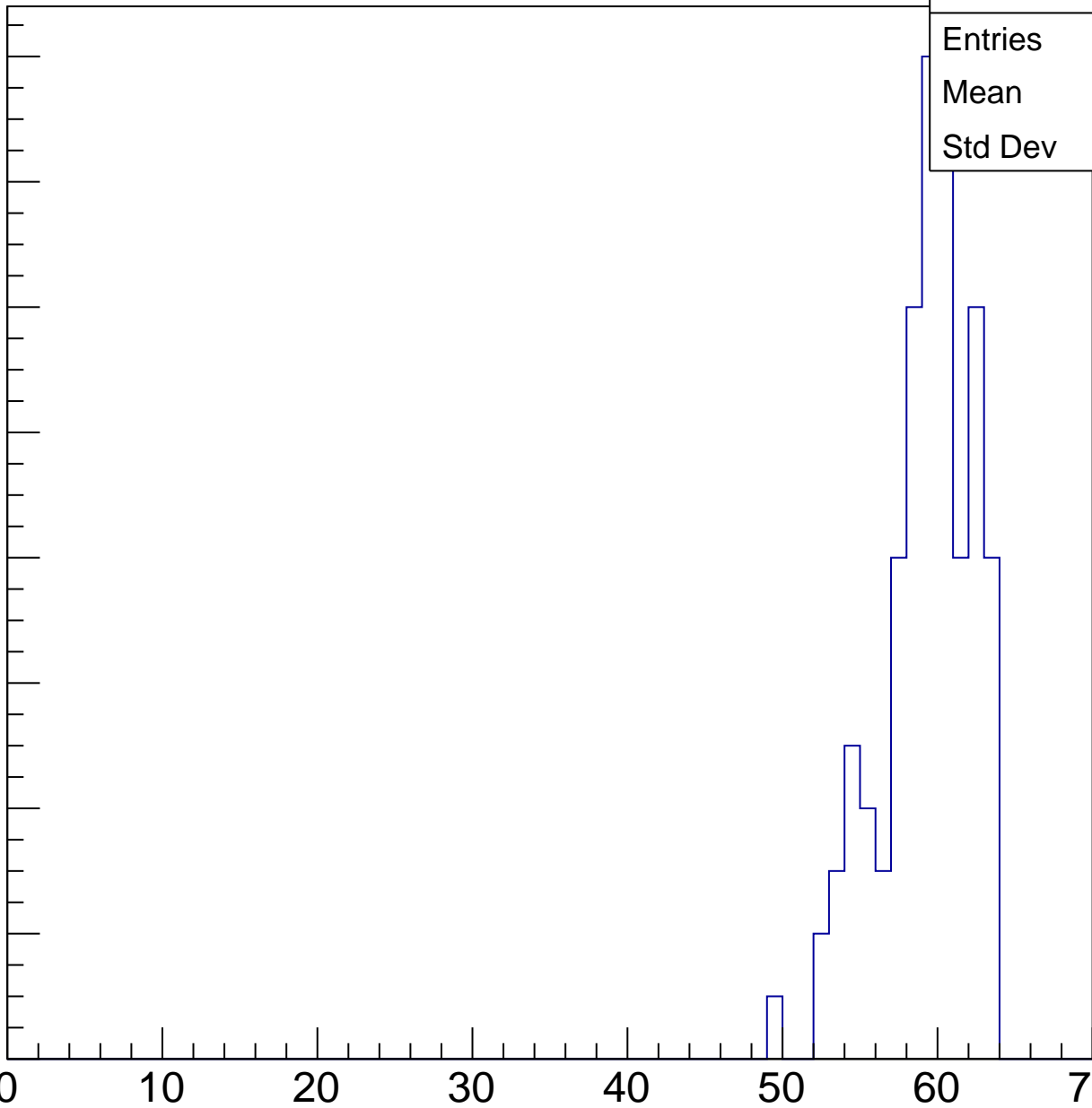
Entries	98
Mean	58.8
Std Dev	2.938

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

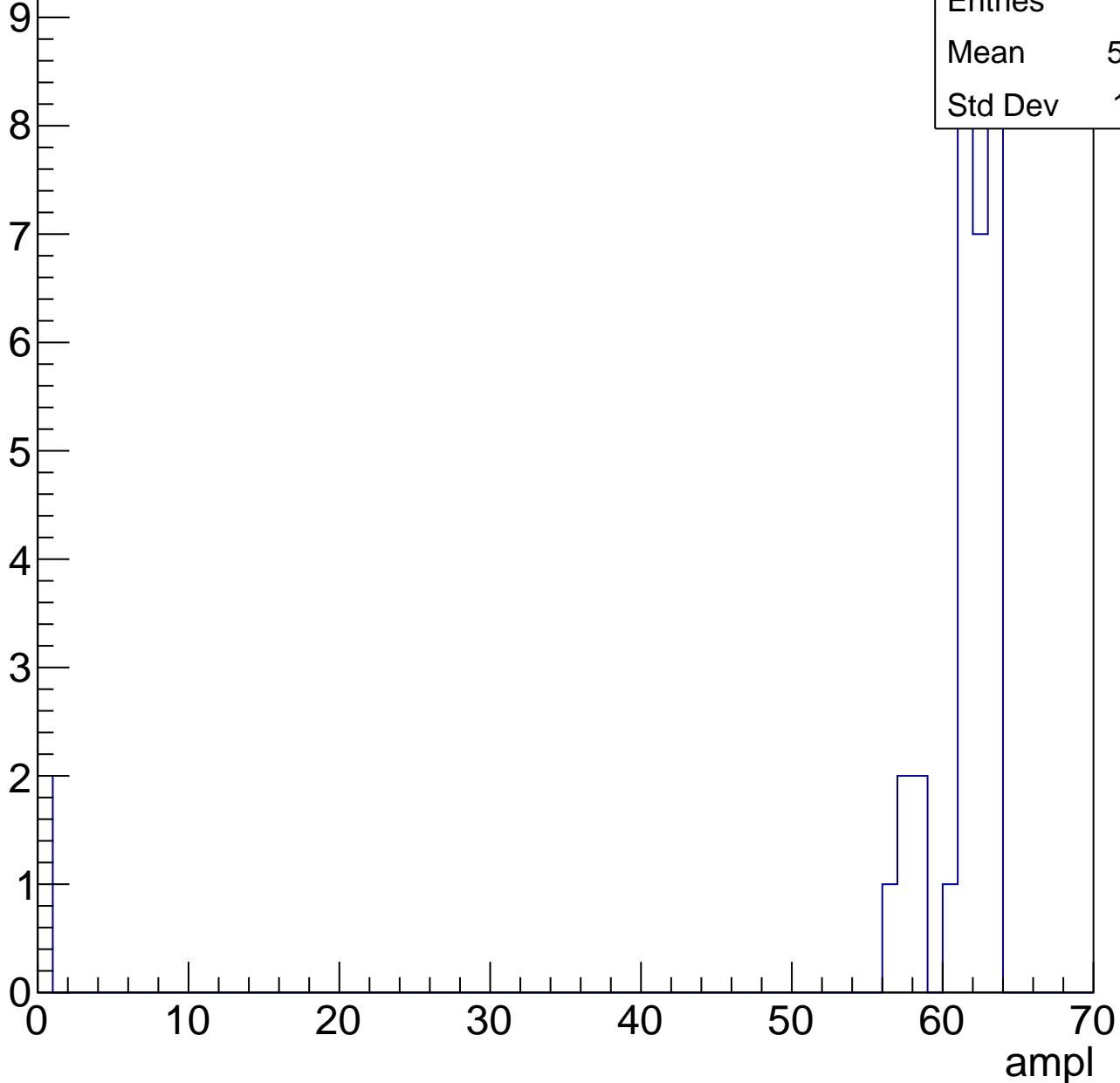


# B0L101S, U26-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	32
Mean	57.28
Std Dev	14.91

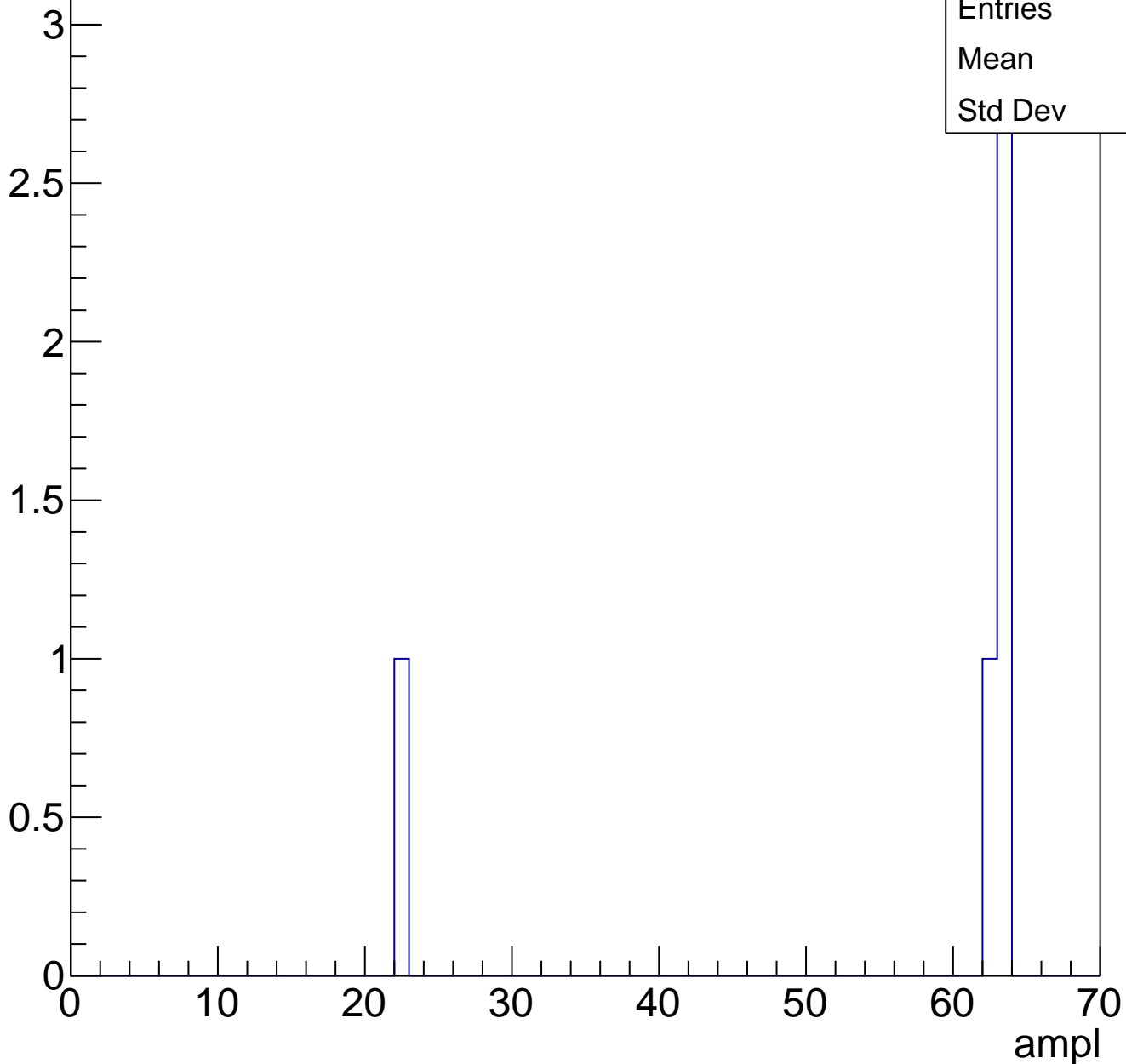




# B0L101S, U26-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch103, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	29.08
Std Dev	4.165

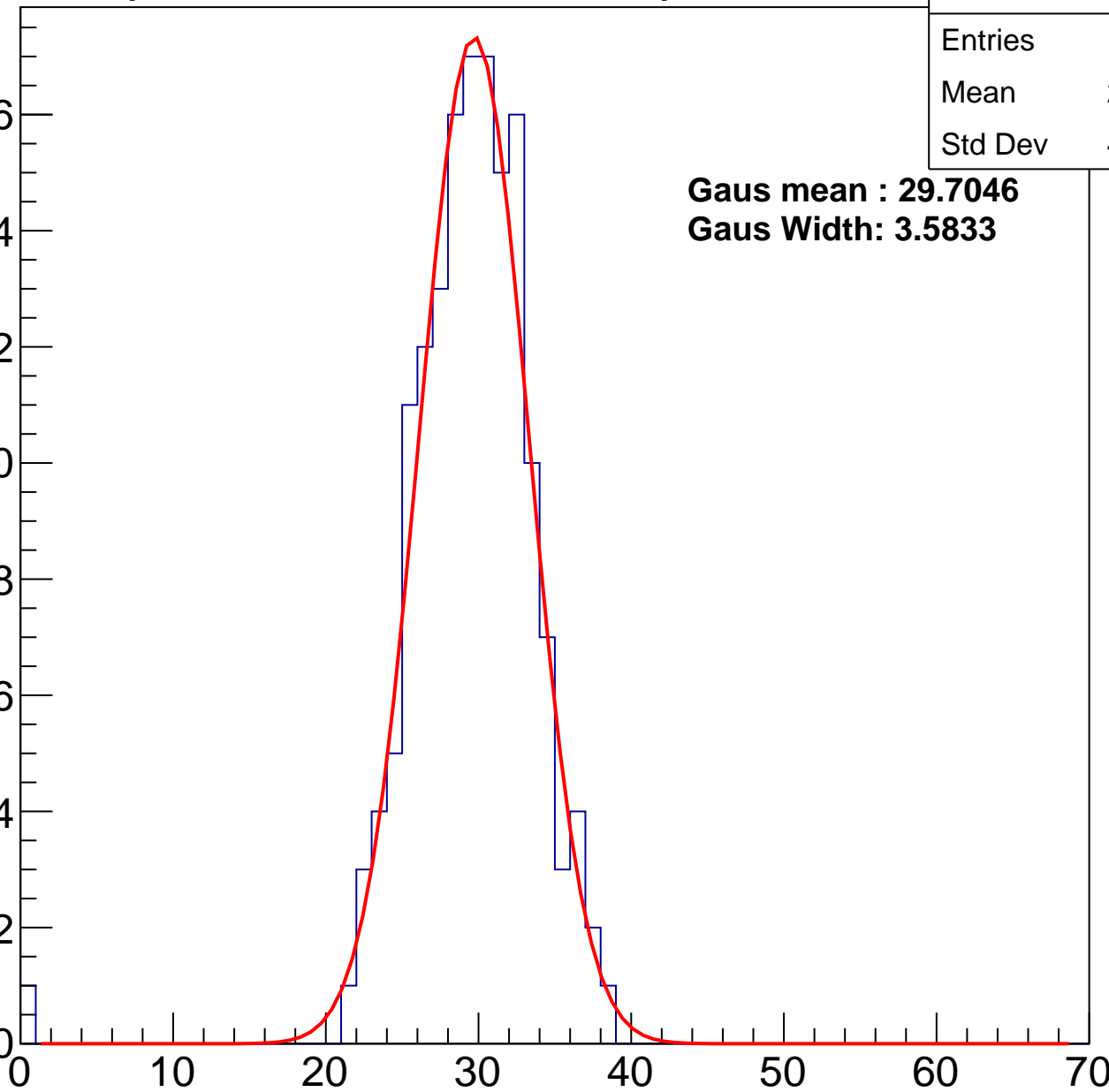
**Gaus mean : 29.7046**

**Gaus Width: 3.5833**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



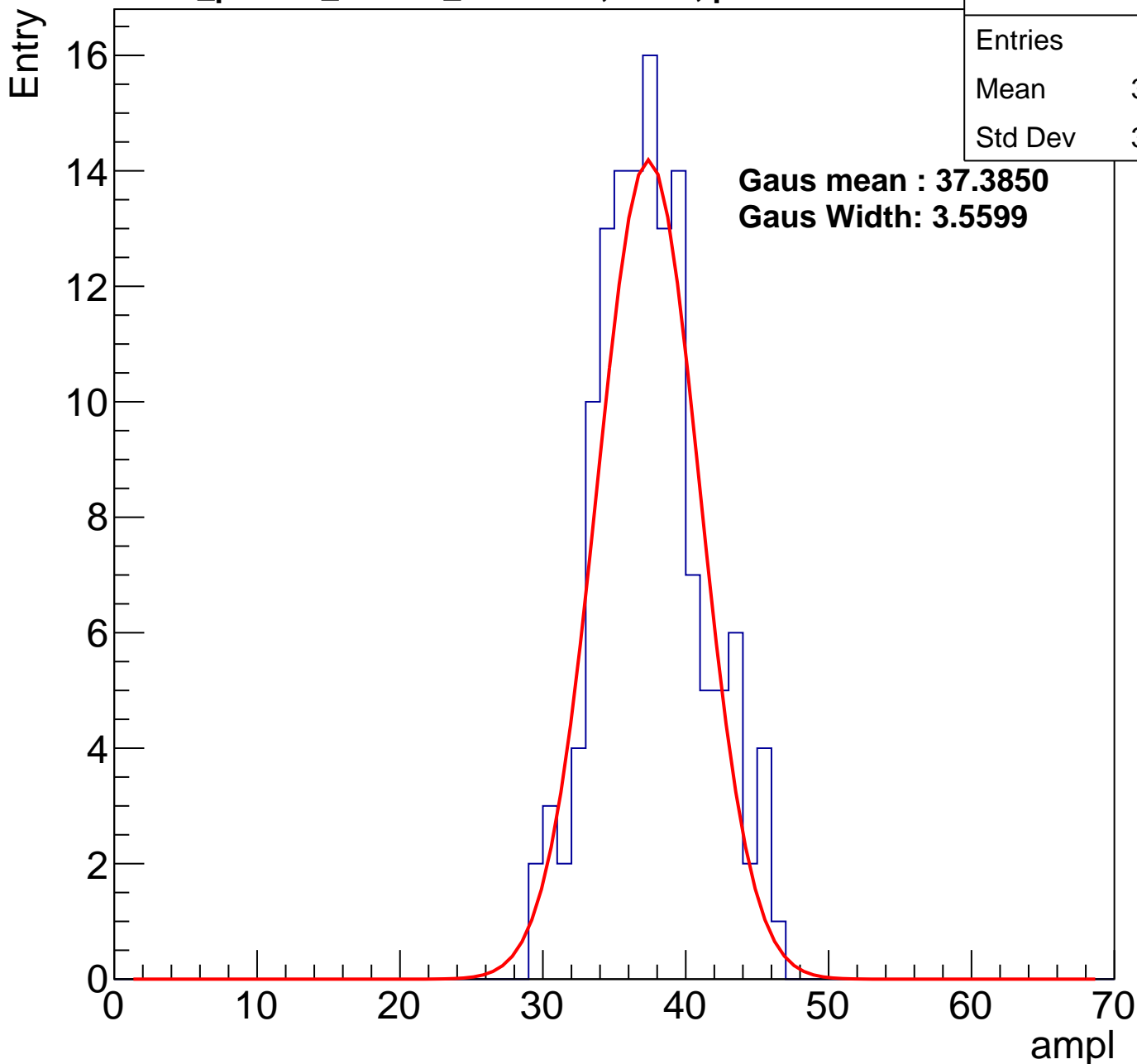
# B0L101S, U26-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	37.06
Std Dev	3.633

**Gaus mean : 37.3850**

**Gaus Width: 3.5599**



# B0L101S, U26-ch103, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

130

Mean

43.04

Std Dev

3.775

**Gaus mean : 43.5084**

**Gaus Width: 3.6482**

0

10

20

30

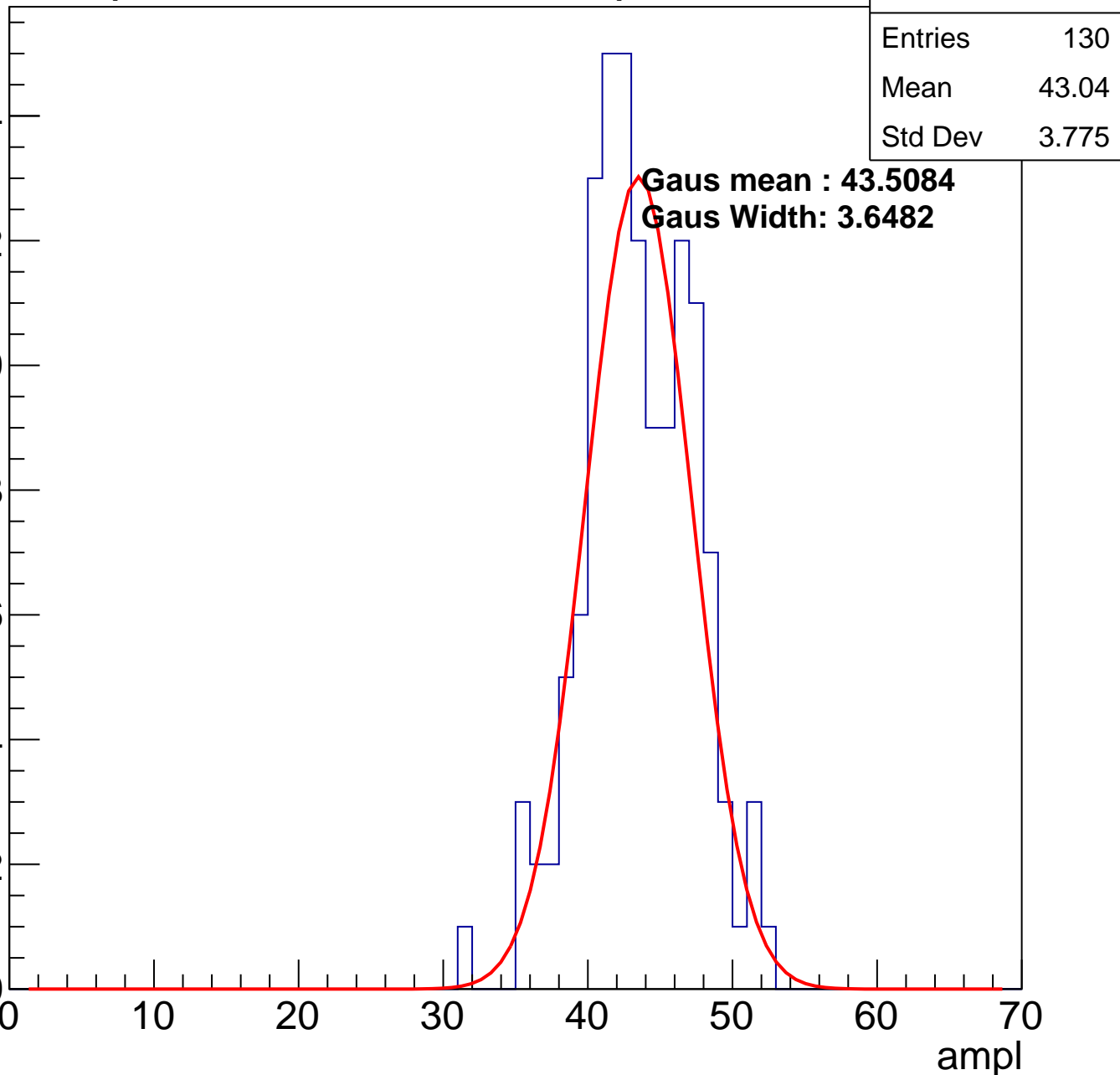
40

50

60

70

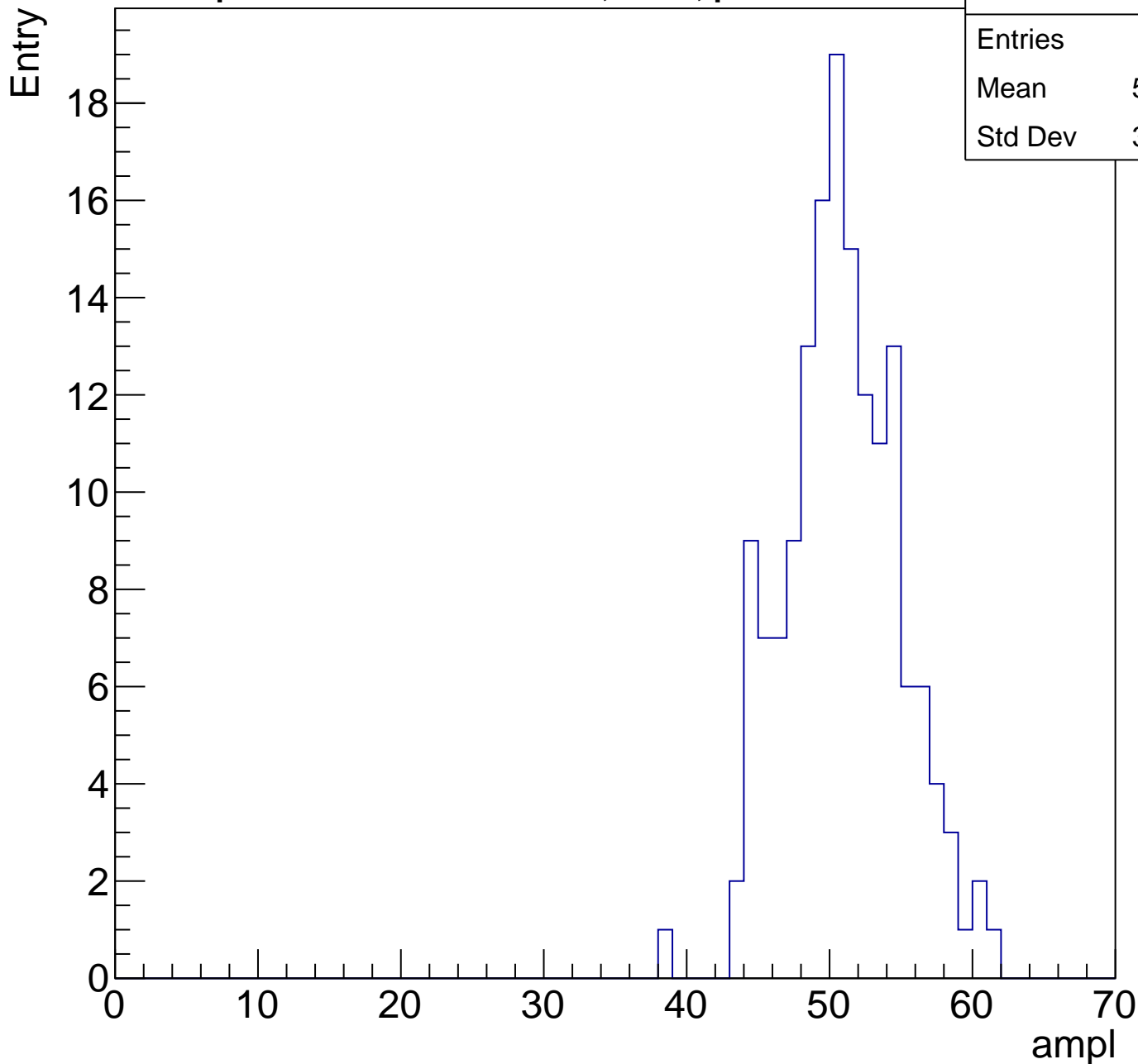
ampl



# B0L101S, U26-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

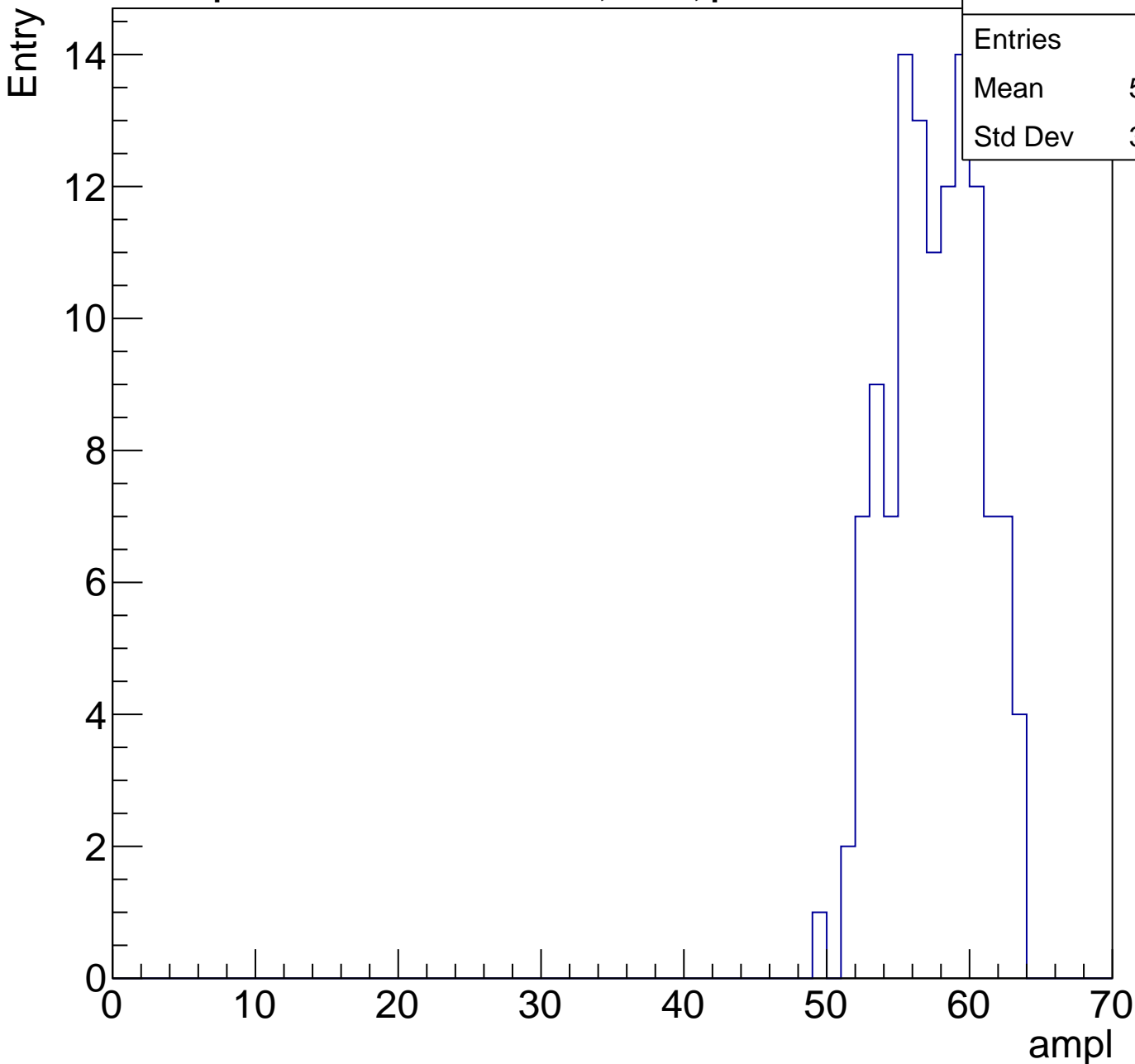
Entries	157
Mean	50.45
Std Dev	3.988



# B0L101S, U26-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	120
Mean	57.08
Std Dev	3.153



# B0L101S, U26-ch103, adc5

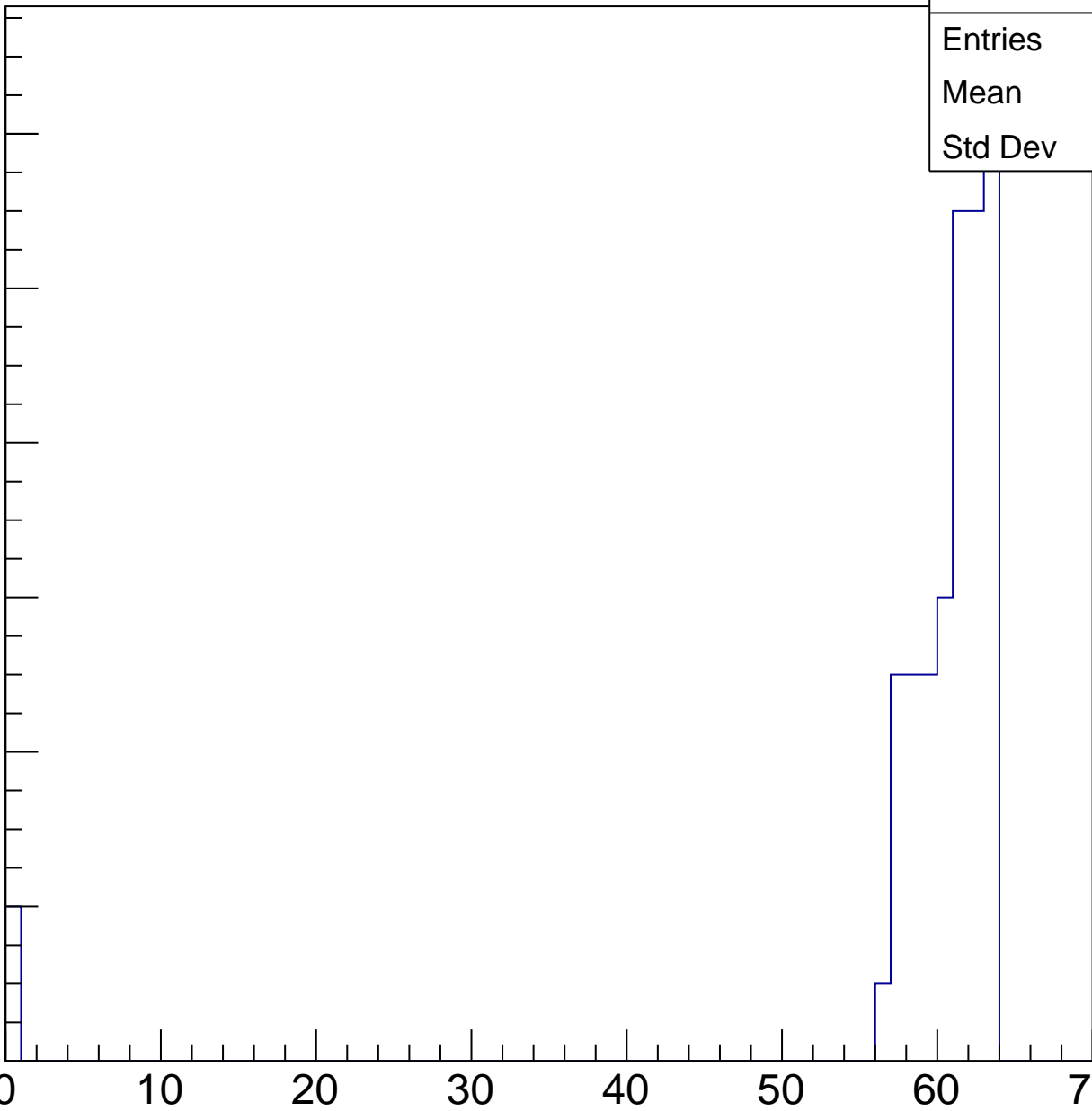
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12  
10  
8  
6  
4  
2  
0

Entries	59
Mean	58.61
Std Dev	11.16

ampl



# B0L101S, U26-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch104, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	28.2
Std Dev	6.599

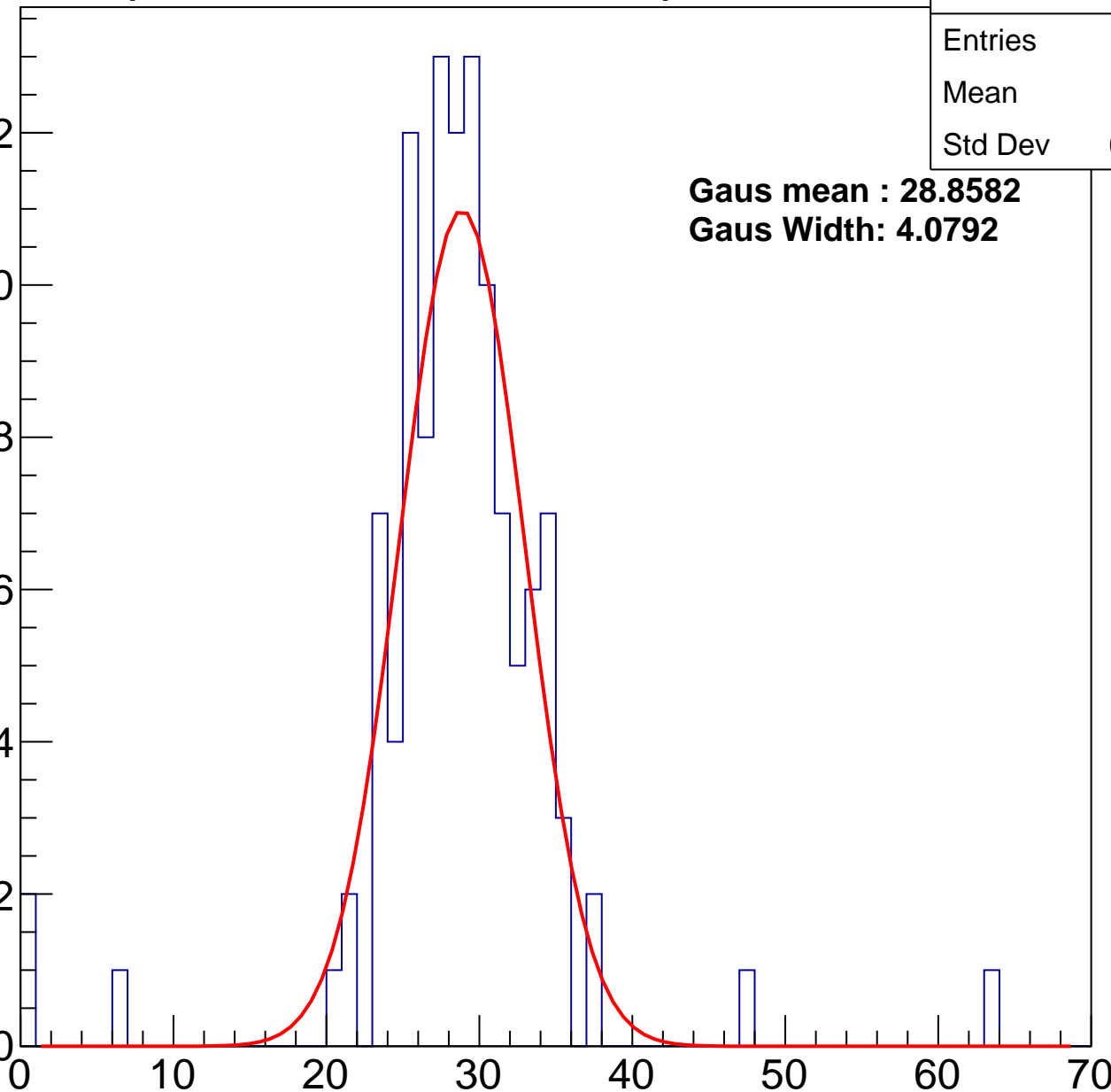
**Gaus mean : 28.8582**

**Gaus Width: 4.0792**

Entry

12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

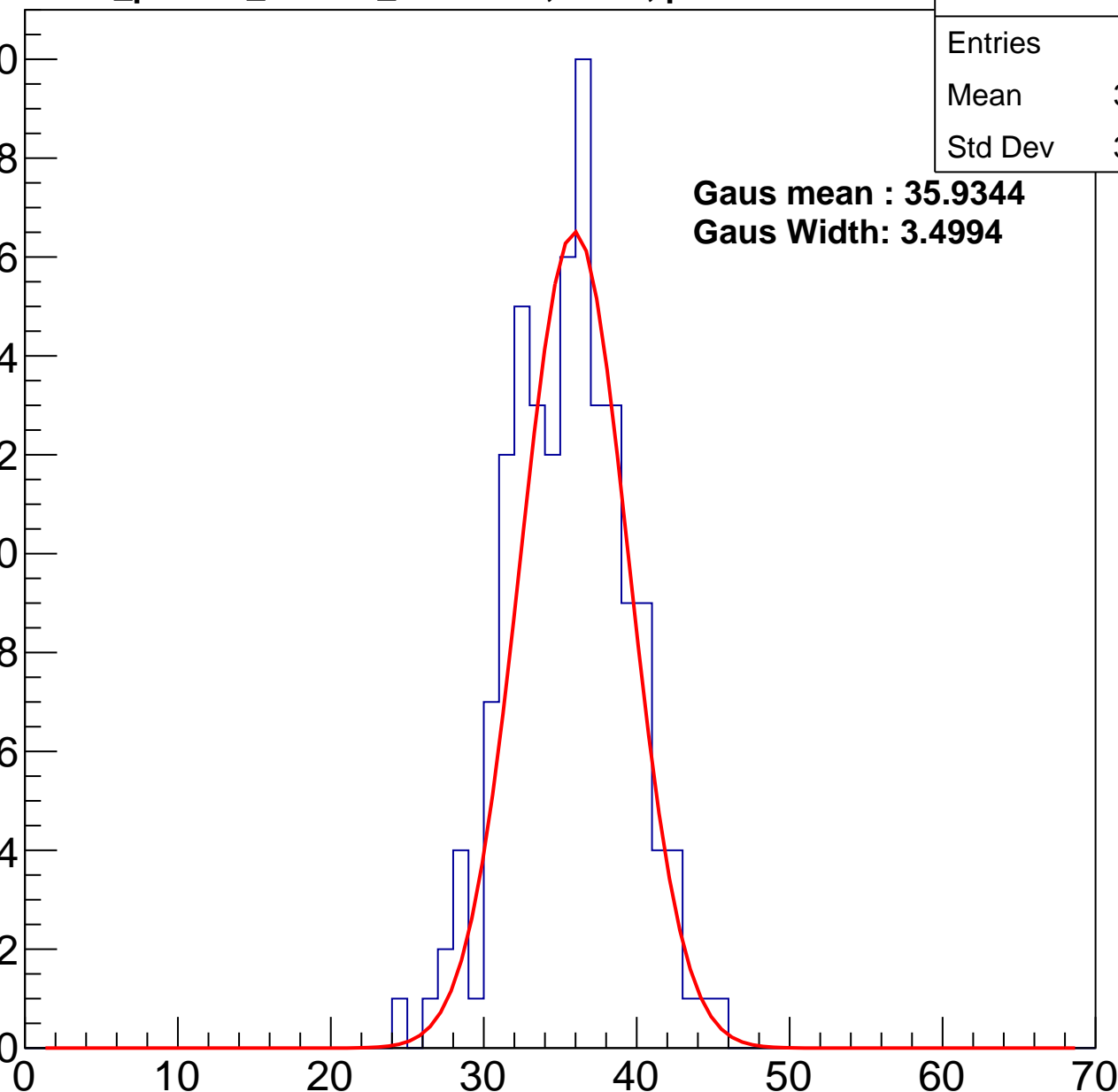
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	159
Mean	35.06
Std Dev	3.779

**Gaus mean : 35.9344**

**Gaus Width: 3.4994**

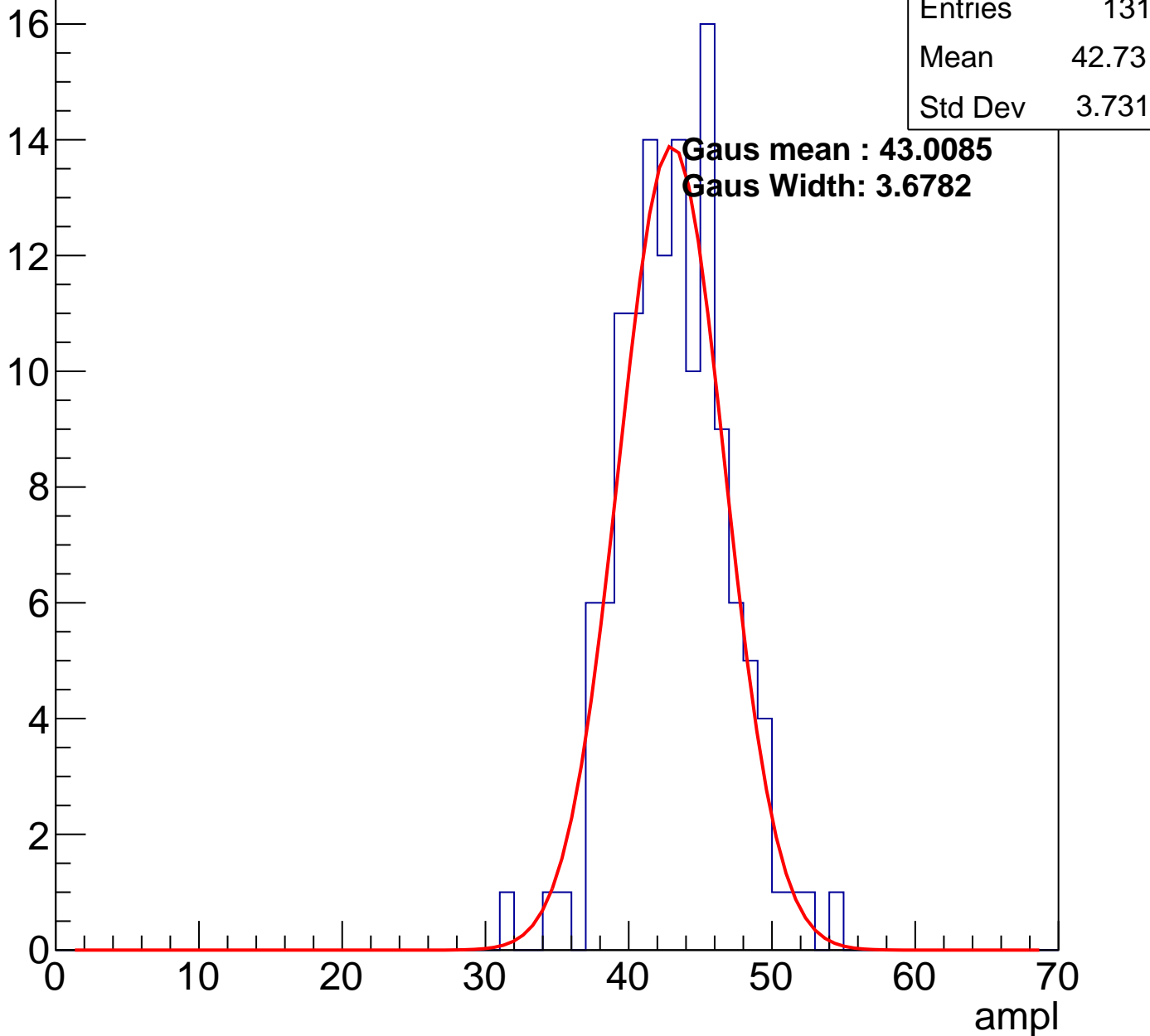


ampl

# B0L101S, U26-ch104, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

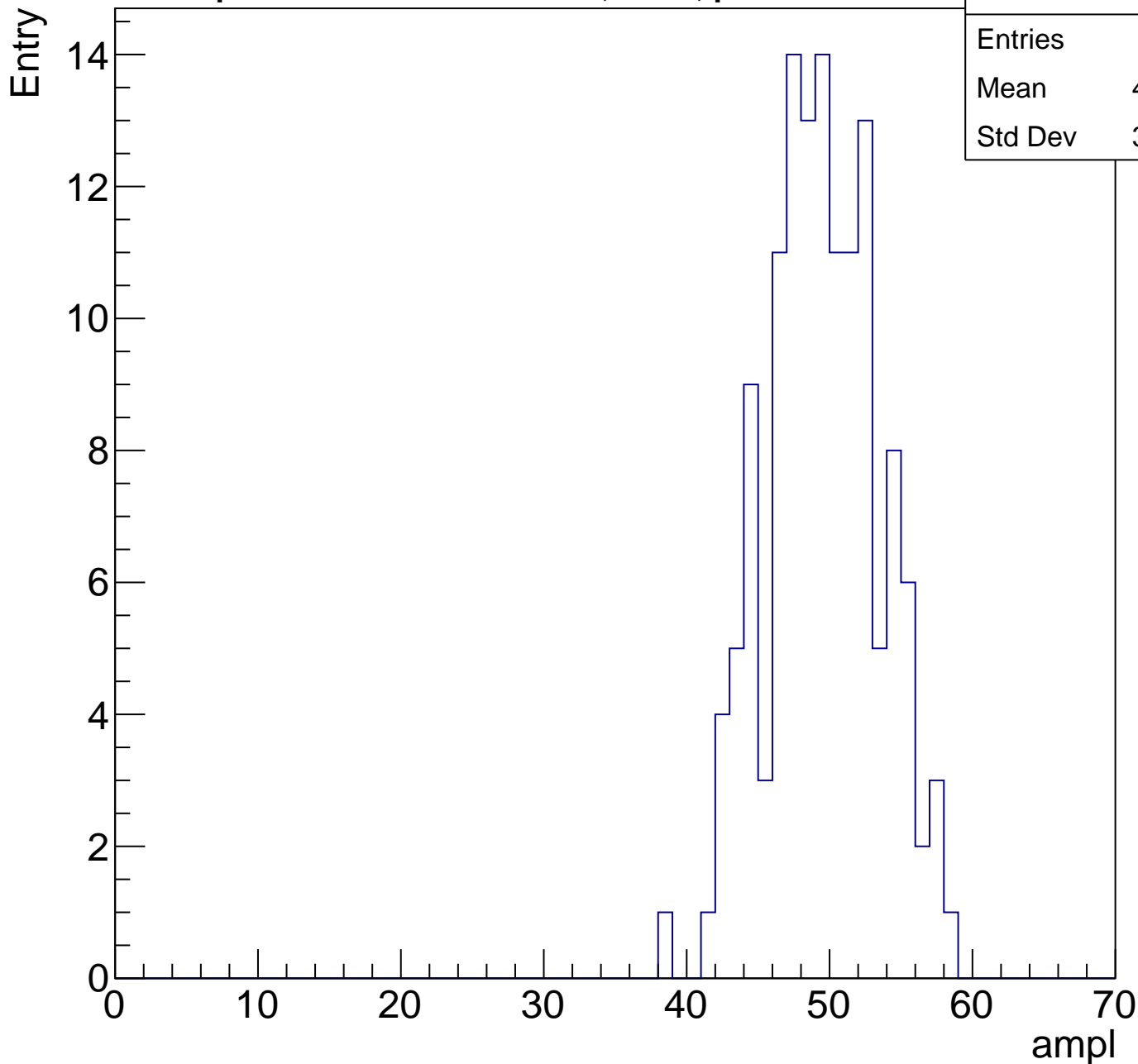
Entry



# B0L101S, U26-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	135
Mean	49.05
Std Dev	3.888

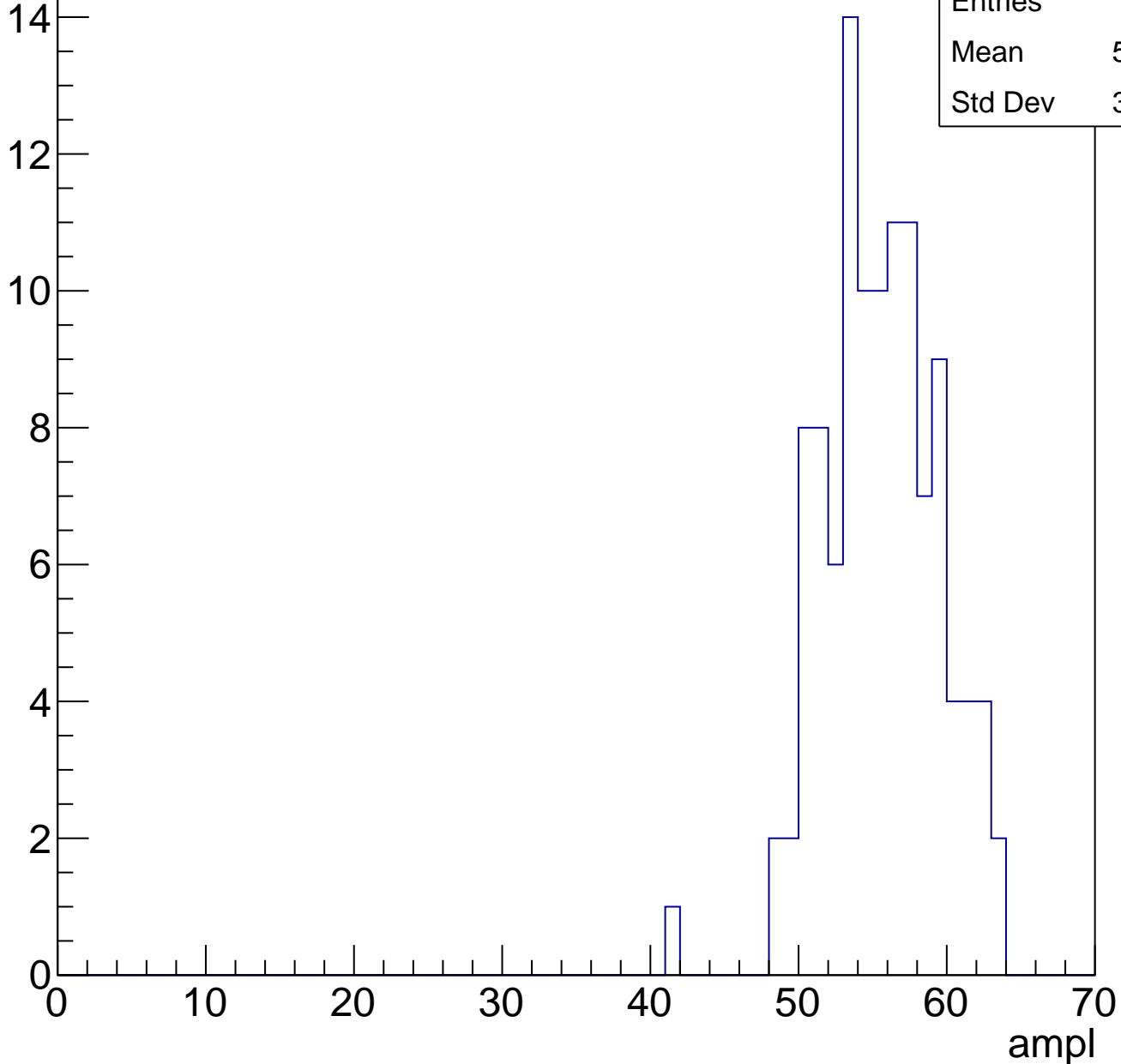


# B0L101S, U26-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	113
Mean	55.09
Std Dev	3.813



# B0L101S, U26-ch104, adc5

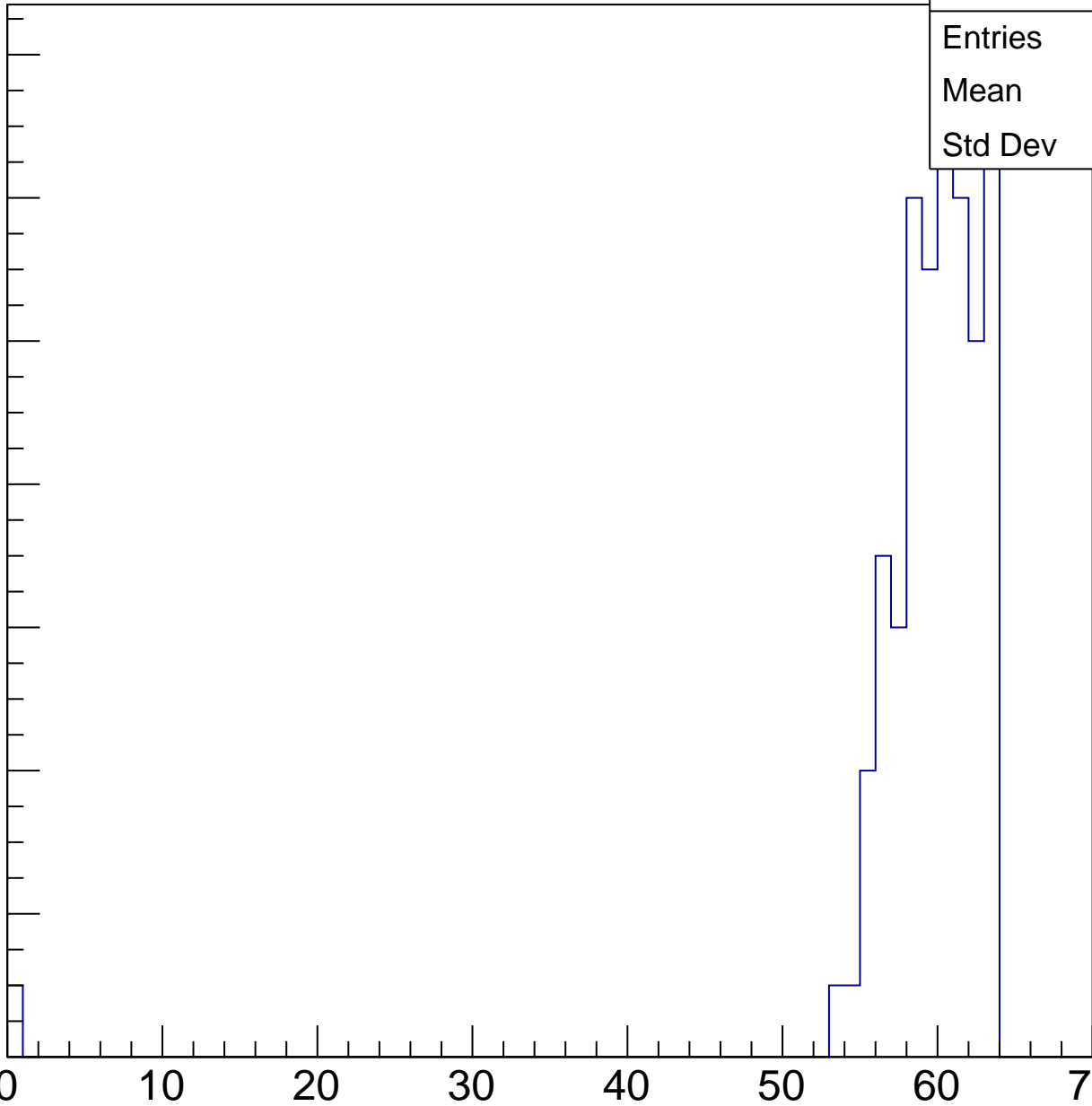
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	92
Mean	58.88
Std Dev	6.646

ampl

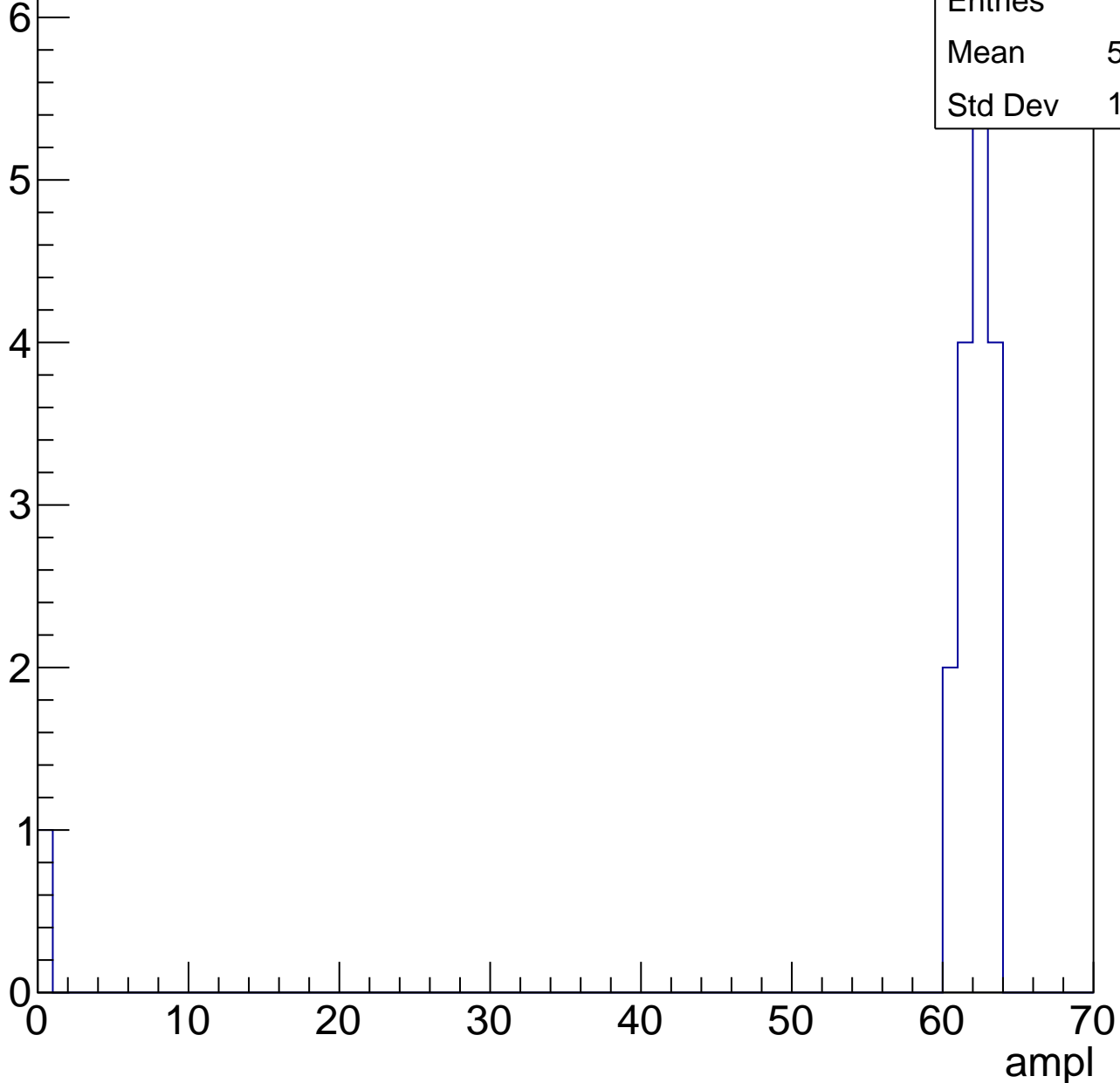


# B0L101S, U26-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	17
Mean	58.12
Std Dev	14.56

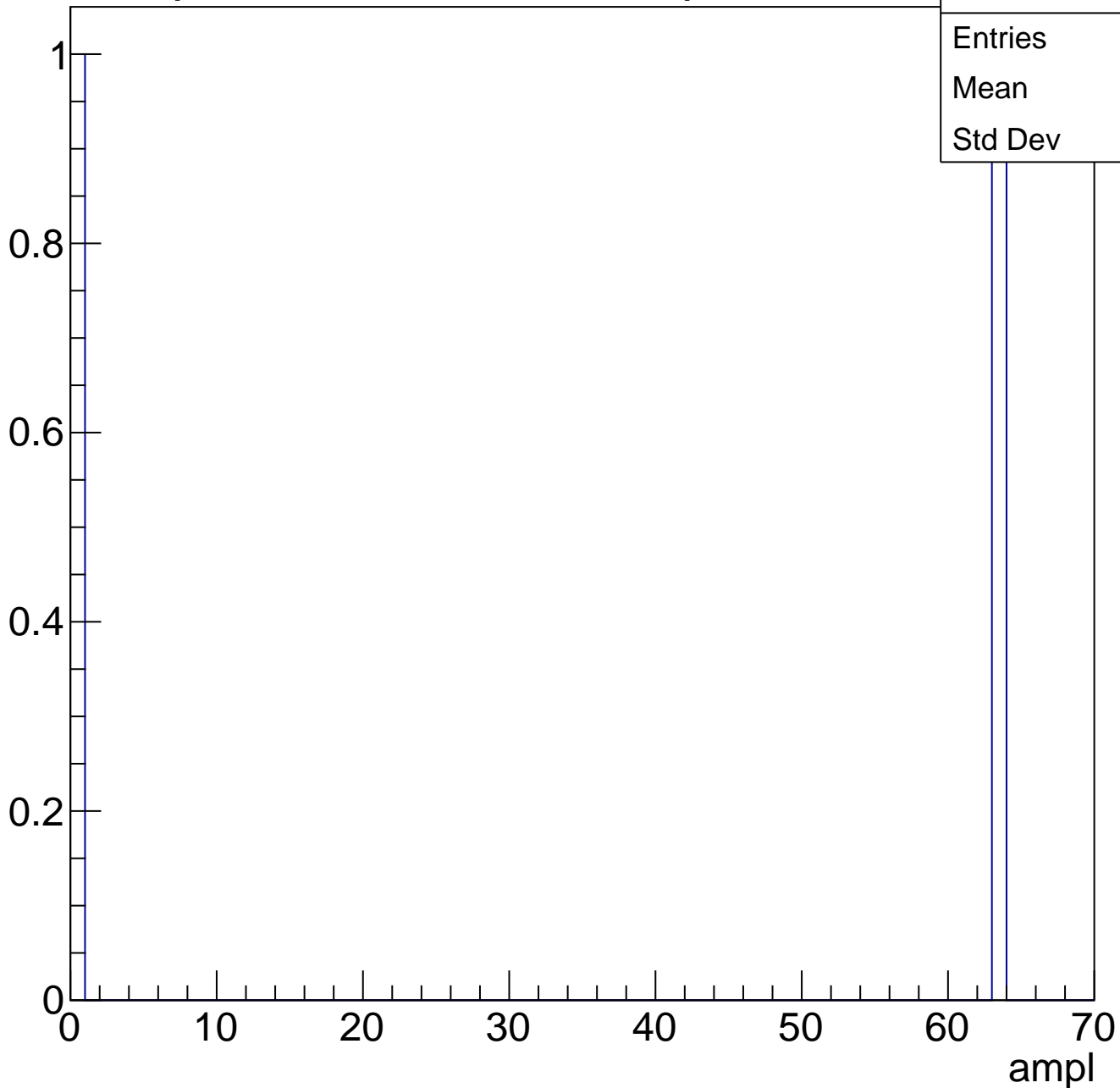




# B0L101S, U26-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

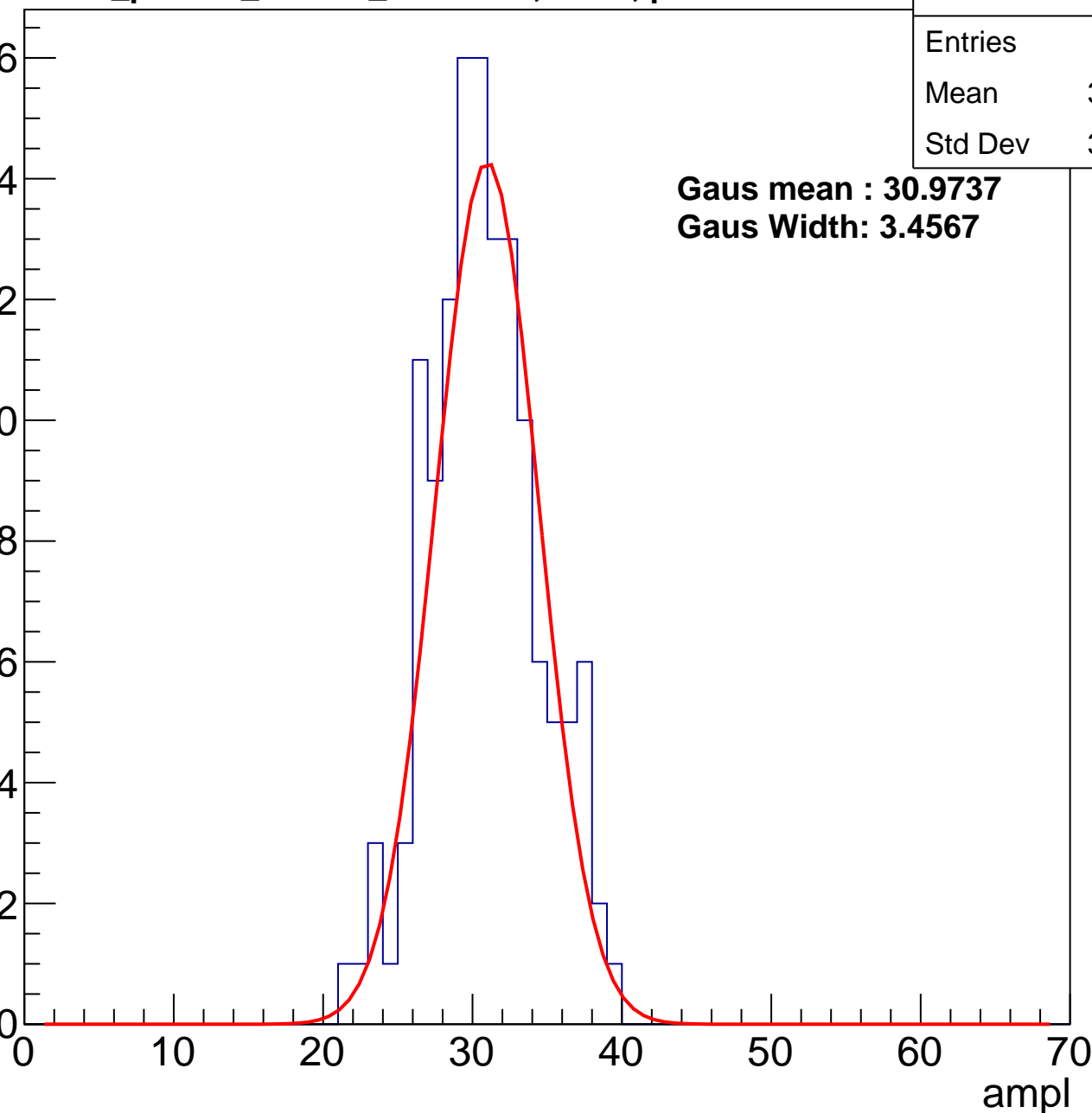
Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	134
Mean	30.34
Std Dev	3.632

**Gaus mean : 30.9737**

**Gaus Width: 3.4567**



# B0L101S, U26-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	184
Mean	38.09
Std Dev	4.124

**Gaus mean : 38.3241**

**Gaus Width: 4.2432**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

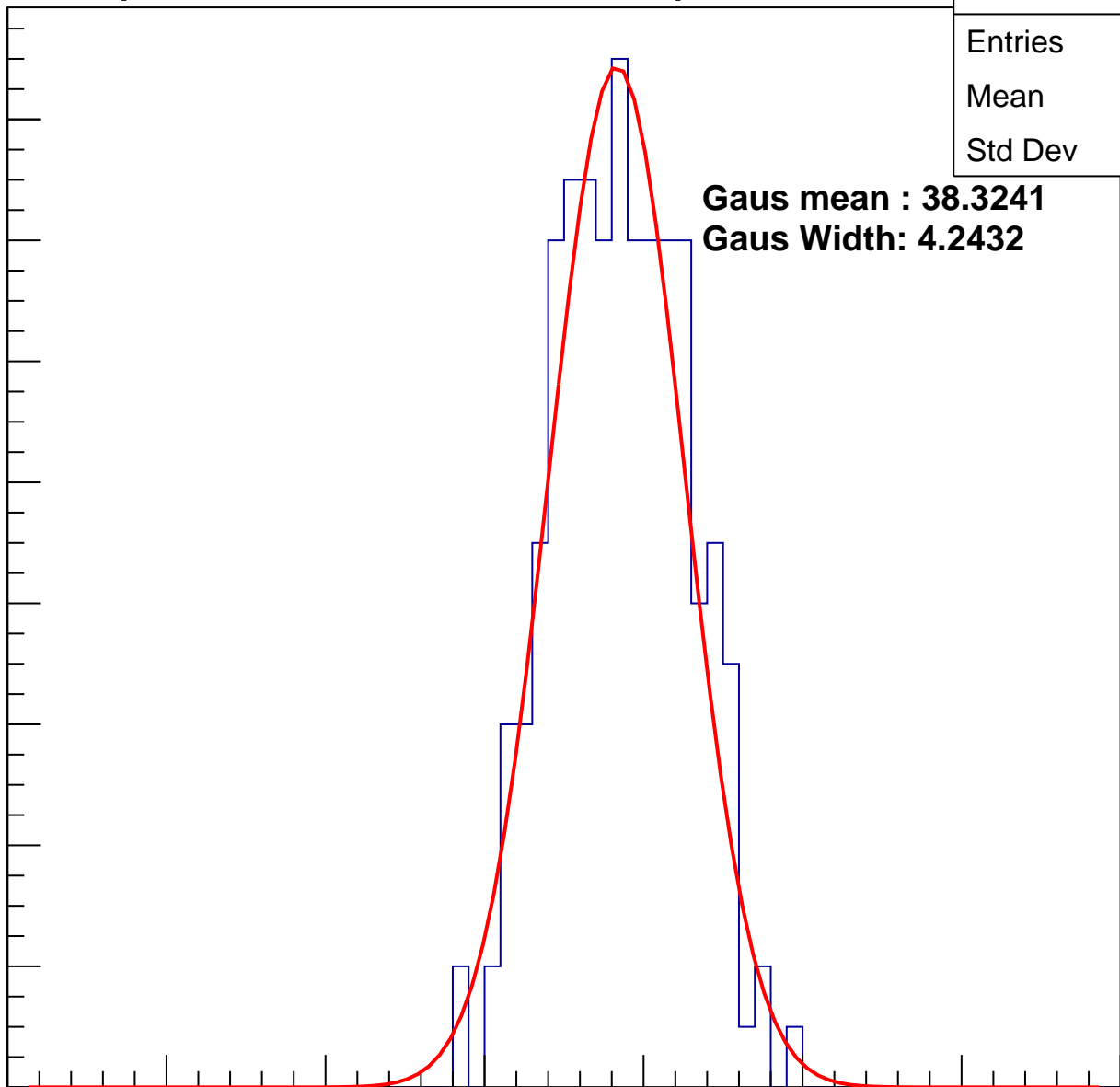
40

50

60

70

ampl



# B0L101S, U26-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

110

Mean

45.29

Std Dev

3.375

Gaus mean : 45.5339

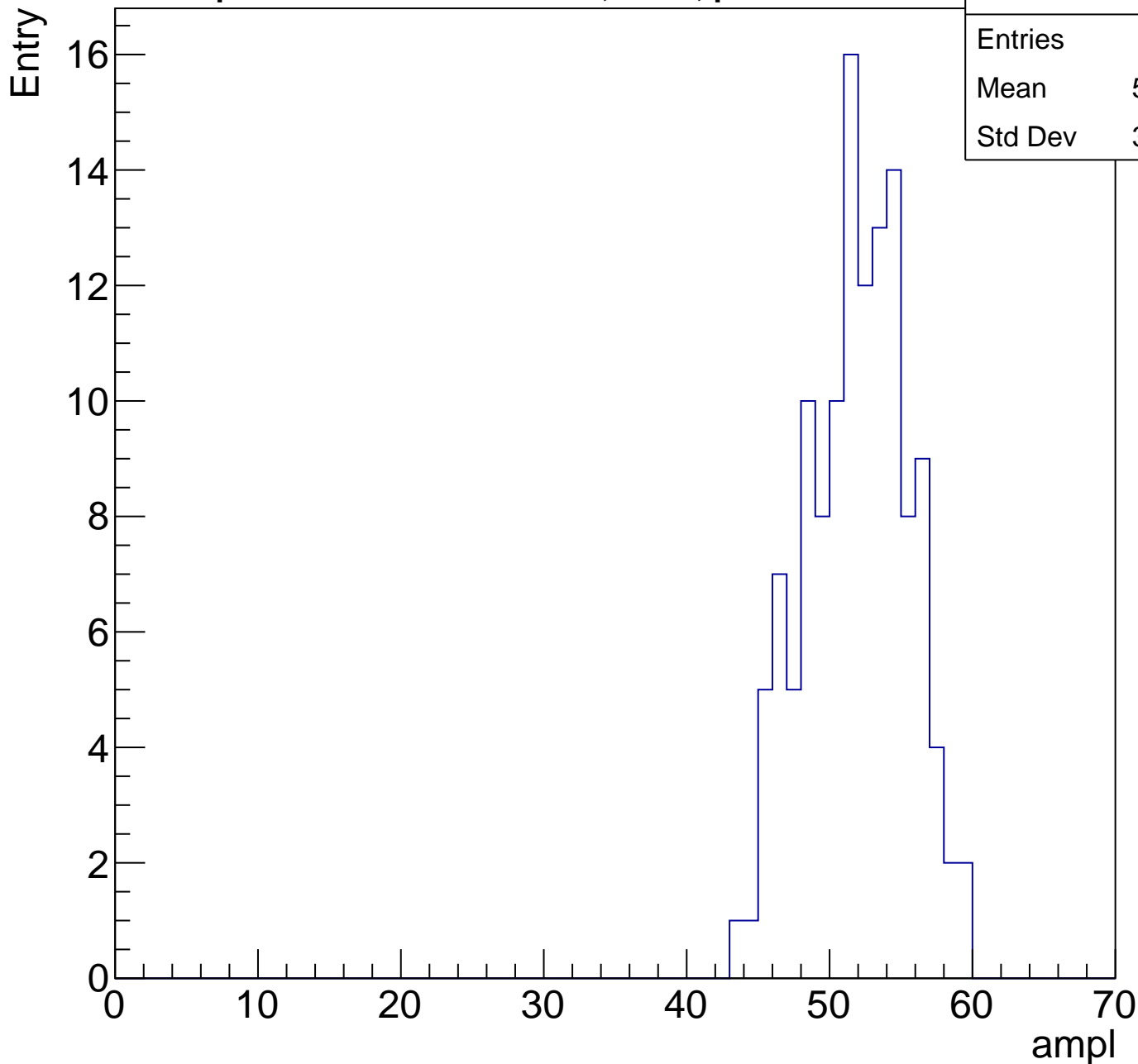
Gaus Width: 3.4139

0

# B0L101S, U26-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

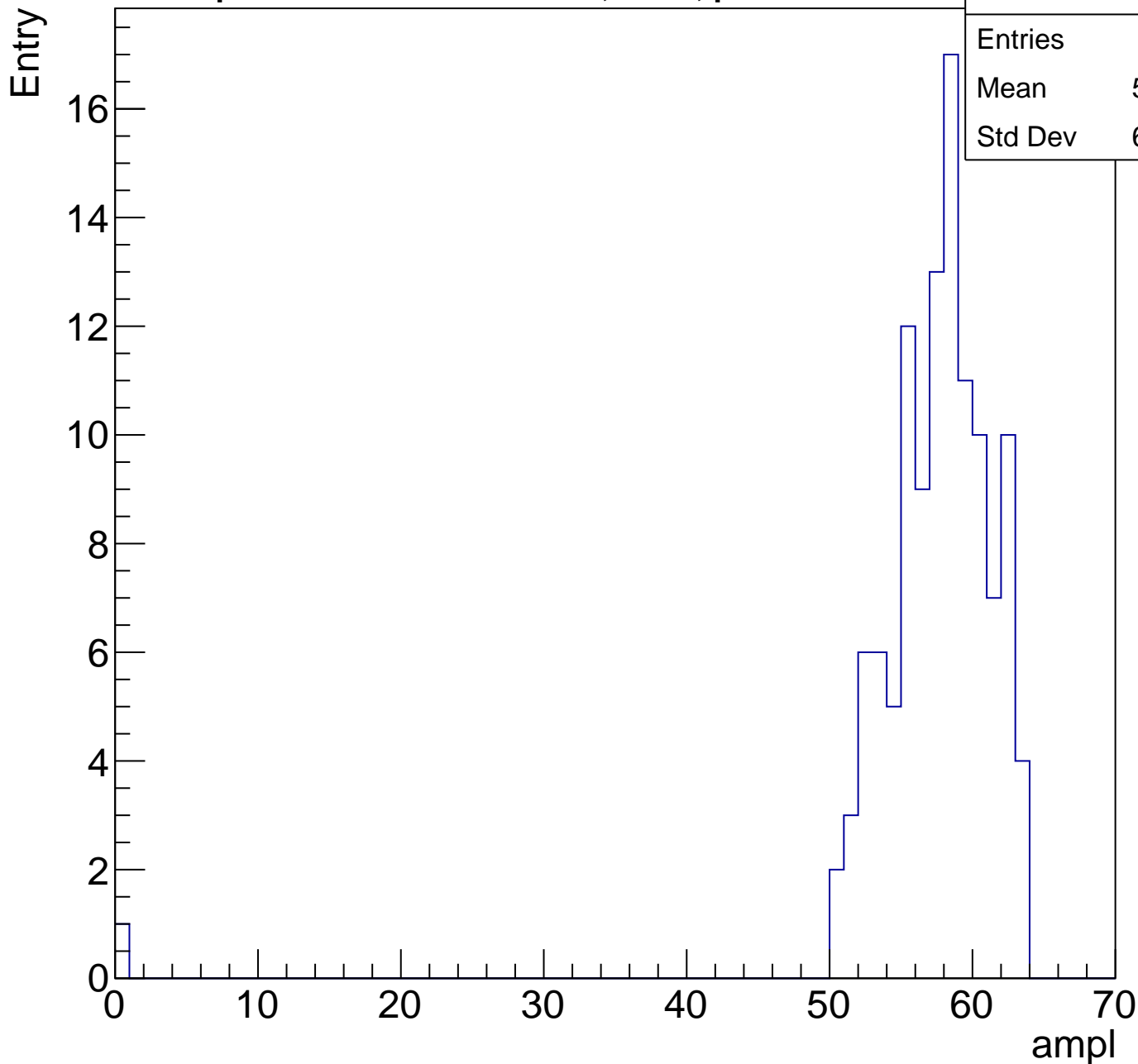
Entries	127
Mean	51.43
Std Dev	3.515



# B0L101S, U26-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	116
Mean	56.83
Std Dev	6.198



# B0L101S, U26-ch105, adc5

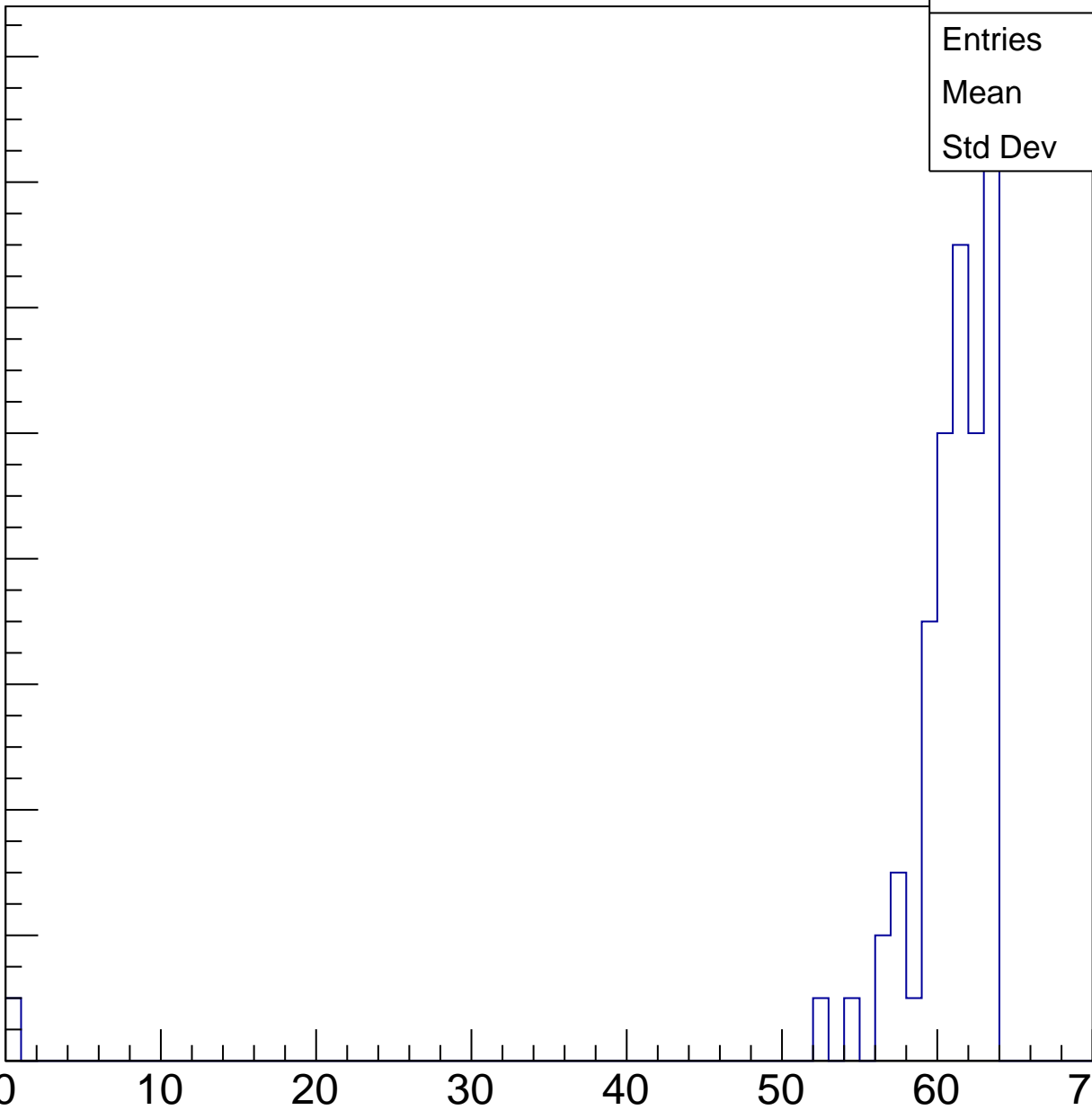
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	65
Mean	59.71
Std Dev	7.807

ampl



# B0L101S, U26-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L101S, U26-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch106, adc0

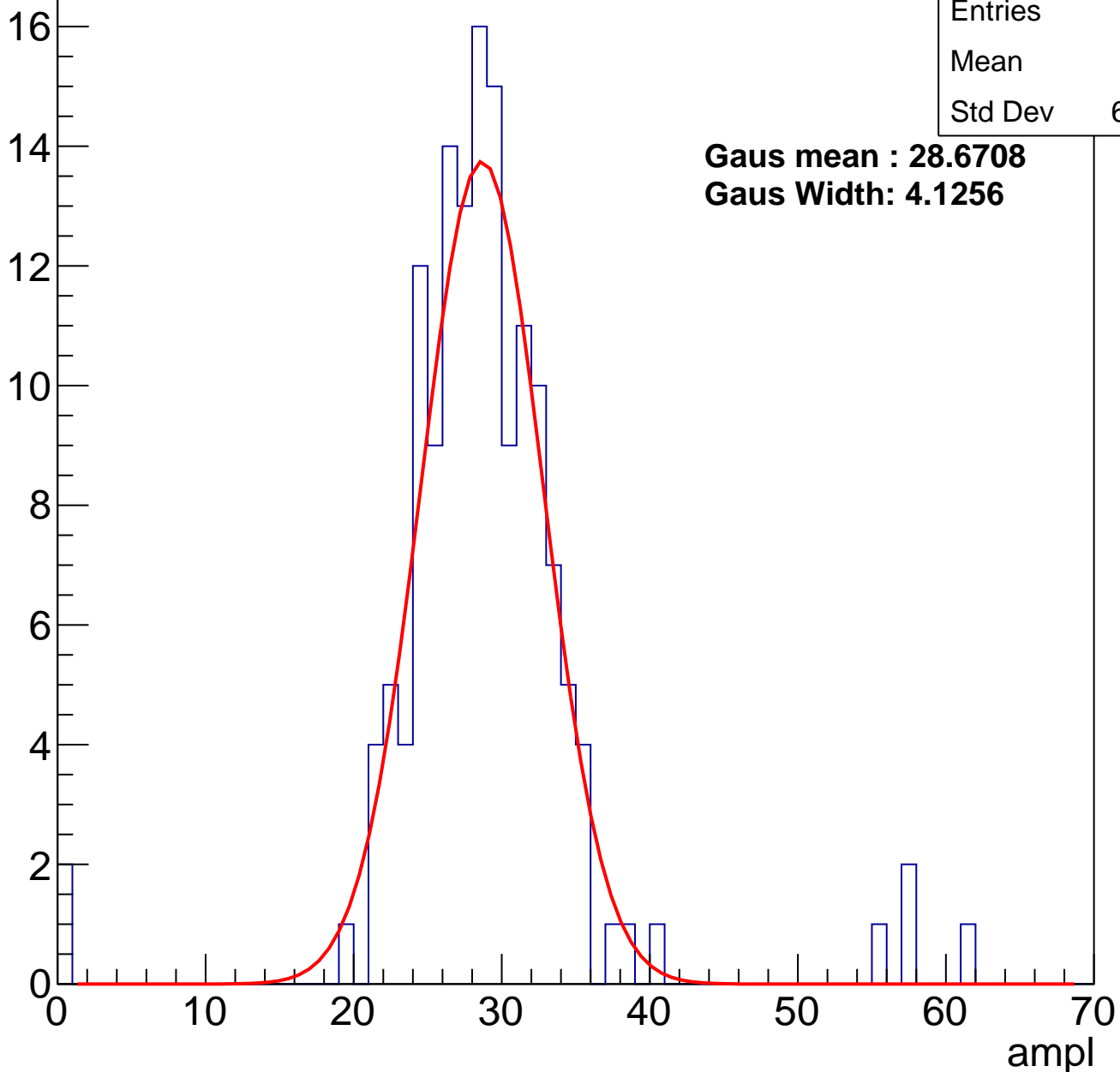
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	148
Mean	28.6
Std Dev	6.926

**Gaus mean : 28.6708**

**Gaus Width: 4.1256**

Entry



# B0L101S, U26-ch106, adc1

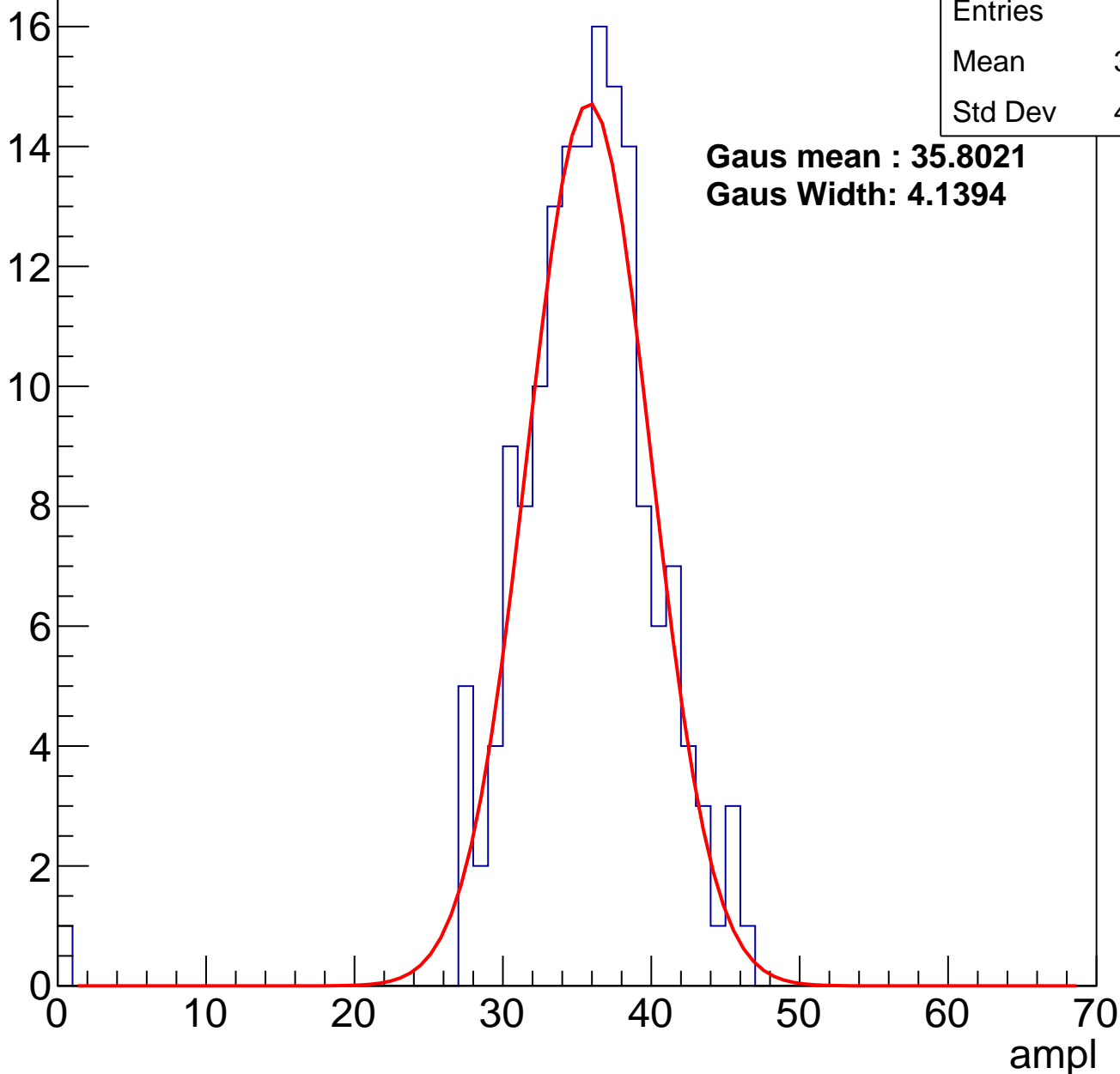
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	158
Mean	35.22
Std Dev	4.975

**Gaus mean : 35.8021**

**Gaus Width: 4.1394**

Entry



# B0L101S, U26-ch106, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	132
Mean	42.58
Std Dev	3.906

**Gaus mean : 43.1106**

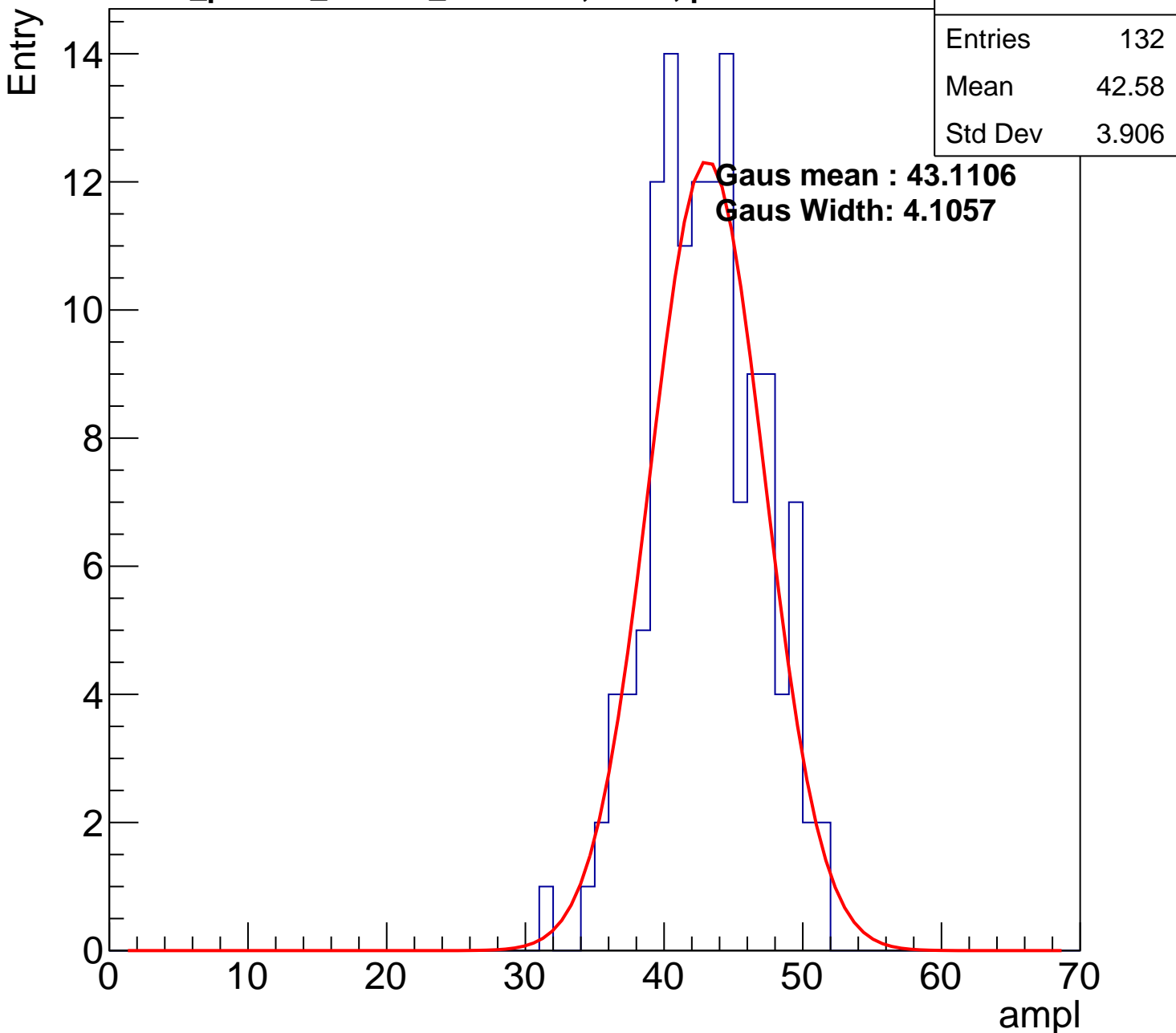
**Gaus Width: 4.1057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

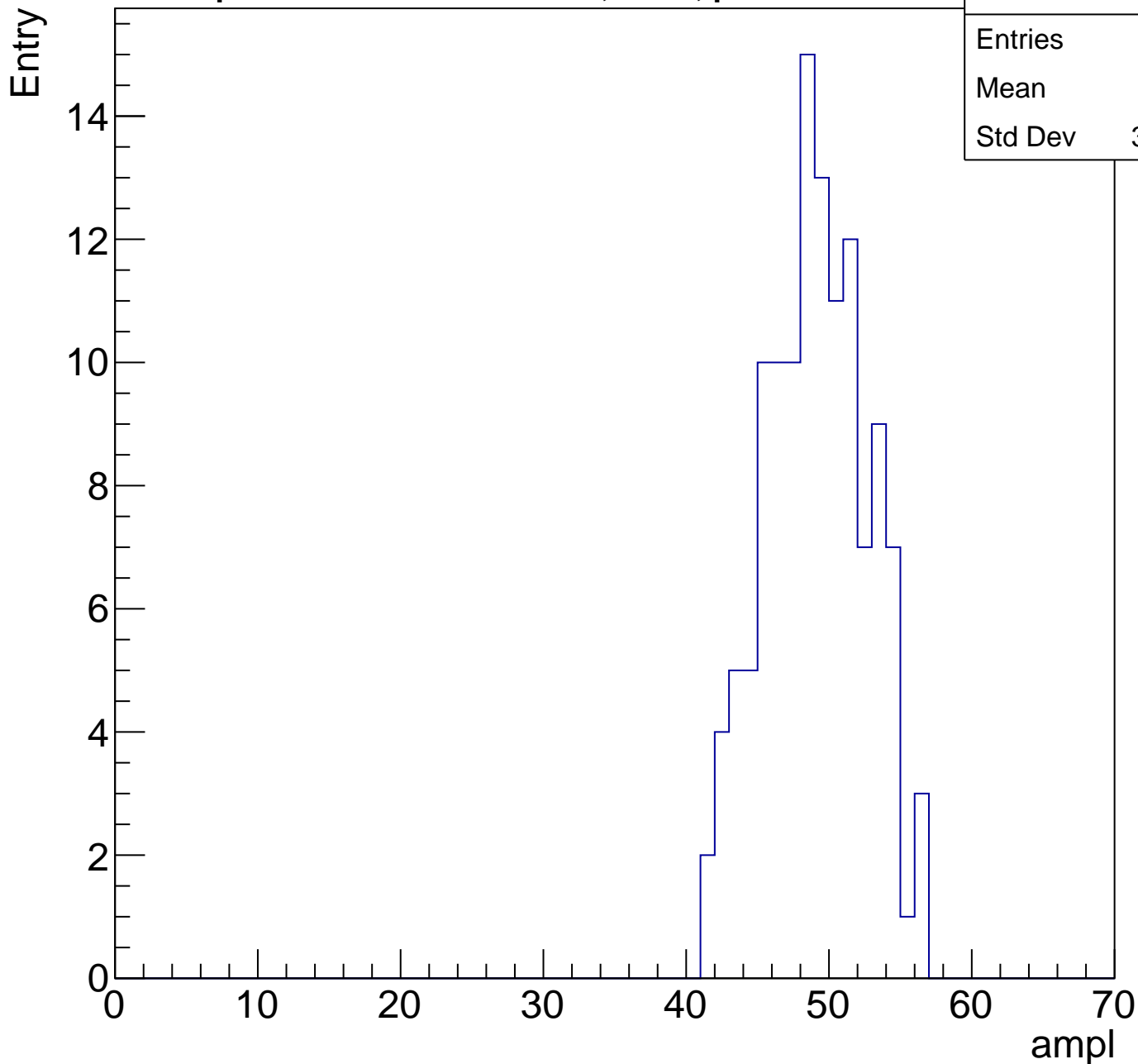
0 10 20 30 40 50 60 70



# B0L101S, U26-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

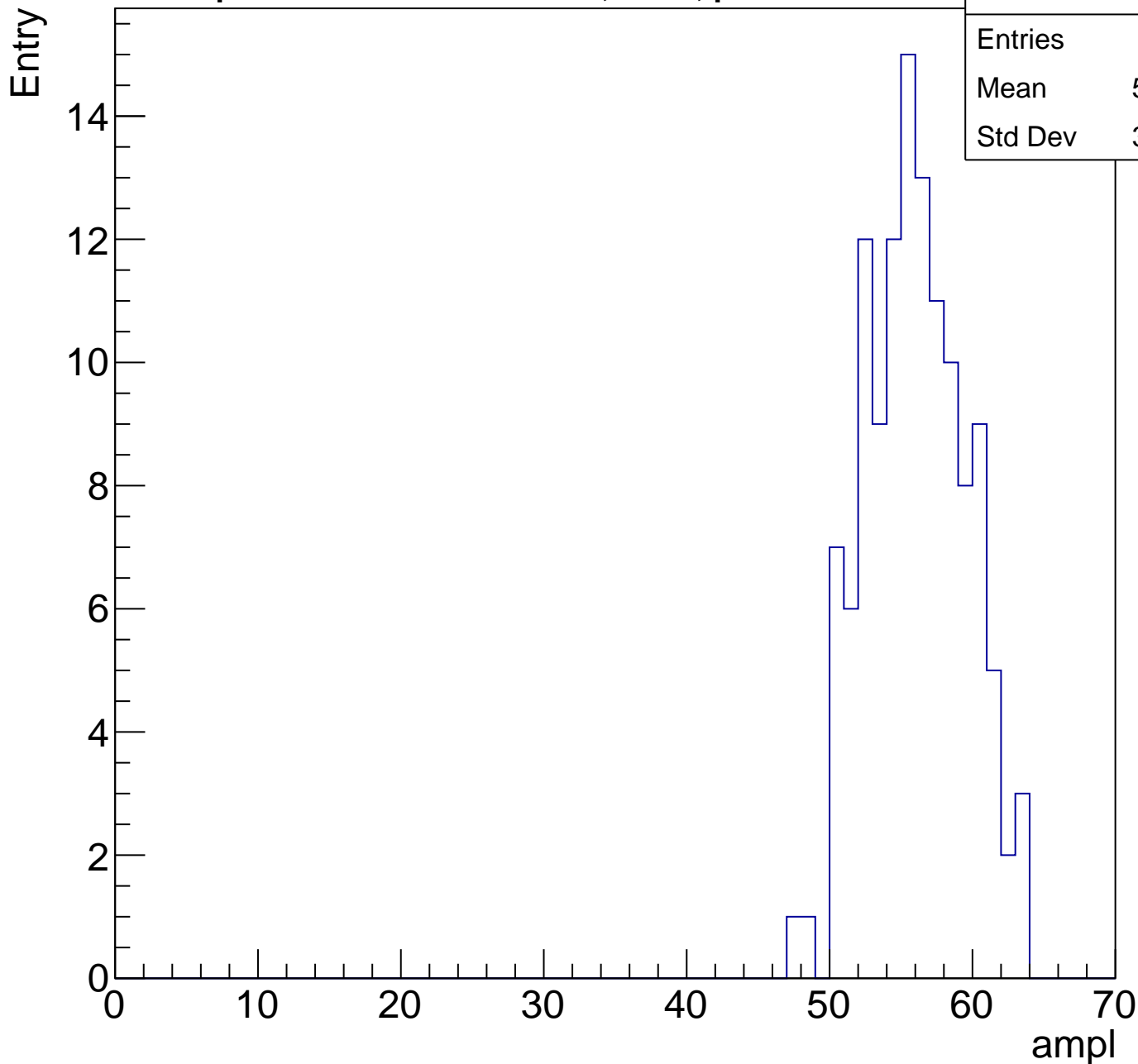
Entries	124
Mean	48.6
Std Dev	3.522



# B0L101S, U26-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	55.56
Std Dev	3.444





# B0L101S, U26-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

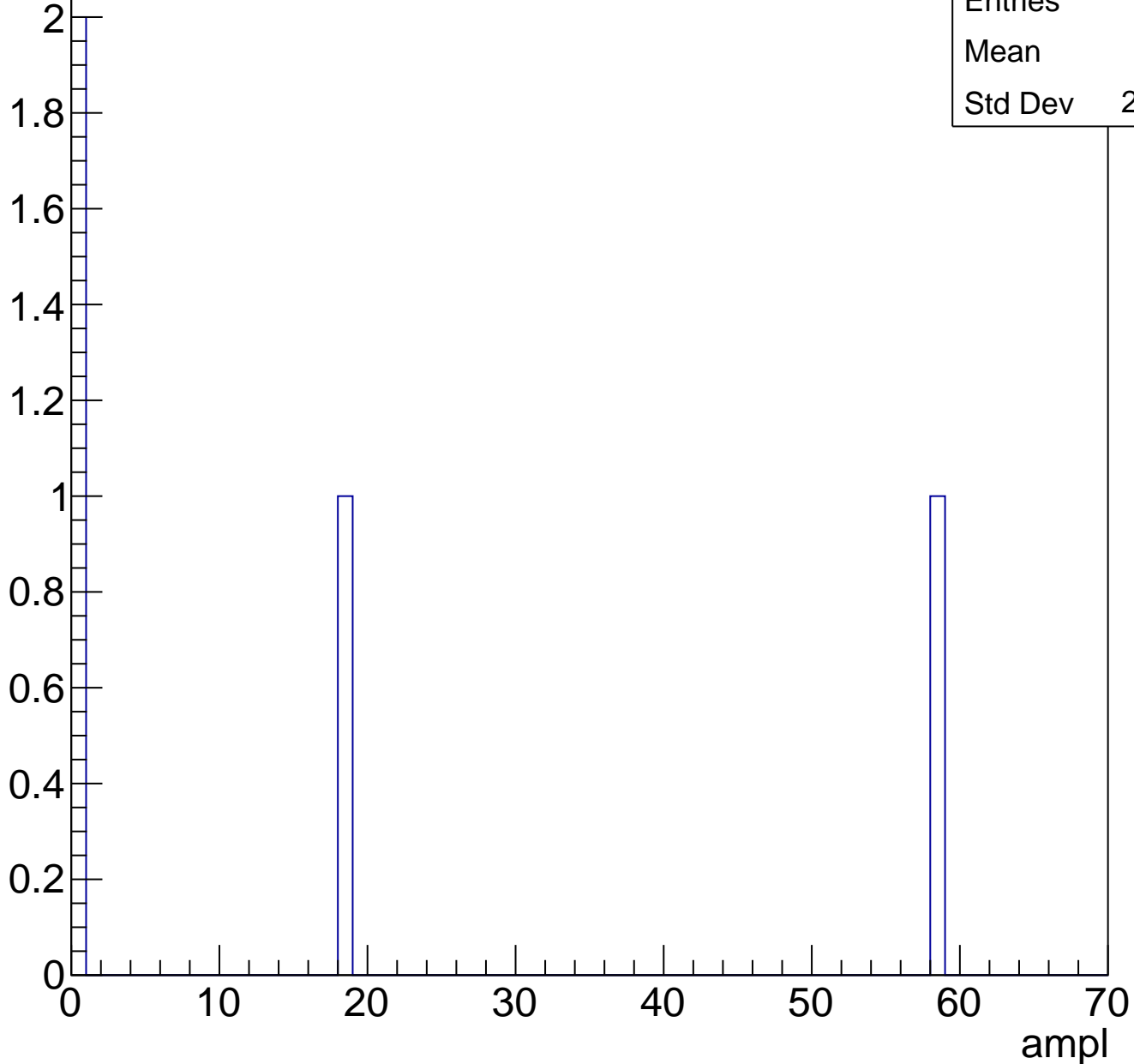
Entries	31
Mean	59.58
Std Dev	11



# B0L101S, U26-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	145
Mean	28.83
Std Dev	3.428

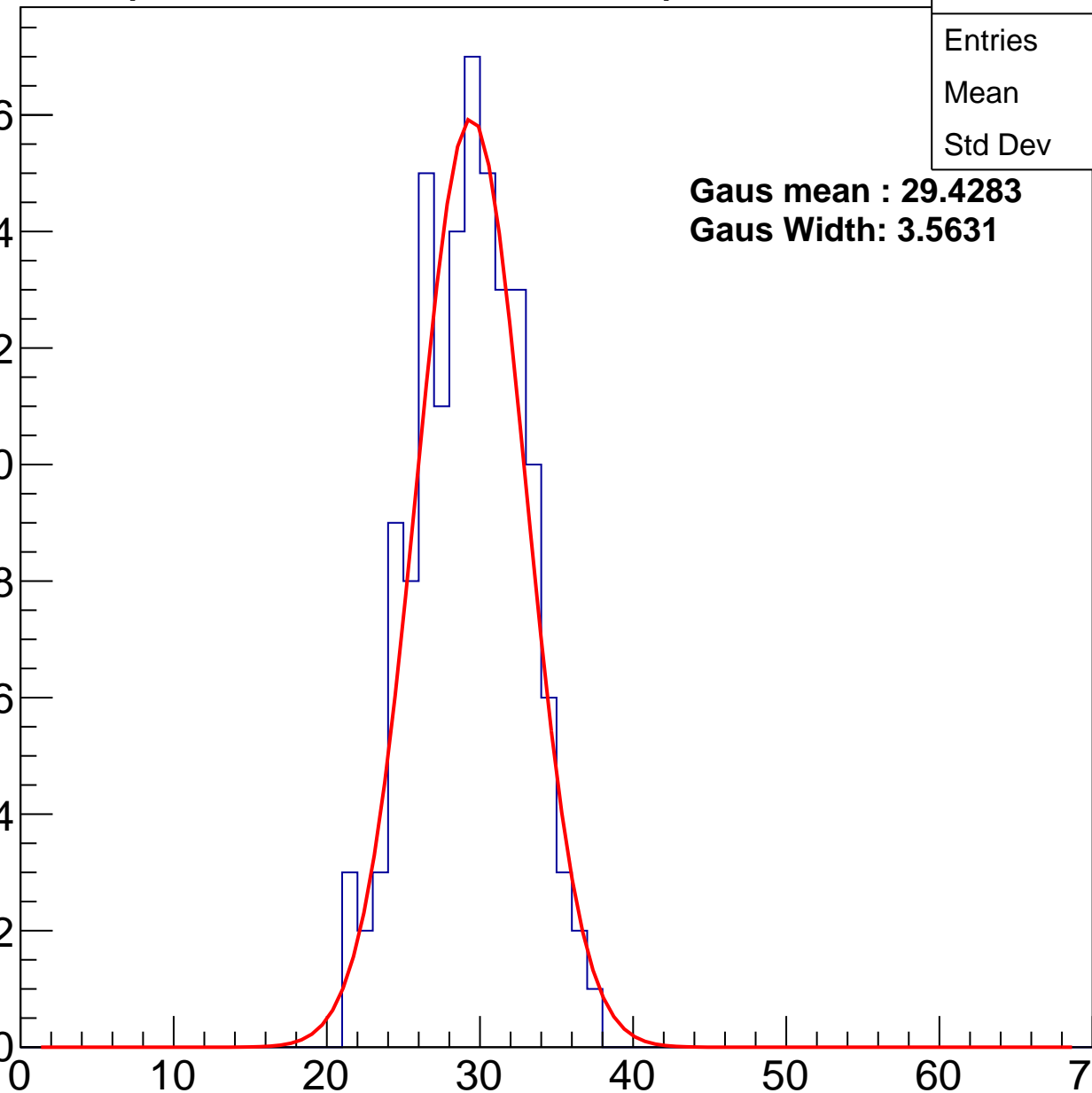
**Gaus mean : 29.4283**

**Gaus Width: 3.5631**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	37.08
Std Dev	4.006

**Gaus mean : 37.5855**

**Gaus Width: 3.9449**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

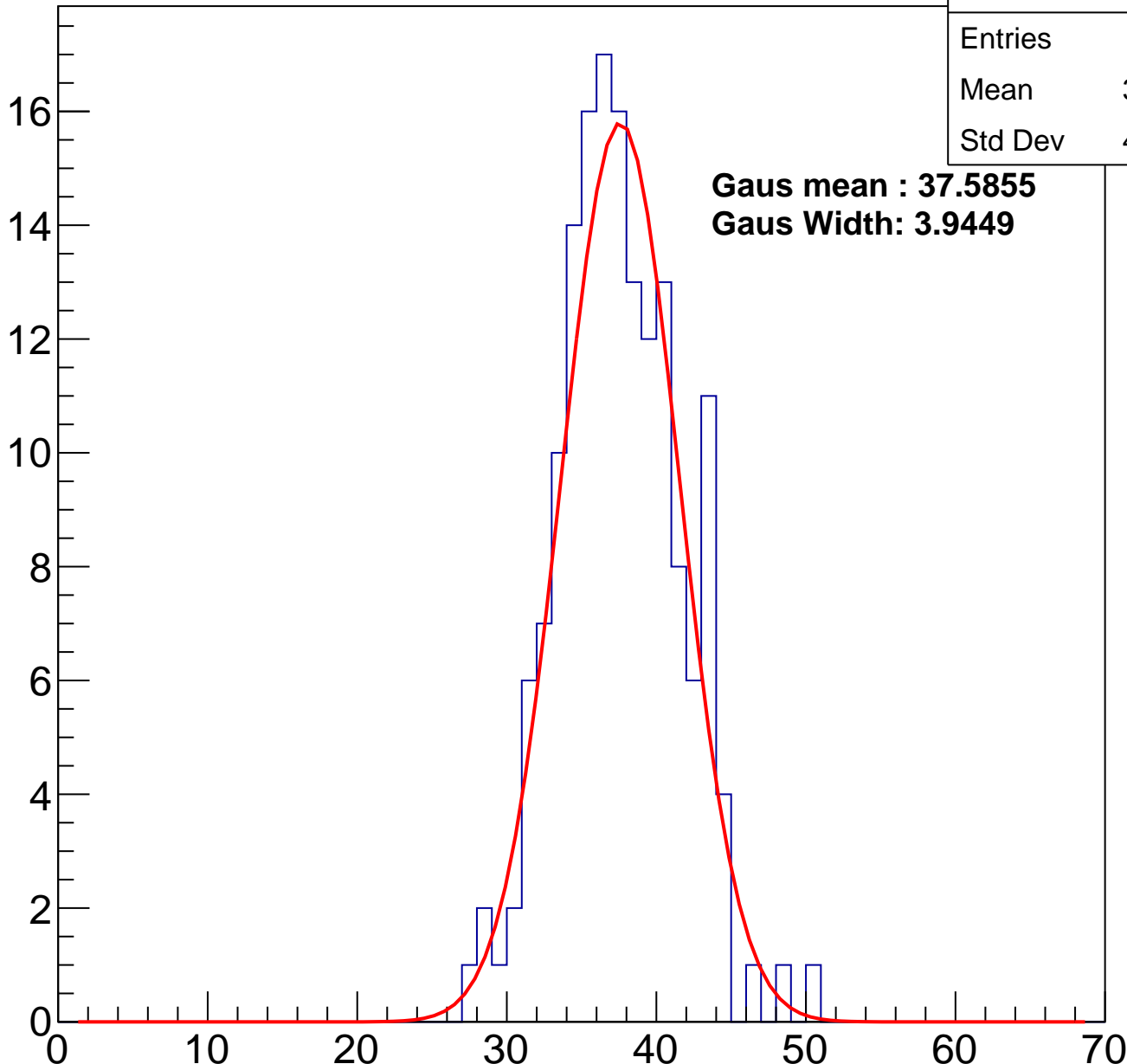
40

50

60

70

ampl



# B0L101S, U26-ch107, adc2

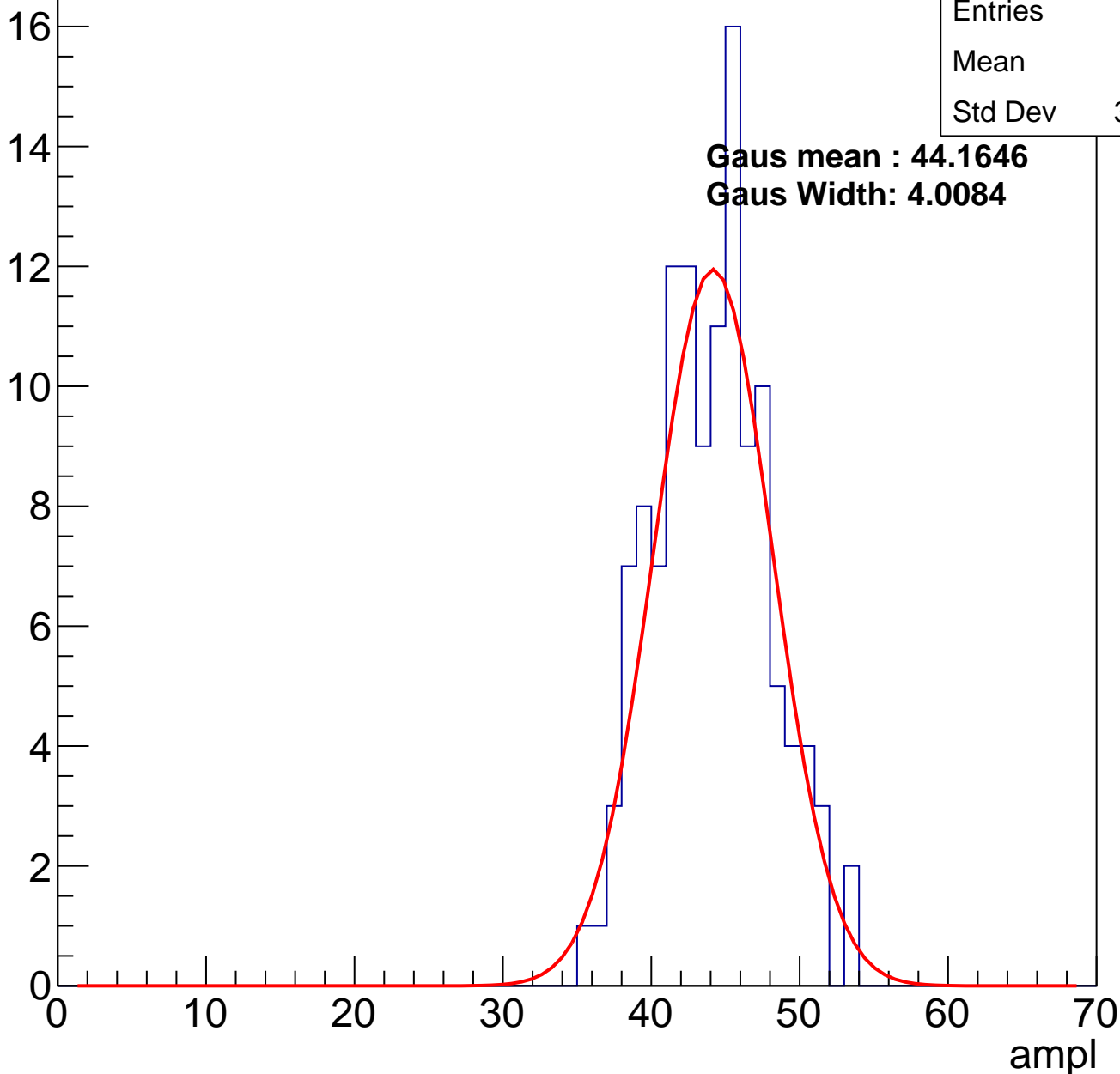
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	43.6
Std Dev	3.784

**Gaus mean : 44.1646**

**Gaus Width: 4.0084**

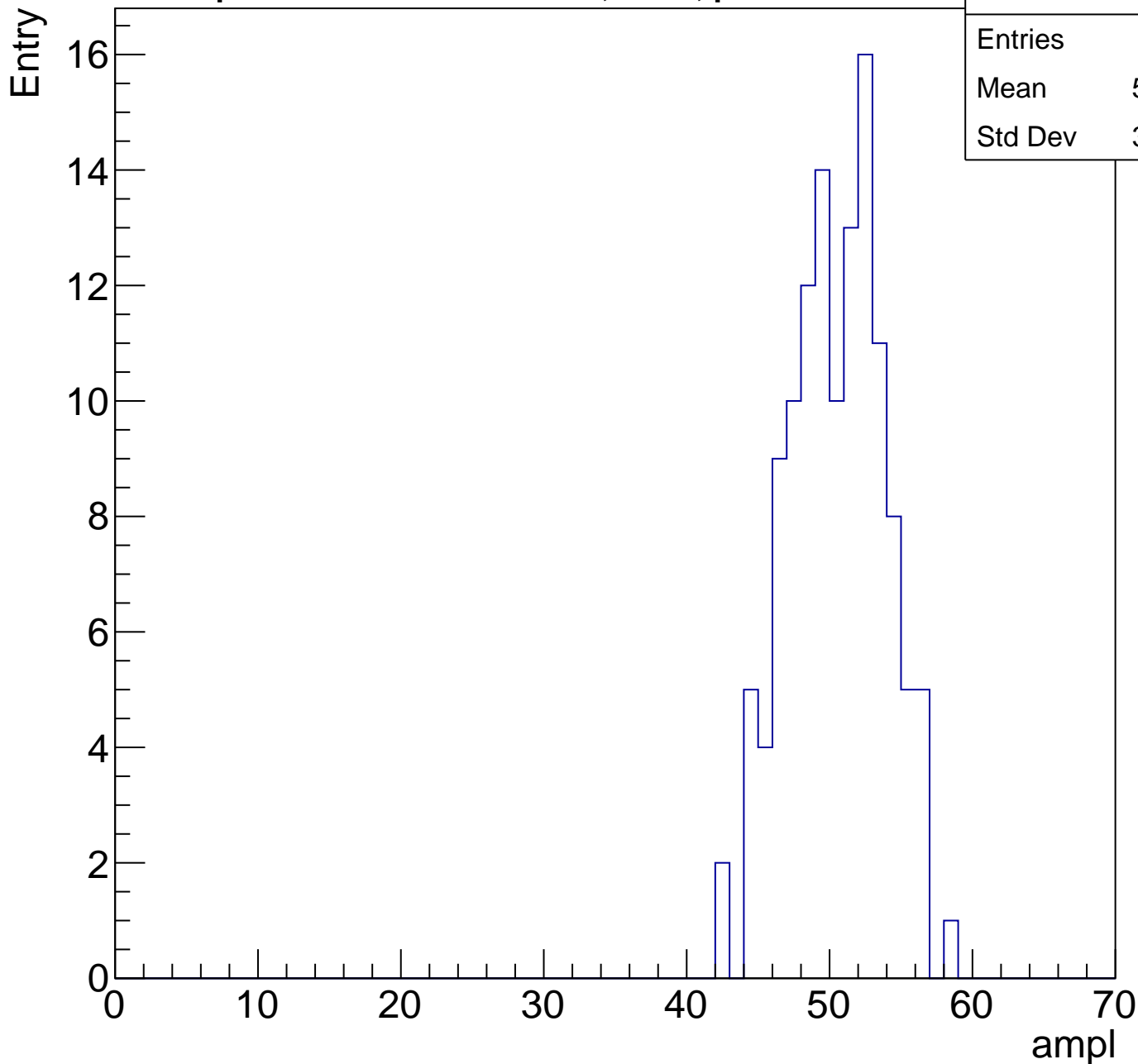
Entry



# B0L101S, U26-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

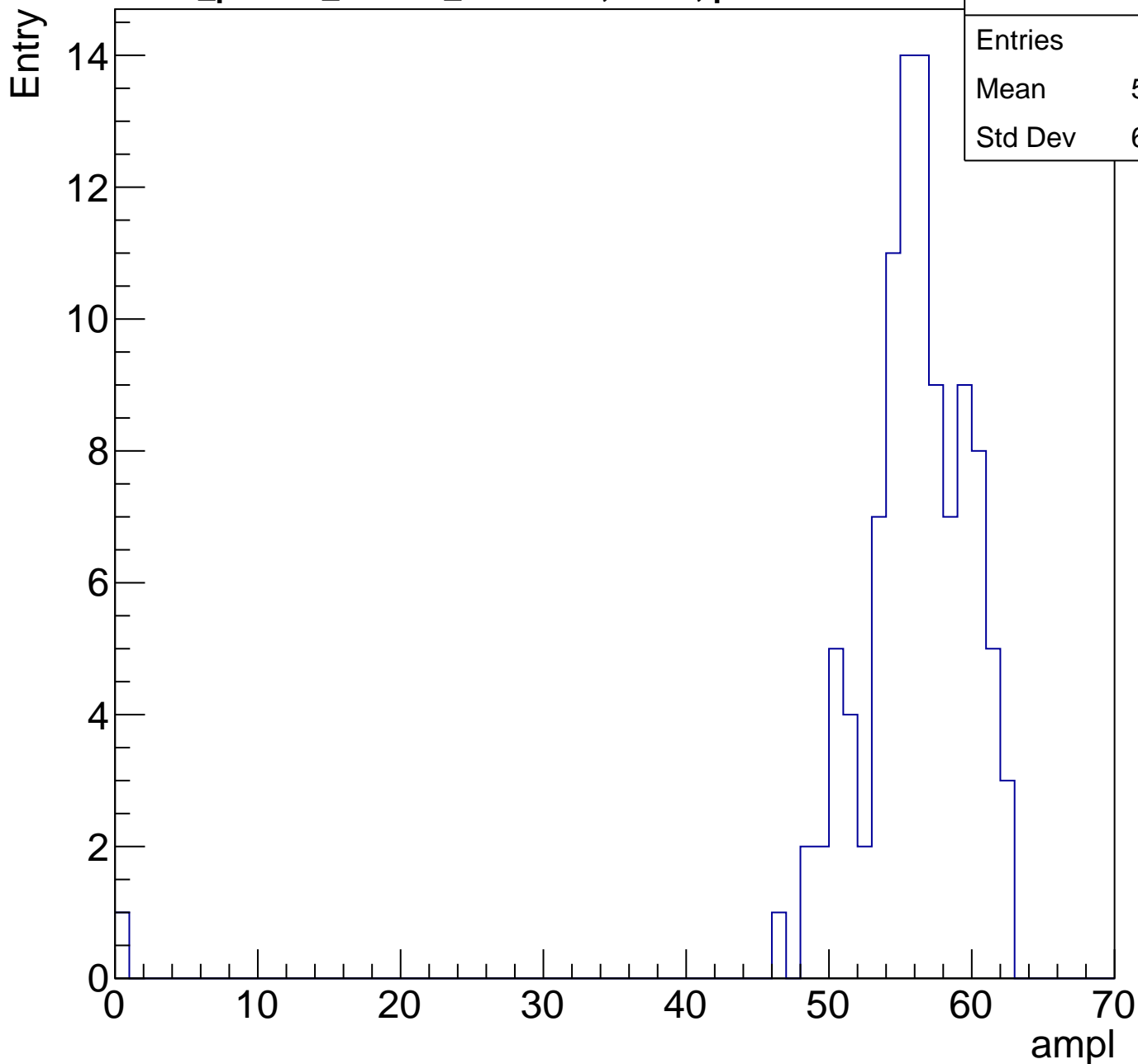
Entries	125
Mean	50.02
Std Dev	3.319



# B0L101S, U26-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	104
Mean	55.17
Std Dev	6.432



# B0L101S, U26-ch107, adc5

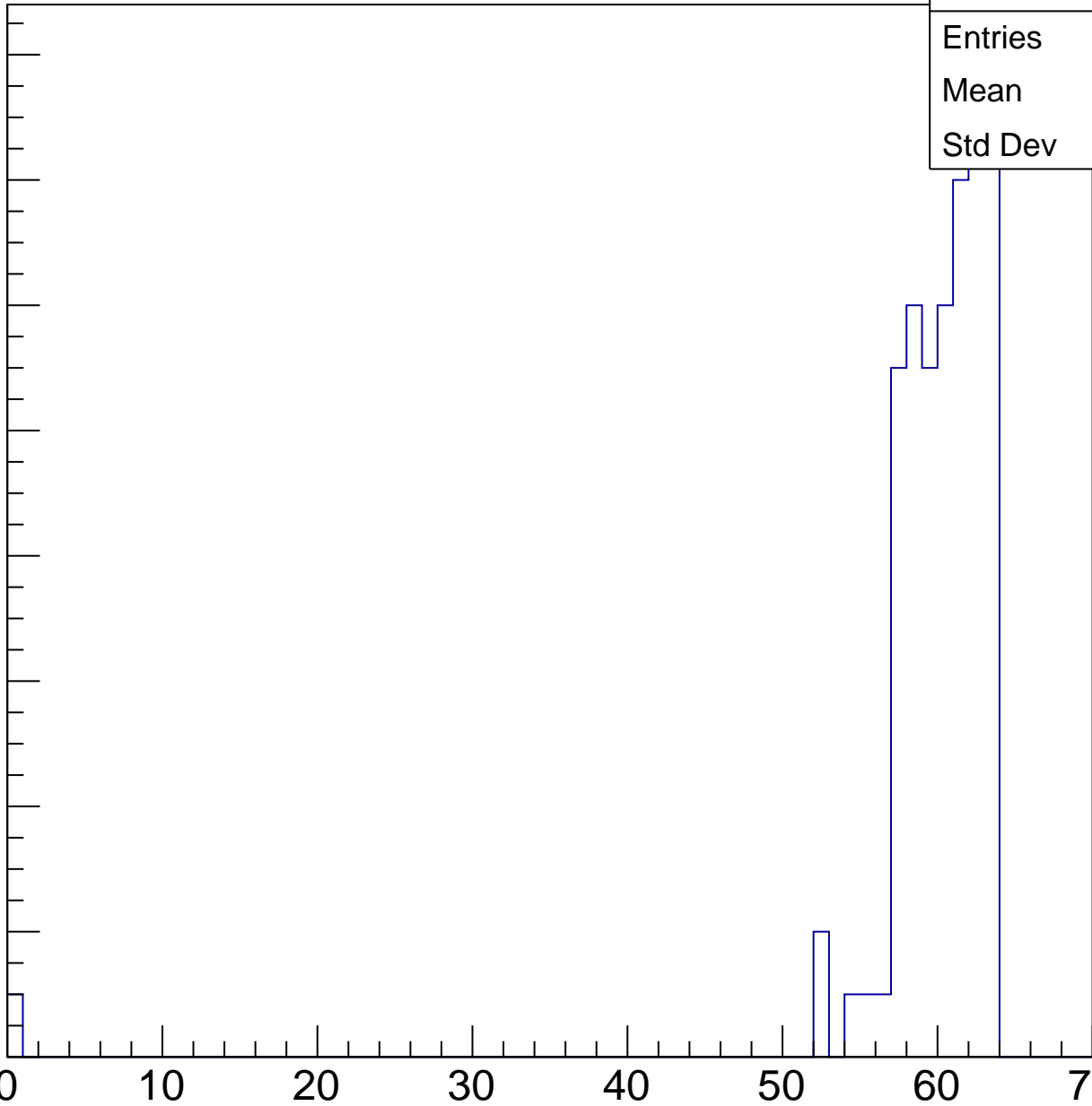
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	98
Mean	59.34
Std Dev	6.501

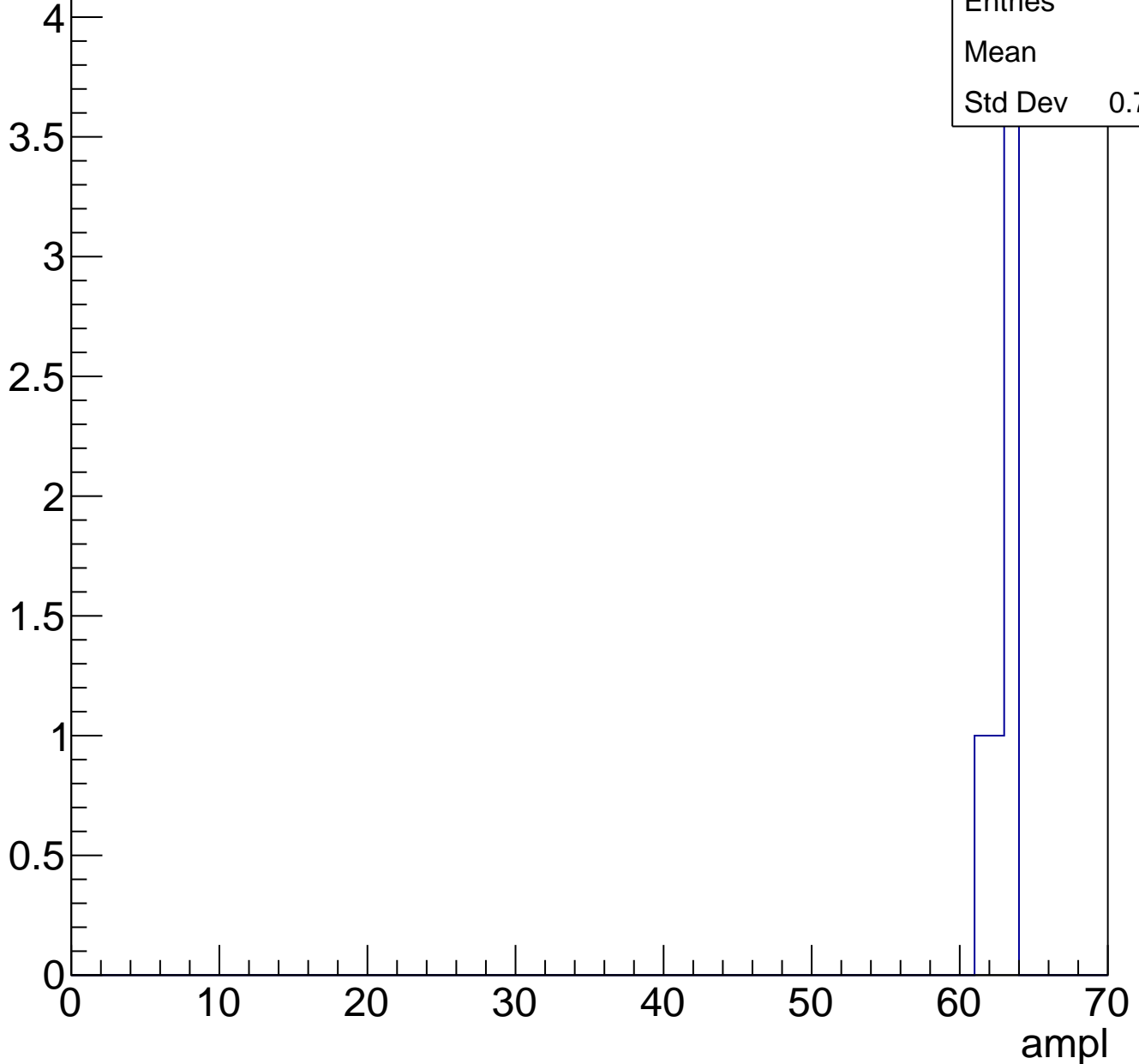
ampl



# B0L101S, U26-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch108, adc0

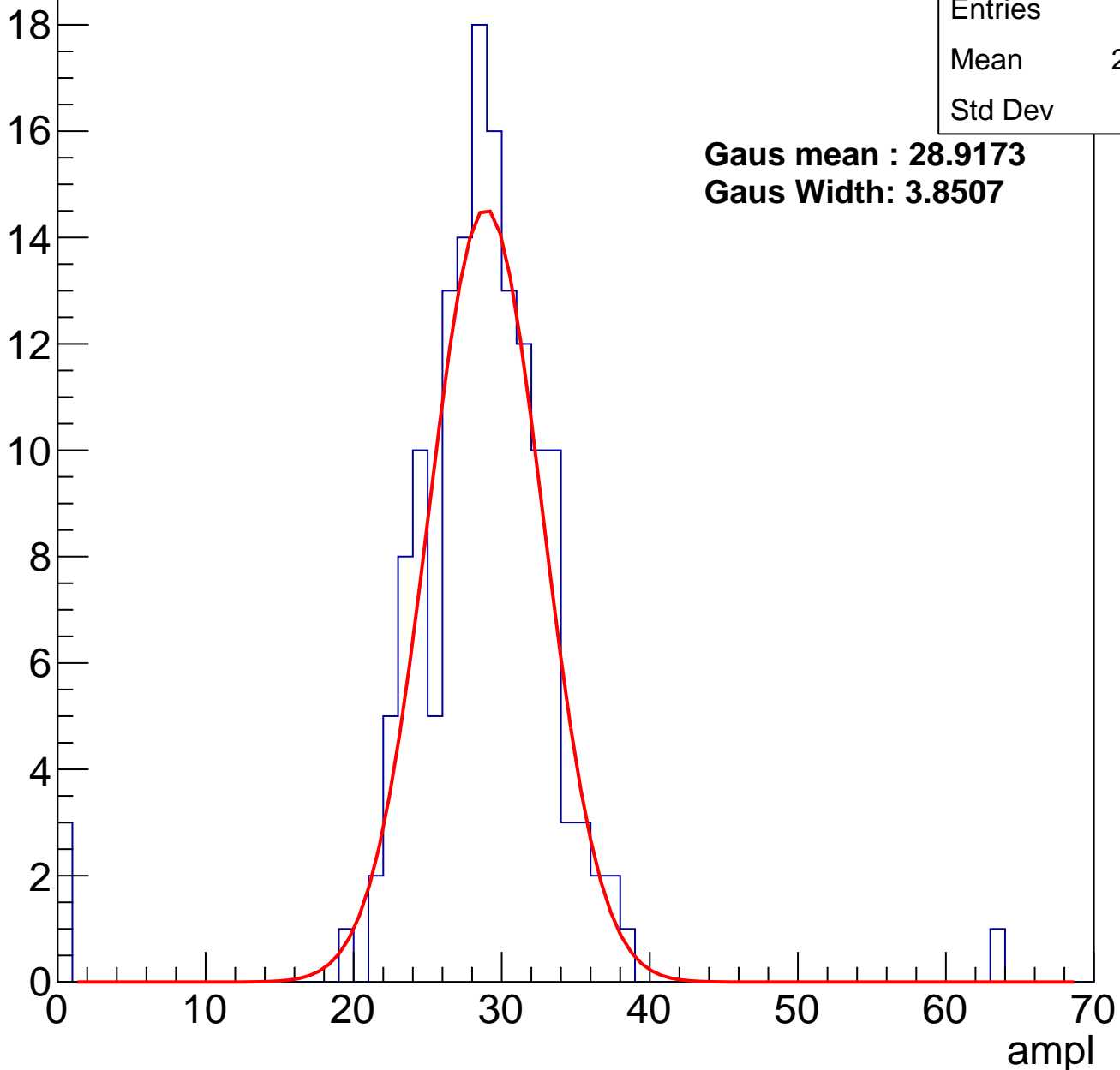
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	28.09
Std Dev	6.09

**Gaus mean : 28.9173**

**Gaus Width: 3.8507**

Entry



# B0L101S, U26-ch108, adc1

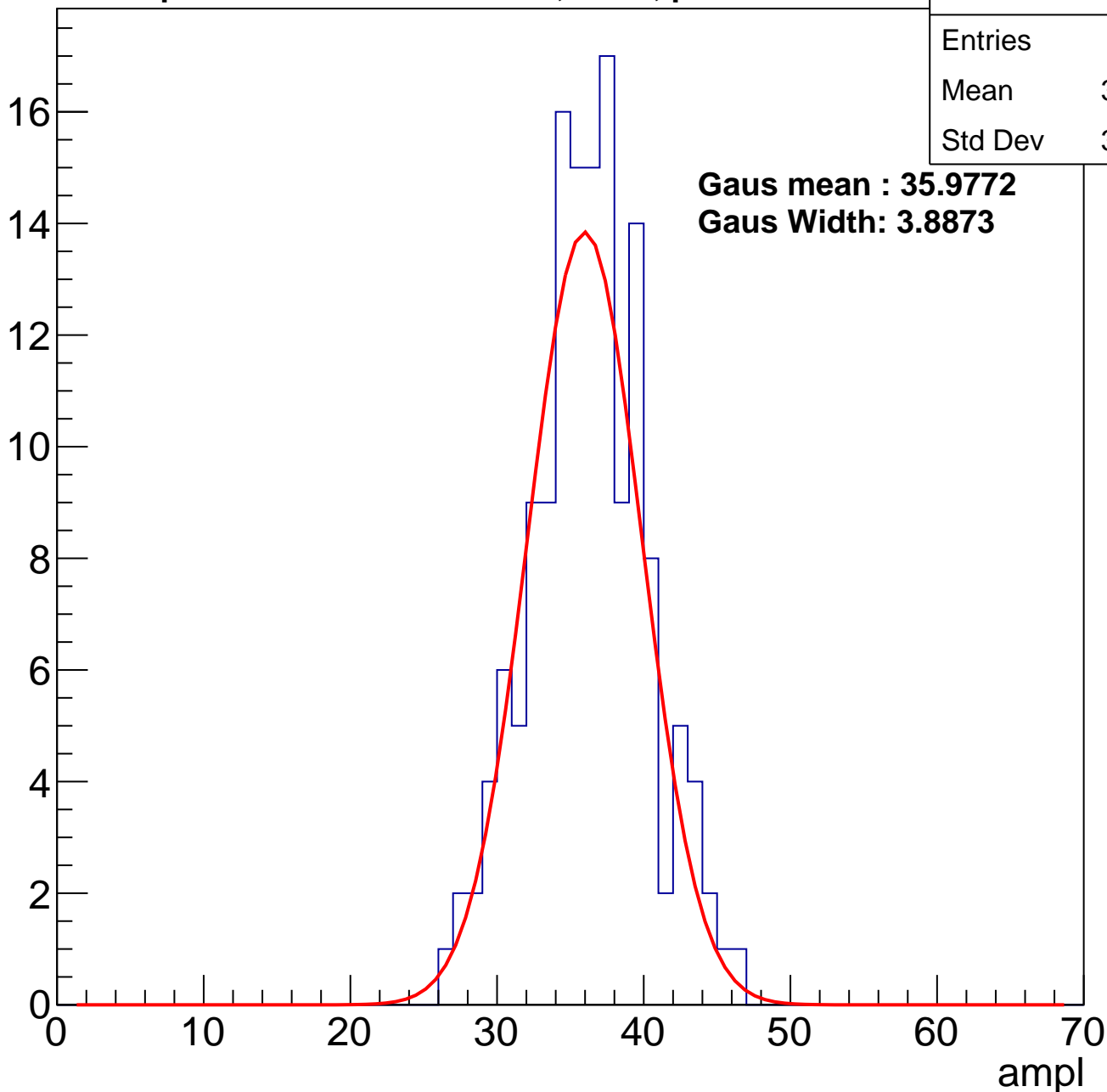
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	147
Mean	35.79
Std Dev	3.927

**Gaus mean : 35.9772**

**Gaus Width: 3.8873**

Entry



# B0L101S, U26-ch108, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

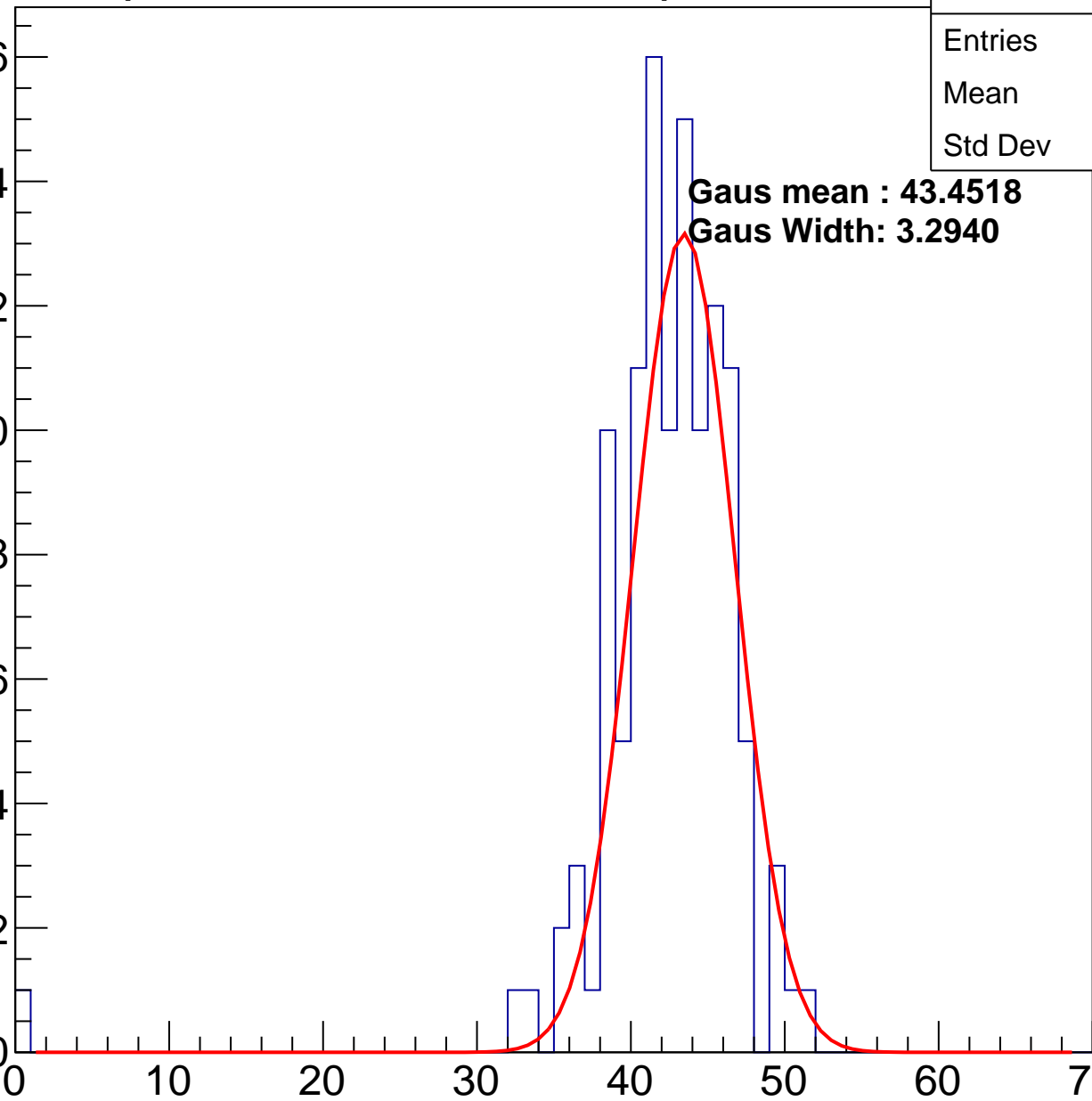
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	119
Mean	41.89
Std Dev	5.182

**Gaus mean : 43.4518**

**Gaus Width: 3.2940**

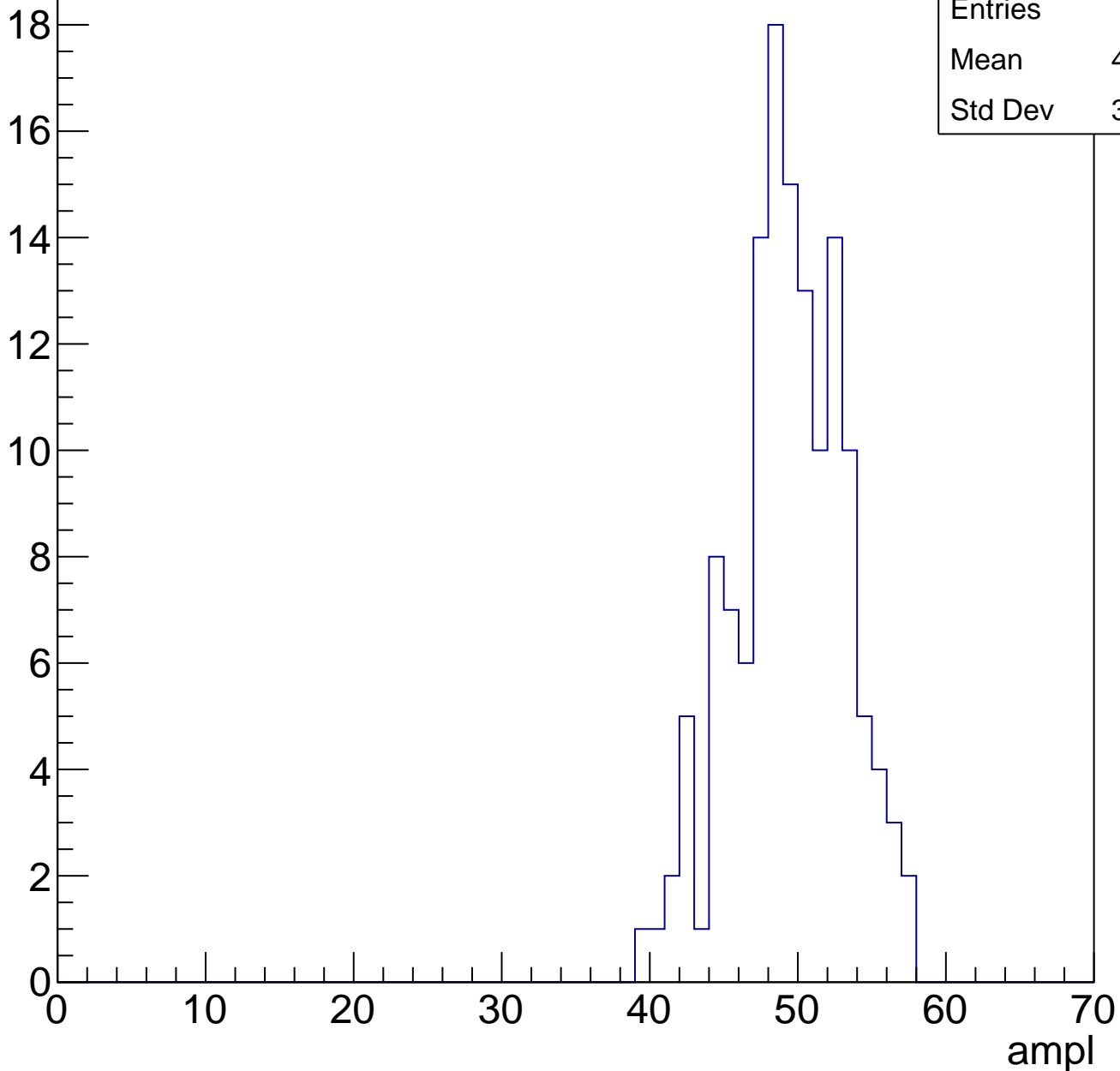
ampl



# B0L101S, U26-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

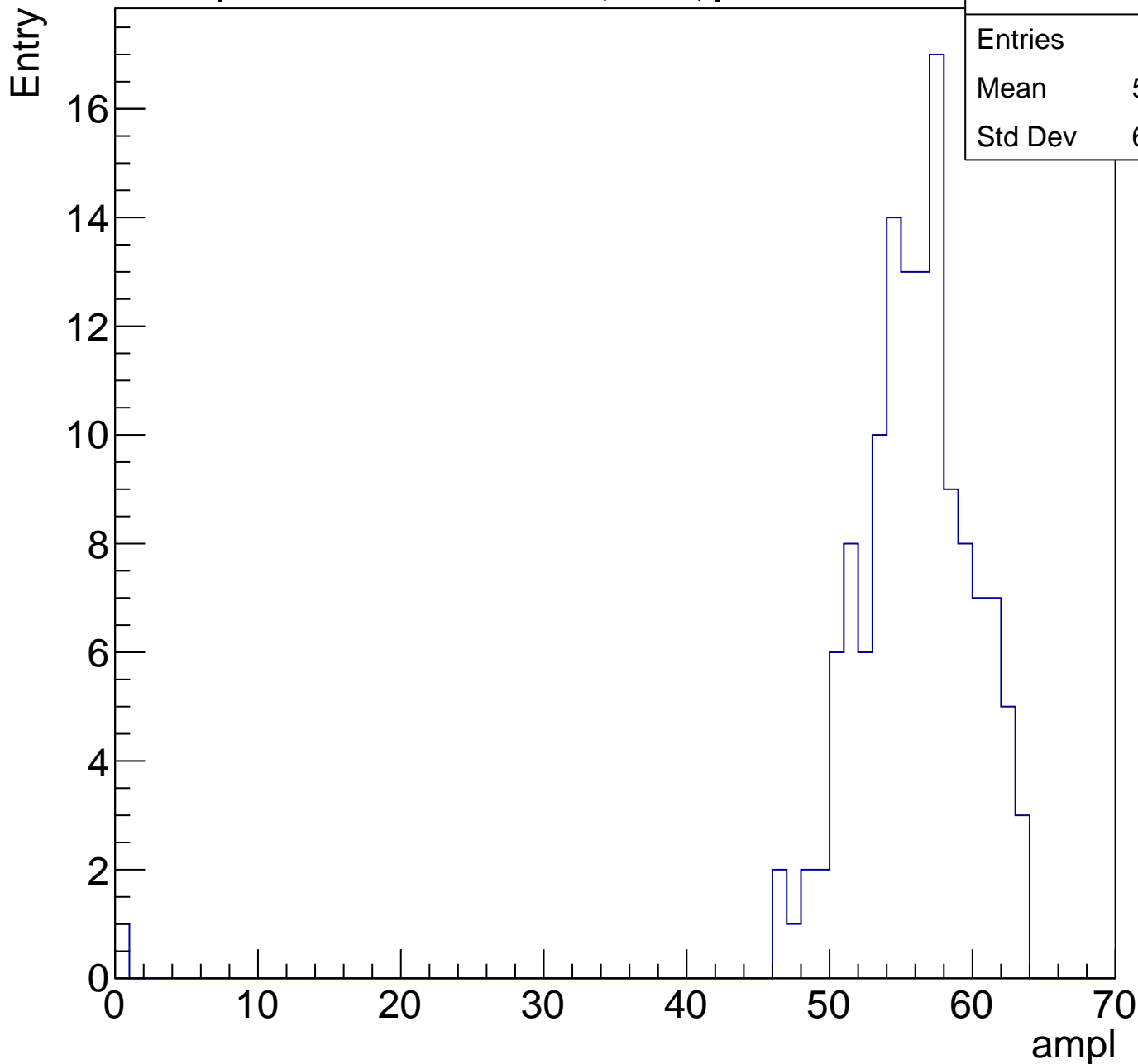
Entry



# B0L101S, U26-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	134
Mean	55.16
Std Dev	6.083



# B0L101S, U26-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

78

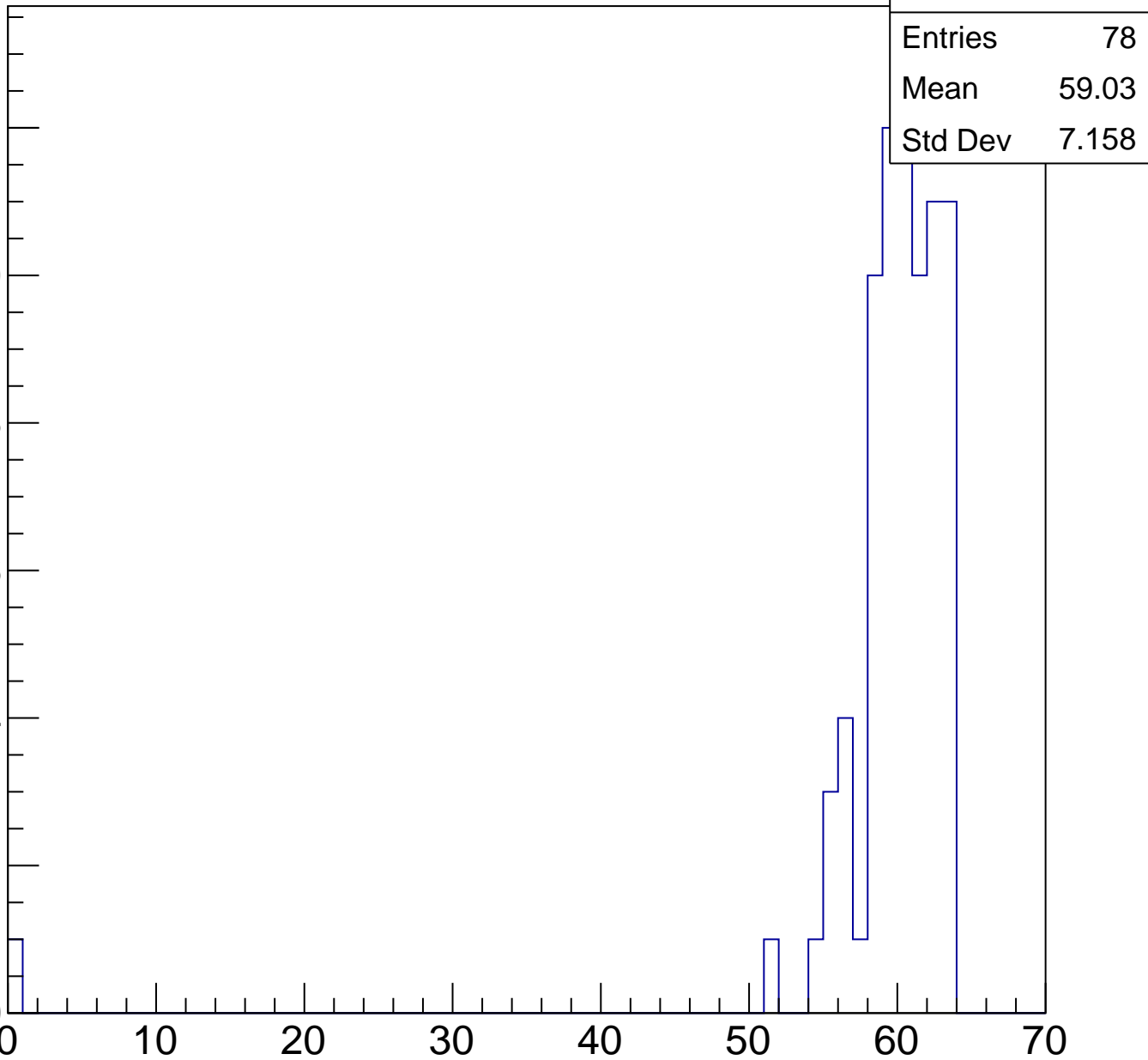
Mean

59.03

Std Dev

7.158

ampl

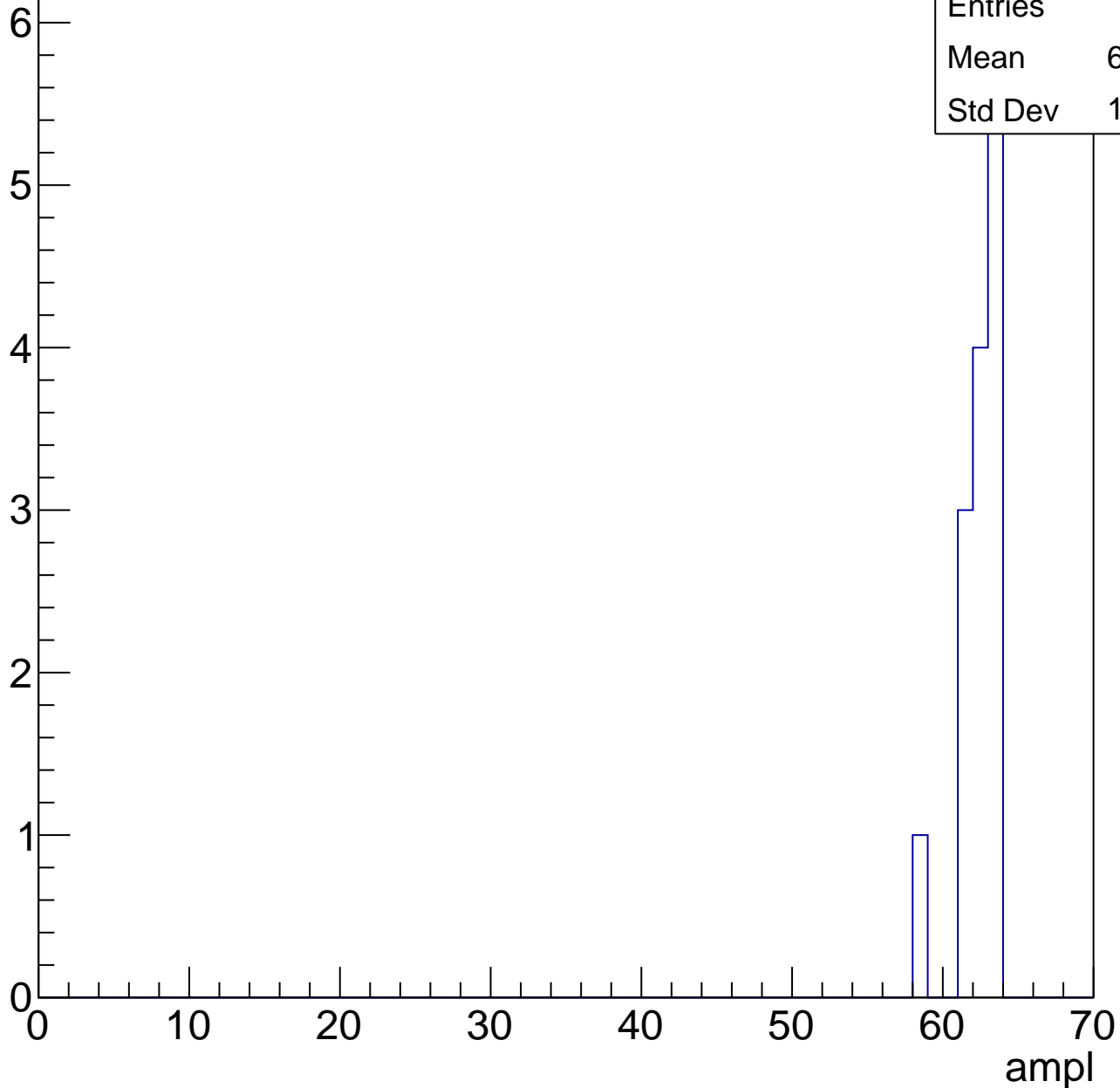


# B0L101S, U26-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.93
Std Dev	1.334





# B0L101S, U26-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch109, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	156
Mean	29.71
Std Dev	6.048

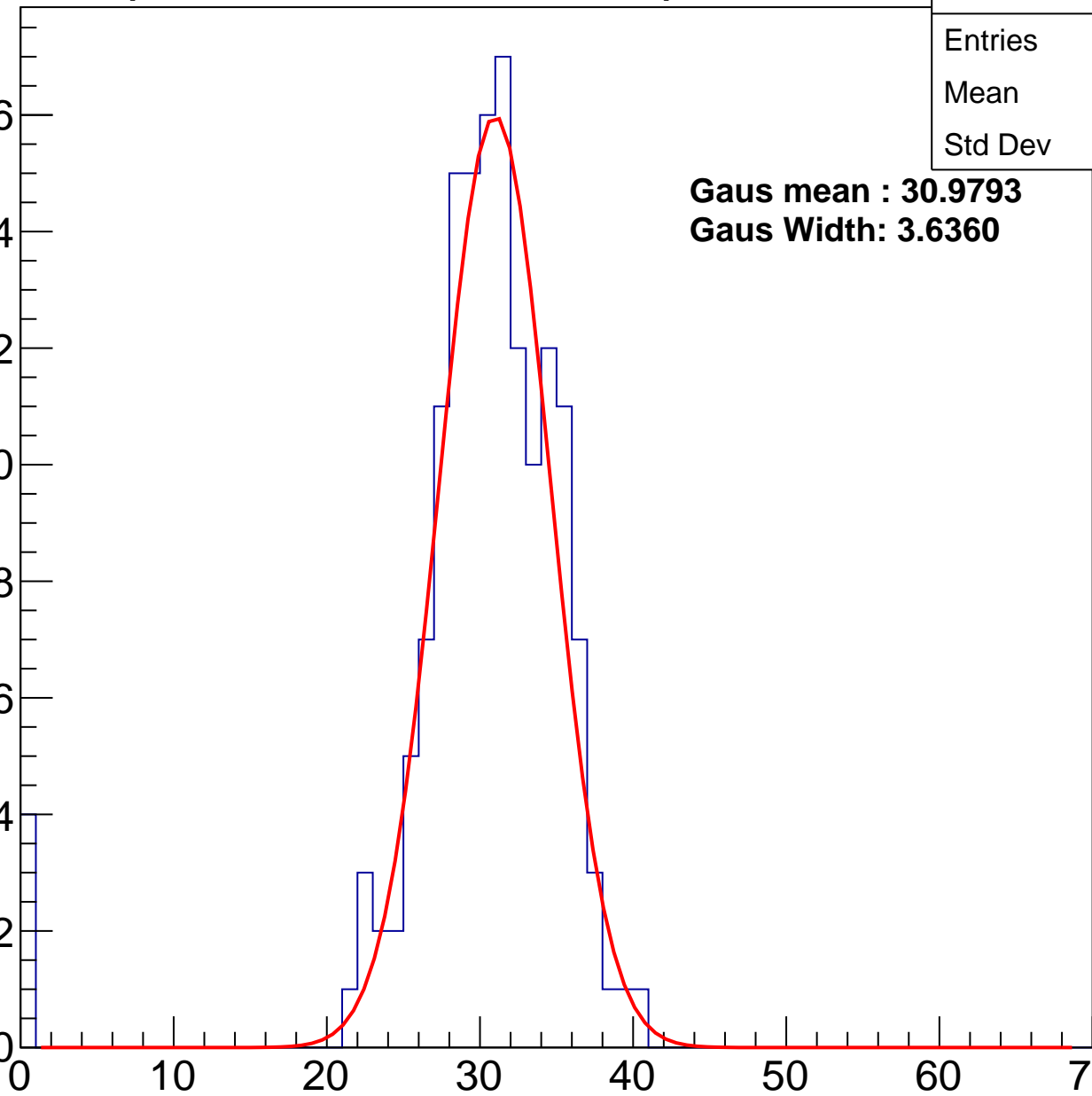
**Gaus mean : 30.9793**

**Gaus Width: 3.6360**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	127
Mean	37.06
Std Dev	5.067

**Gaus mean : 37.7859**

**Gaus Width: 3.9859**

# B0L101S, U26-ch109, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

144

Mean

43.9

Std Dev

3.813

**Gaus mean : 44.4672**

**Gaus Width: 3.9140**

0

10

20

30

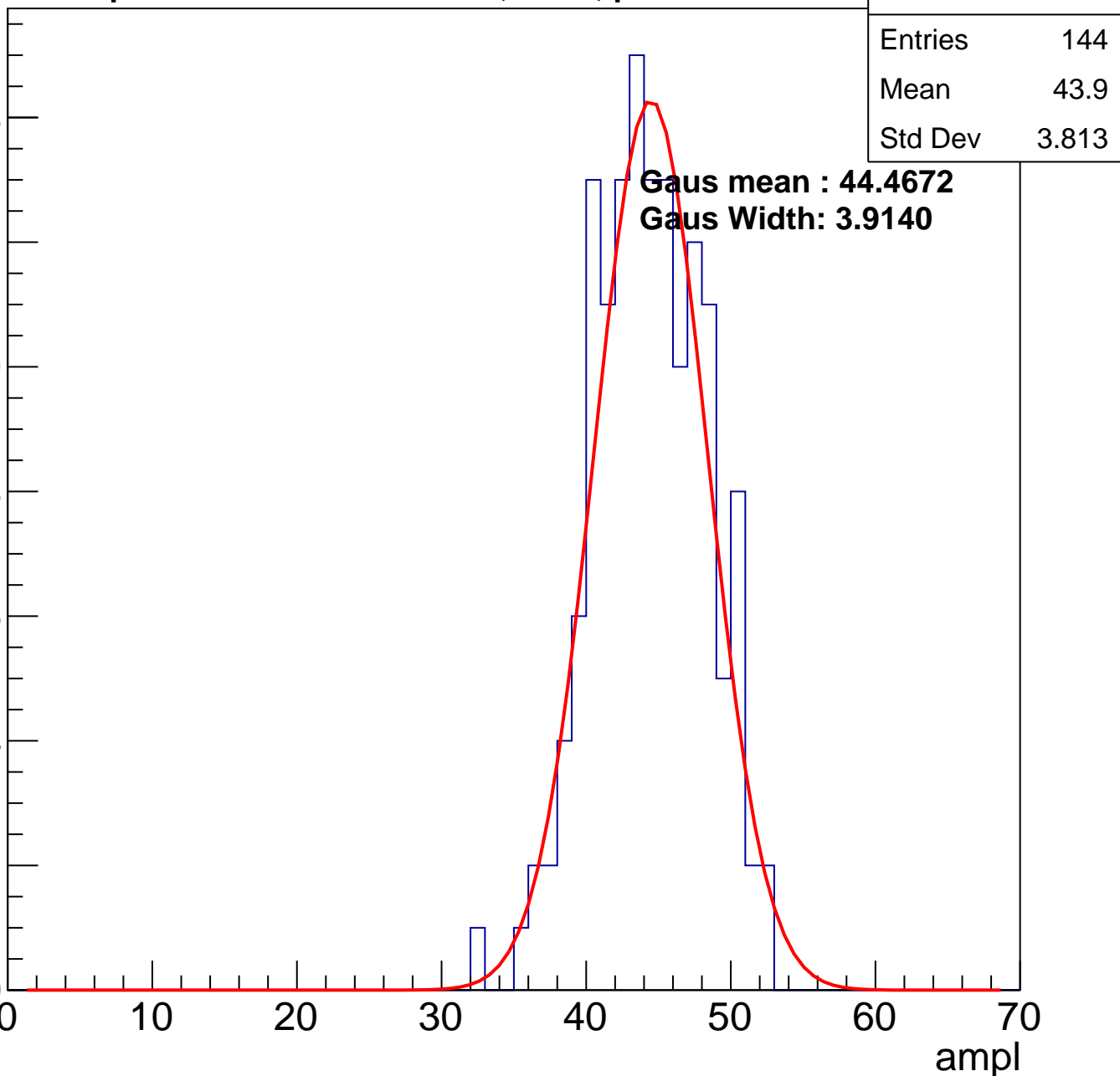
40

50

60

70

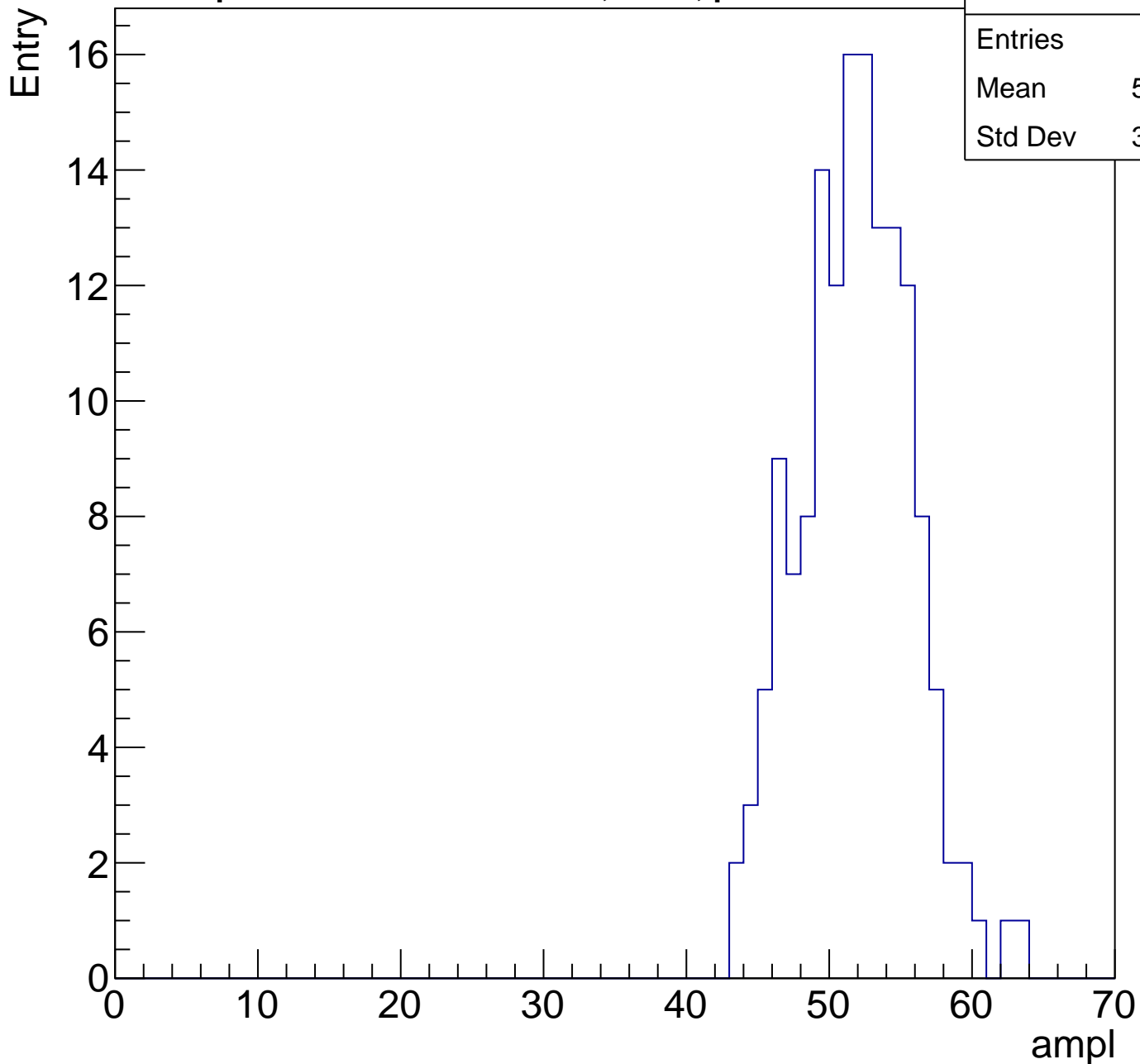
ampl



# B0L101S, U26-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	51.38
Std Dev	3.864



# B0L101S, U26-ch109, adc4

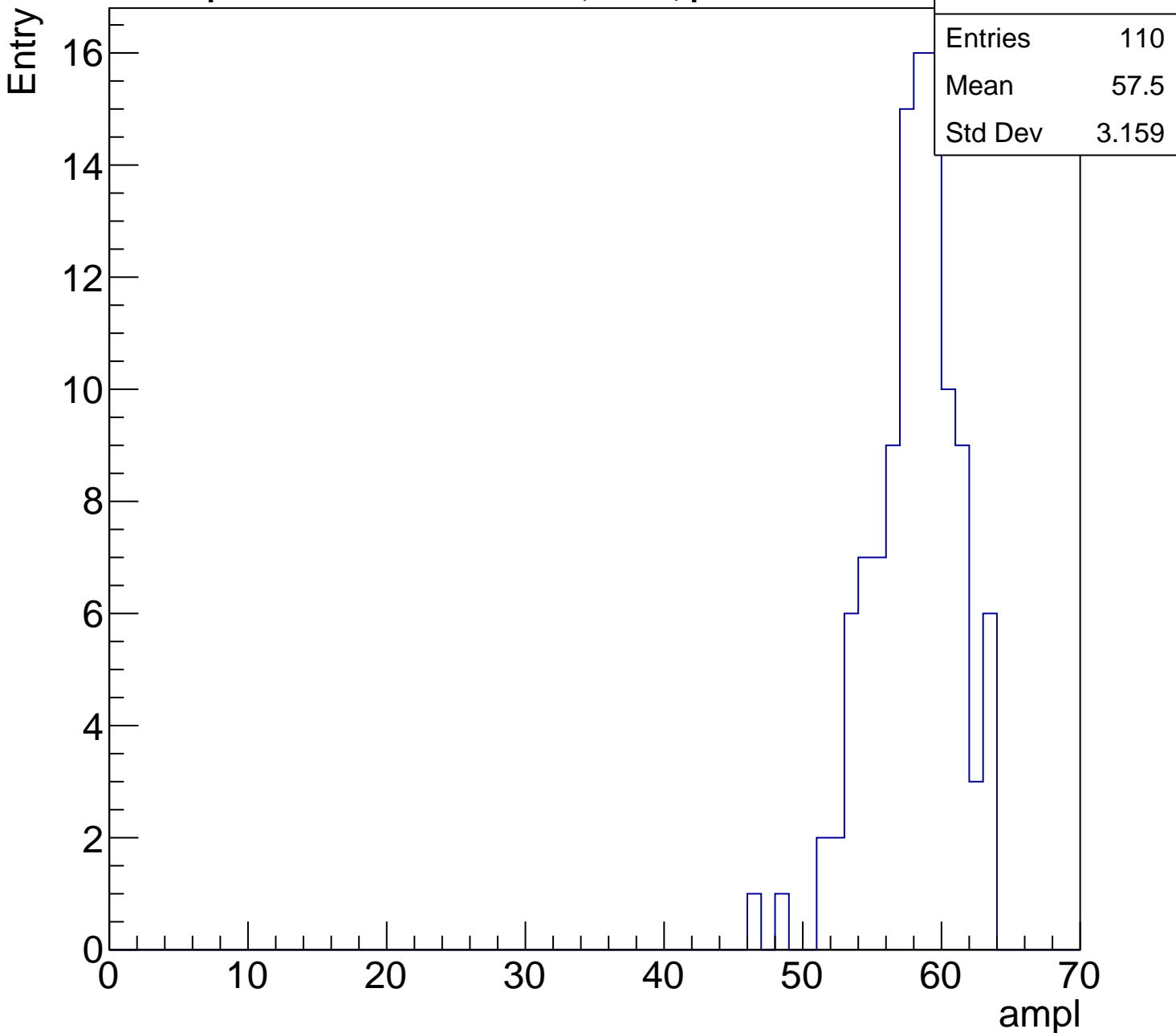
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	110
Mean	57.5
Std Dev	3.159

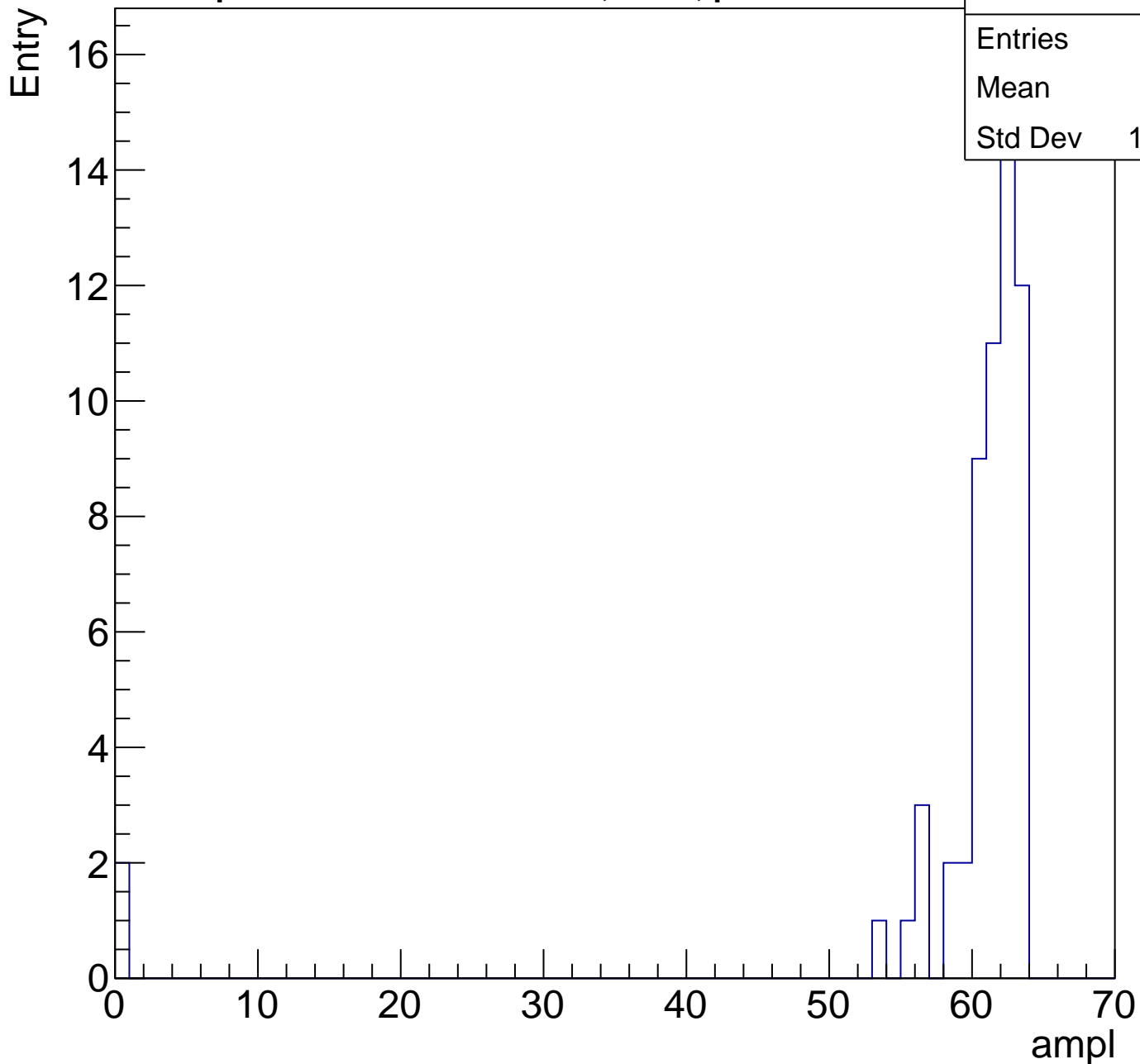
ampl



# B0L101S, U26-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	59
Mean	58.8
Std Dev	11.22



# B0L101S, U26-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

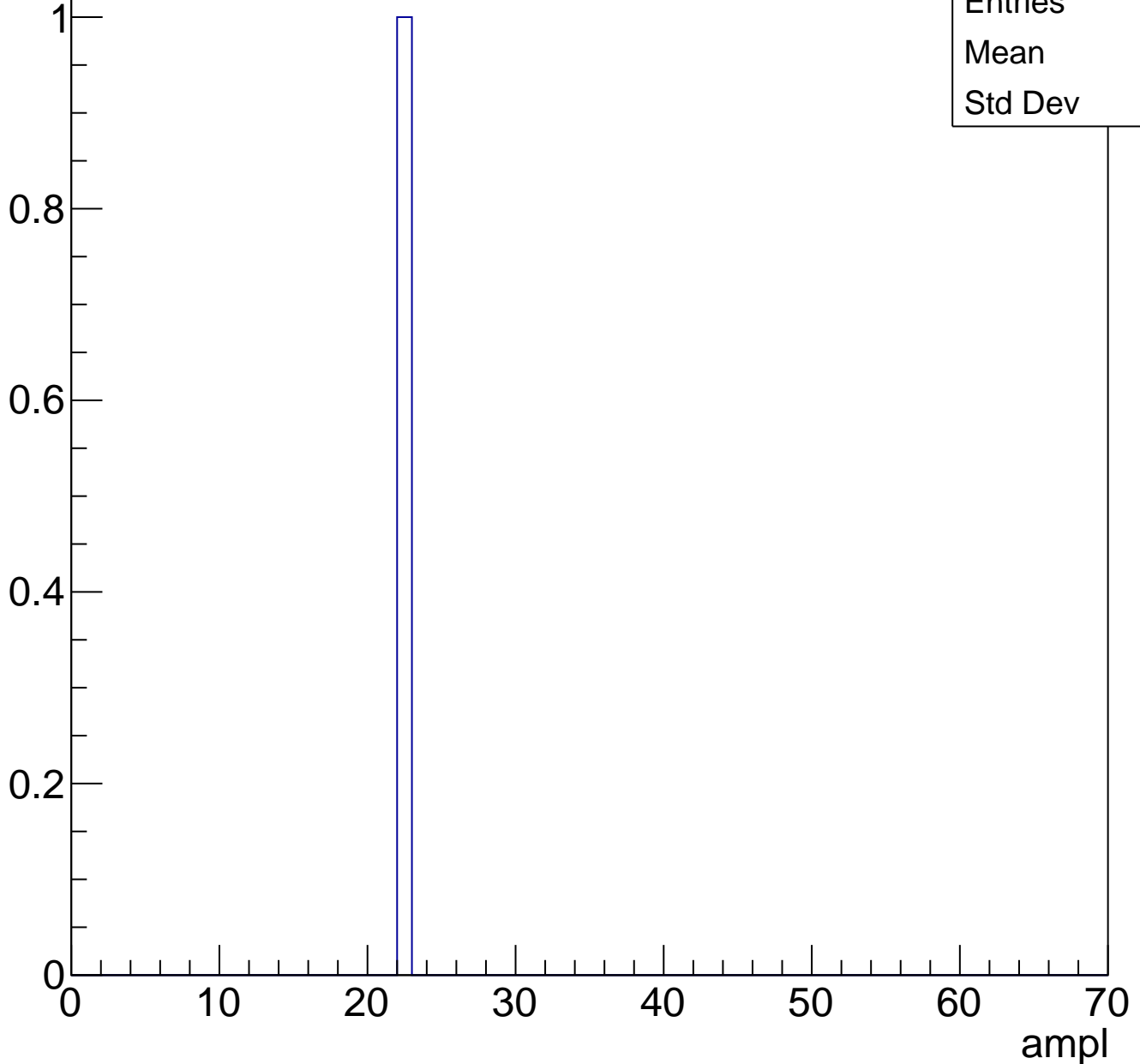




# B0L101S, U26-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



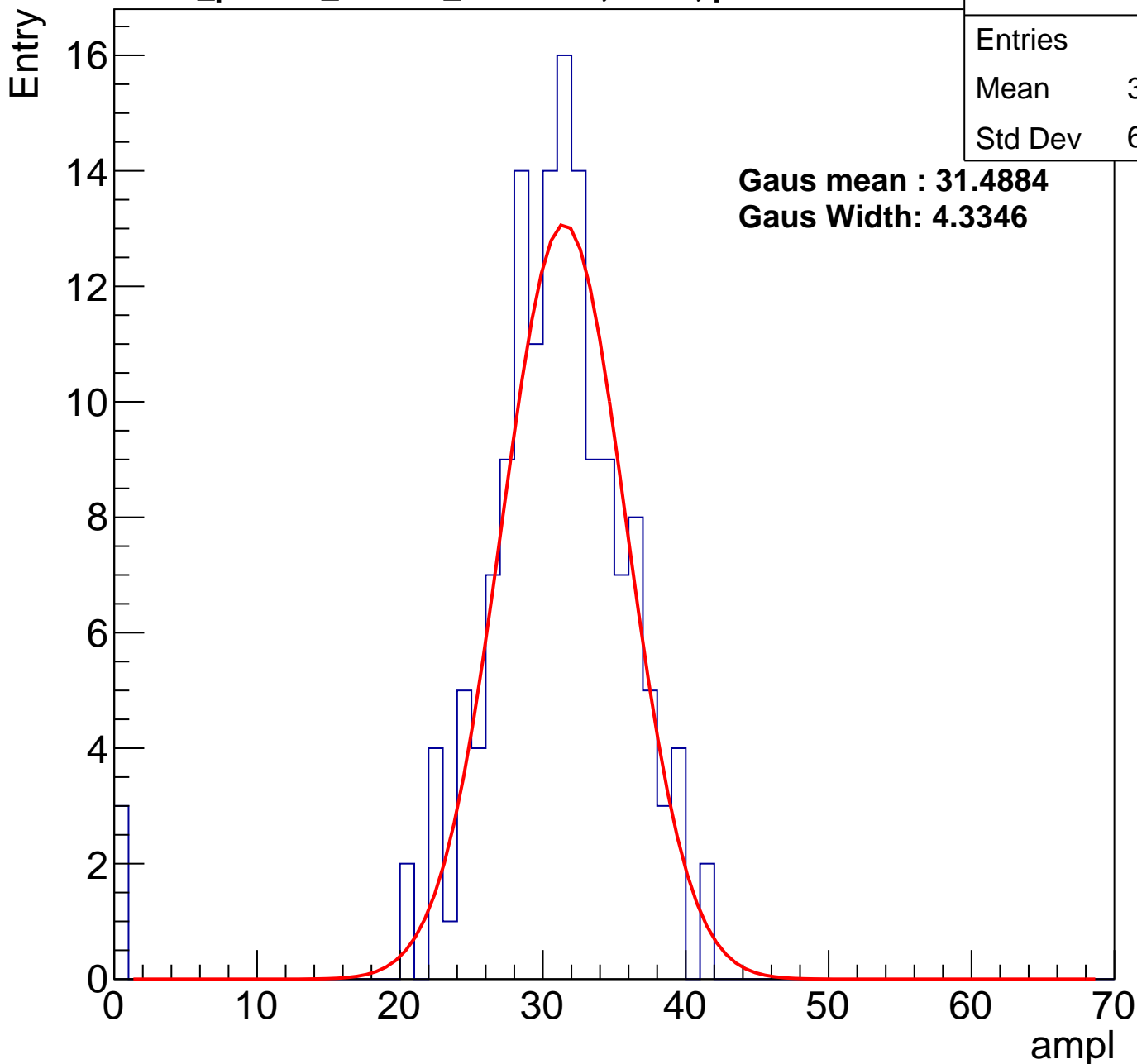
# B0L101S, U26-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	30.09
Std Dev	6.039

**Gaus mean : 31.4884**

**Gaus Width: 4.3346**



# B0L101S, U26-ch110, adc1

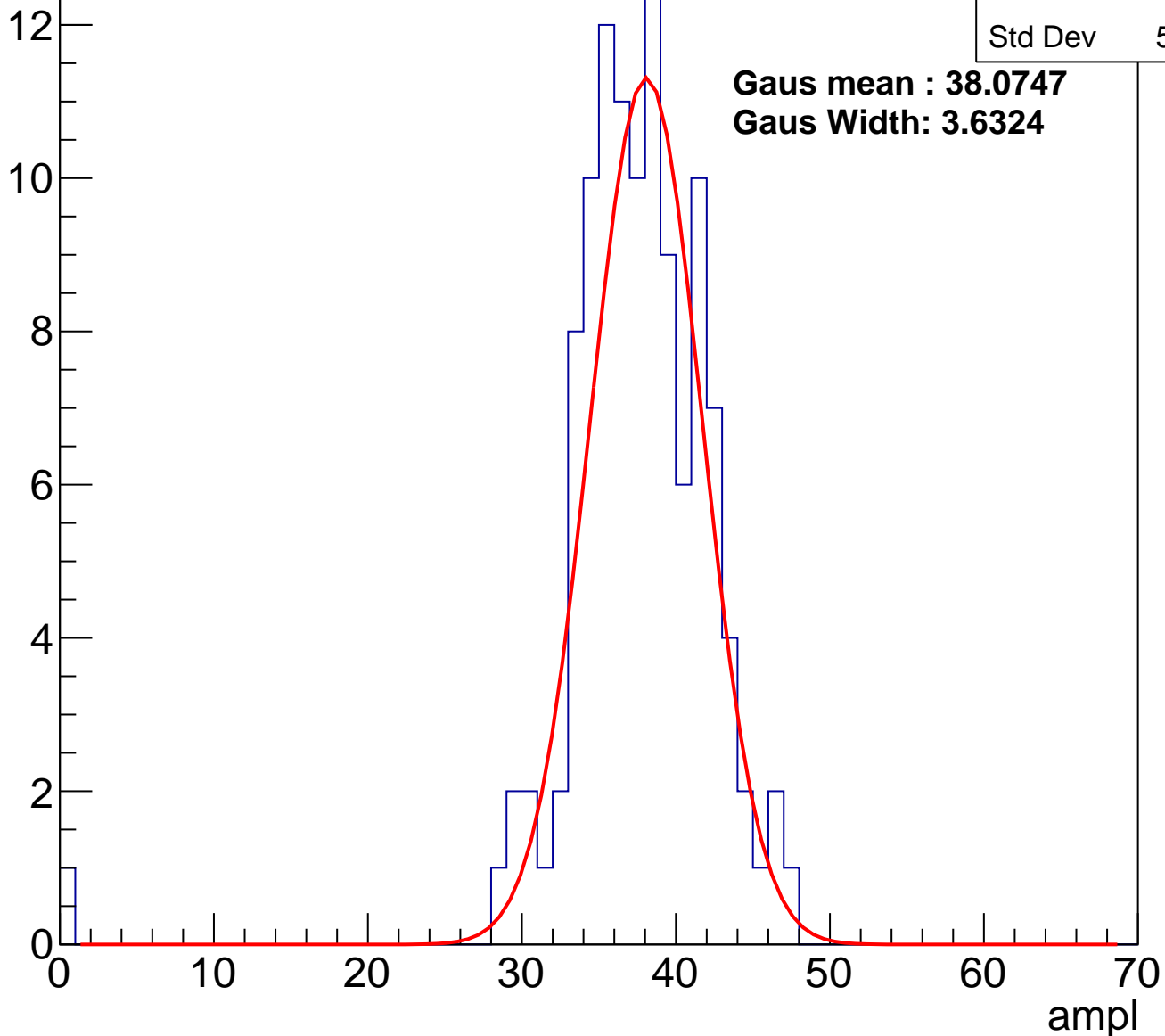
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	115
Mean	37.08
Std Dev	5.145

**Gaus mean : 38.0747**

**Gaus Width: 3.6324**

Entry



# B0L101S, U26-ch110, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

138

Mean

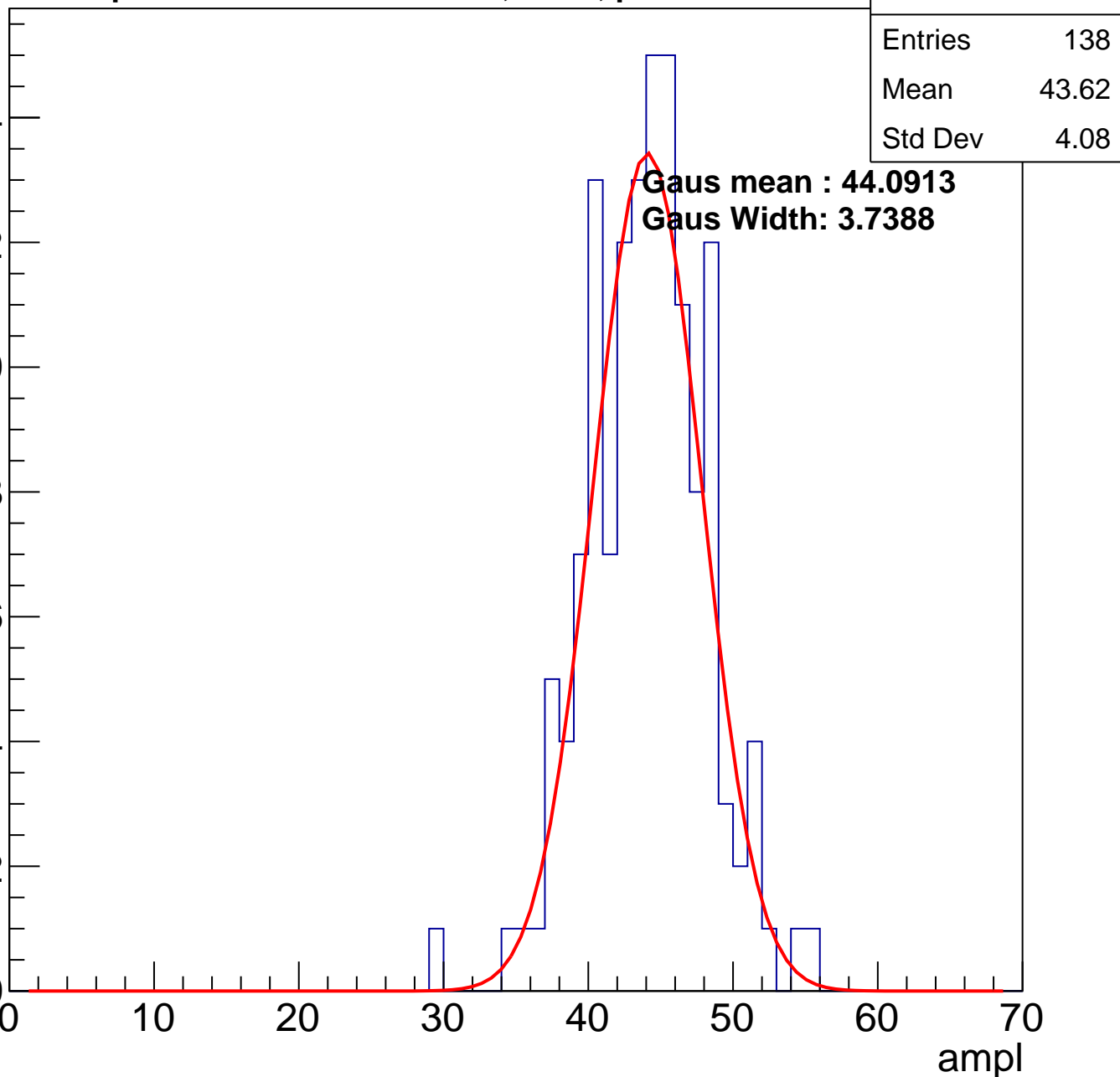
43.62

Std Dev

4.08

Gaus mean : 44.0913

Gaus Width: 3.7388



# B0L101S, U26-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	51.21
Std Dev	4.076

Entry

16

14

12

10

8

6

4

2

0

0

10

20

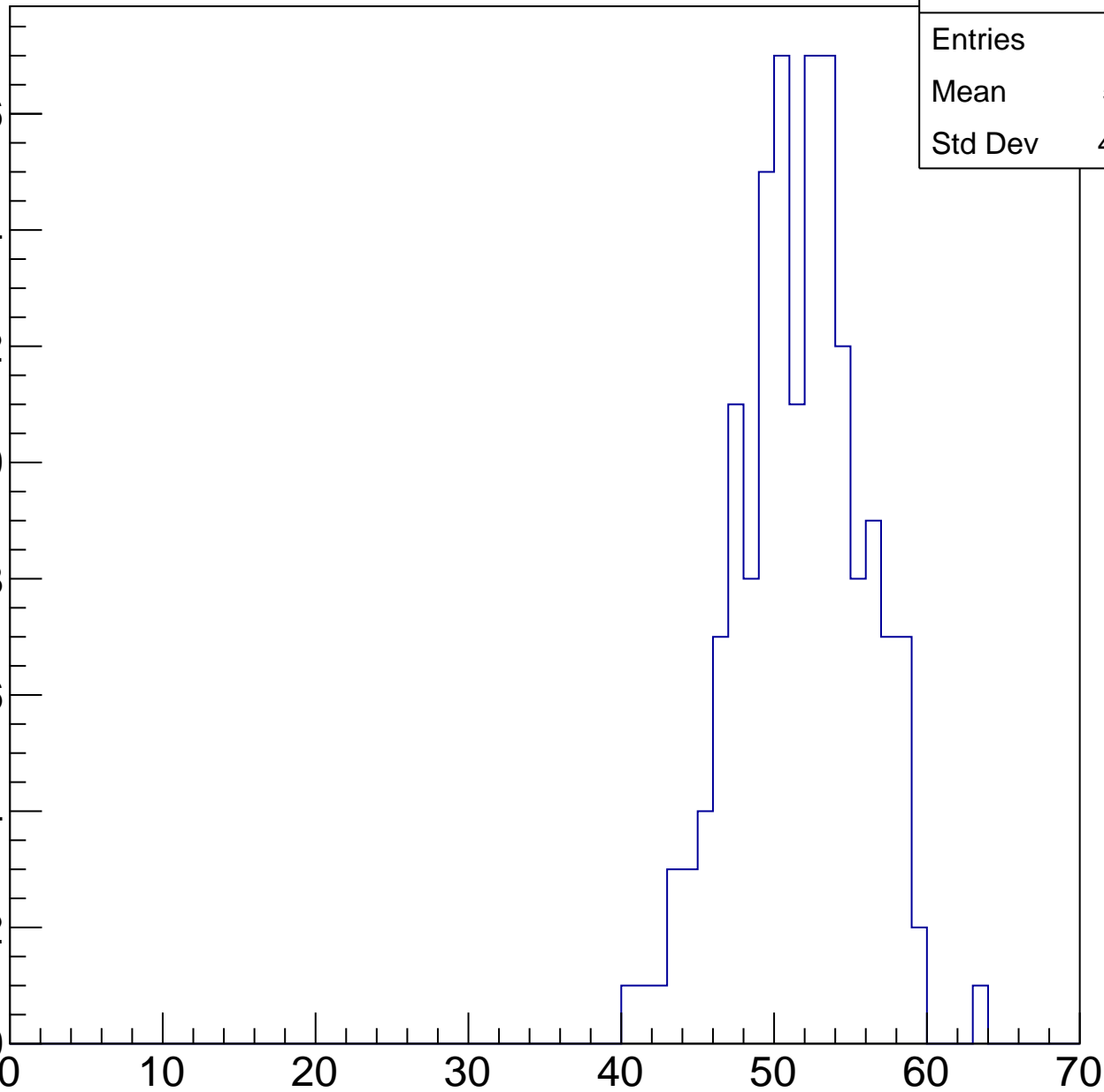
30

40

50

60

ampl



# B0L101S, U26-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	81
Mean	56.64
Std Dev	3.271

Entry

10

8

6

4

2

0

0

10

20

30

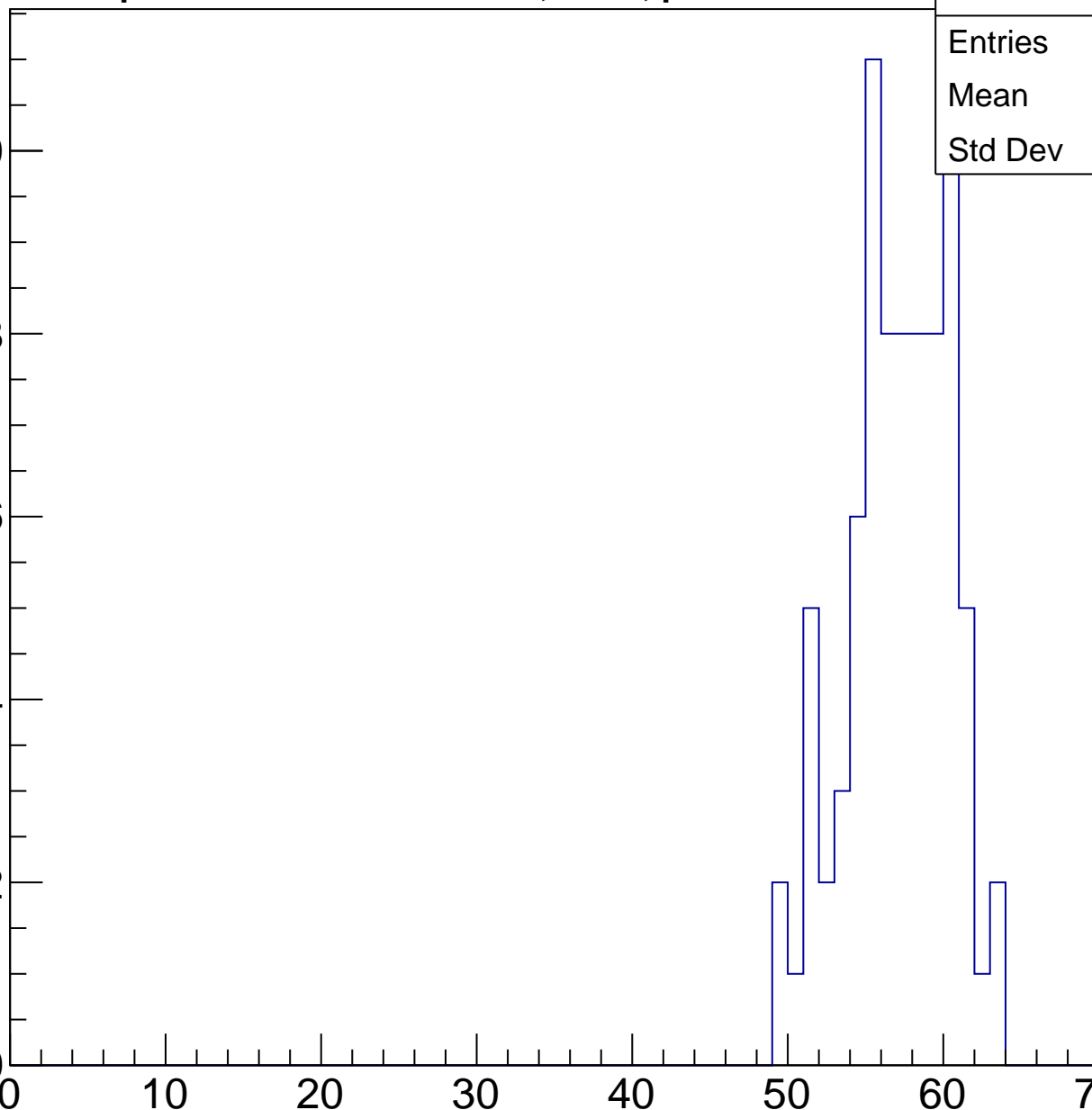
40

50

60

70

ampl



# B0L101S, U26-ch110, adc5

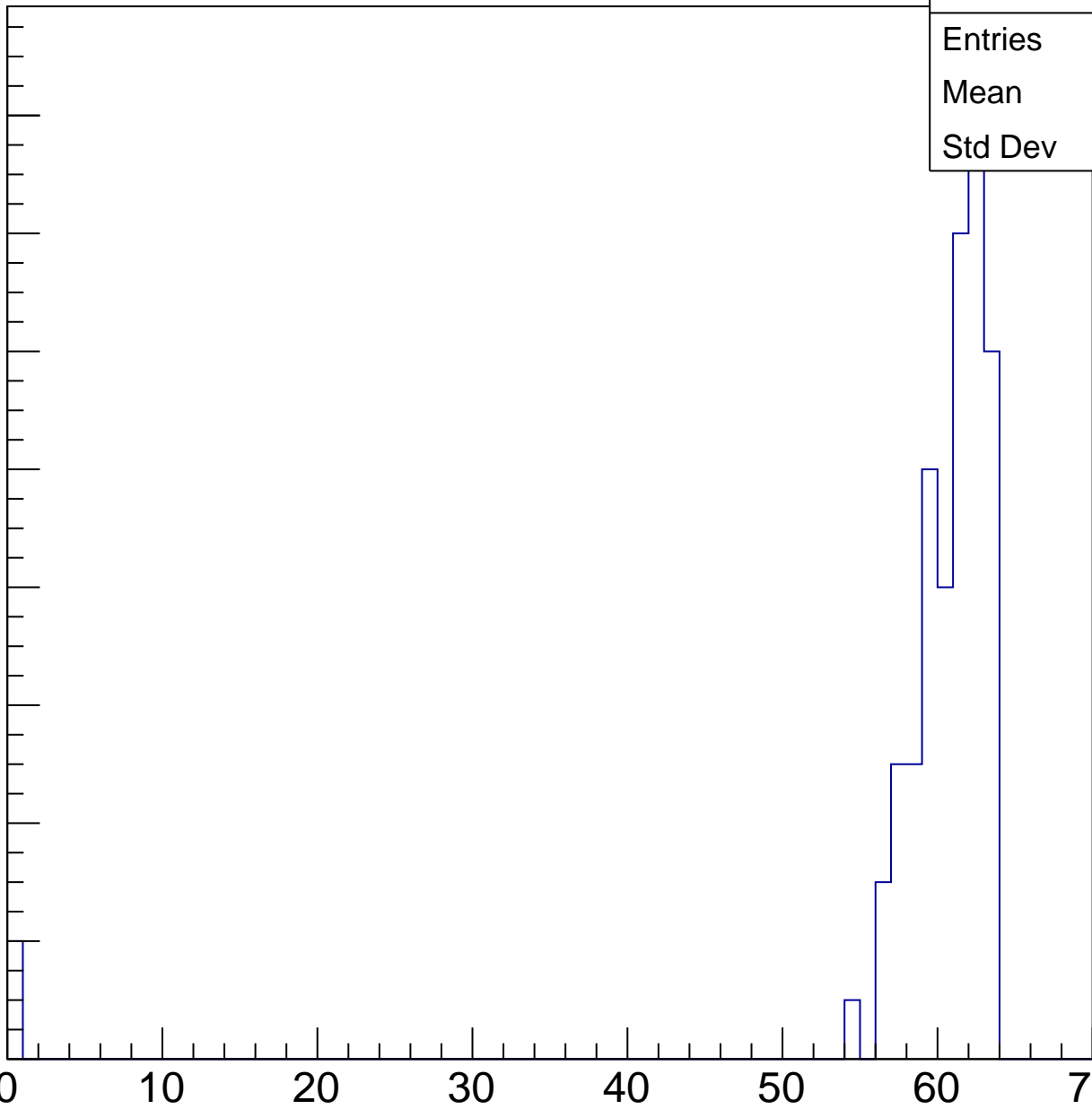
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	77
Mean	58.84
Std Dev	9.834

ampl

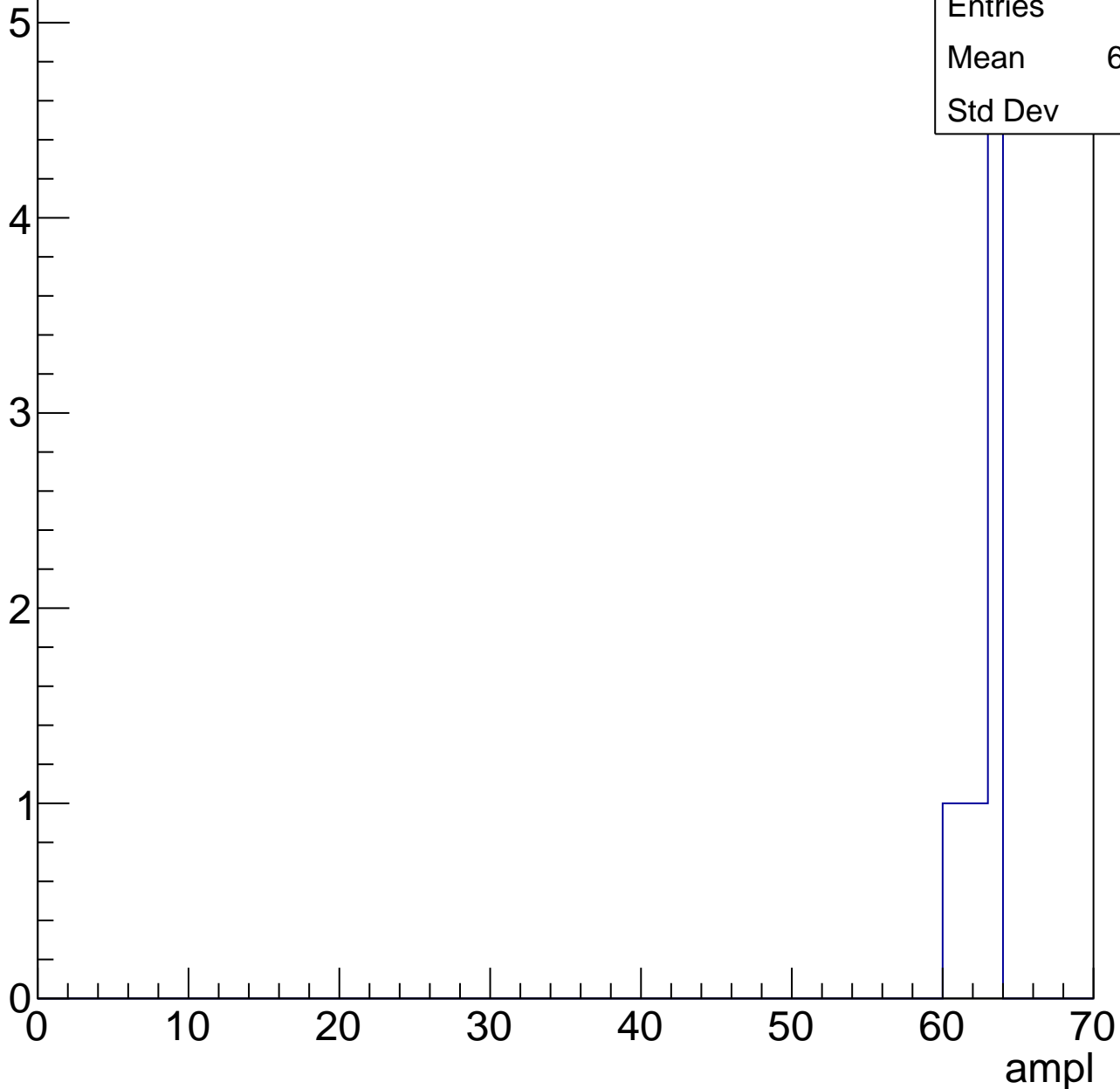


# B0L101S, U26-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	8
Mean	62.25
Std Dev	1.09





# B0L101S, U26-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

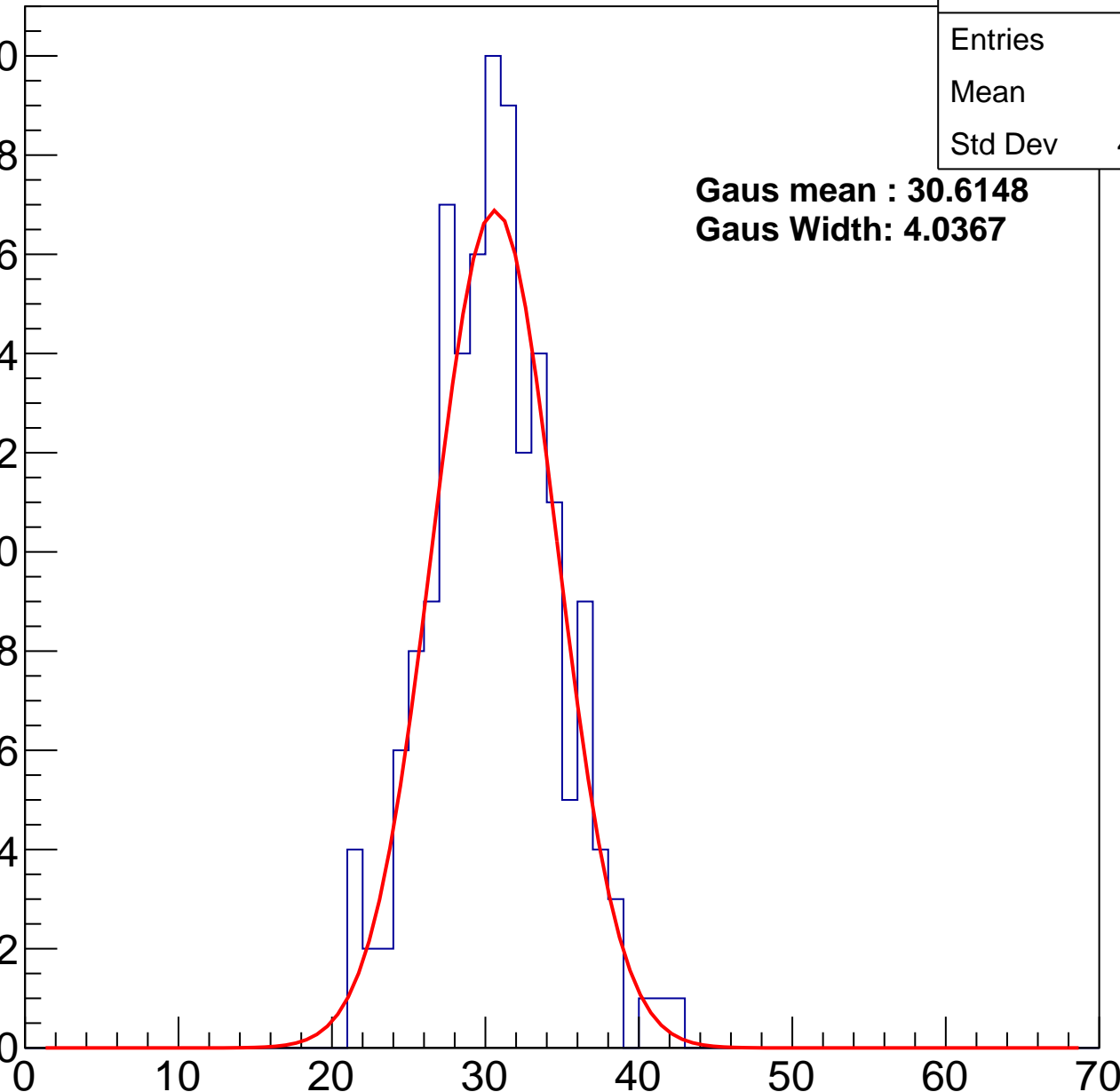
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	178
Mean	30.11
Std Dev	4.039

**Gaus mean : 30.6148**

**Gaus Width: 4.0367**



ampl

# B0L101S, U26-ch111, adc1

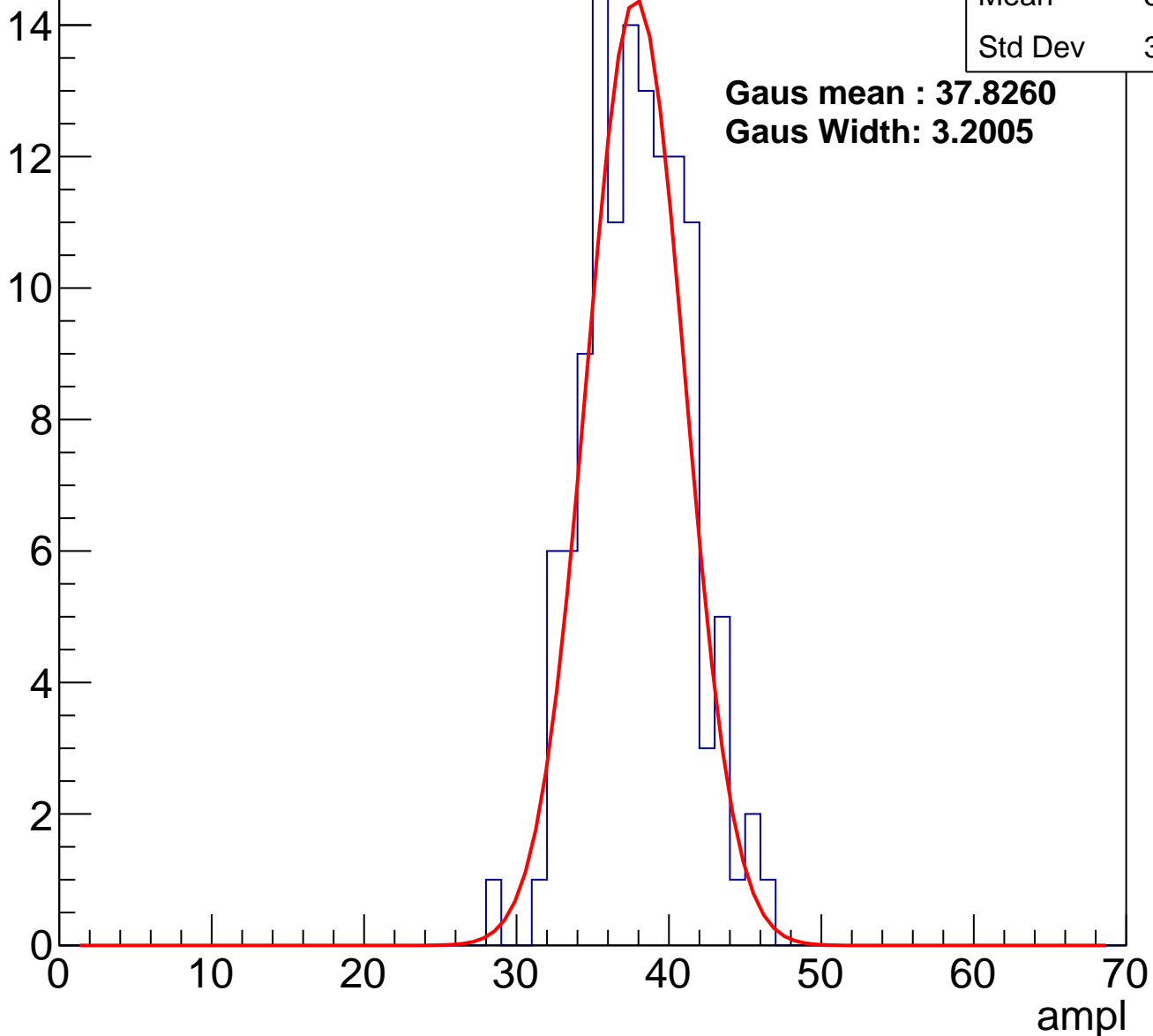
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	123
Mean	37.46
Std Dev	3.309

**Gaus mean : 37.8260**

**Gaus Width: 3.2005**

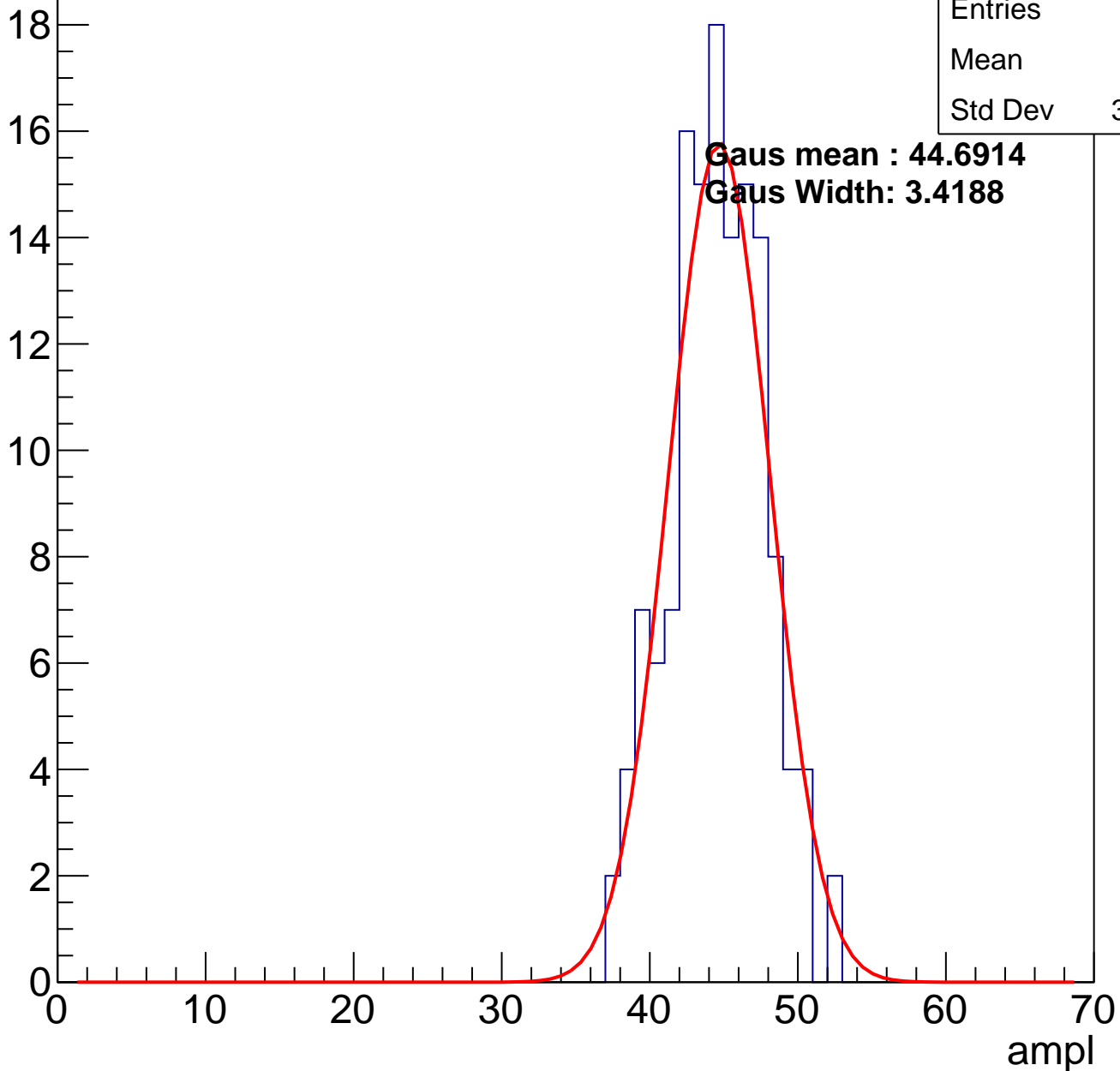
Entry



# B0L101S, U26-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

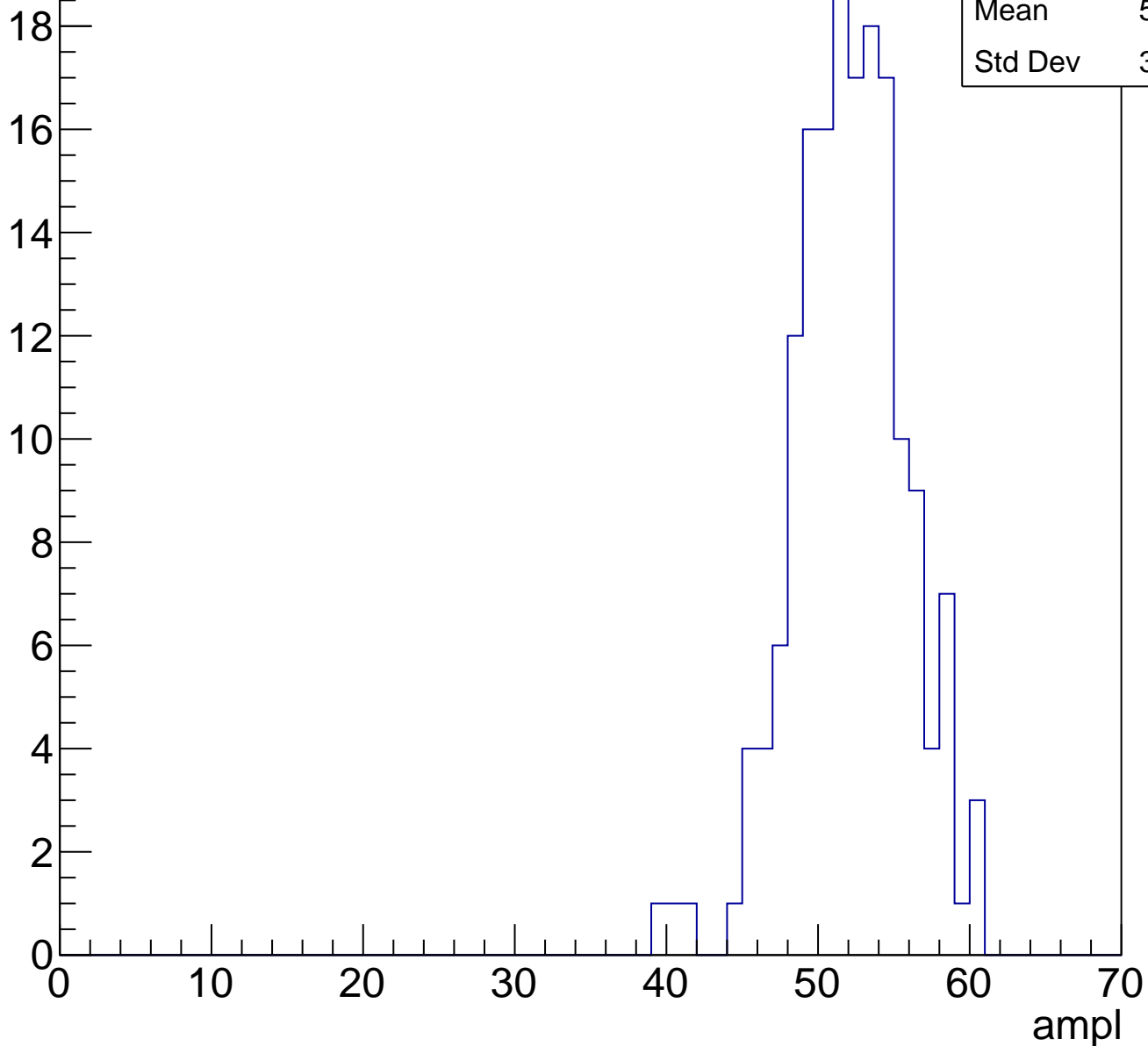


# B0L101S, U26-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	167
Mean	51.63
Std Dev	3.709

Entry



# B0L101S, U26-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

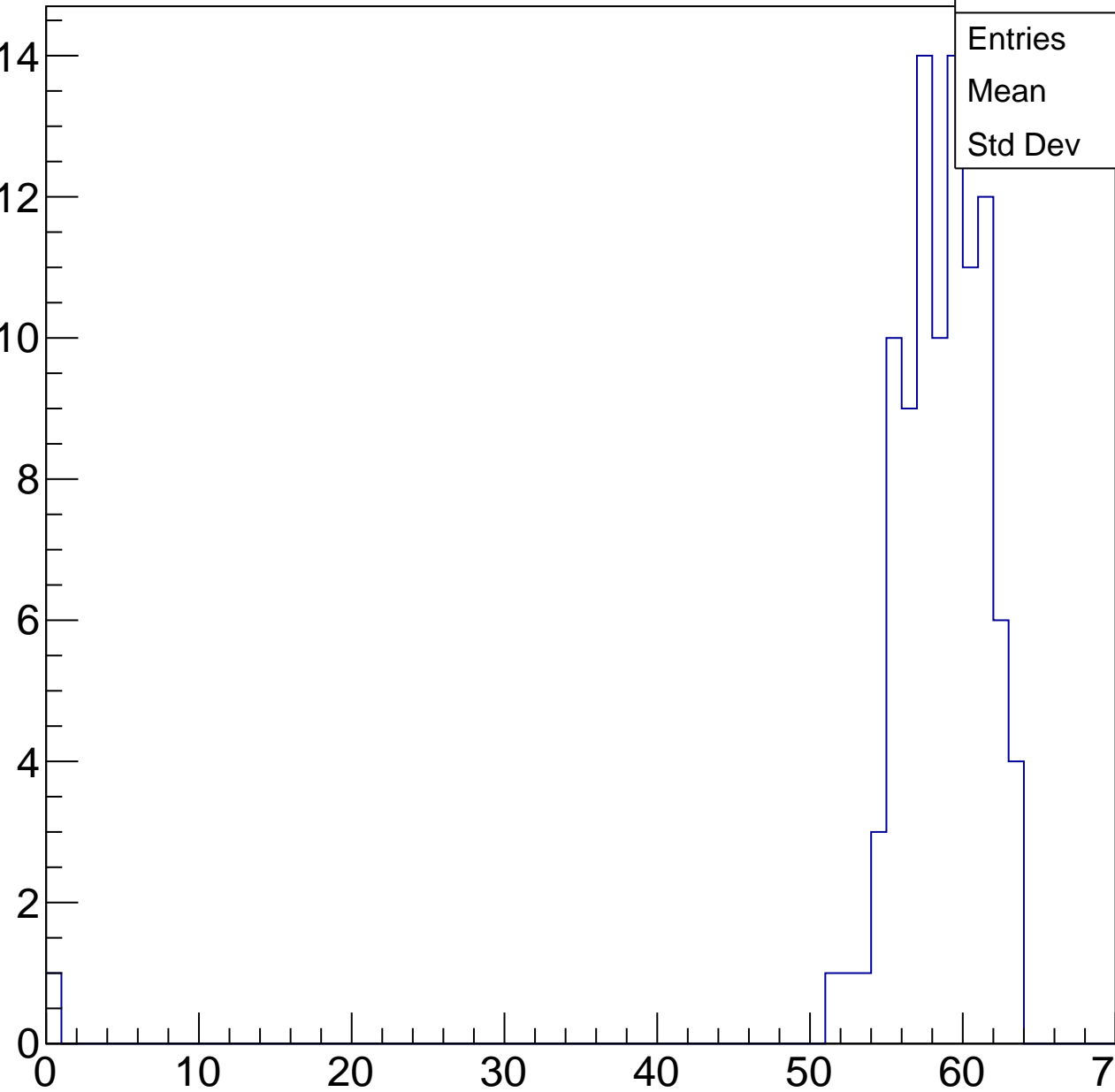
Entries	97
Mean	57.65
Std Dev	6.431

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch111, adc5

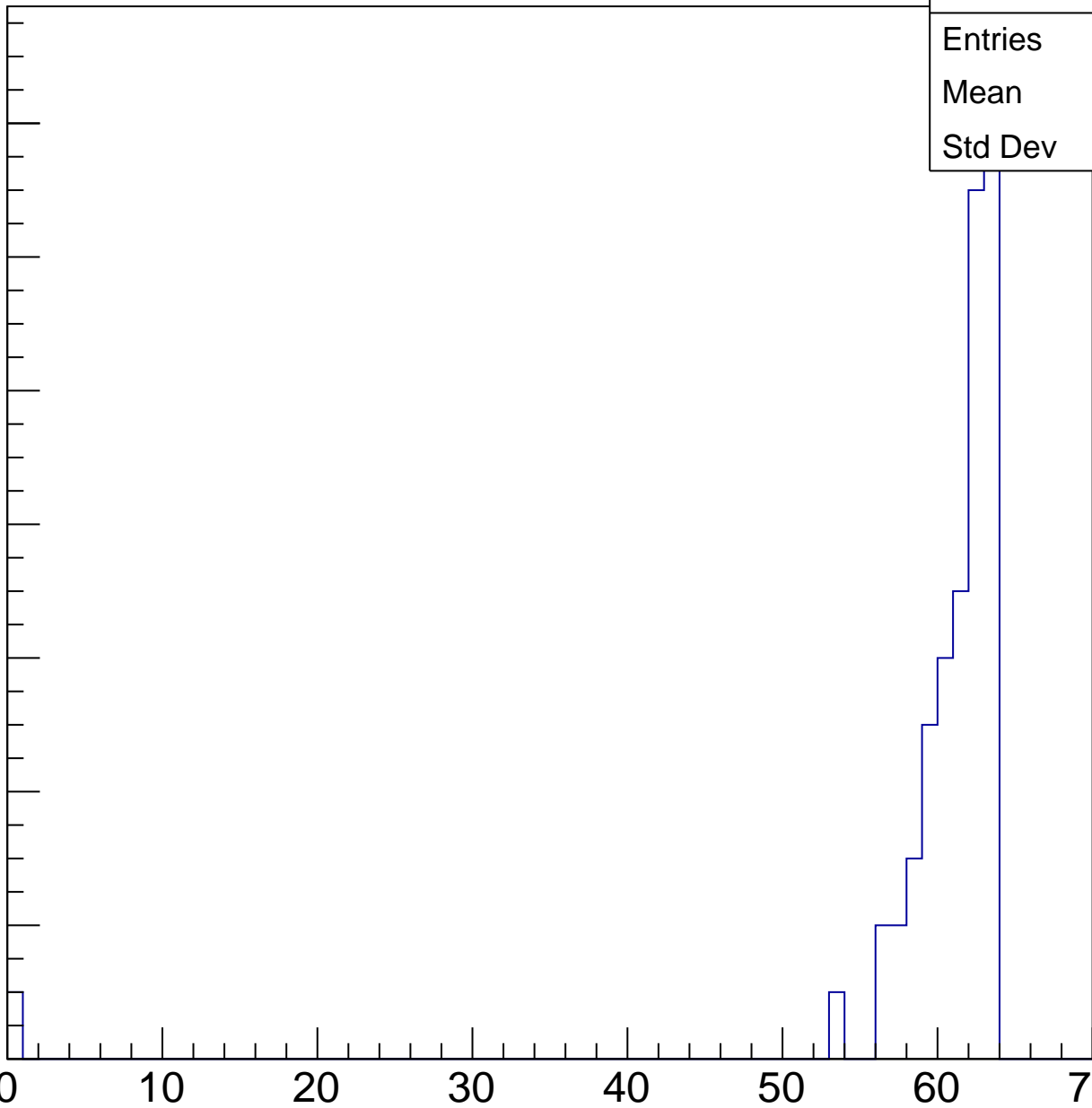
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	55
Mean	59.75
Std Dev	8.428

ampl



# B0L101S, U26-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch112, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	29.37
Std Dev	4.432

**Gaus mean : 29.7971**

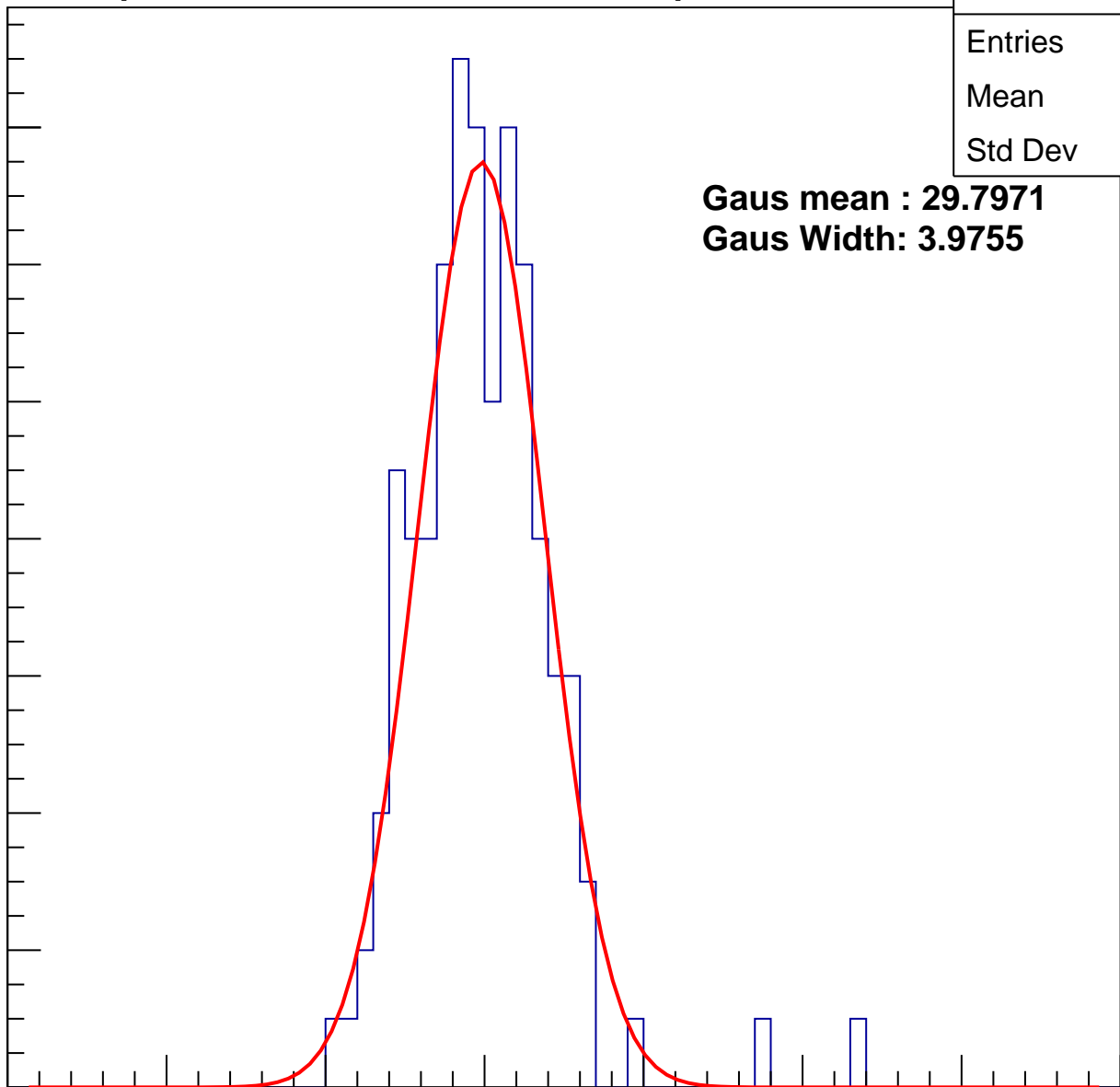
**Gaus Width: 3.9755**

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

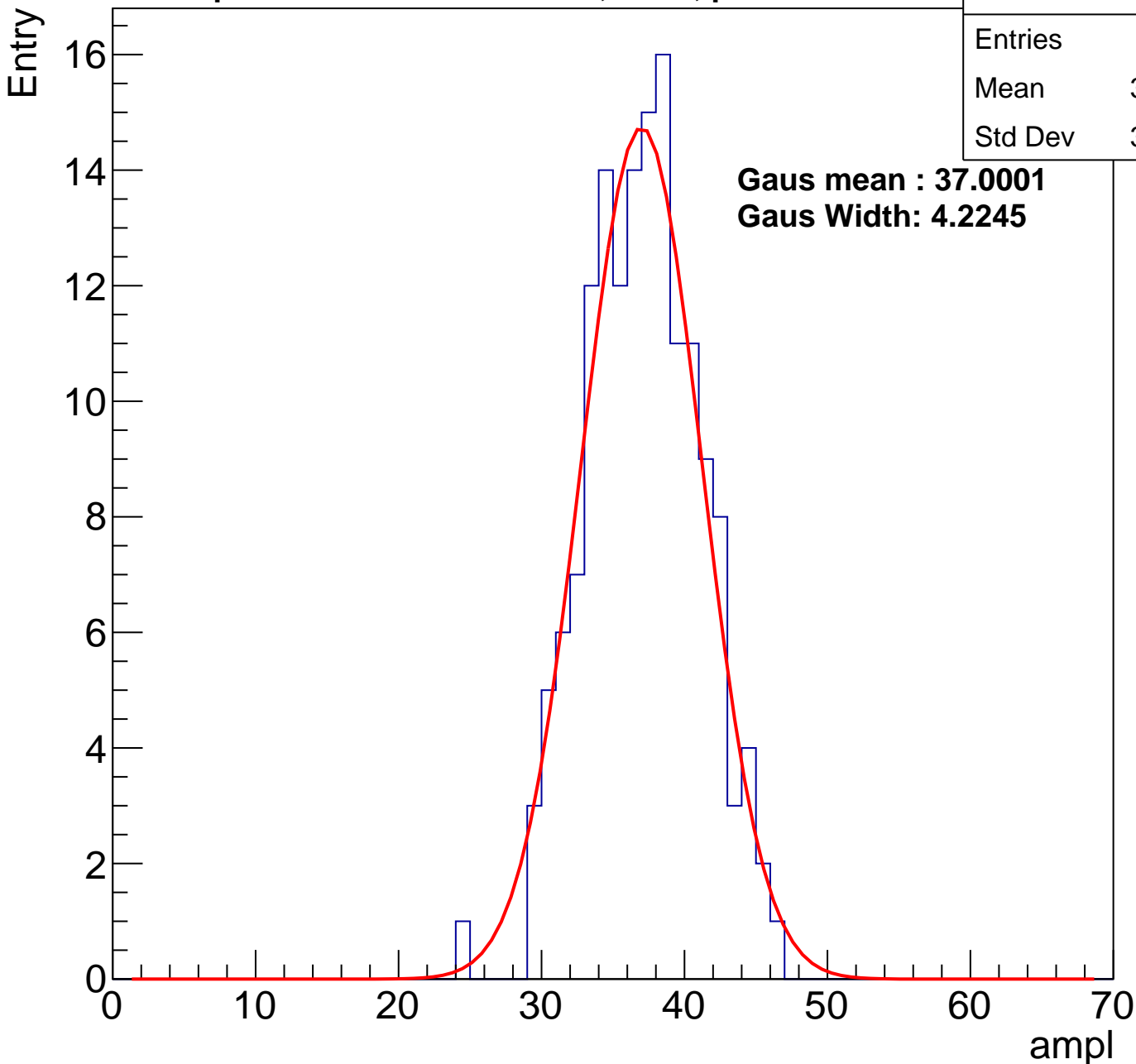


# B0L101S, U26-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	36.66
Std Dev	3.927

**Gaus mean : 37.0001**  
**Gaus Width: 4.2245**



# B0L101S, U26-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	100
Mean	42.96
Std Dev	3.939

**Gaus mean : 43.2037**

**Gaus Width: 4.1911**

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

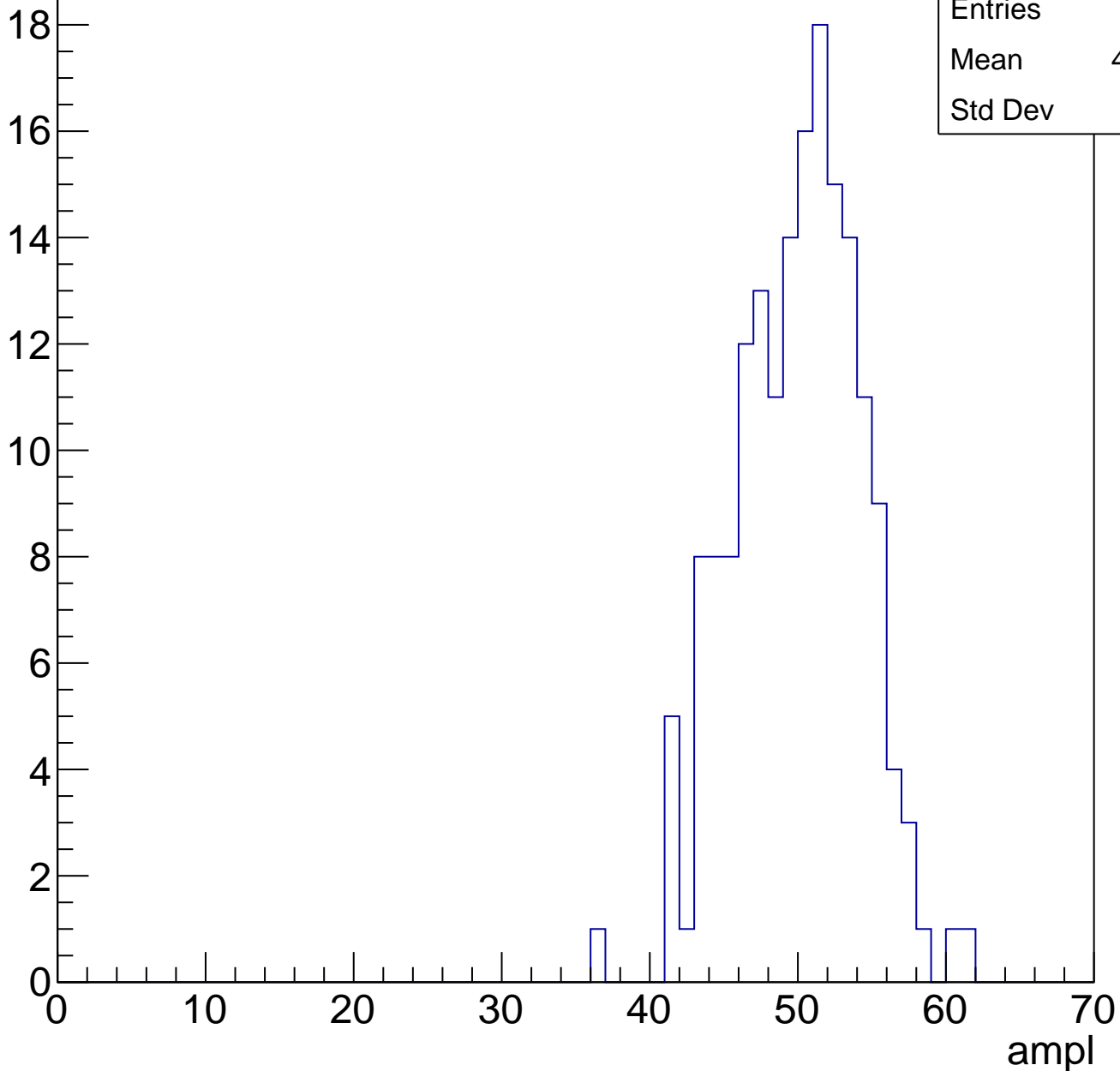
ampl



# B0L101S, U26-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

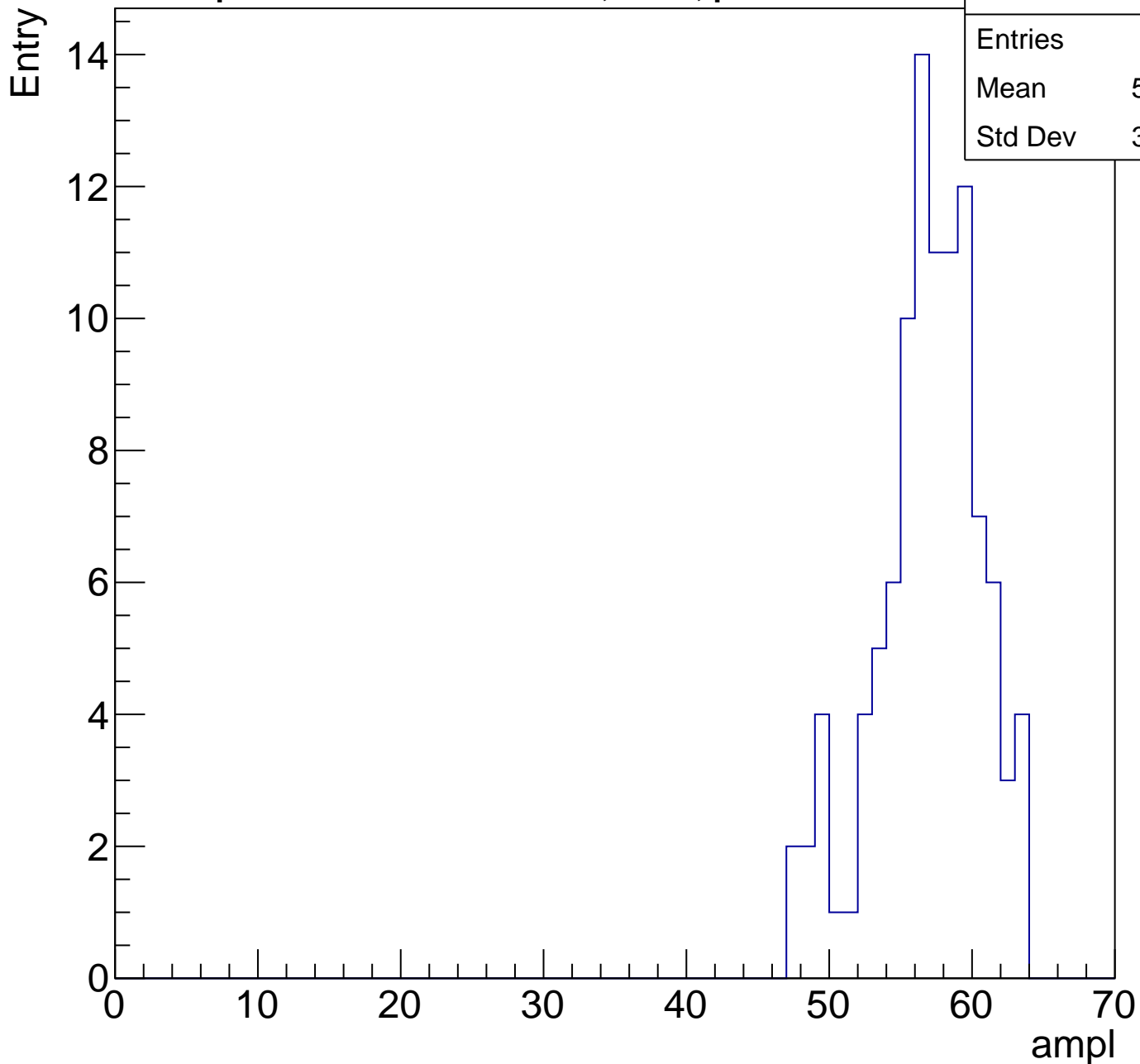
Entry



# B0L101S, U26-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	103
Mean	56.46
Std Dev	3.684



# B0L101S, U26-ch112, adc5

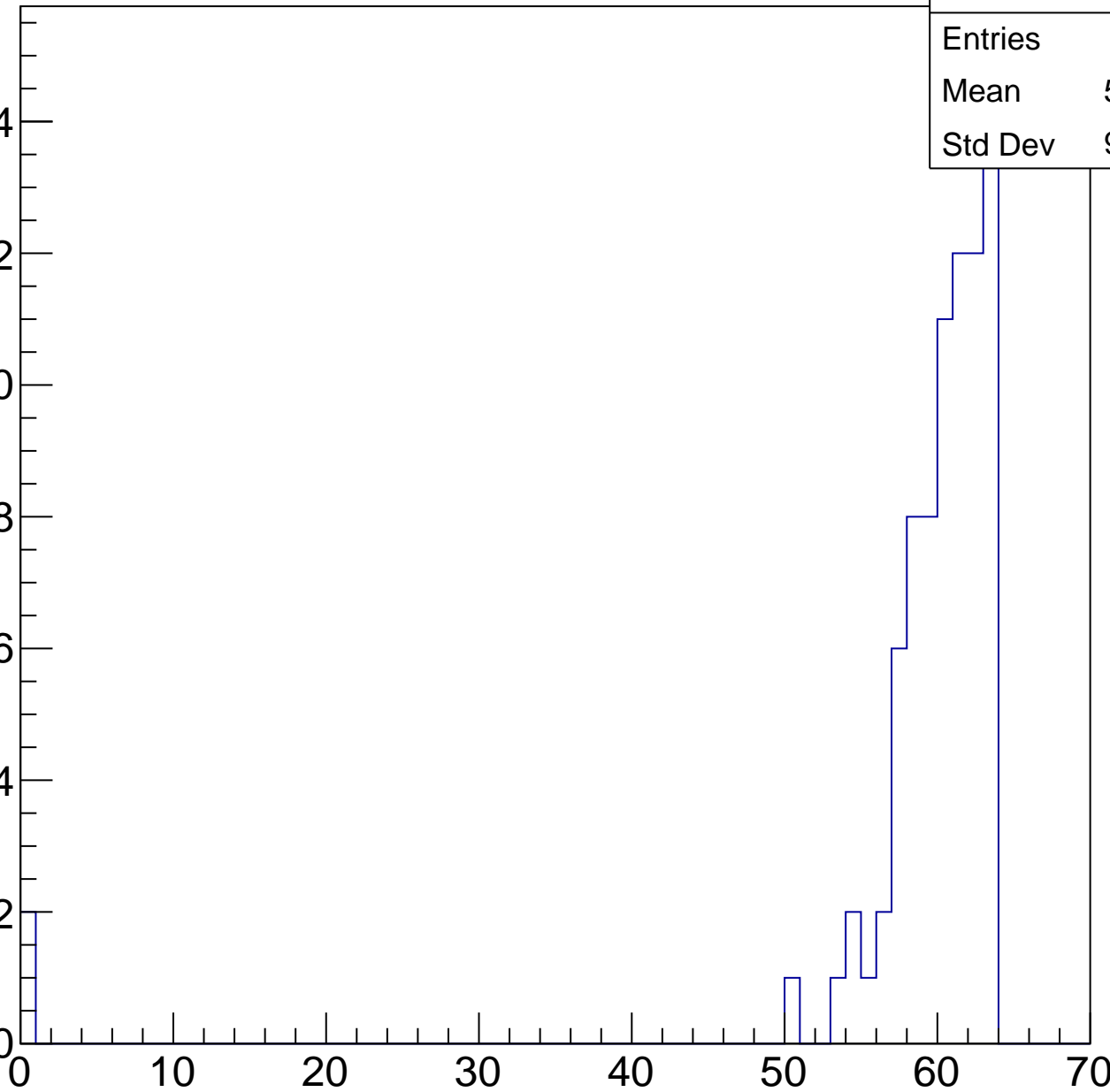
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	58.48
Std Dev	9.674

ampl

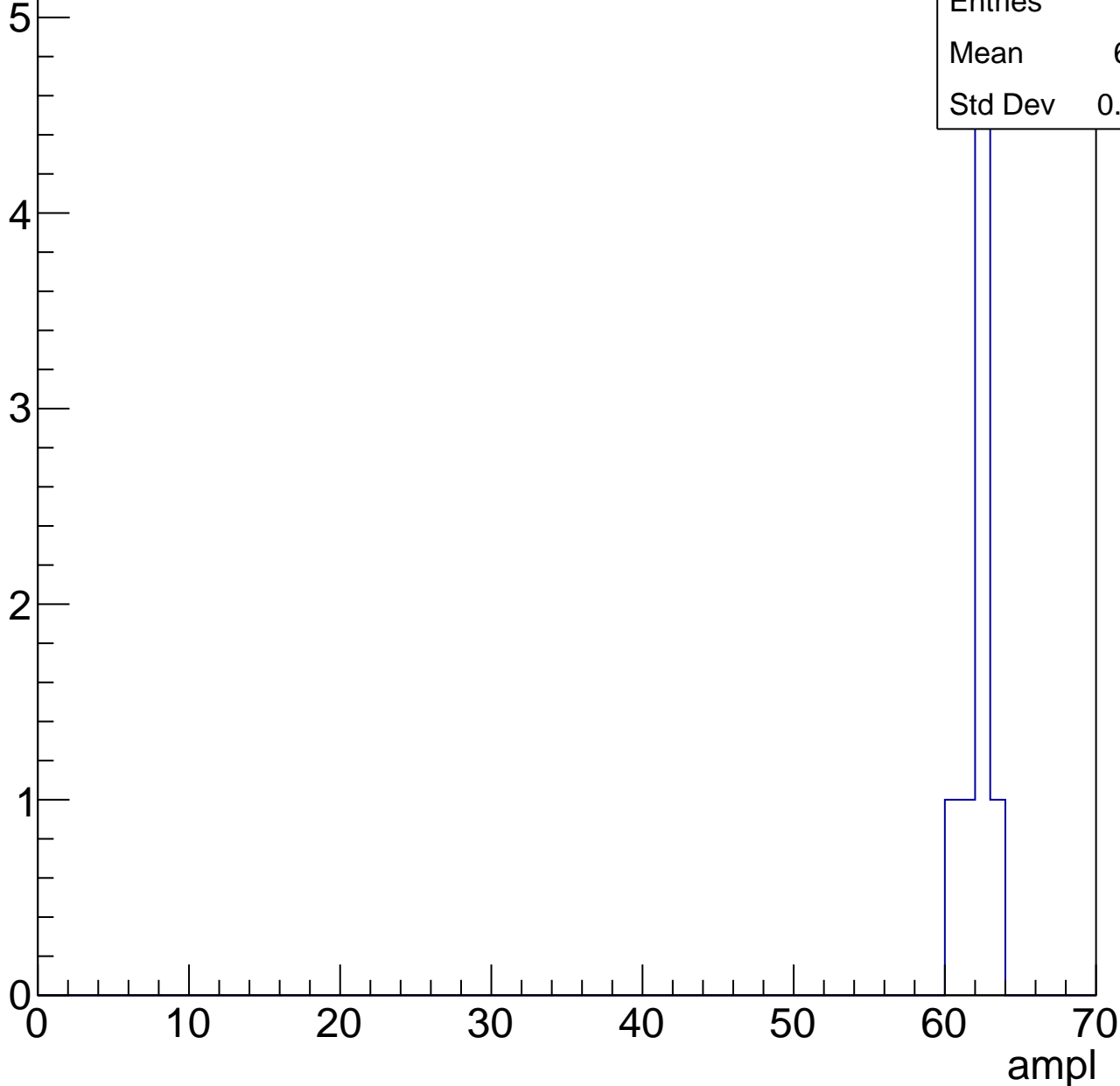


# B0L101S, U26-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	8
Mean	61.75
Std Dev	0.8292





# B0L101S, U26-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L101S, U26-ch113, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	30.66
Std Dev	5.707

**Gaus mean : 32.1692**

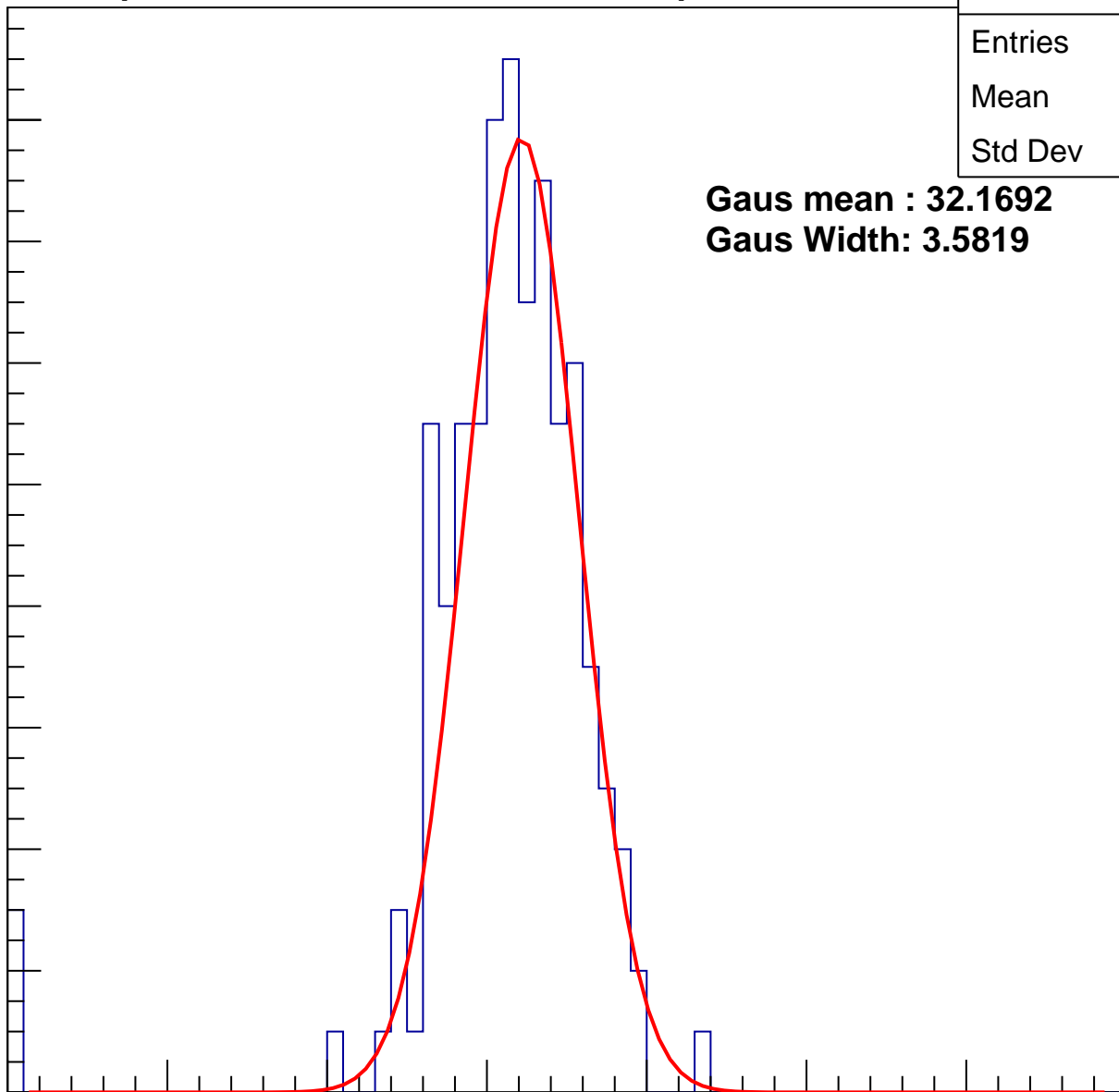
**Gaus Width: 3.5819**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch113, adc1

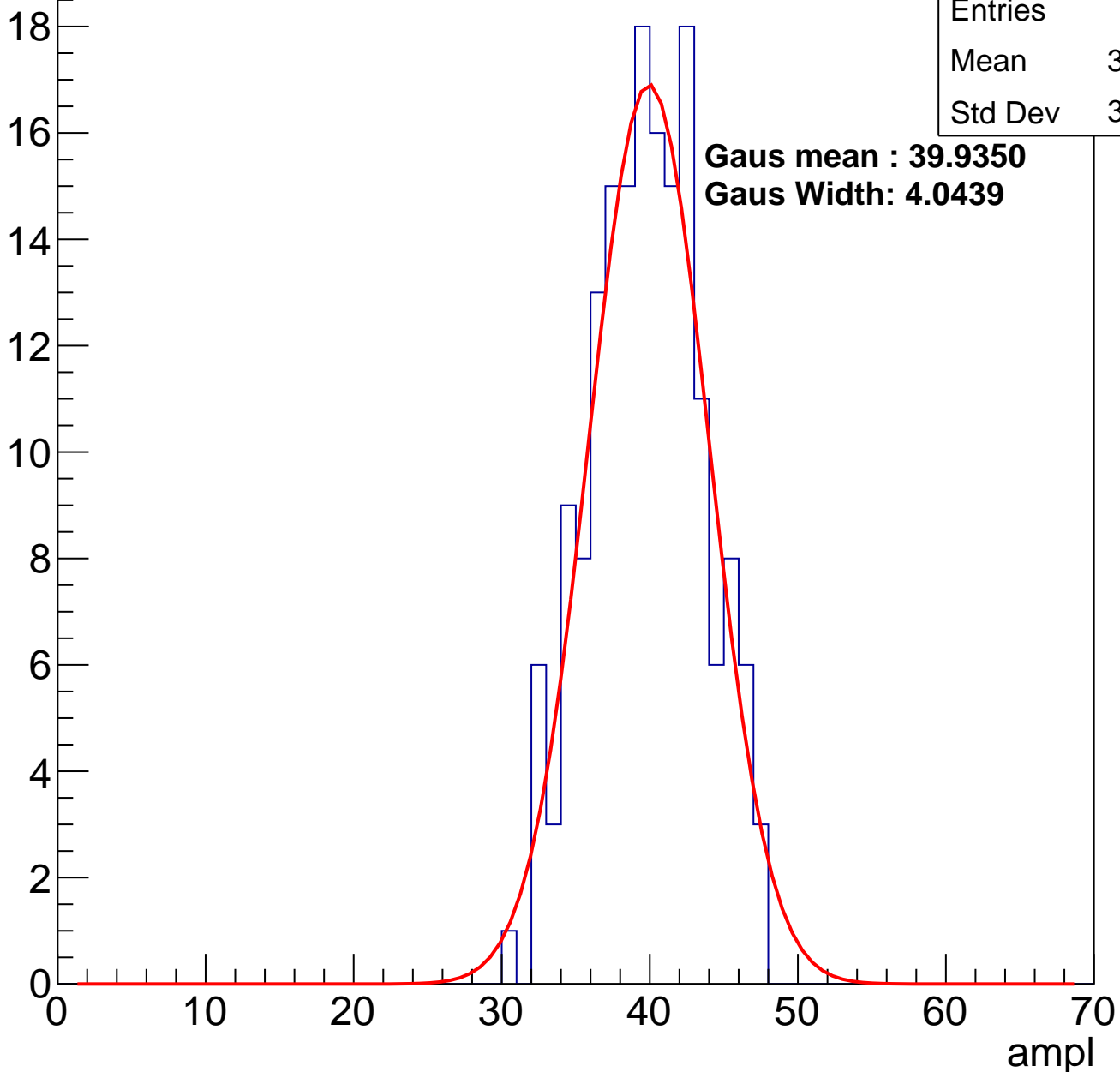
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	39.34
Std Dev	3.704

**Gaus mean : 39.9350**

**Gaus Width: 4.0439**

Entry



# B0L101S, U26-ch113, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

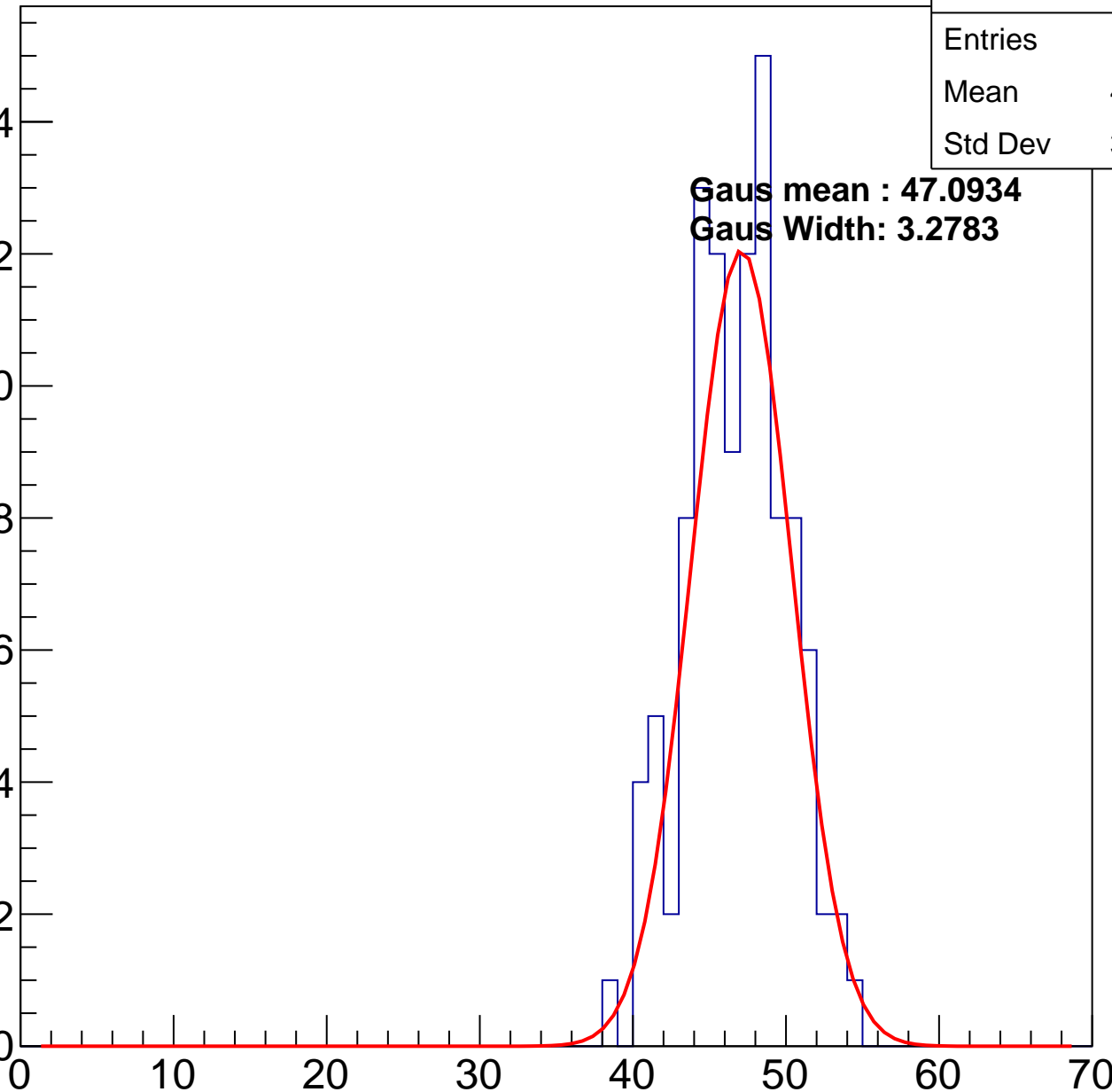
14  
12  
10  
8  
6  
4  
2  
0

Entries	108
Mean	46.32
Std Dev	3.257

**Gaus mean : 47.0934**

**Gaus Width: 3.2783**

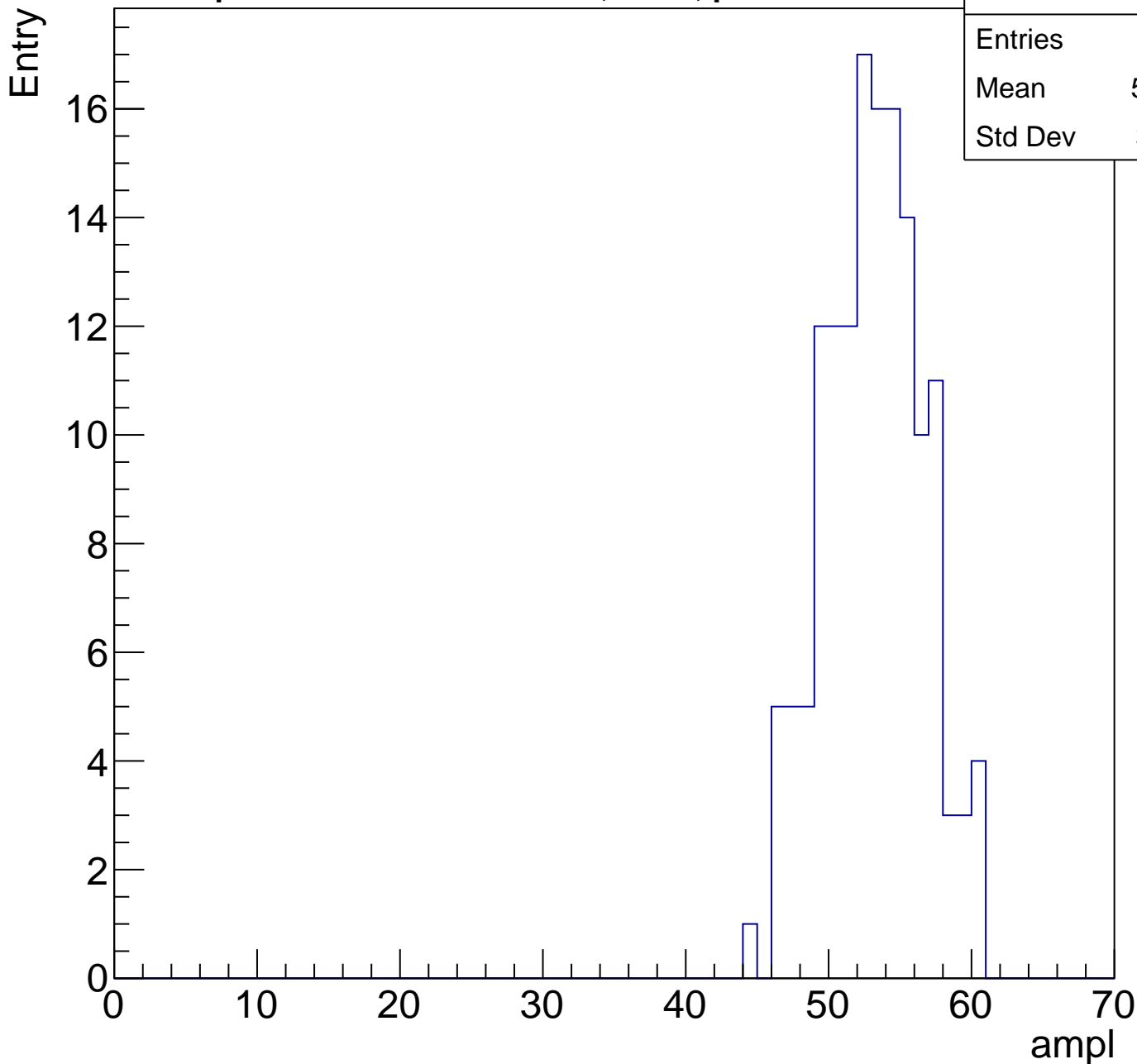
ampl



# B0L101S, U26-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	52.69
Std Dev	3.411



# B0L101S, U26-ch113, adc4

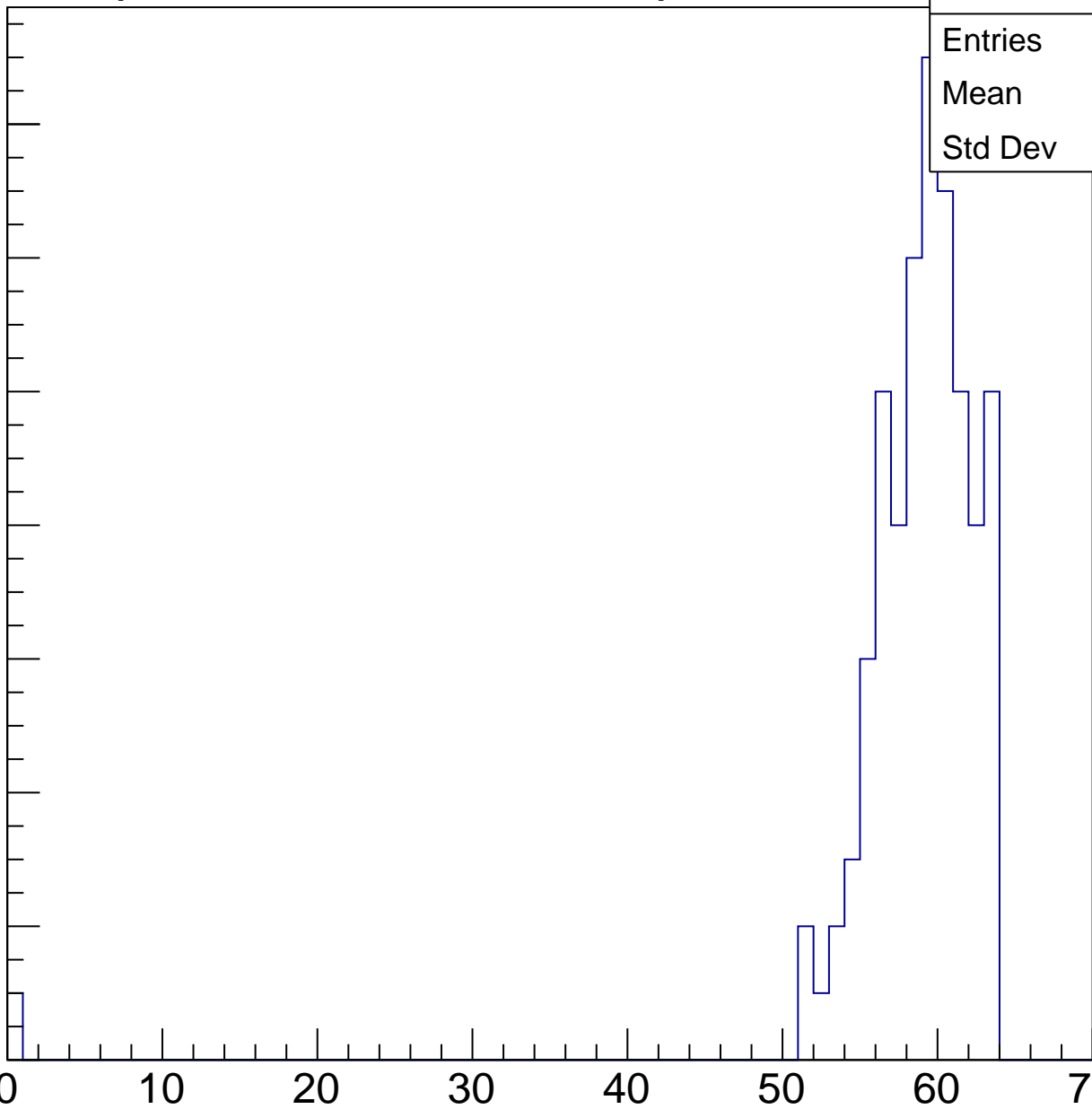
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	101
Mean	58.07
Std Dev	6.473

ampl



# B0L101S, U26-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

43

Mean

59.67

Std Dev

9.358

ampl

0

10

20

30

40

50

60

70

# B0L101S, U26-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	63
Std Dev	0

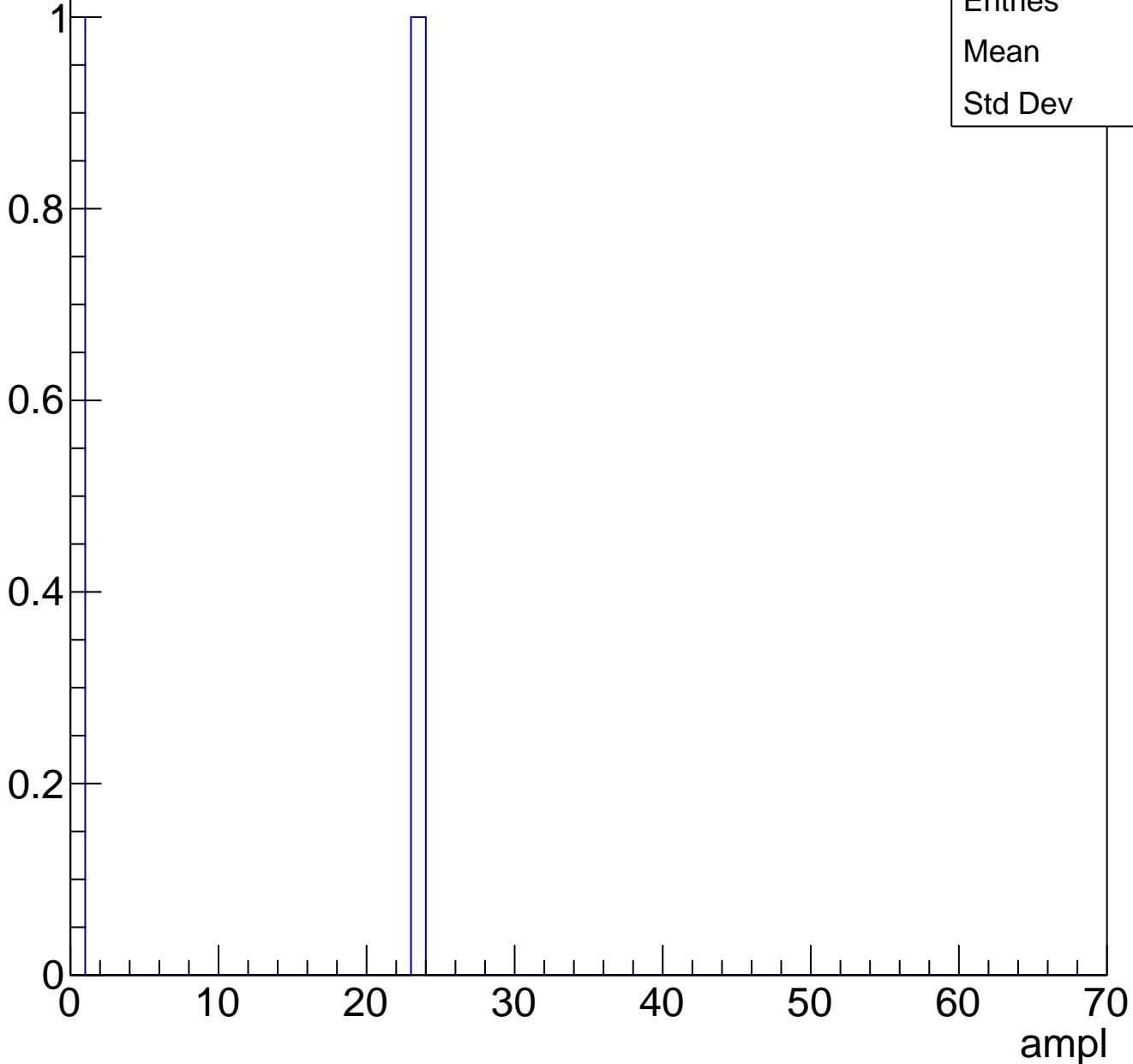
ampl



# B0L101S, U26-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L101S, U26-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	151
Mean	28.74
Std Dev	5.5

**Gaus mean : 29.5070**

**Gaus Width: 3.7635**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

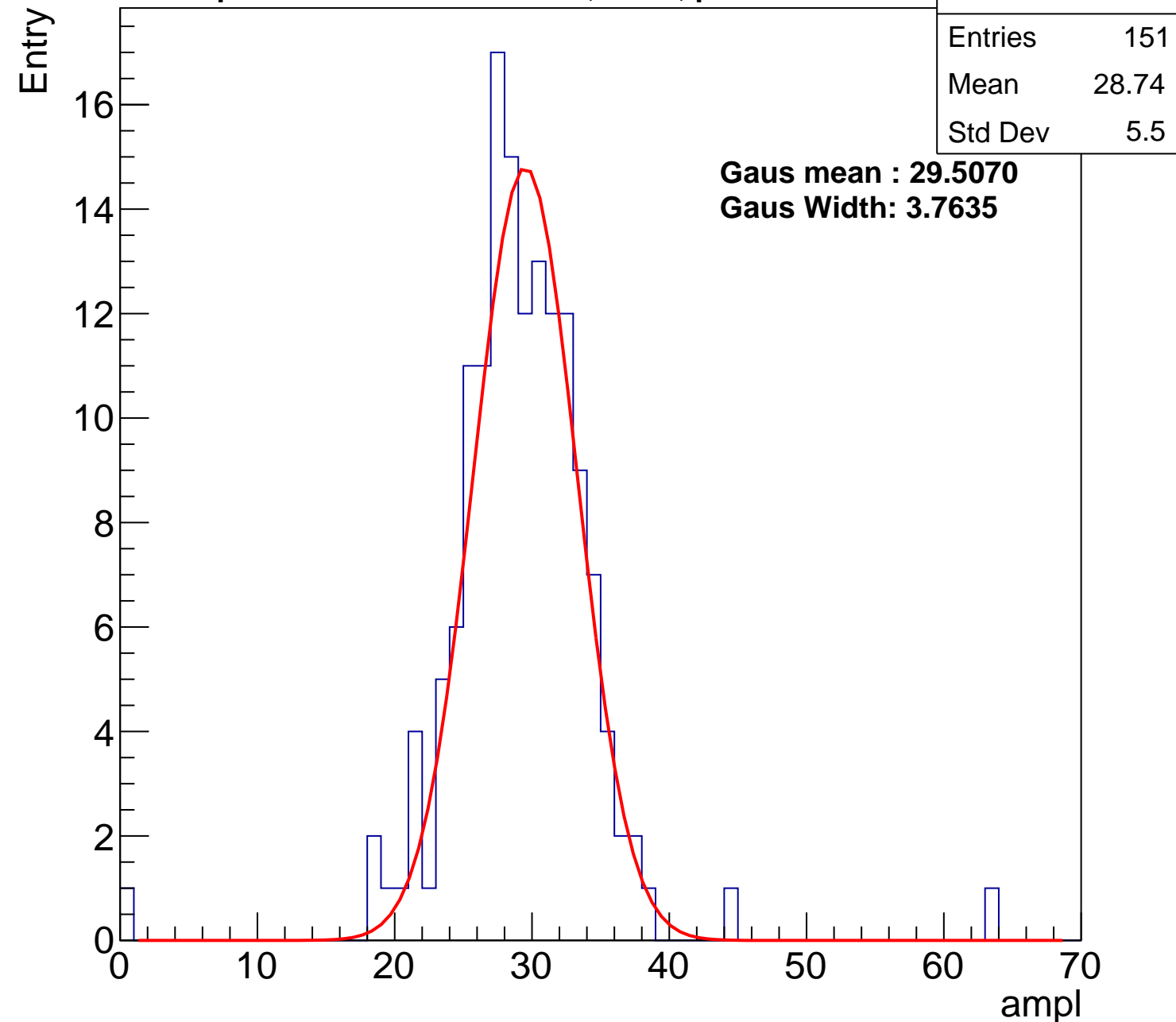
40

50

60

70

ampl



# B0L101S, U26-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	165
Mean	36.38
Std Dev	4.905

**Gaus mean : 36.9920**

**Gaus Width: 4.1231**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

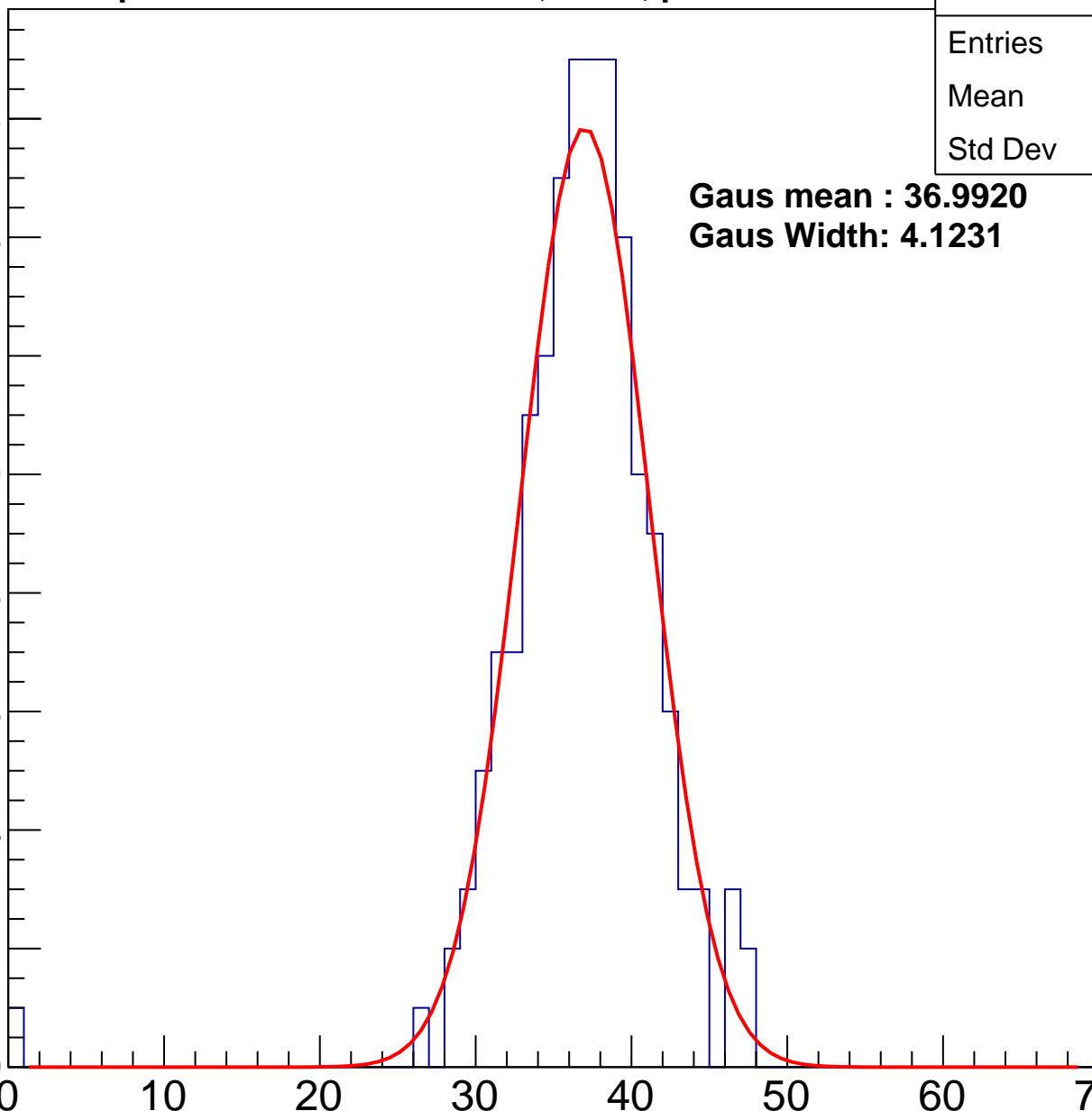
40

50

60

70

ampl



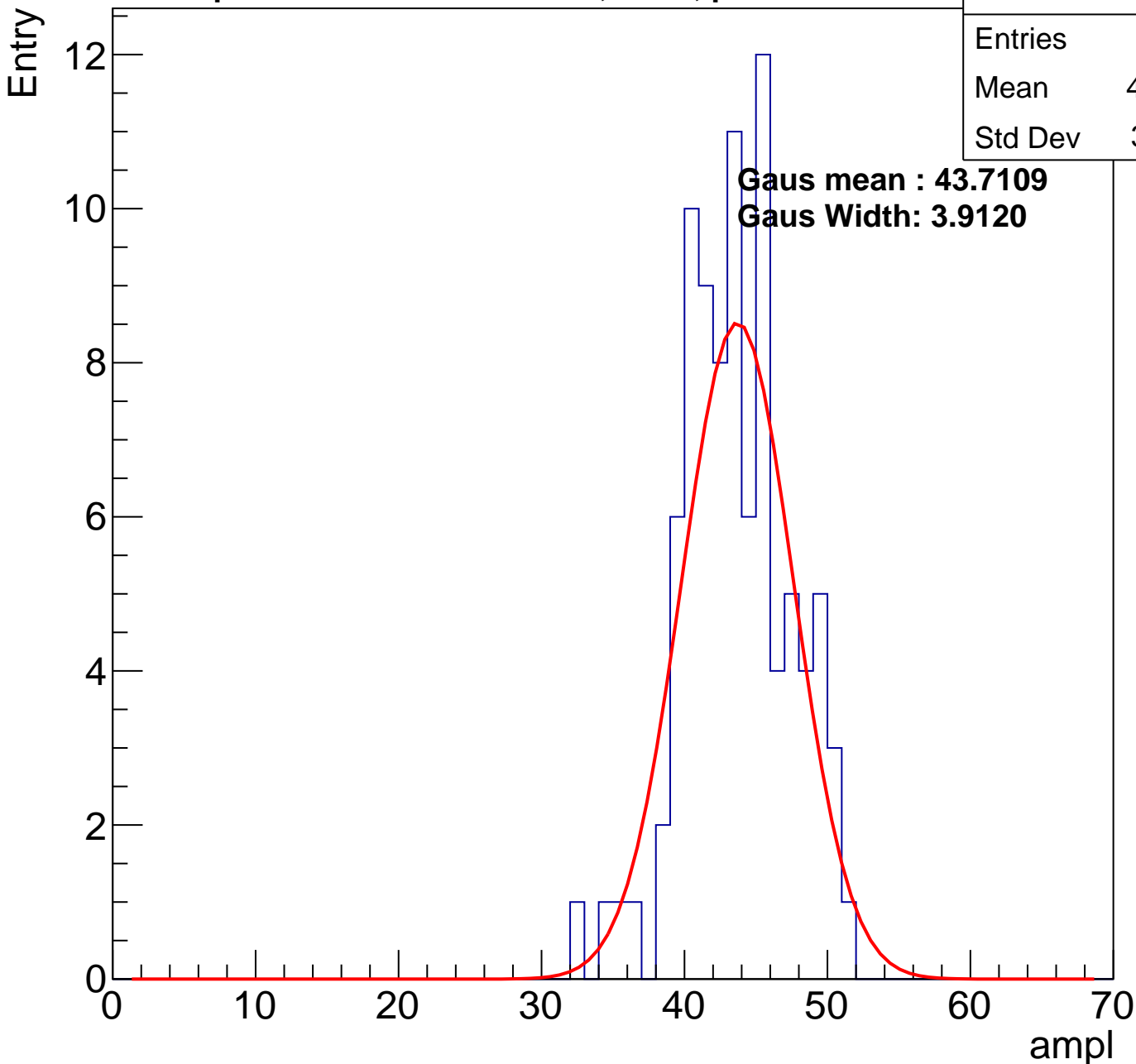
# B0L101S, U26-ch114, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	90
Mean	43.18
Std Dev	3.731

**Gaus mean : 43.7109**

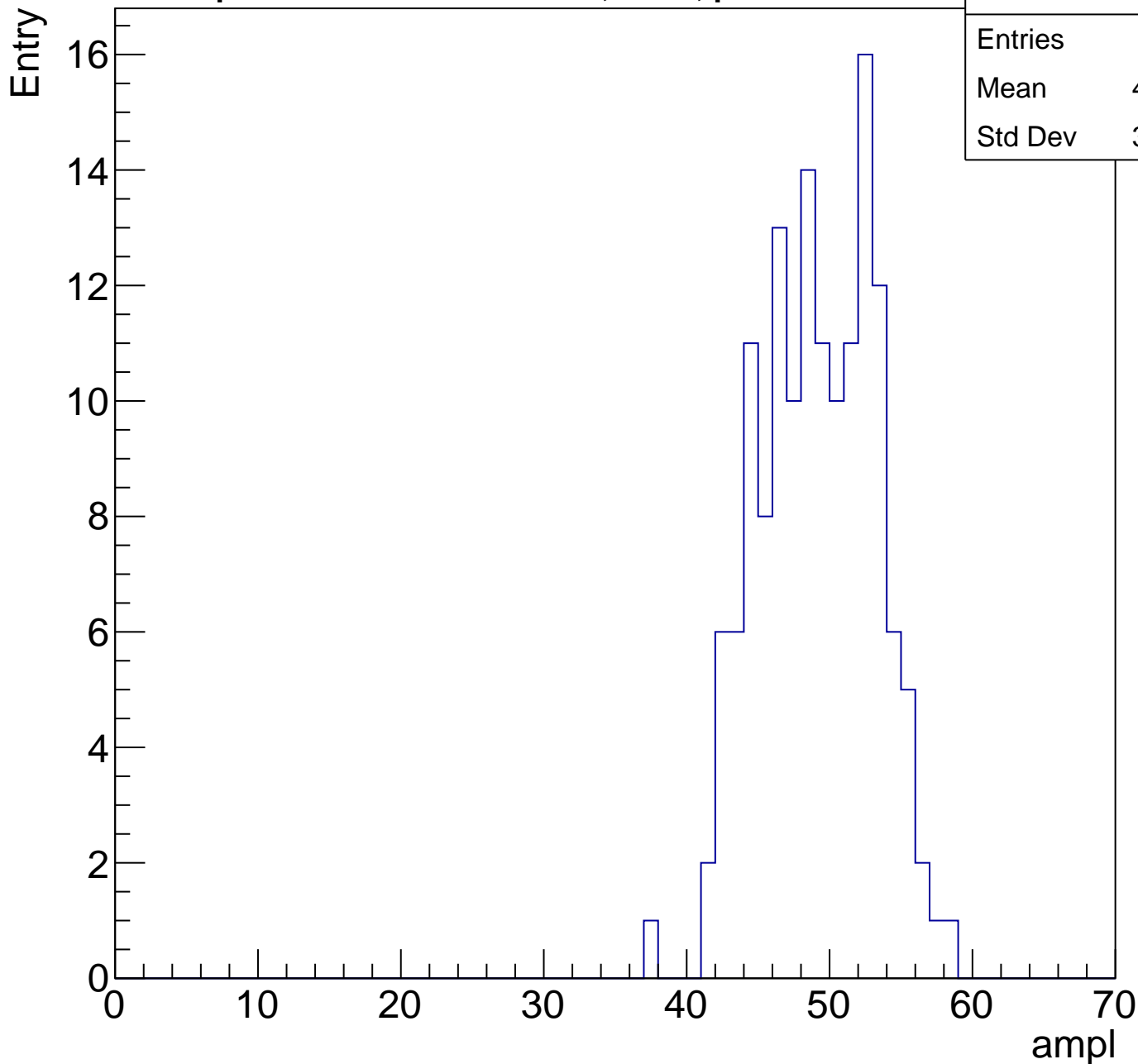
**Gaus Width: 3.9120**



# B0L101S, U26-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	146
Mean	48.68
Std Dev	3.972



# B0L101S, U26-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

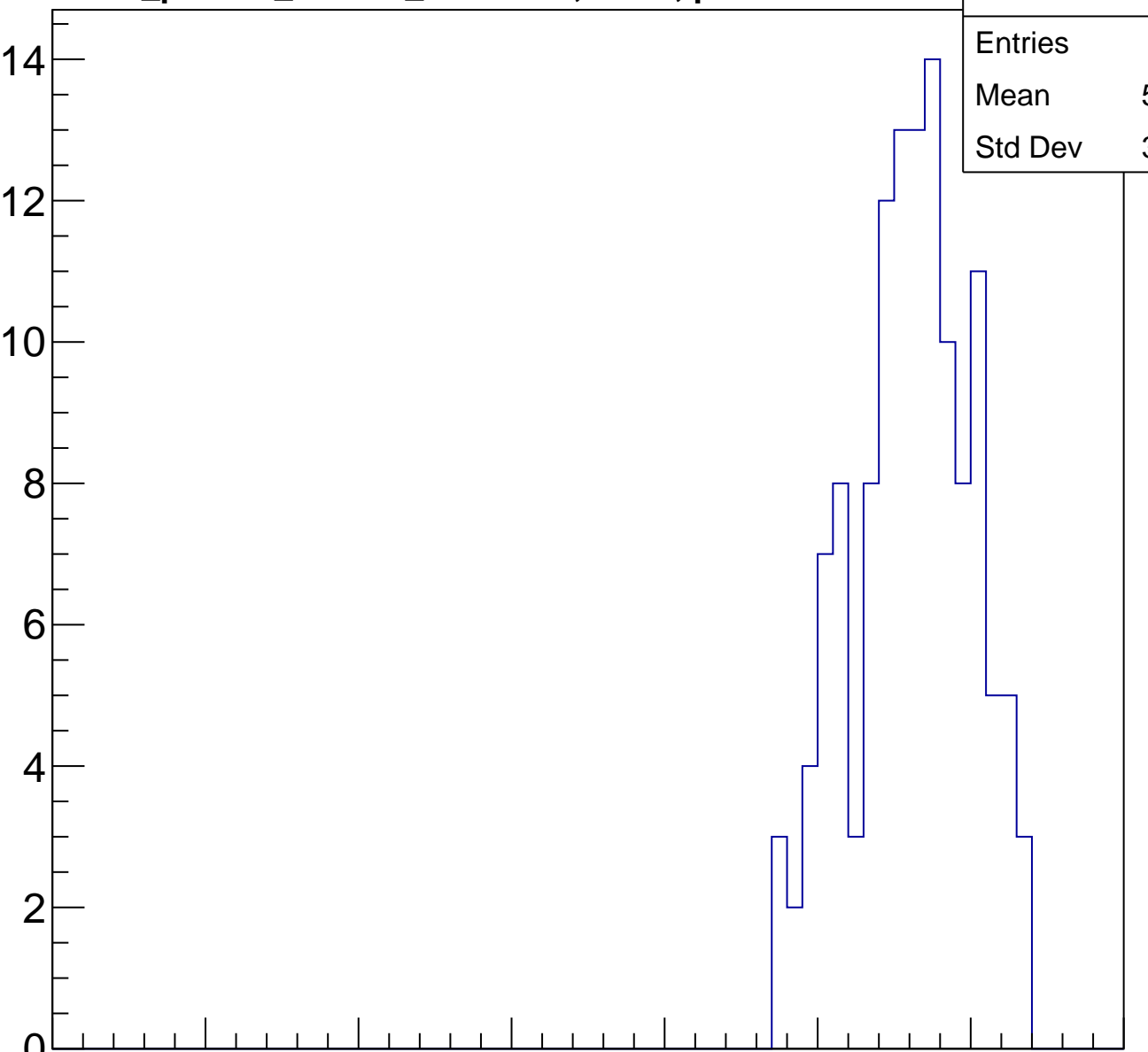
Entries	129
Mean	55.63
Std Dev	3.874

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch114, adc5

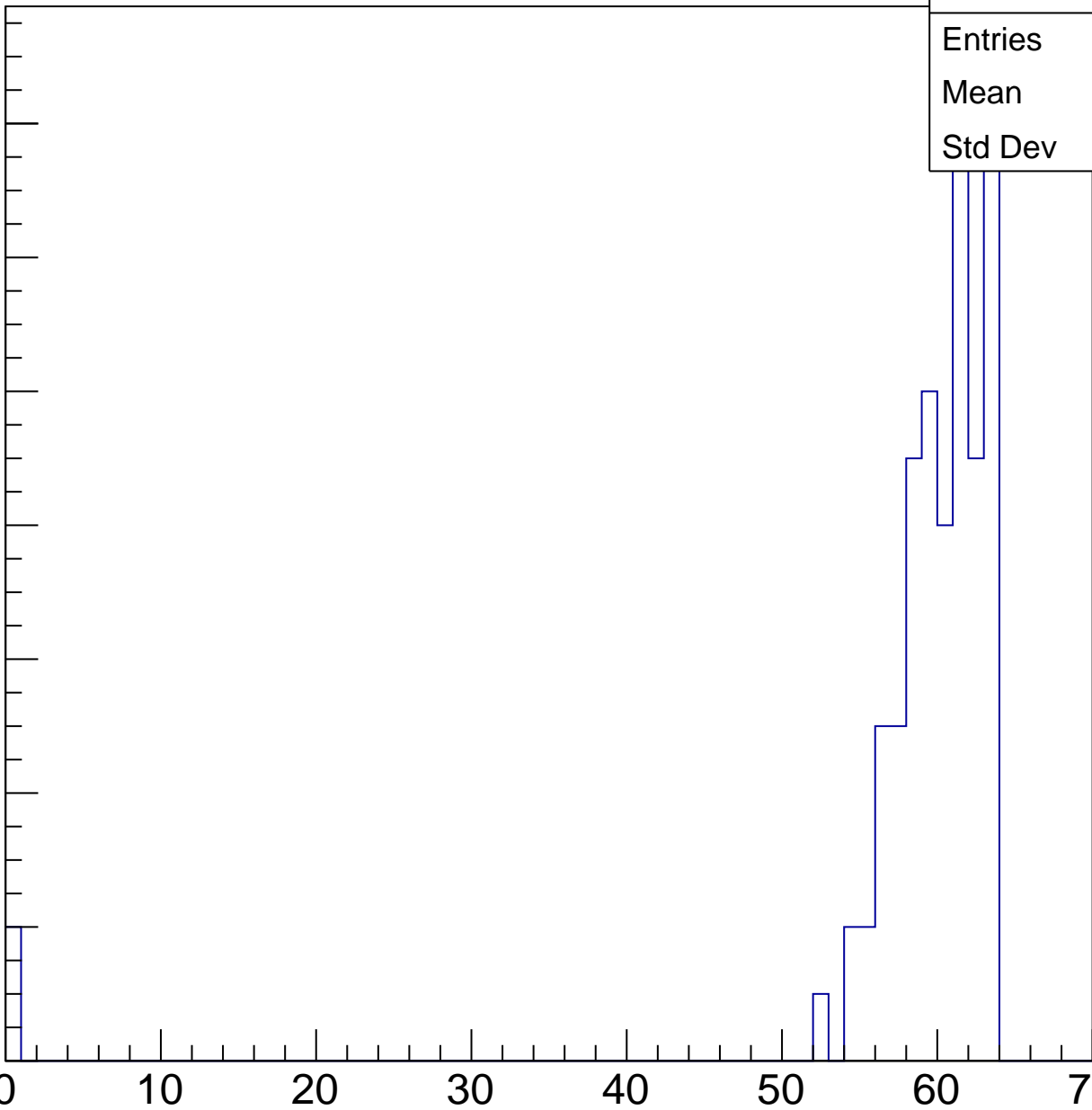
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	82
Mean	58.32
Std Dev	9.565

ampl

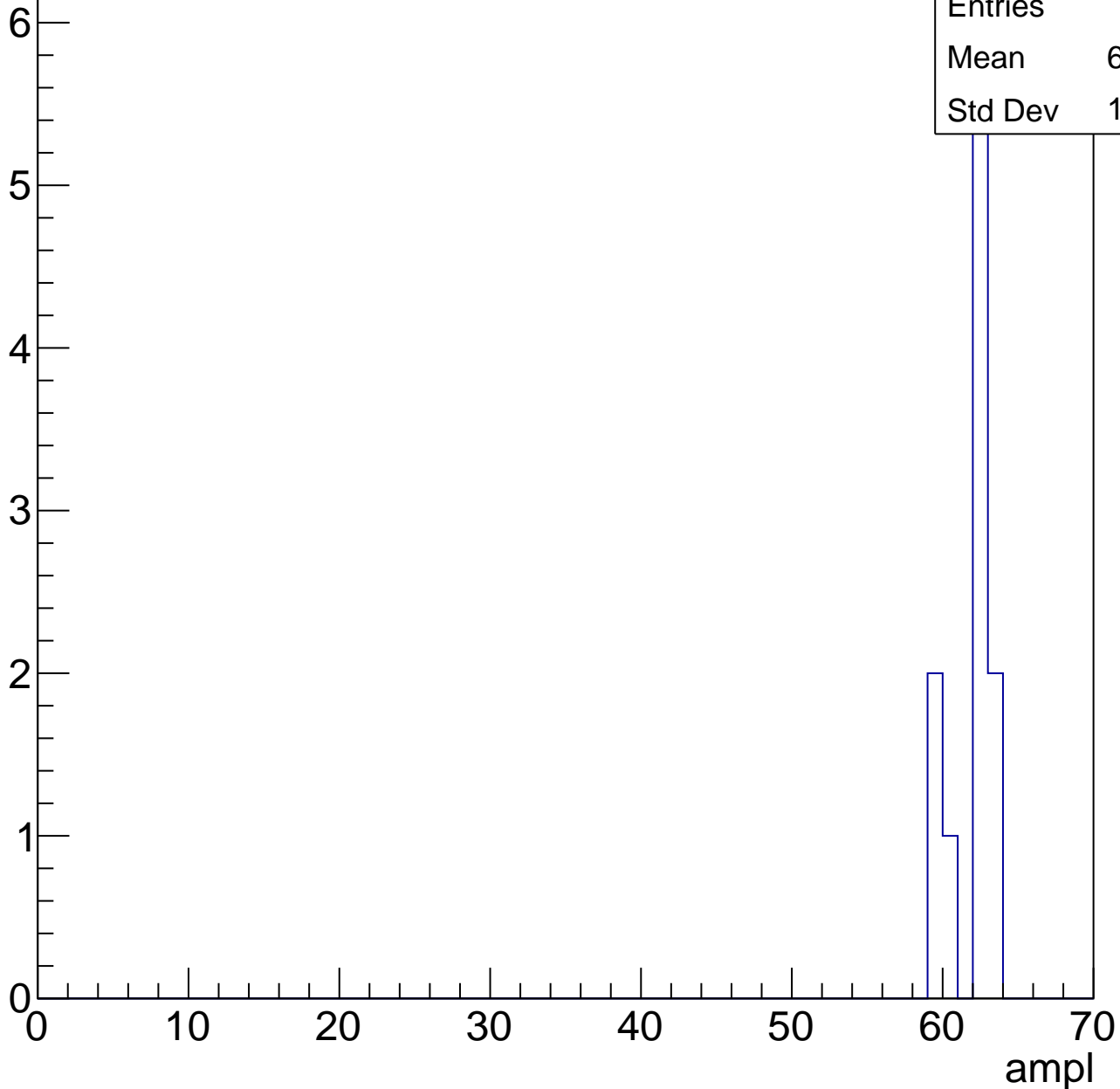


# B0L101S, U26-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	11
Mean	61.45
Std Dev	1.373

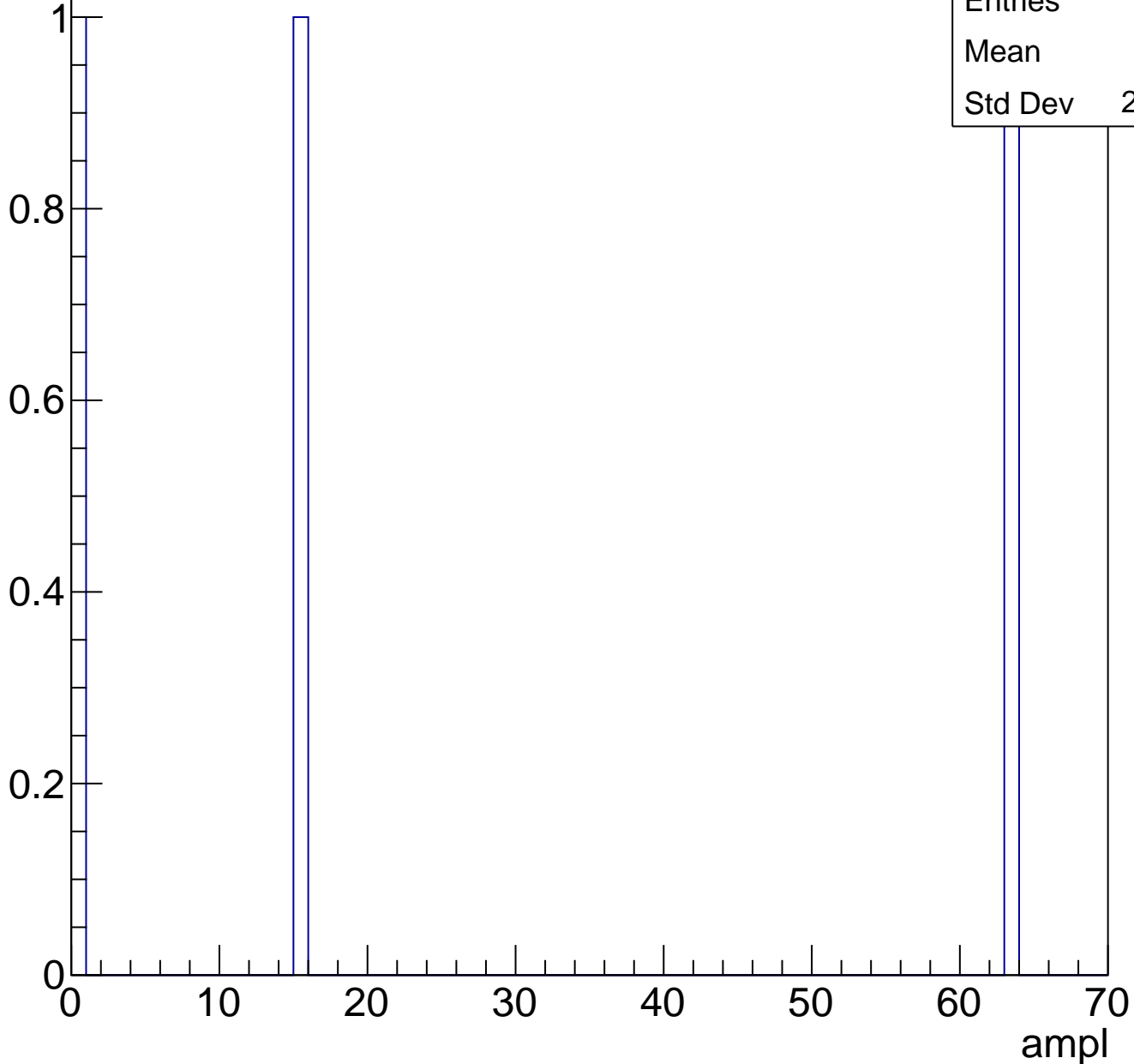




# B0L101S, U26-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26
Std Dev	26.87

# B0L101S, U26-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	160
Mean	27.8
Std Dev	4.91

**Gaus mean : 28.4119**

**Gaus Width: 3.9469**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

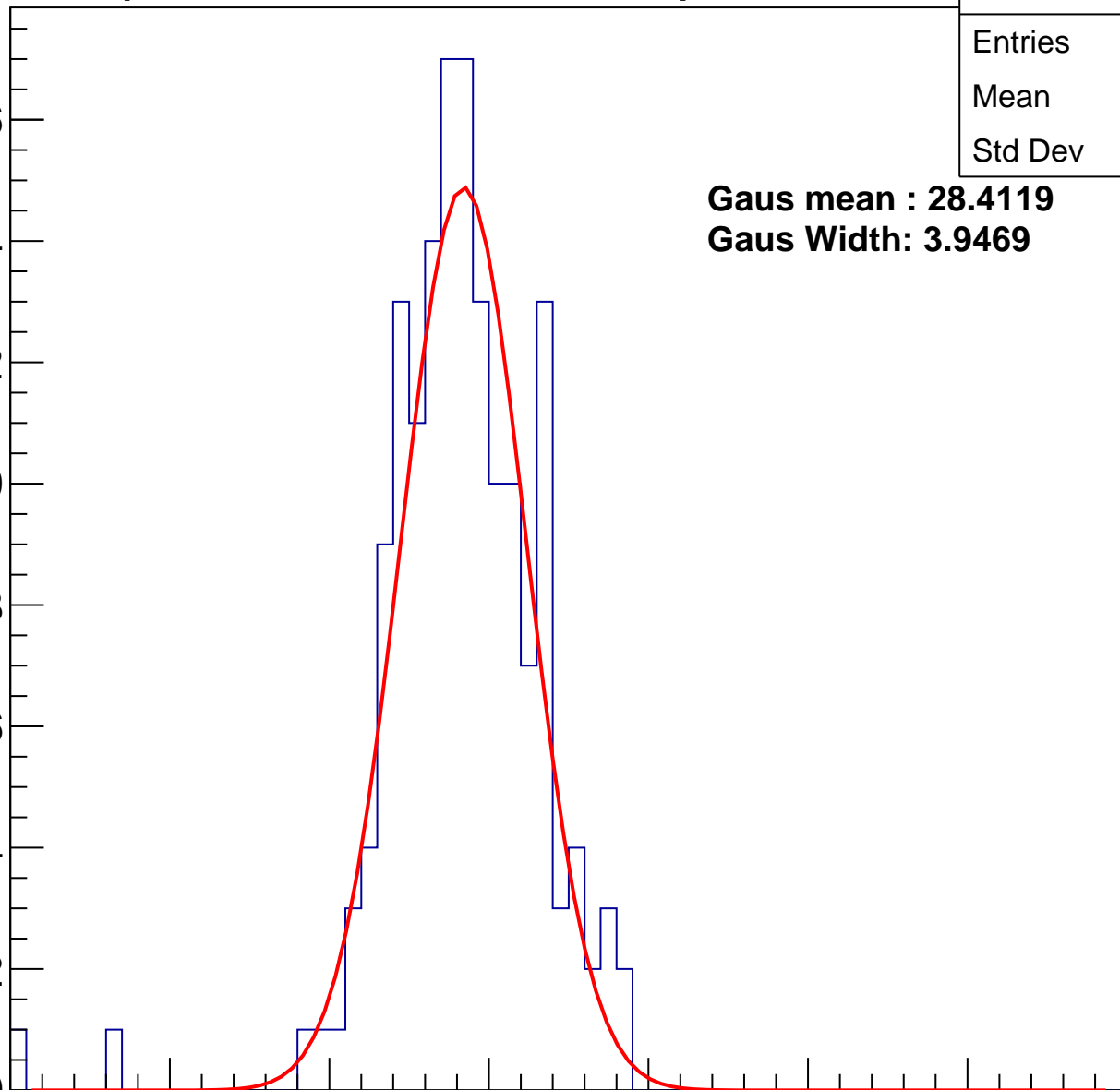
40

50

60

70

ampl



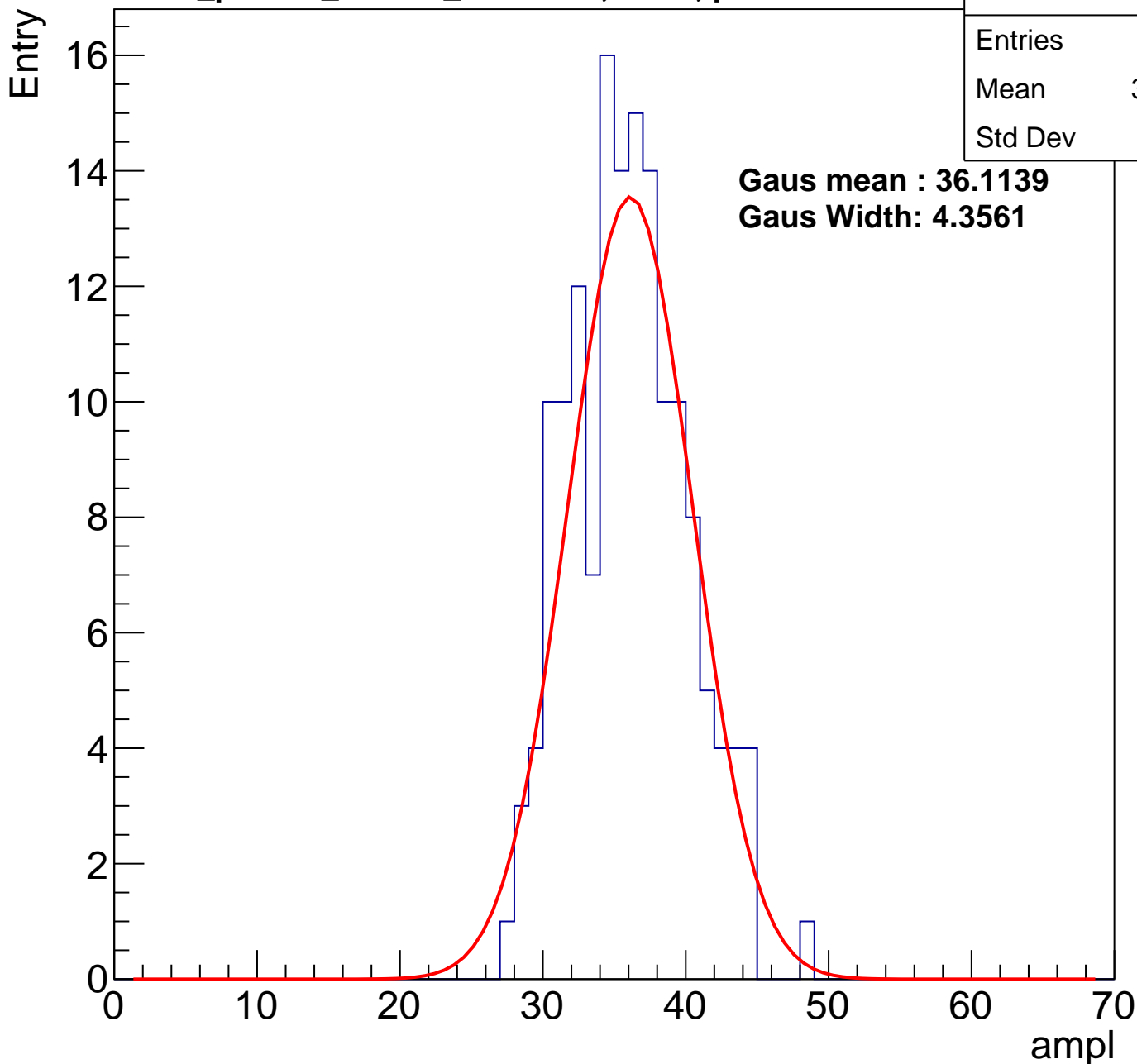
# B0L101S, U26-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	152
Mean	35.55
Std Dev	4.07

**Gaus mean : 36.1139**

**Gaus Width: 4.3561**



# B0L101S, U26-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

128

Mean

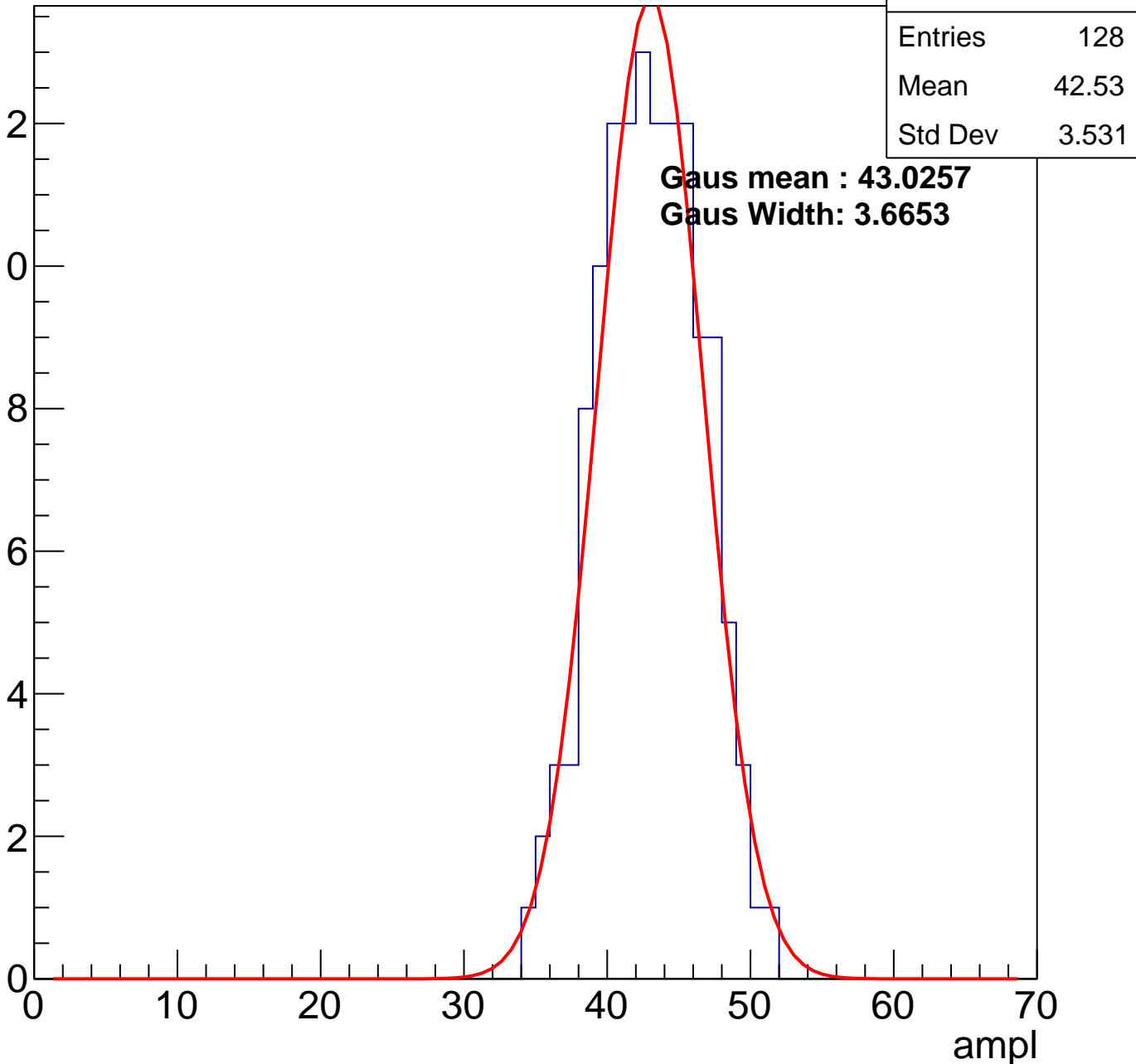
42.53

Std Dev

3.531

**Gaus mean : 43.0257**

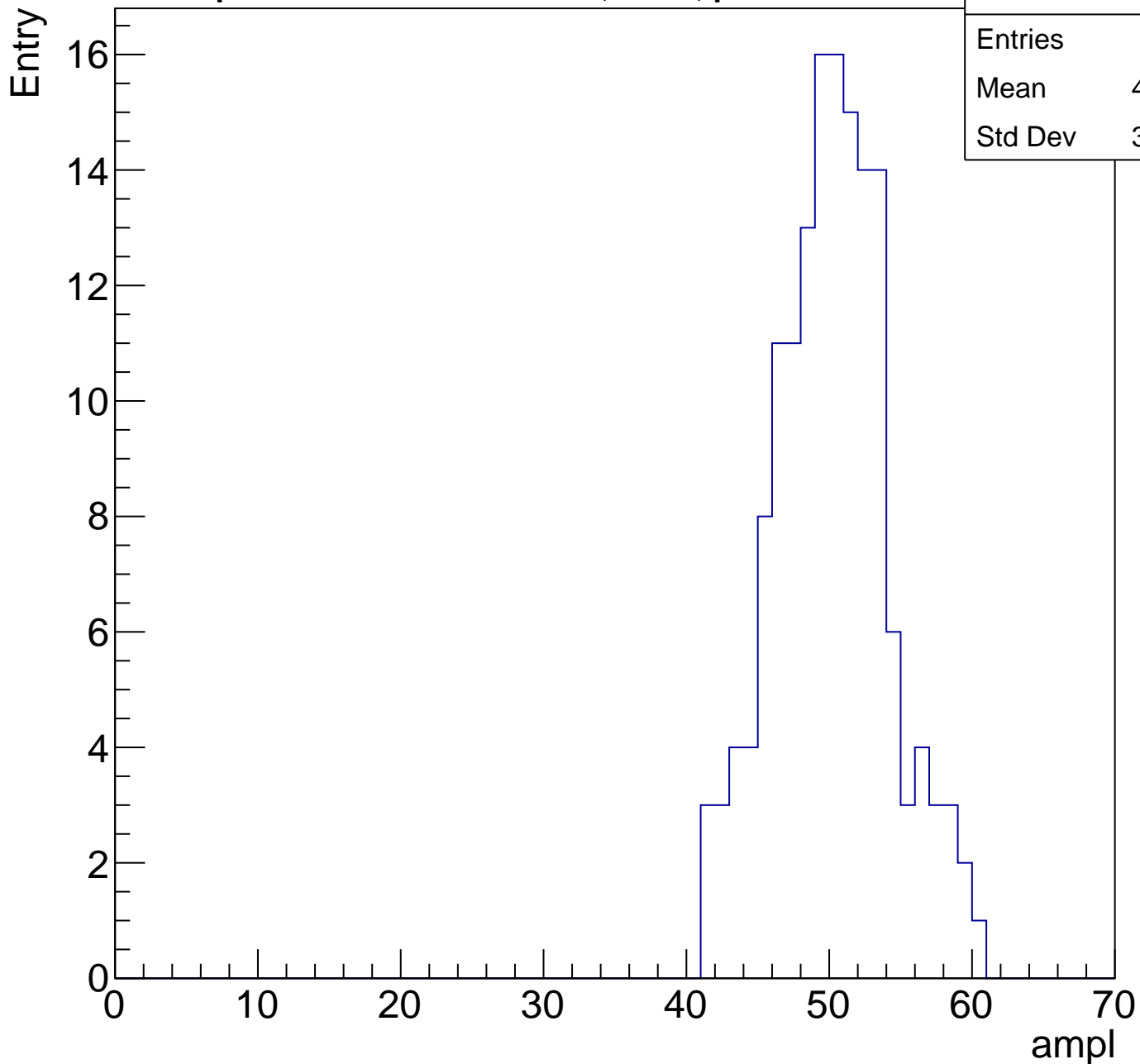
**Gaus Width: 3.6653**



# B0L101S, U26-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	154
Mean	49.73
Std Dev	3.964



# B0L101S, U26-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

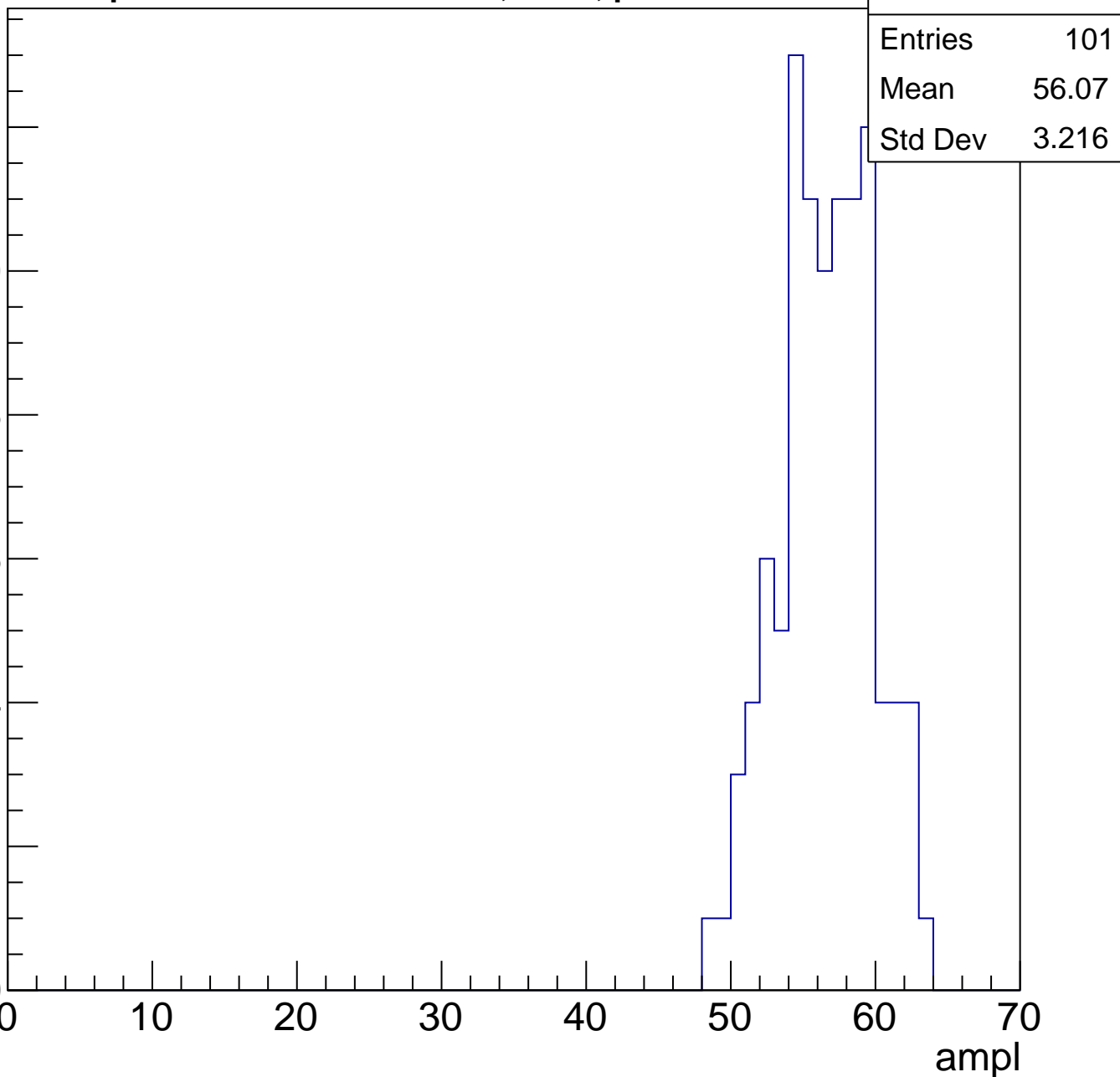
50

60

70

ampl

Entries	101
Mean	56.07
Std Dev	3.216



# B0L101S, U26-ch115, adc5

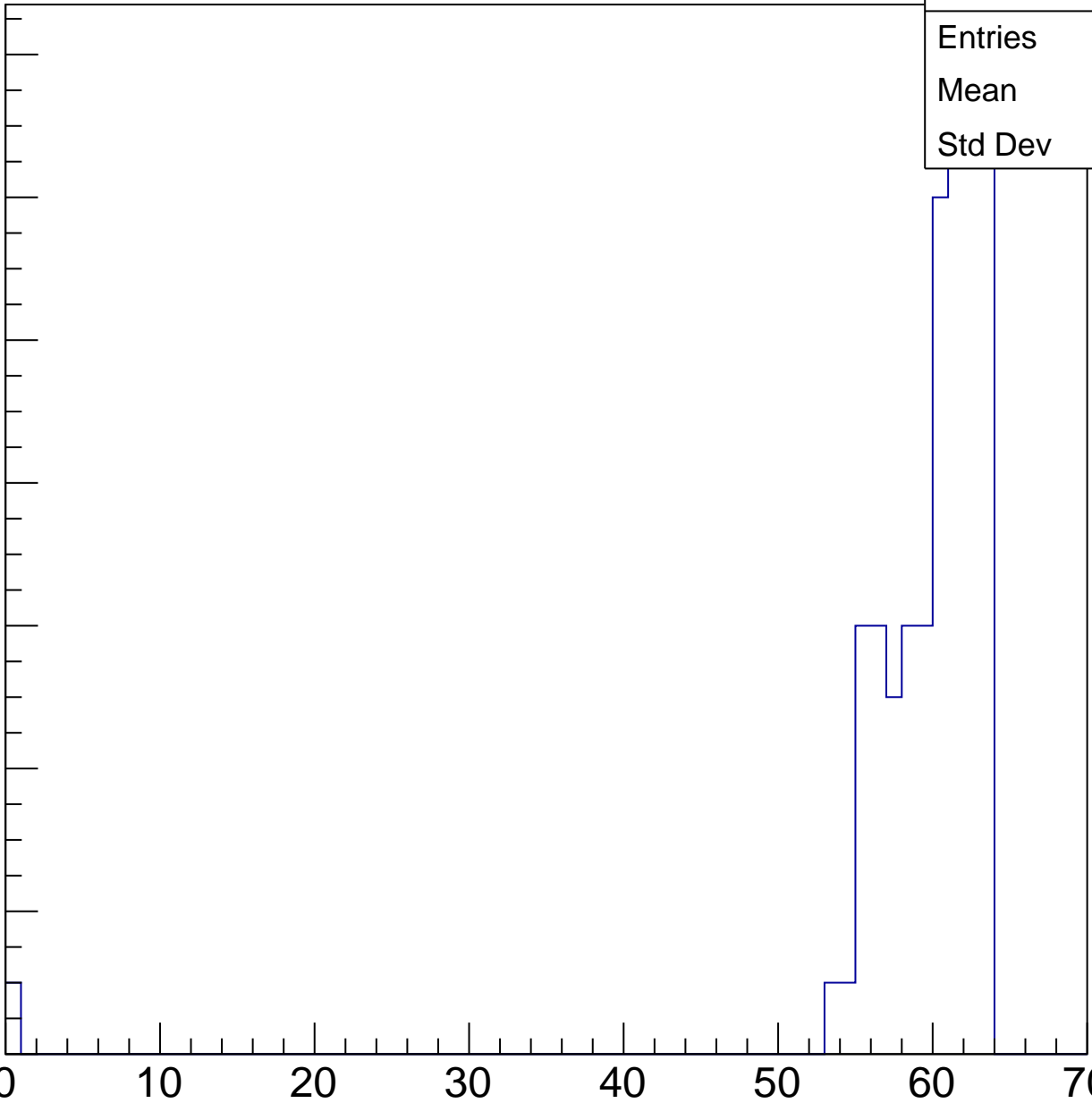
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	85
Mean	59.08
Std Dev	6.968

ampl

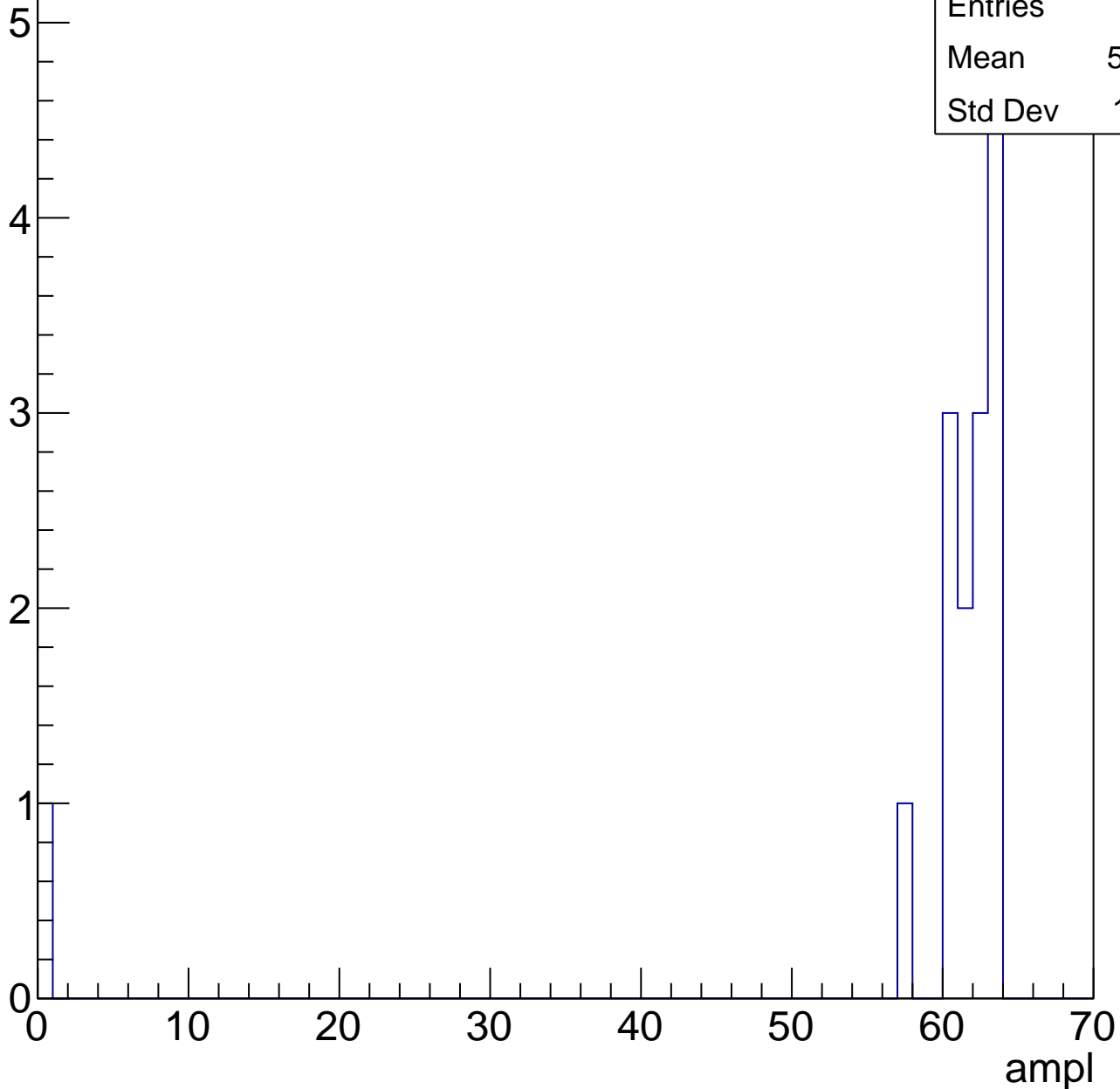


# B0L101S, U26-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.33
Std Dev	15.41





# B0L101S, U26-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch116, adc0

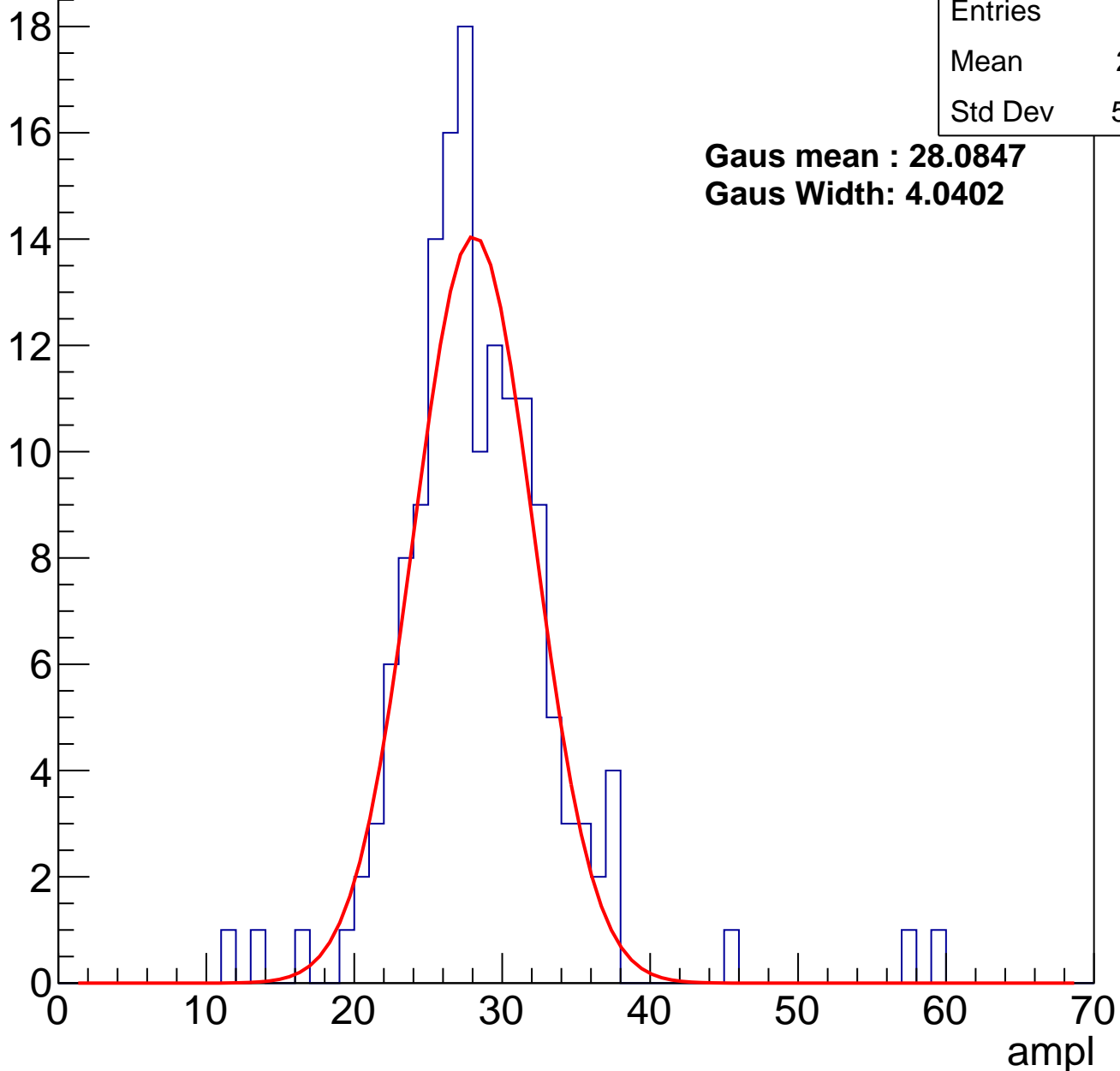
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	153
Mean	28.01
Std Dev	5.719

**Gaus mean : 28.0847**

**Gaus Width: 4.0402**

Entry



# B0L101S, U26-ch116, adc1

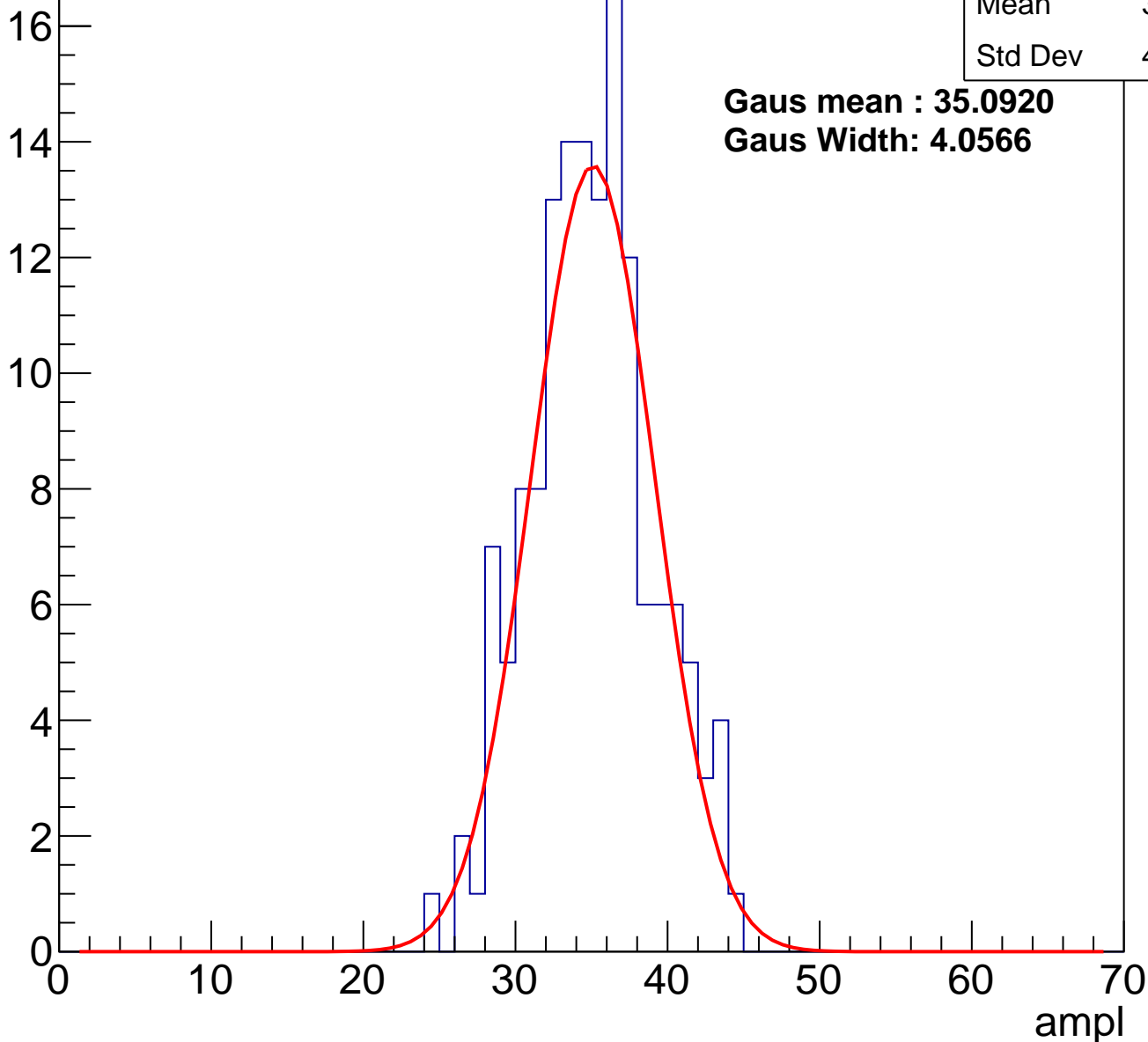
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	146
Mean	34.56
Std Dev	4.043

**Gaus mean : 35.0920**

**Gaus Width: 4.0566**



# B0L101S, U26-ch116, adc2

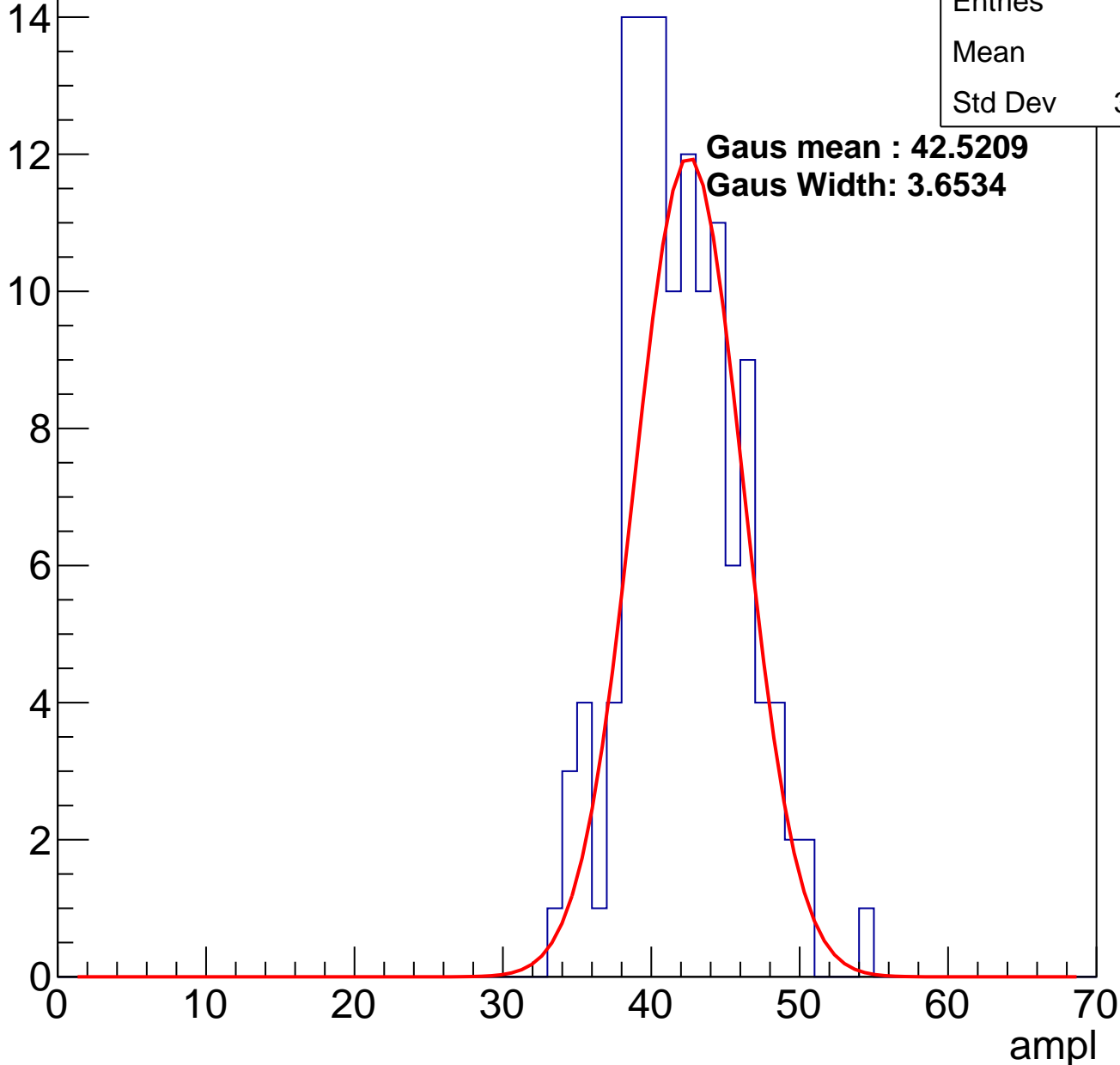
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	41.6
Std Dev	3.849

**Gaus mean : 42.5209**

**Gaus Width: 3.6534**

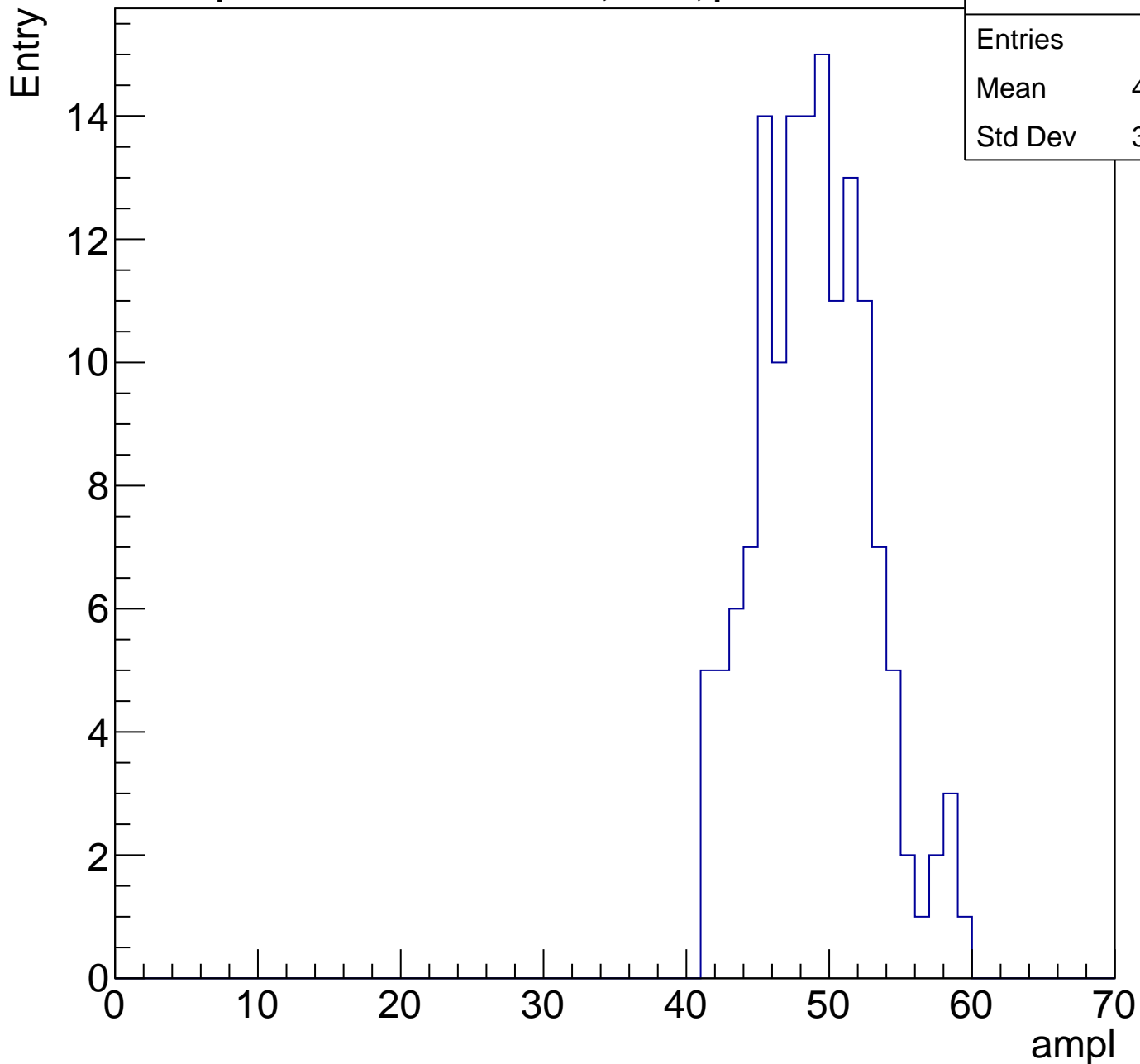
Entry



# B0L101S, U26-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

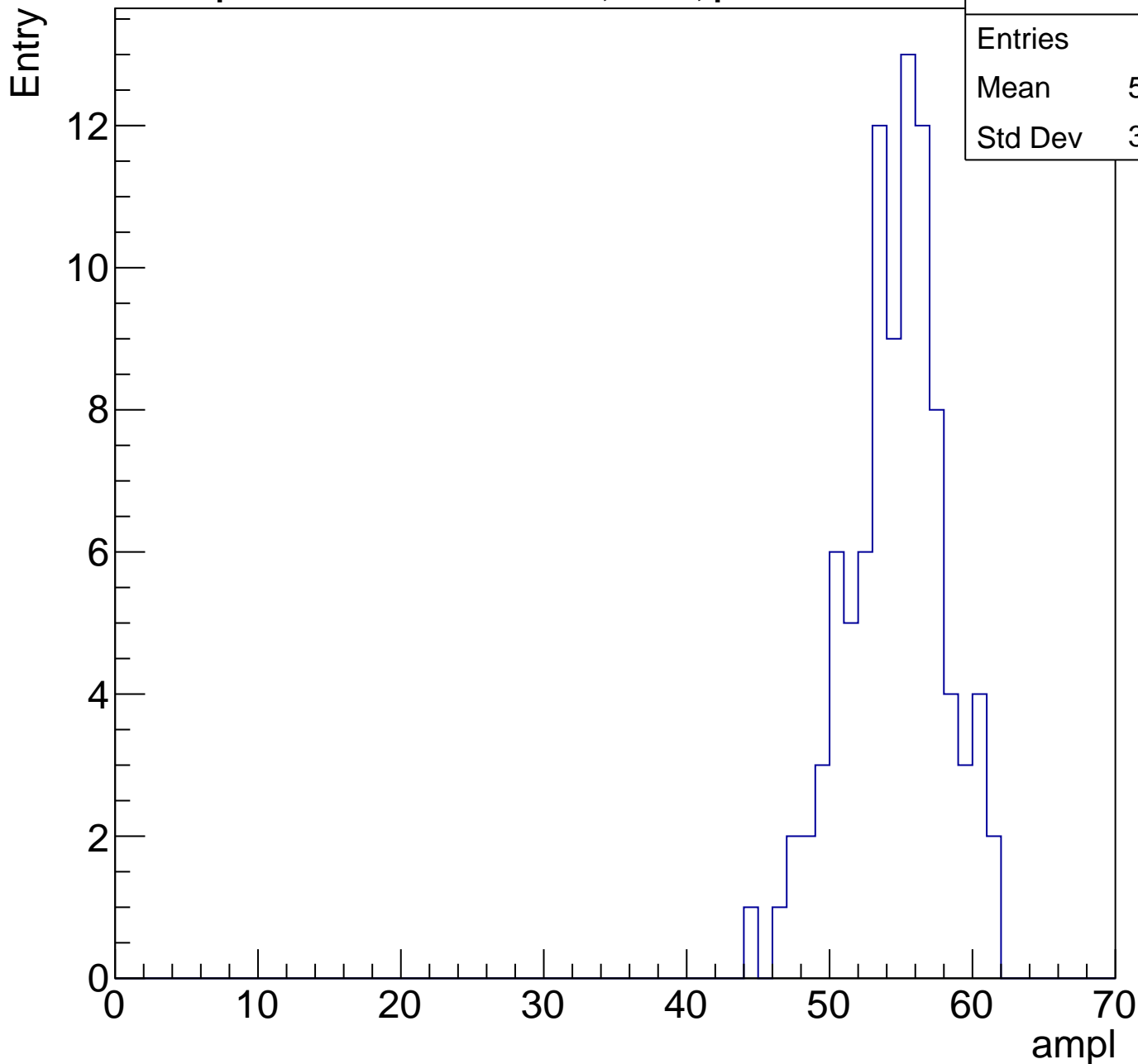
Entries	146
Mean	48.46
Std Dev	3.959



# B0L101S, U26-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	93
Mean	54.09
Std Dev	3.438



# B0L101S, U26-ch116, adc5

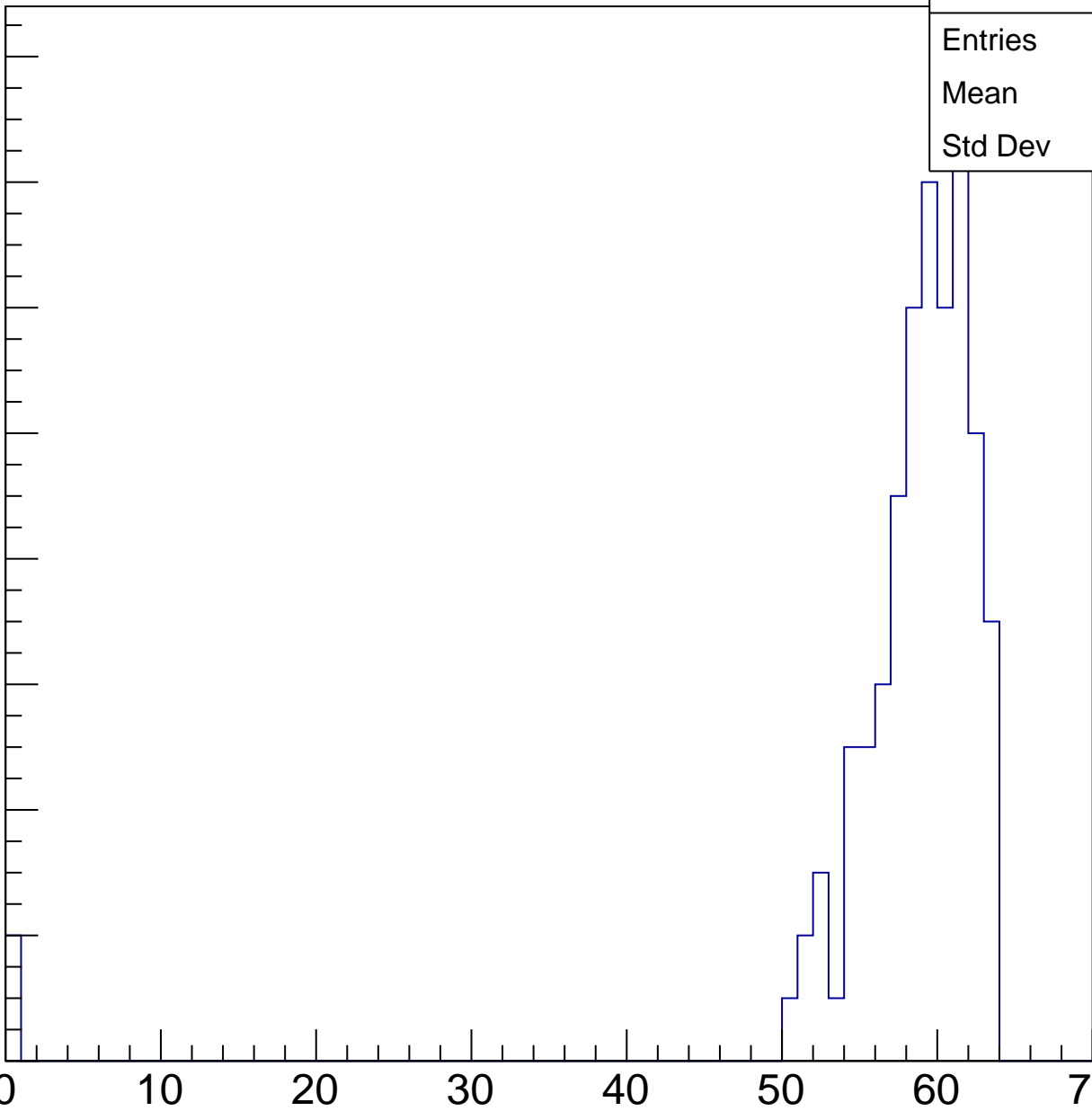
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	105
Mean	57.47
Std Dev	8.559

ampl



# B0L101S, U26-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

34

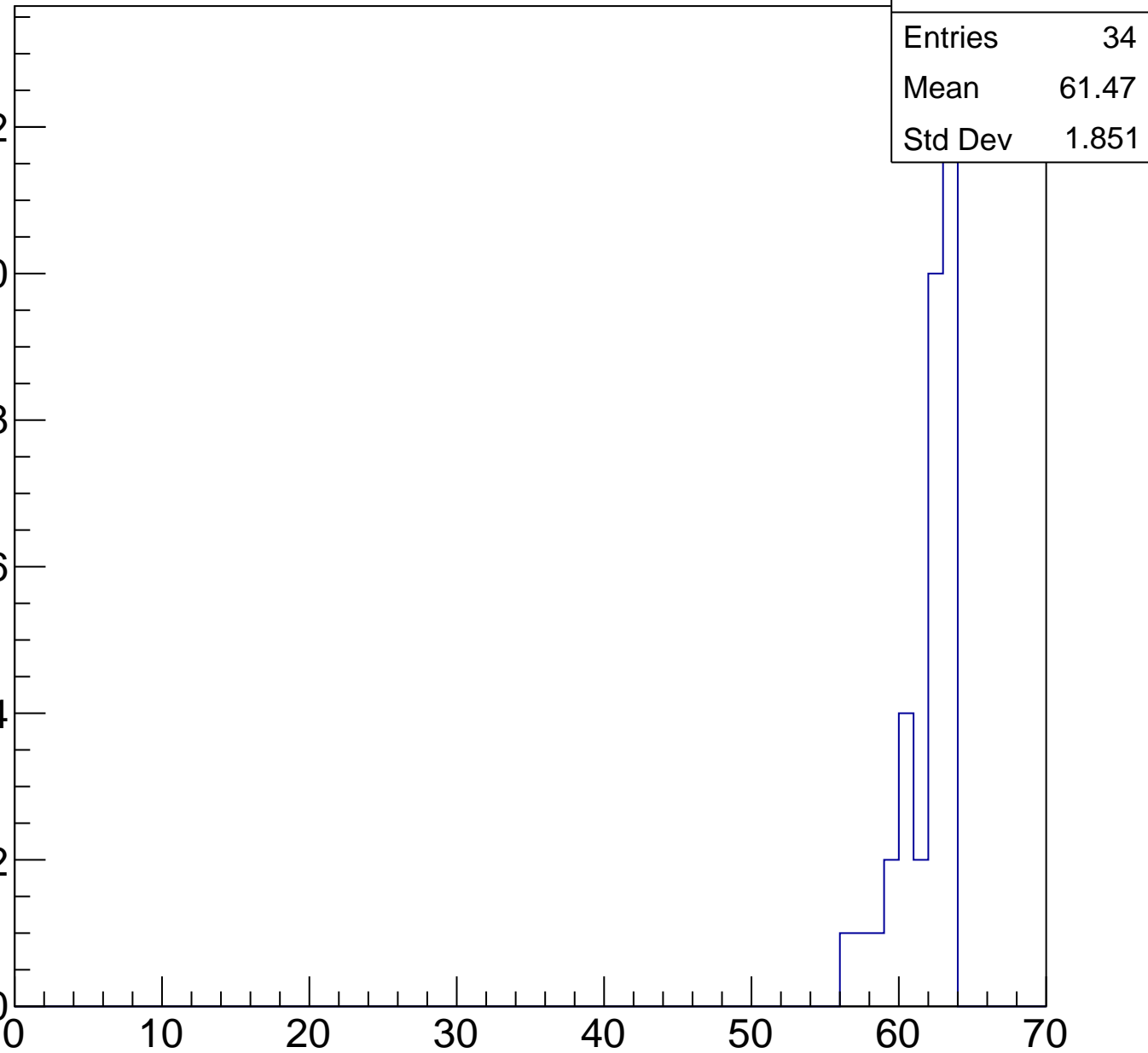
Mean

61.47

Std Dev

1.851

ampl





# B0L101S, U26-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L101S, U26-ch117, adc0

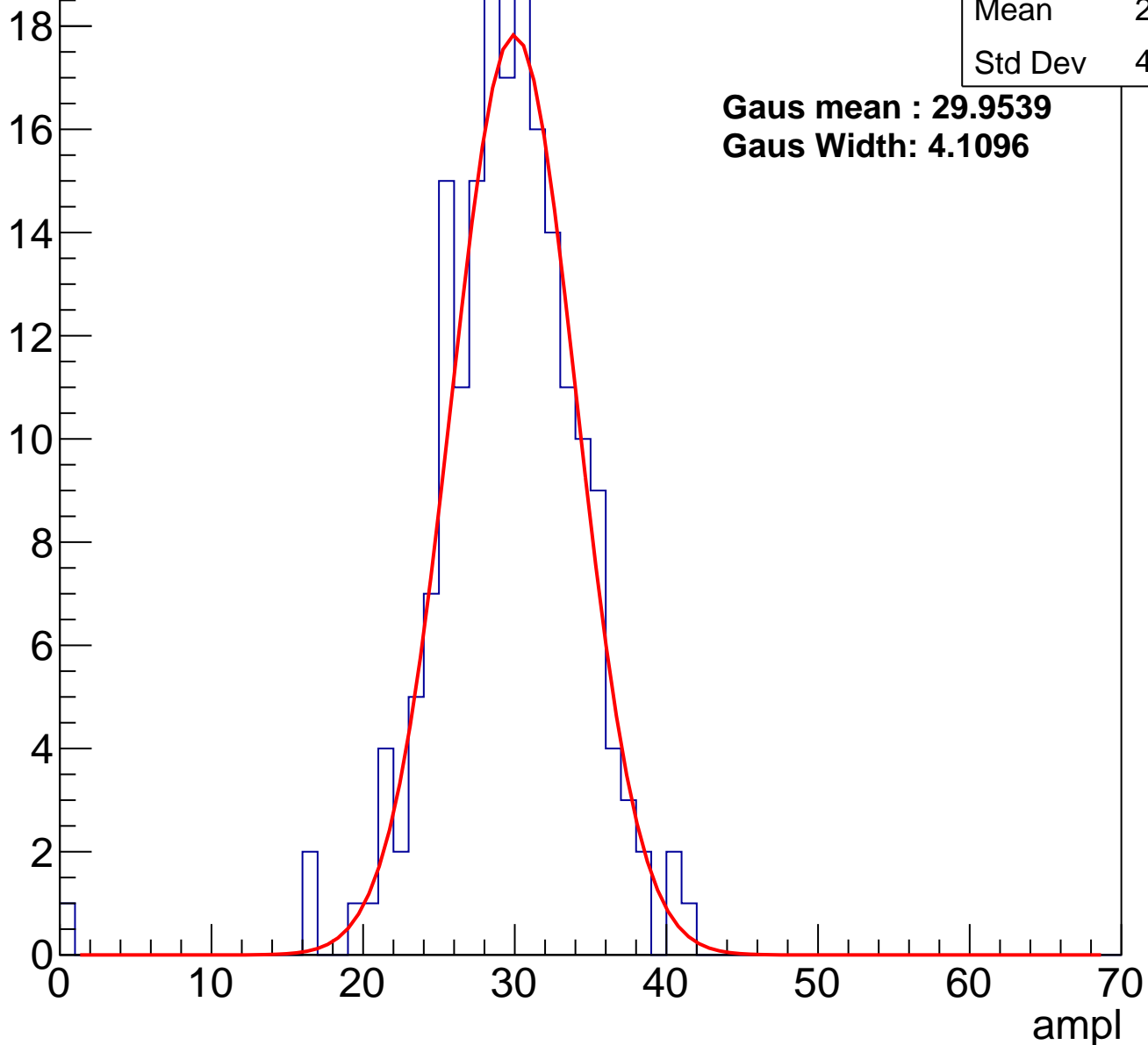
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	191
Mean	29.09
Std Dev	4.789

**Gaus mean : 29.9539**

**Gaus Width: 4.1096**

Entry



# B0L101S, U26-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	37.11
Std Dev	3.699

**Gaus mean : 37.7500**

**Gaus Width: 3.7814**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

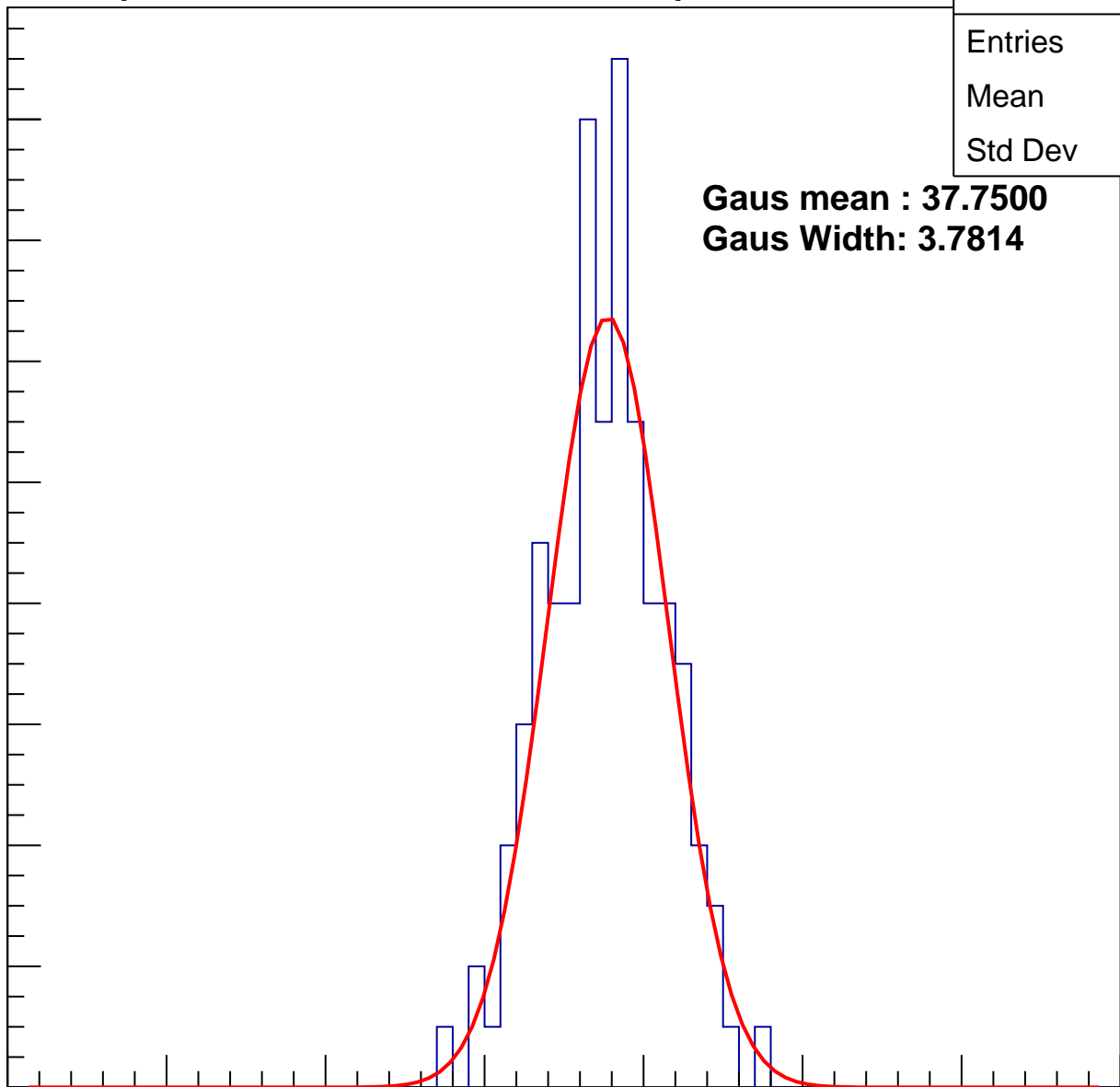
40

50

60

70

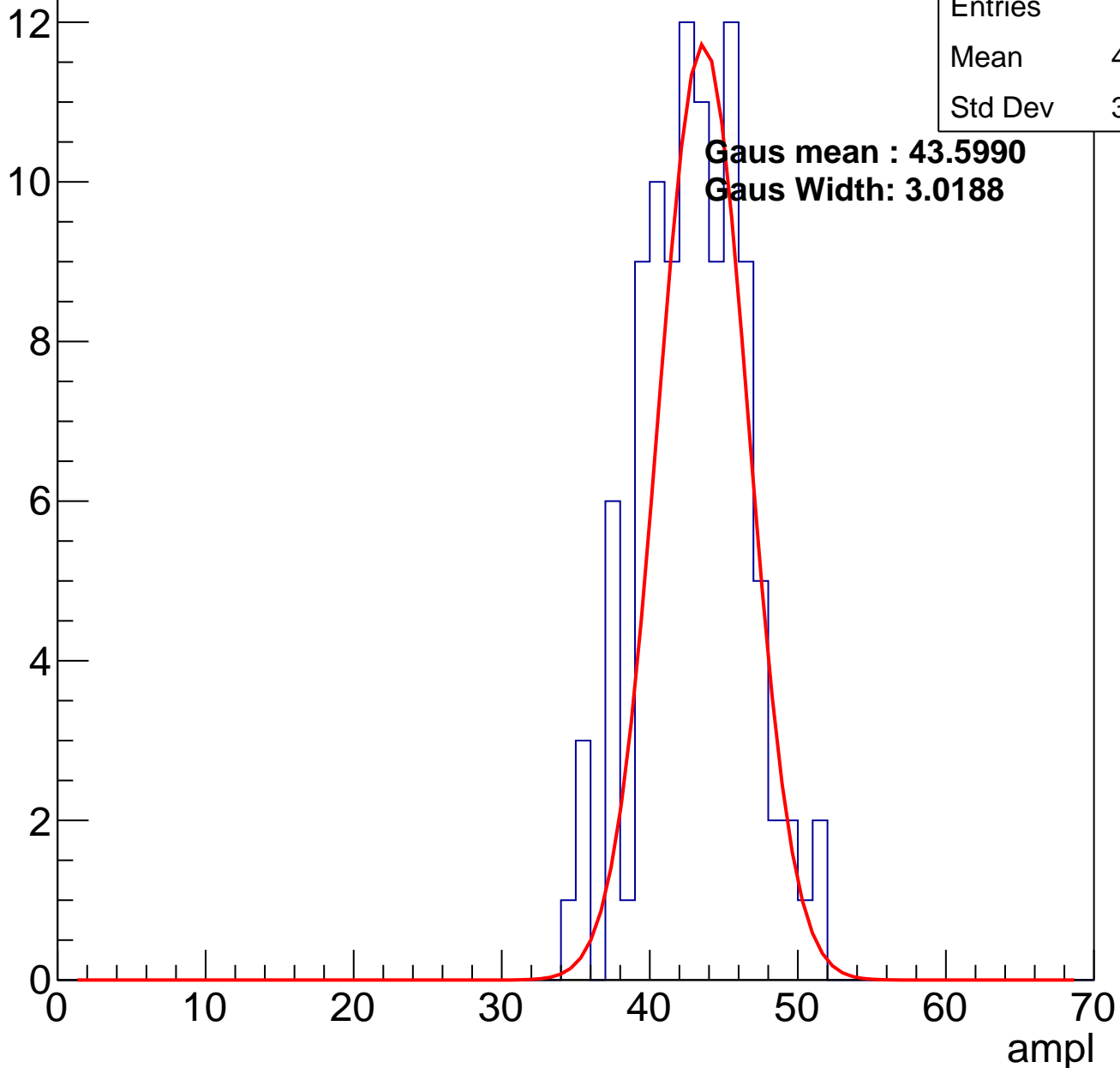
ampl



# B0L101S, U26-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch117, adc3

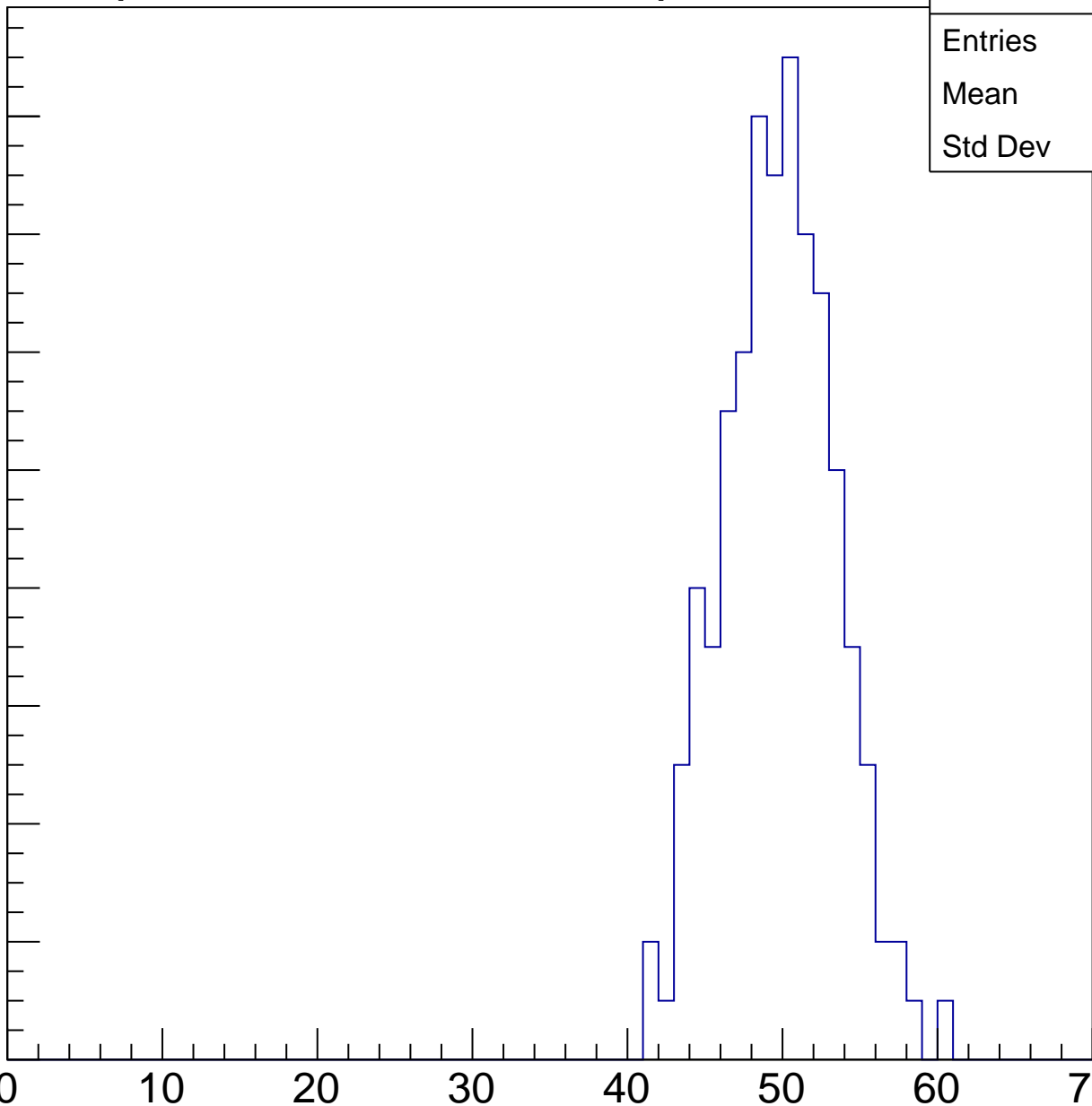
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	149
Mean	49.3
Std Dev	3.619

ampl



# B0L101S, U26-ch117, adc4

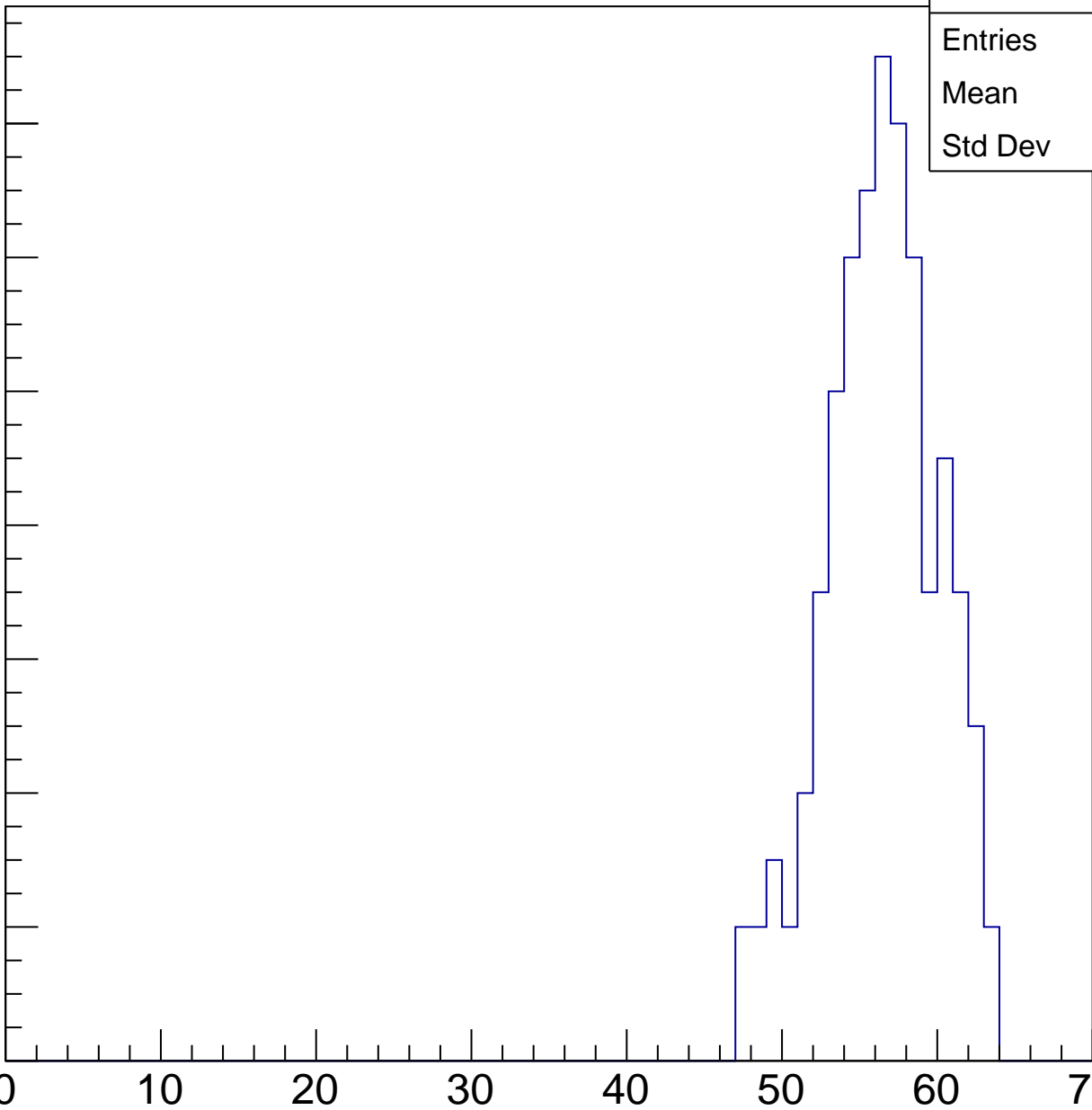
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	55.94
Std Dev	3.556

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L101S, U26-ch117, adc5

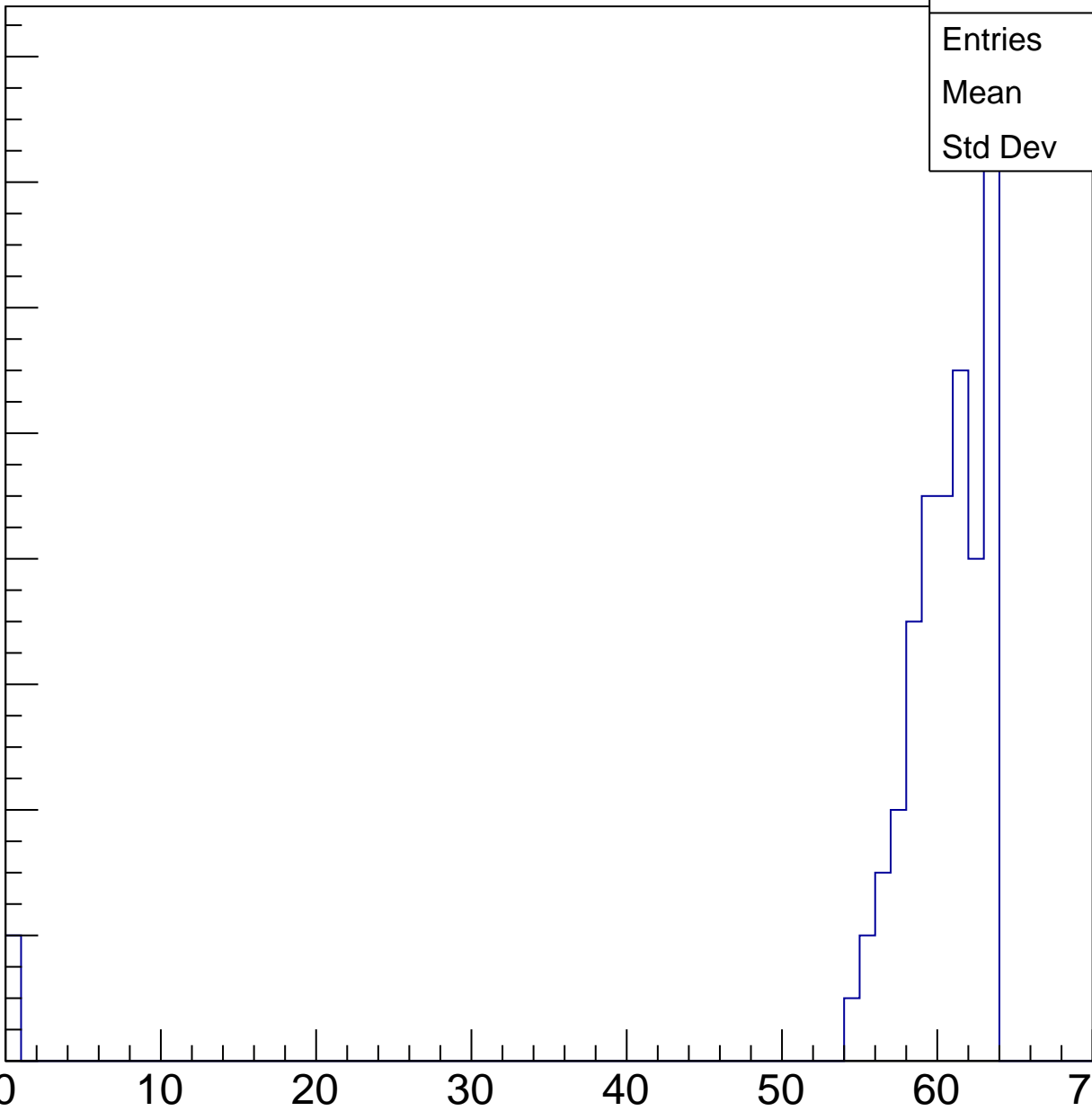
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	72
Mean	58.5
Std Dev	10.16

ampl

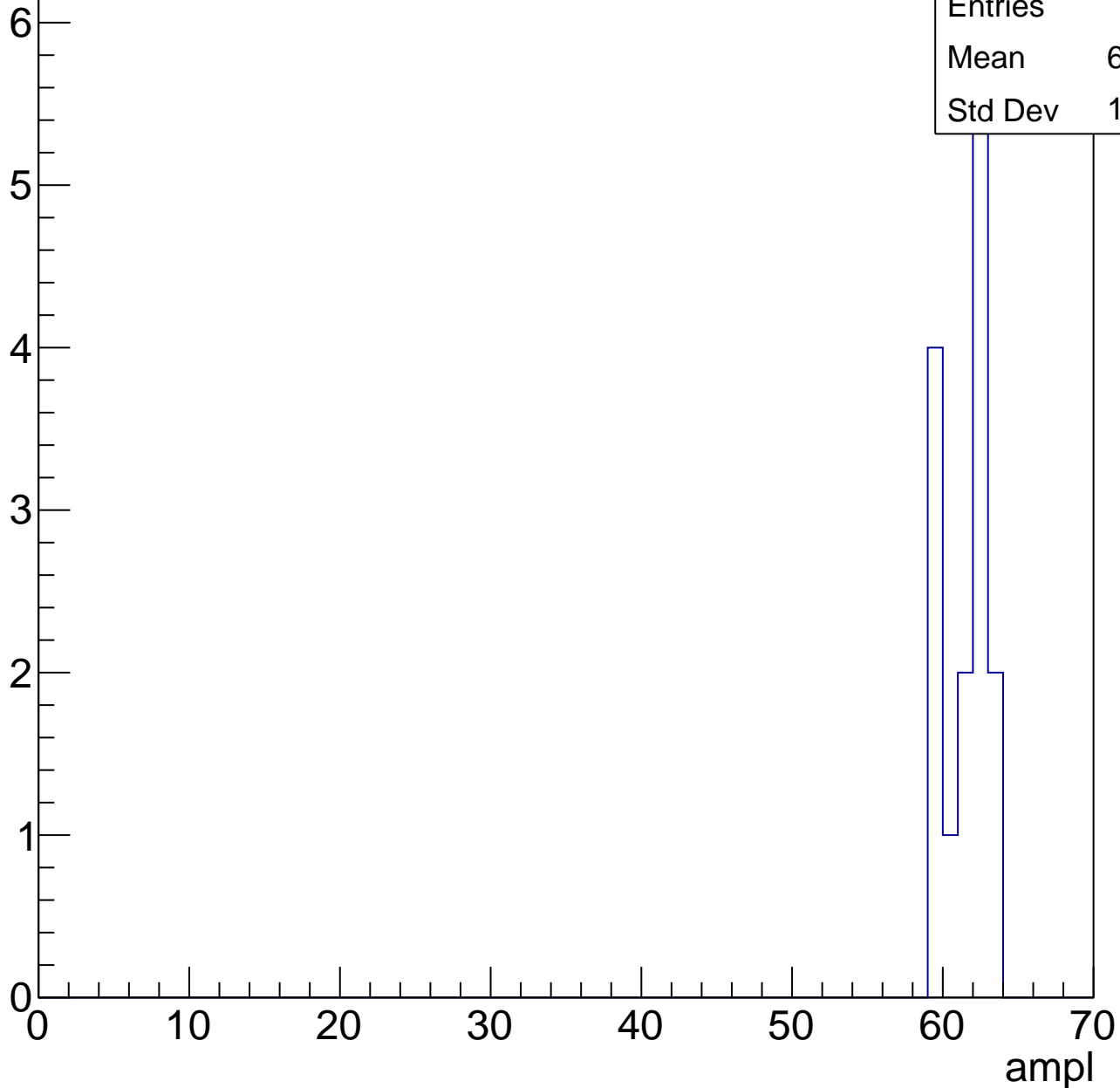


# B0L101S, U26-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.07
Std Dev	1.436





# B0L101S, U26-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch118, adc0

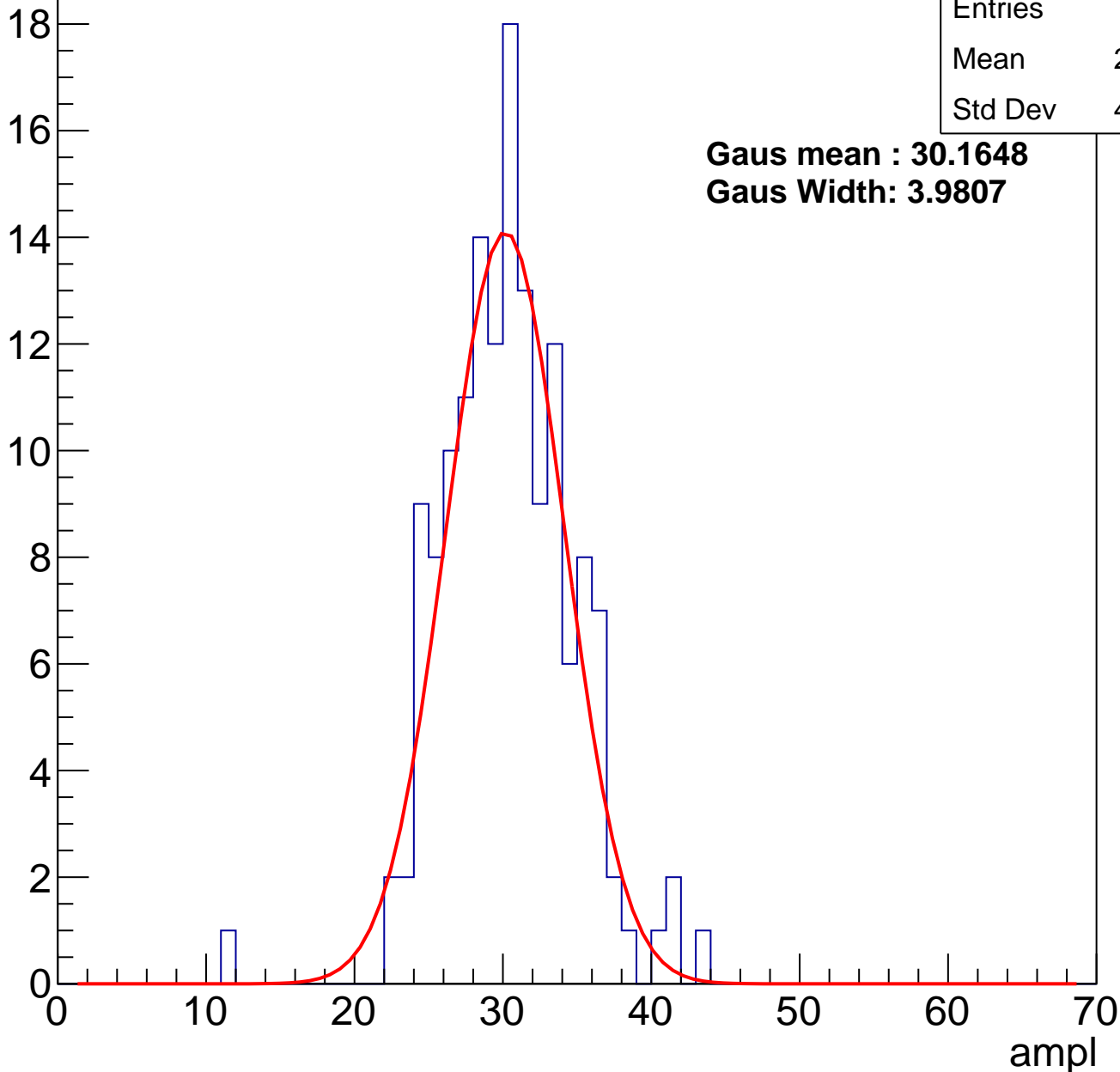
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	29.89
Std Dev	4.346

**Gaus mean : 30.1648**

**Gaus Width: 3.9807**

Entry



# B0L101S, U26-ch118, adc1

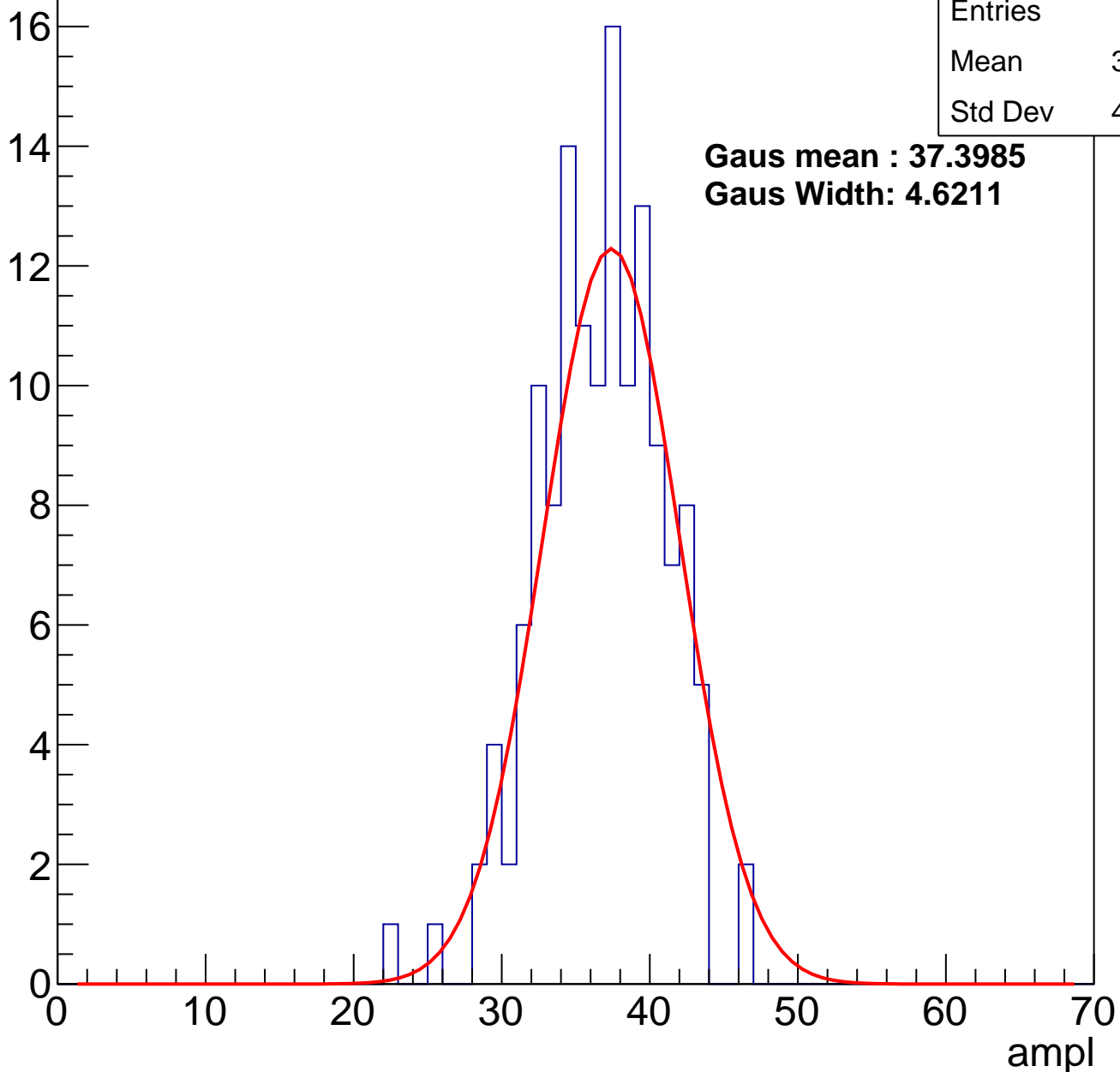
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	36.25
Std Dev	4.136

**Gaus mean : 37.3985**

**Gaus Width: 4.6211**

Entry



# B0L101S, U26-ch118, adc2

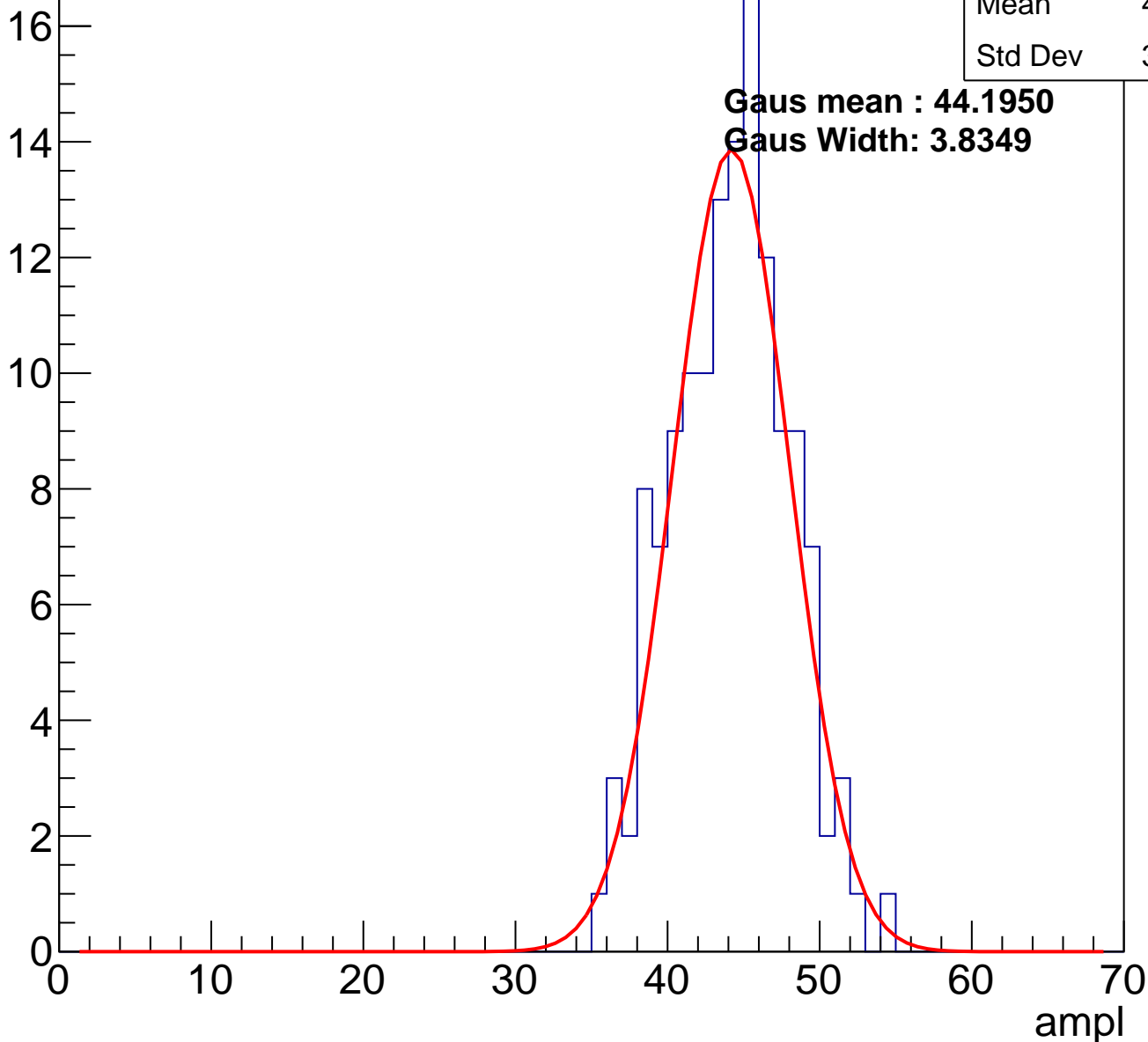
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	138
Mean	43.72
Std Dev	3.767

**Gaus mean : 44.1950**

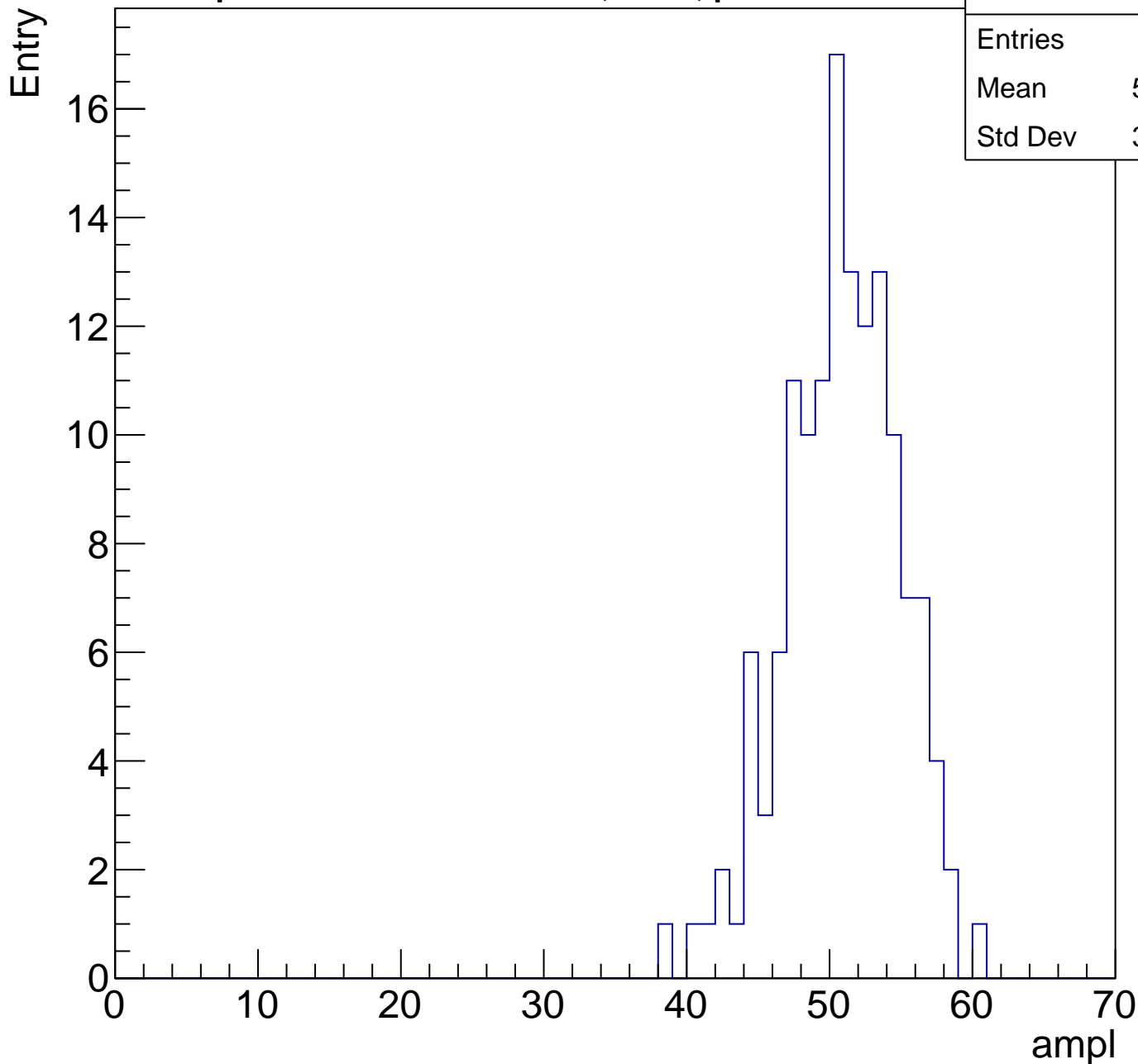
**Gaus Width: 3.8349**



# B0L101S, U26-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

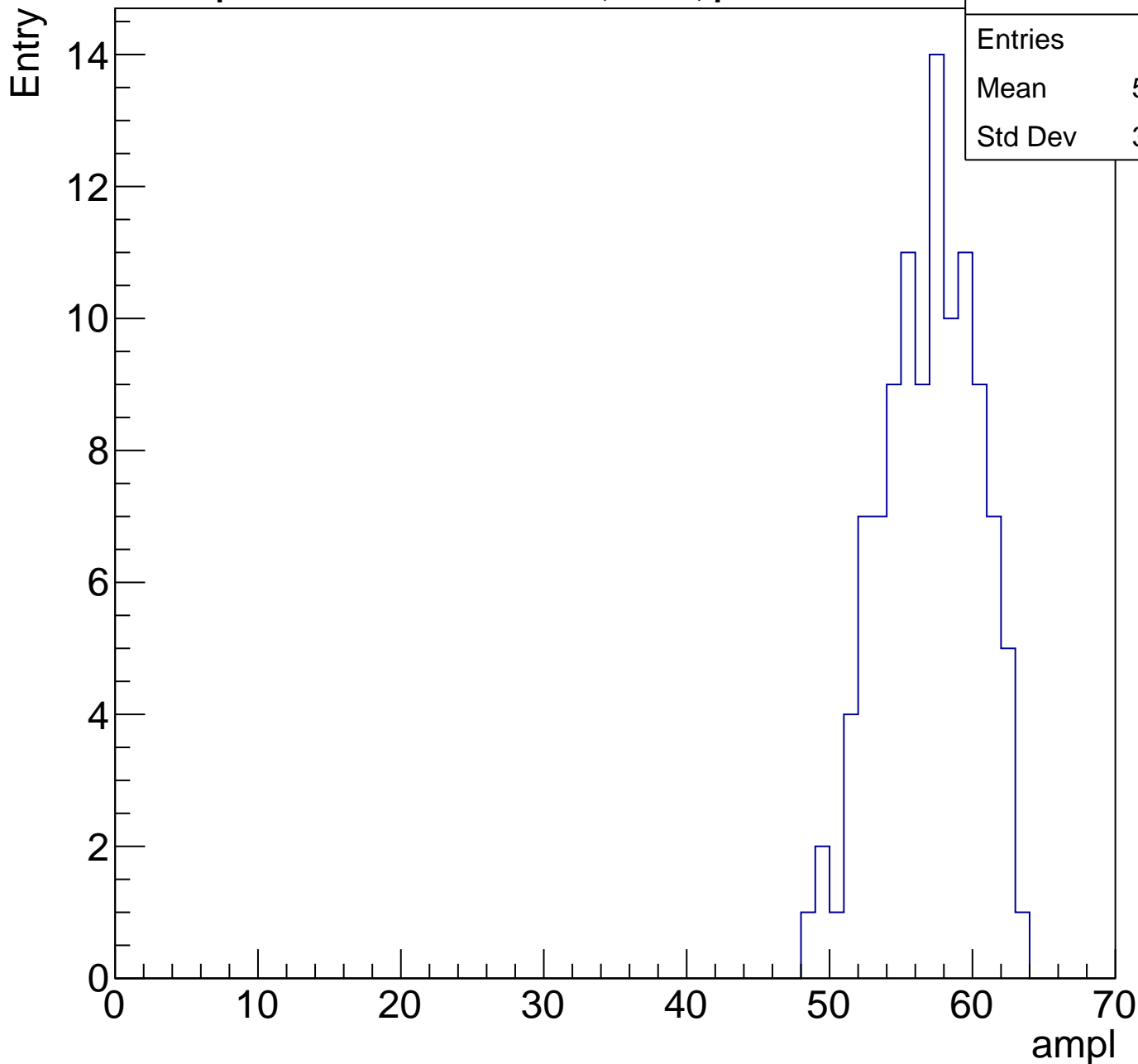
Entries	139
Mean	50.39
Std Dev	3.985



# B0L101S, U26-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	108
Mean	56.45
Std Dev	3.332



# B0L101S, U26-ch118, adc5

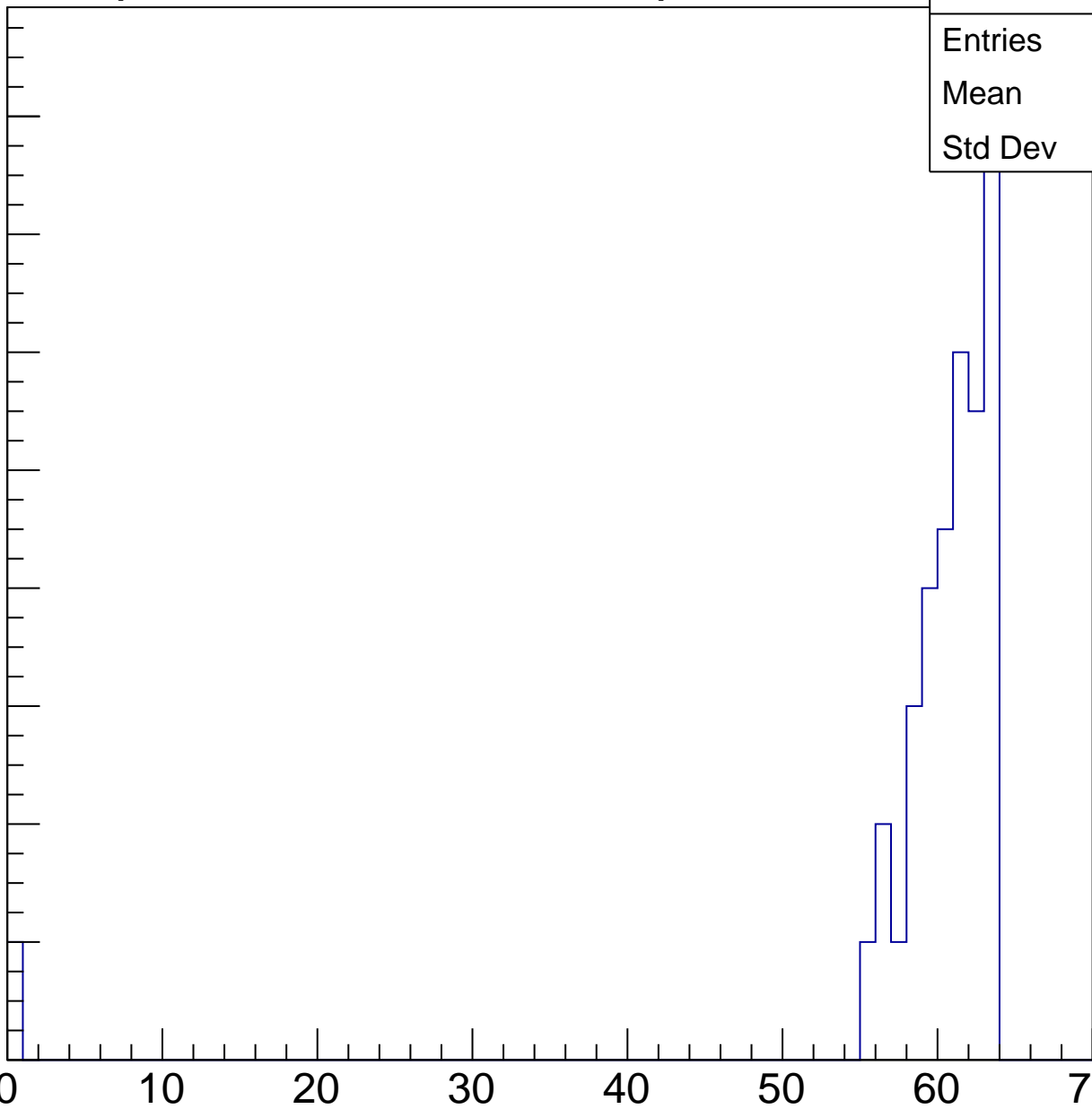
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	73
Mean	58.81
Std Dev	10.12

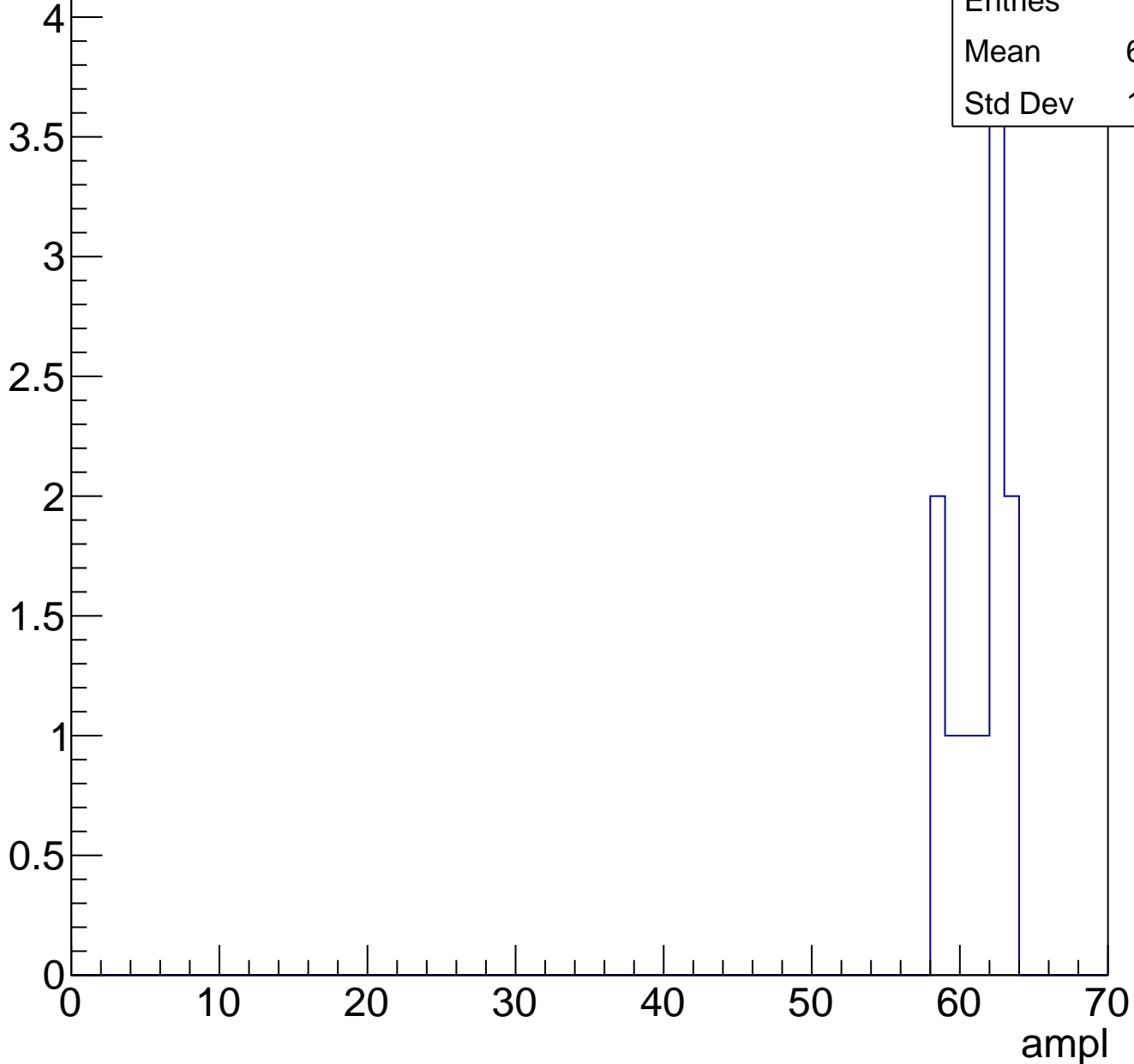
ampl



# B0L101S, U26-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	129
Mean	29.16
Std Dev	4.471

**Gaus mean : 29.6118**

**Gaus Width: 3.8323**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

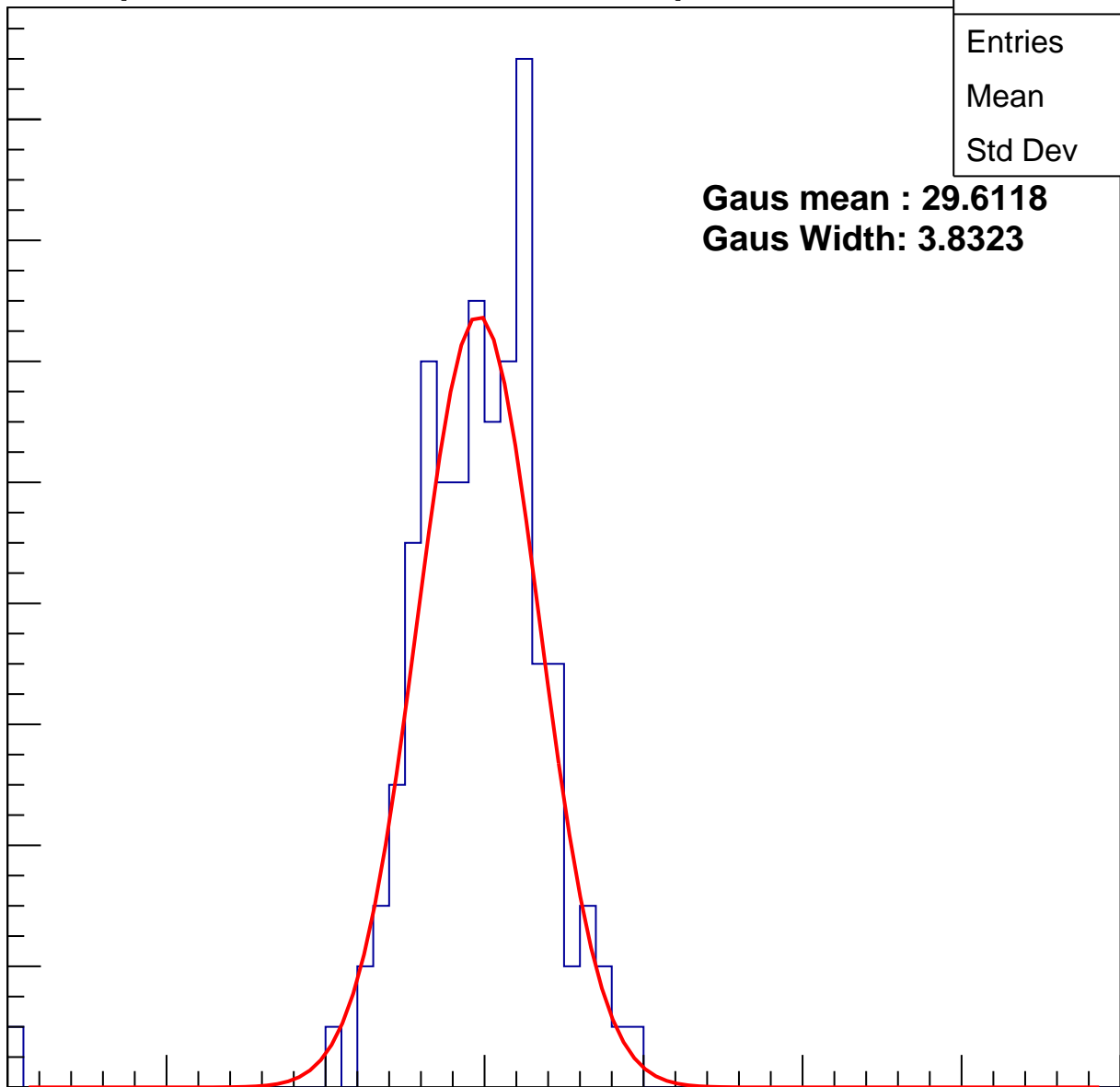
40

50

60

70

ampl



# B0L101S, U26-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	150
Mean	36.67
Std Dev	3.737

**Gaus mean : 37.5158**

**Gaus Width: 3.5684**

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

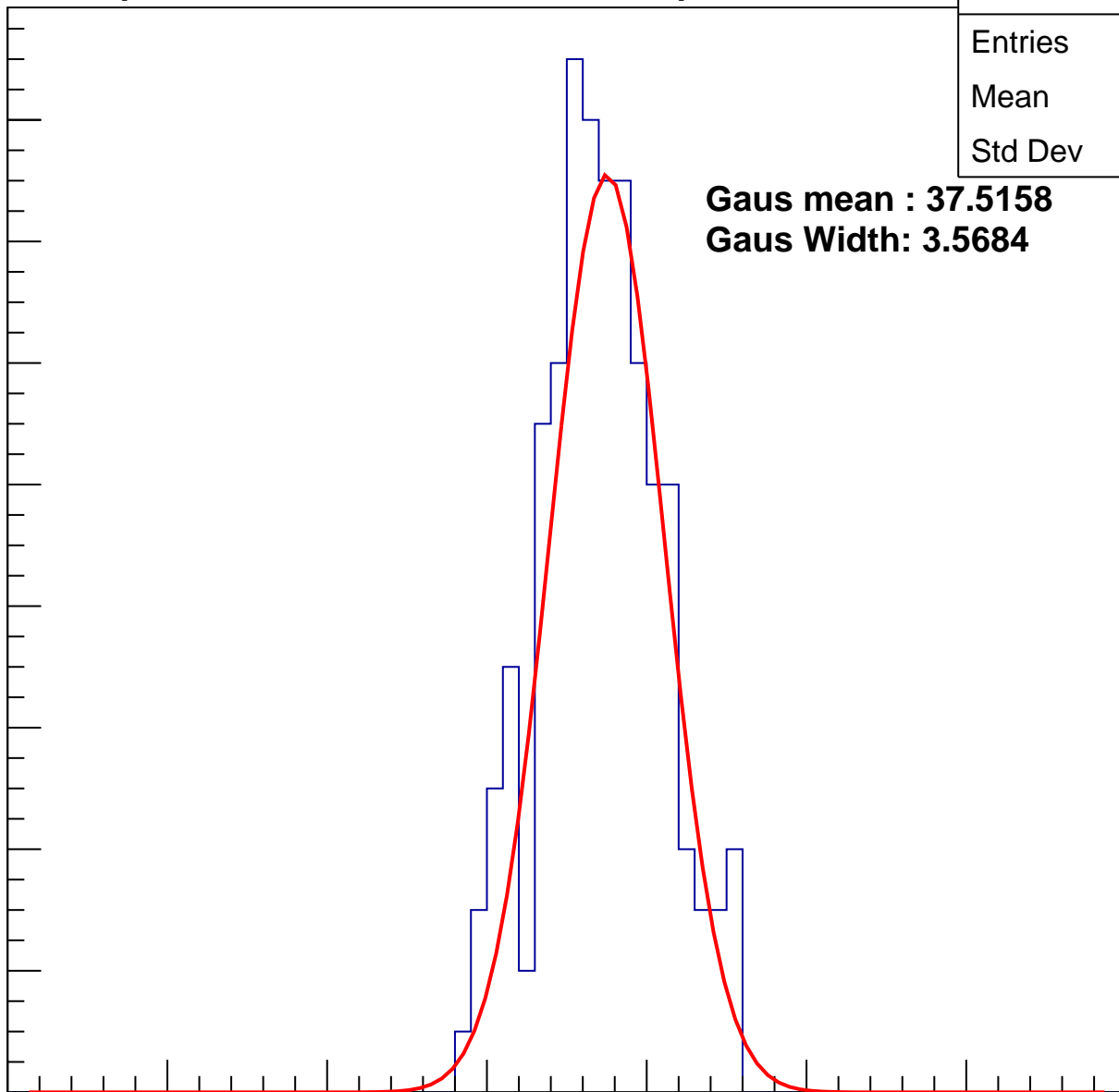
40

50

60

70

ampl



# B0L101S, U26-ch119, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

43.87

Std Dev

3.761

**Gaus mean : 44.6705**

**Gaus Width: 4.0209**

0

10

20

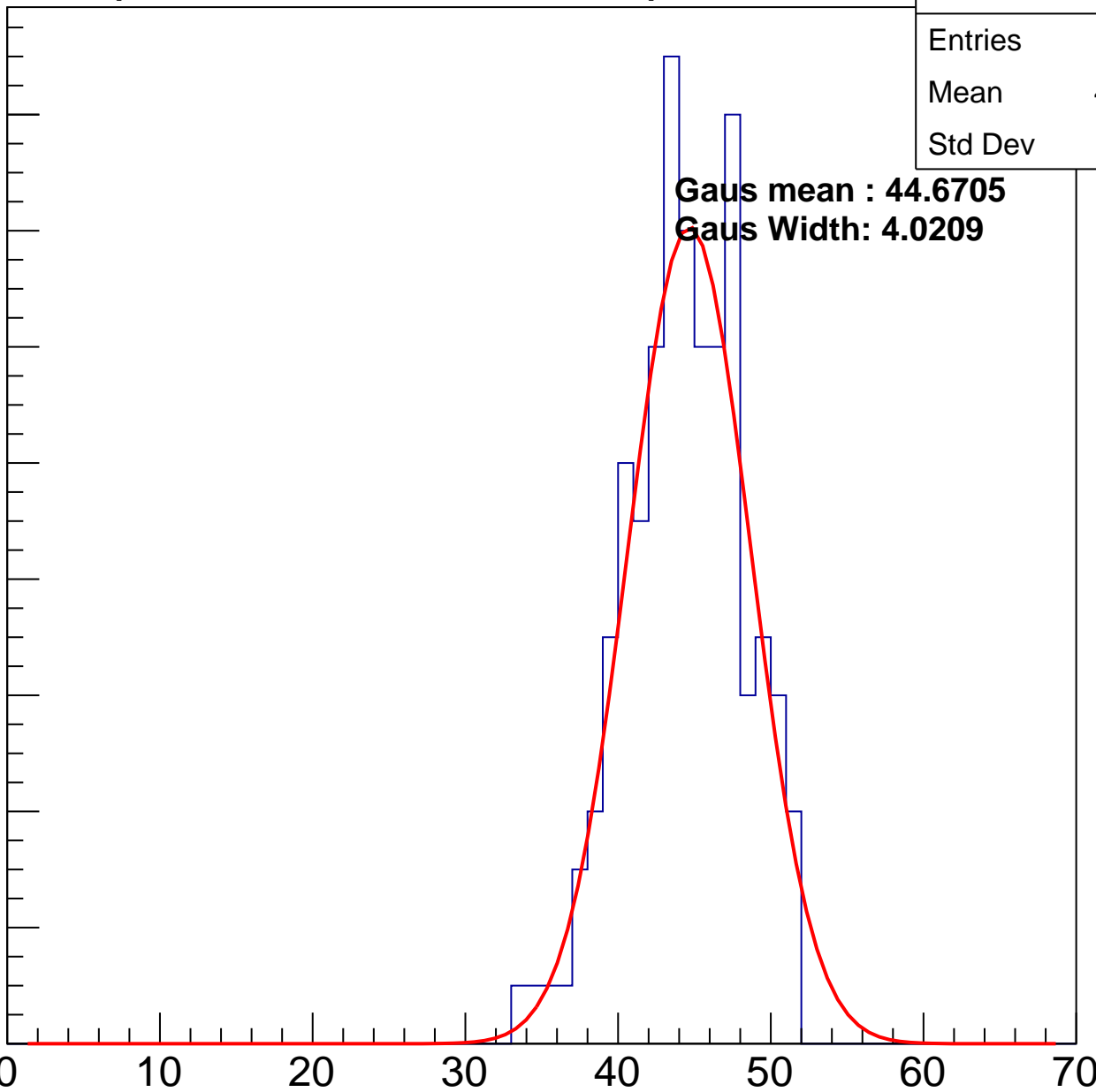
30

40

50

60

ampl

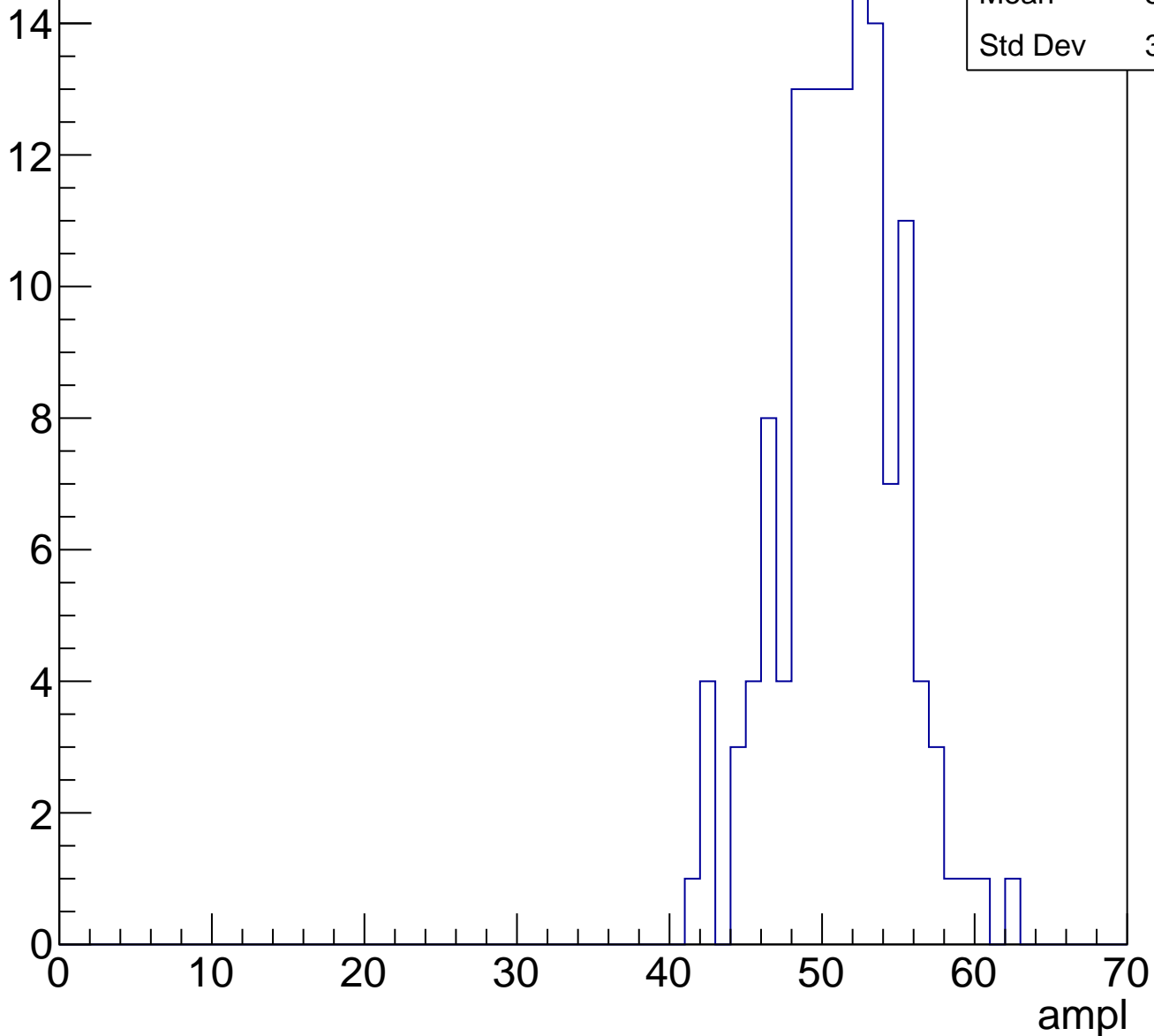


# B0L101S, U26-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	134
Mean	50.67
Std Dev	3.844



# B0L101S, U26-ch119, adc4

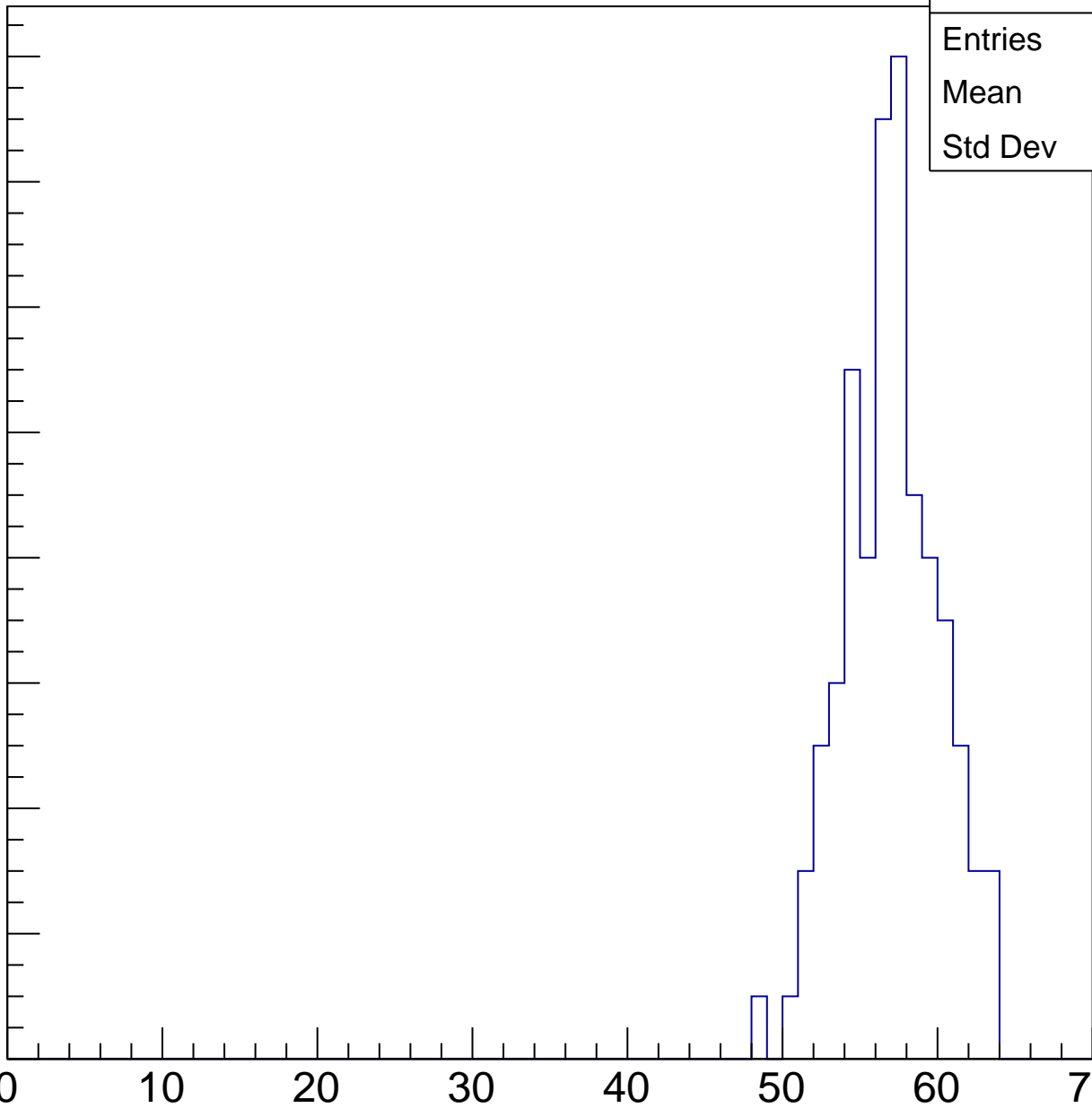
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	101
Mean	56.52
Std Dev	3.066

ampl



# B0L101S, U26-ch119, adc5

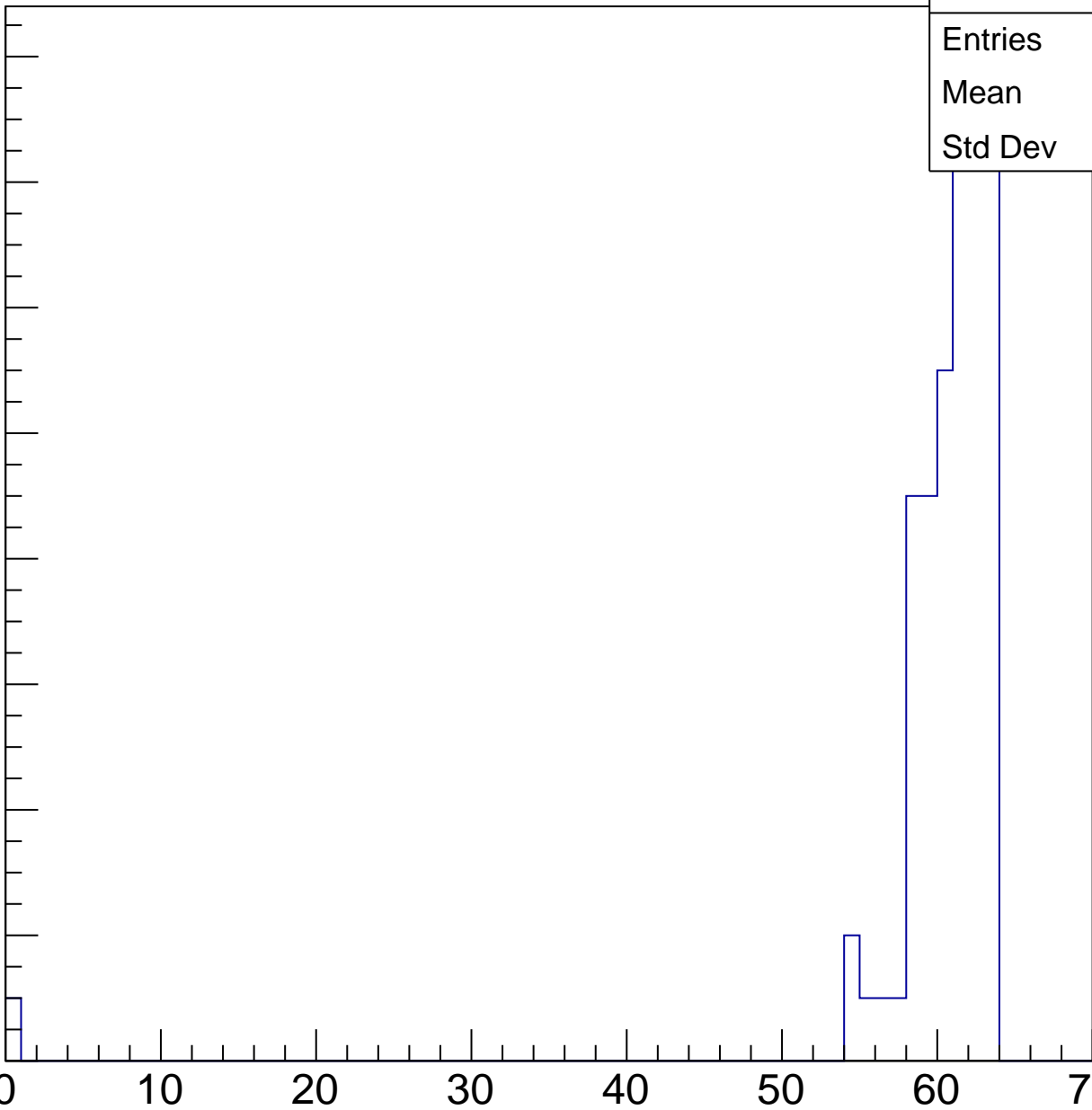
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	81
Mean	59.78
Std Dev	7.011

ampl



# B0L101S, U26-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

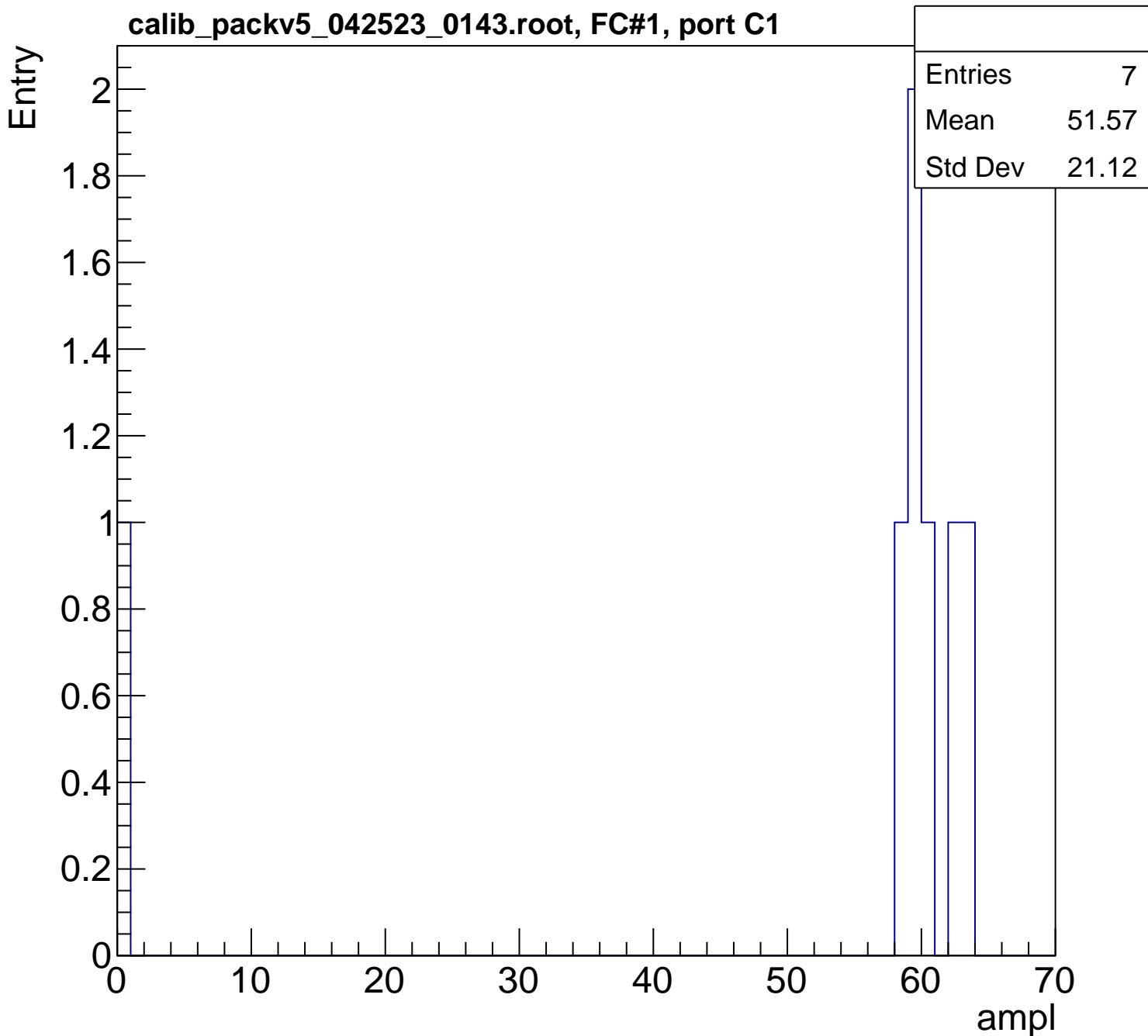
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	51.57
Std Dev	21.12

0 10 20 30 40 50 60 70

ampl





# B0L101S, U26-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	171
Mean	29.67
Std Dev	5.552

**Gaus mean : 30.4132**

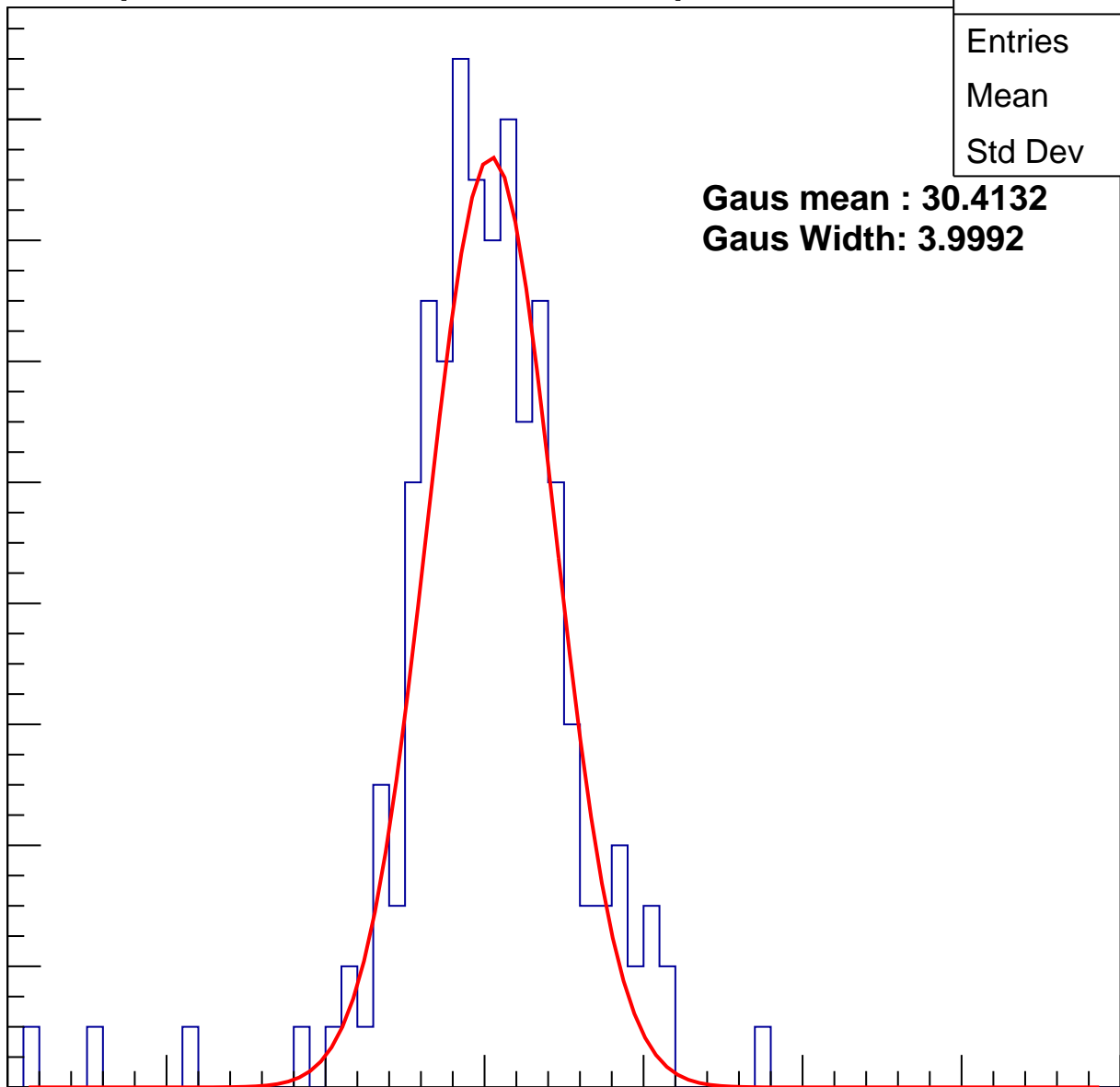
**Gaus Width: 3.9992**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch120, adc1

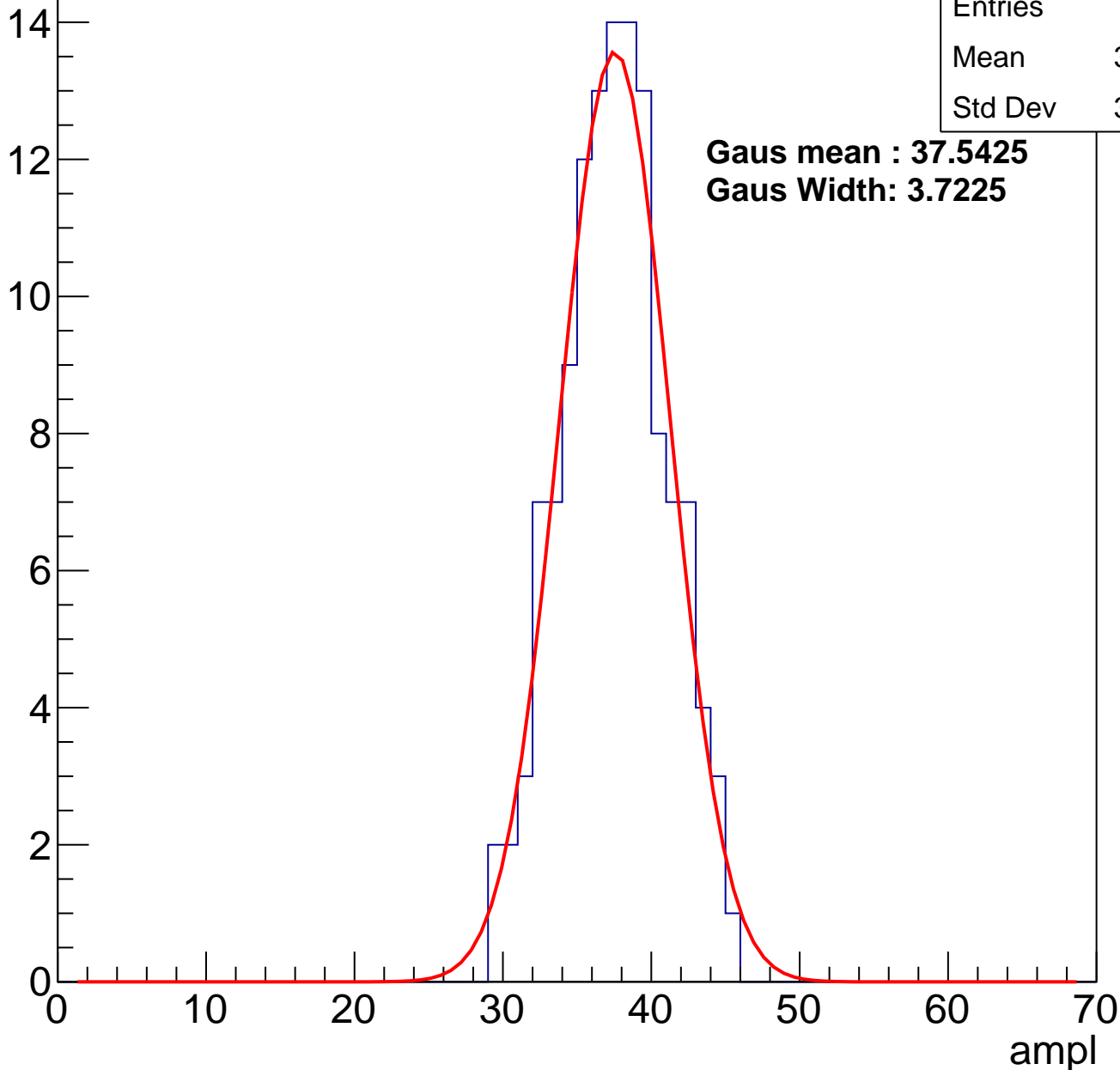
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	126
Mean	37.04
Std Dev	3.499

**Gaus mean : 37.5425**

**Gaus Width: 3.7225**

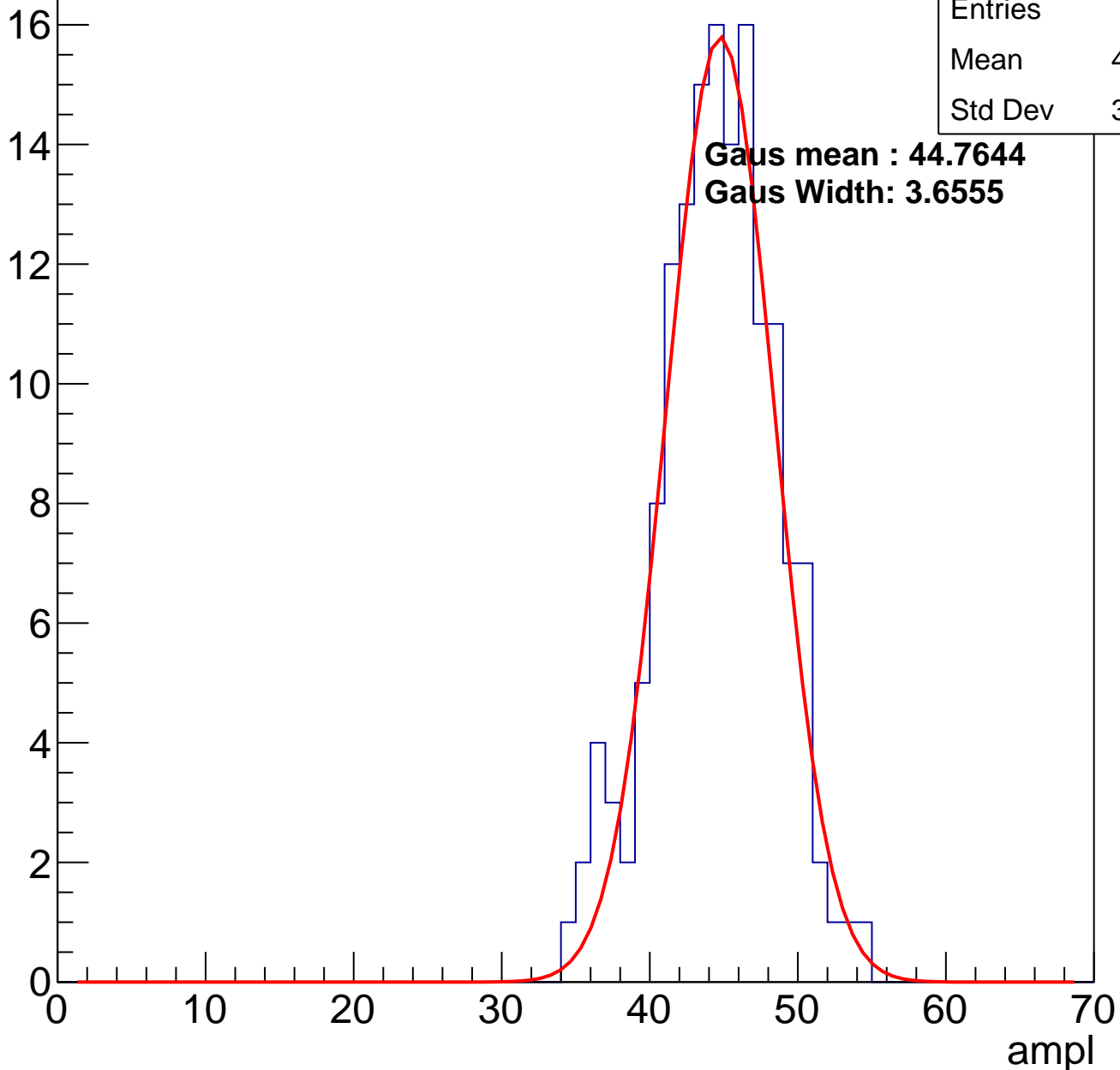
Entry



# B0L101S, U26-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

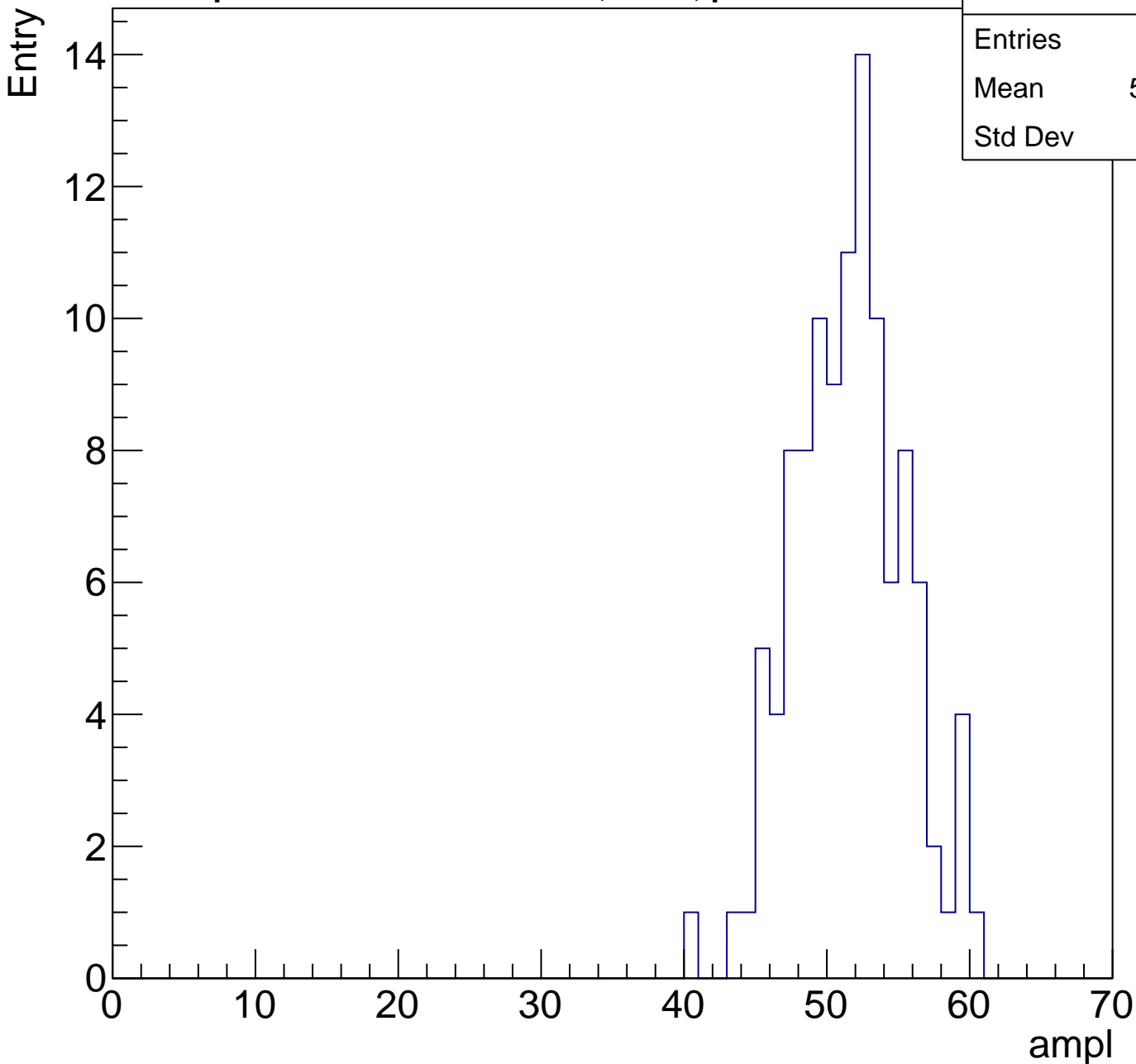
Entry



# B0L101S, U26-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	110
Mean	51.12
Std Dev	3.82



# B0L101S, U26-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

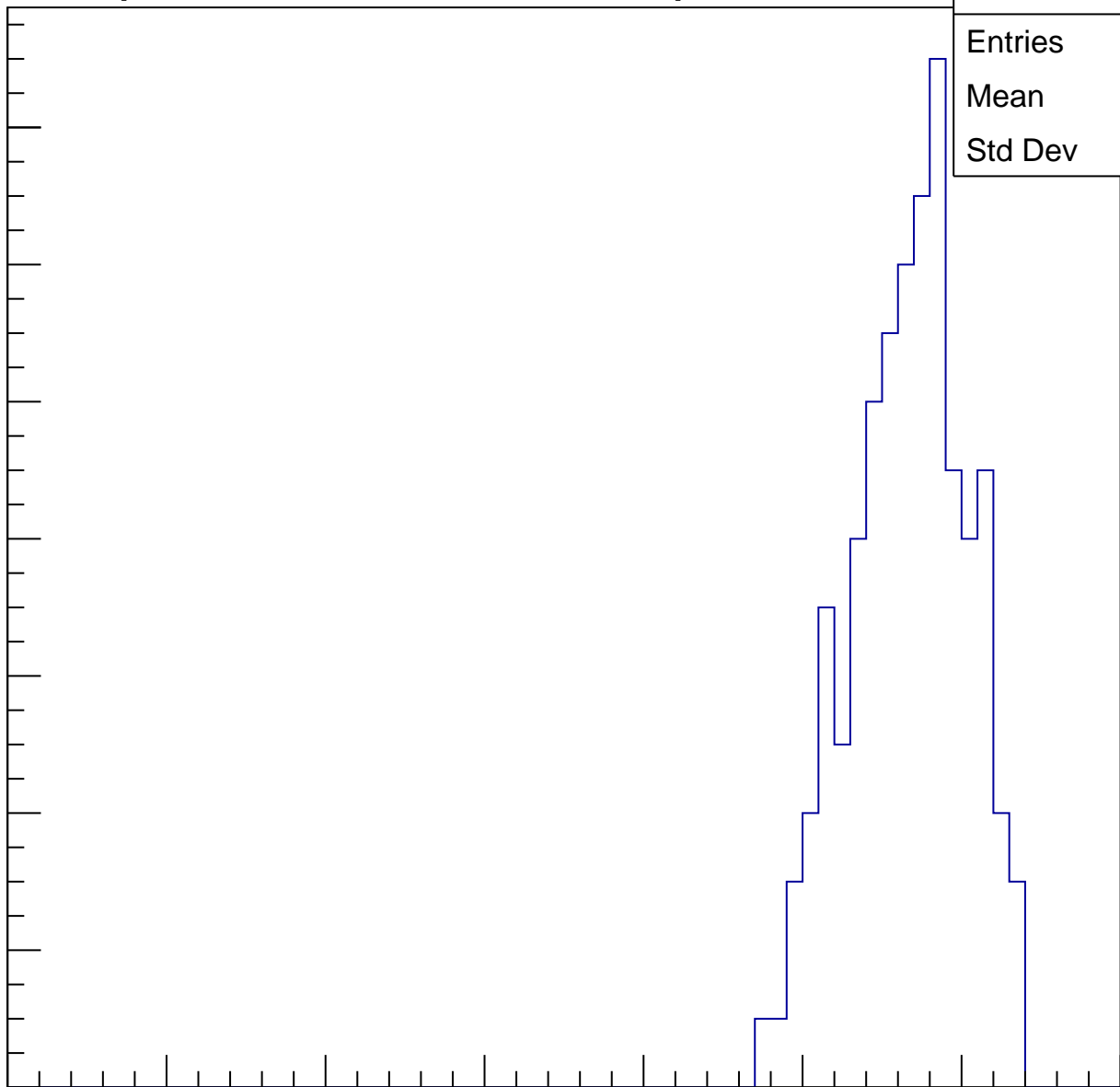
Entries	123
Mean	56.16
Std Dev	3.617

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

67

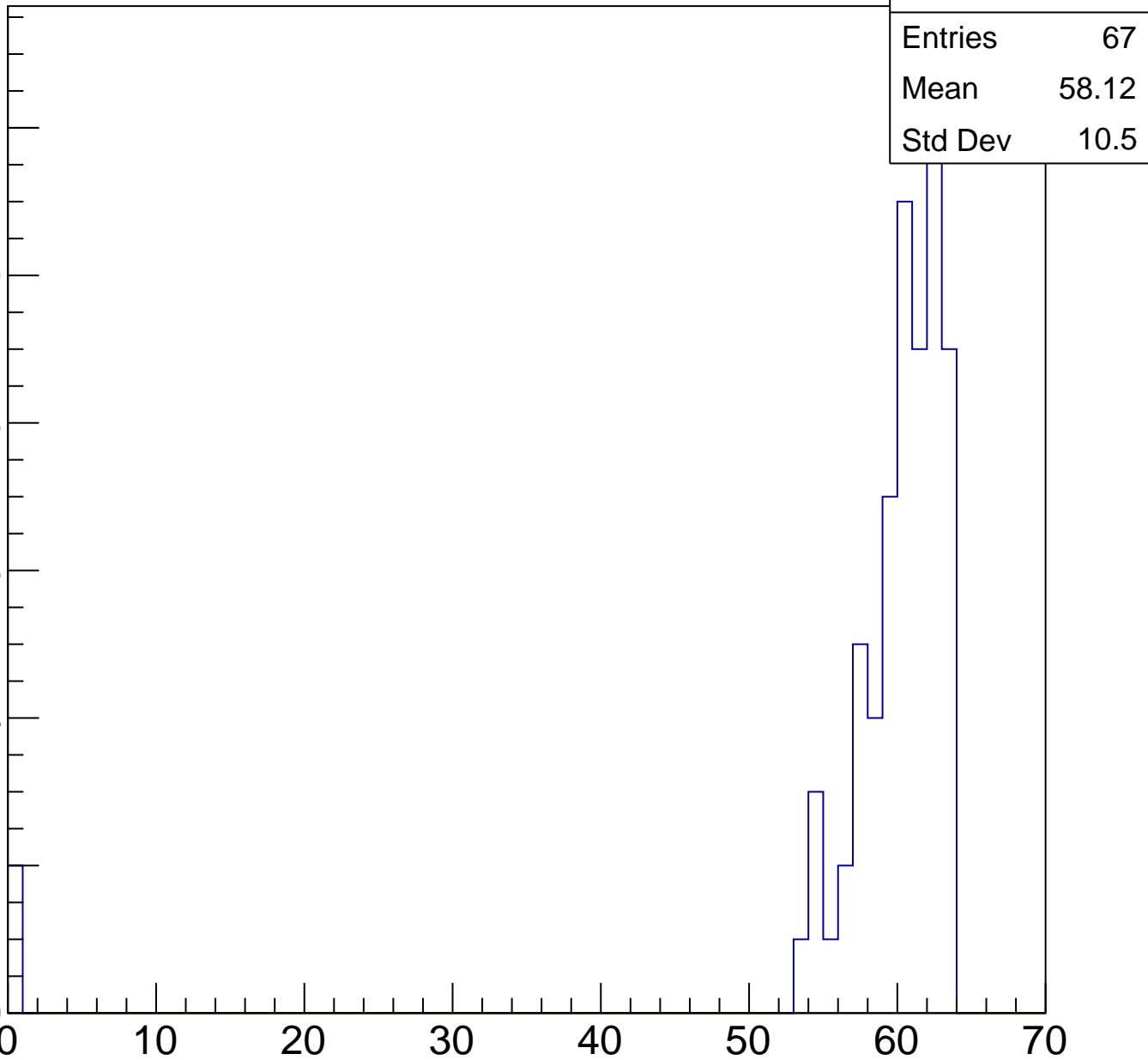
Mean

58.12

Std Dev

10.5

ampl

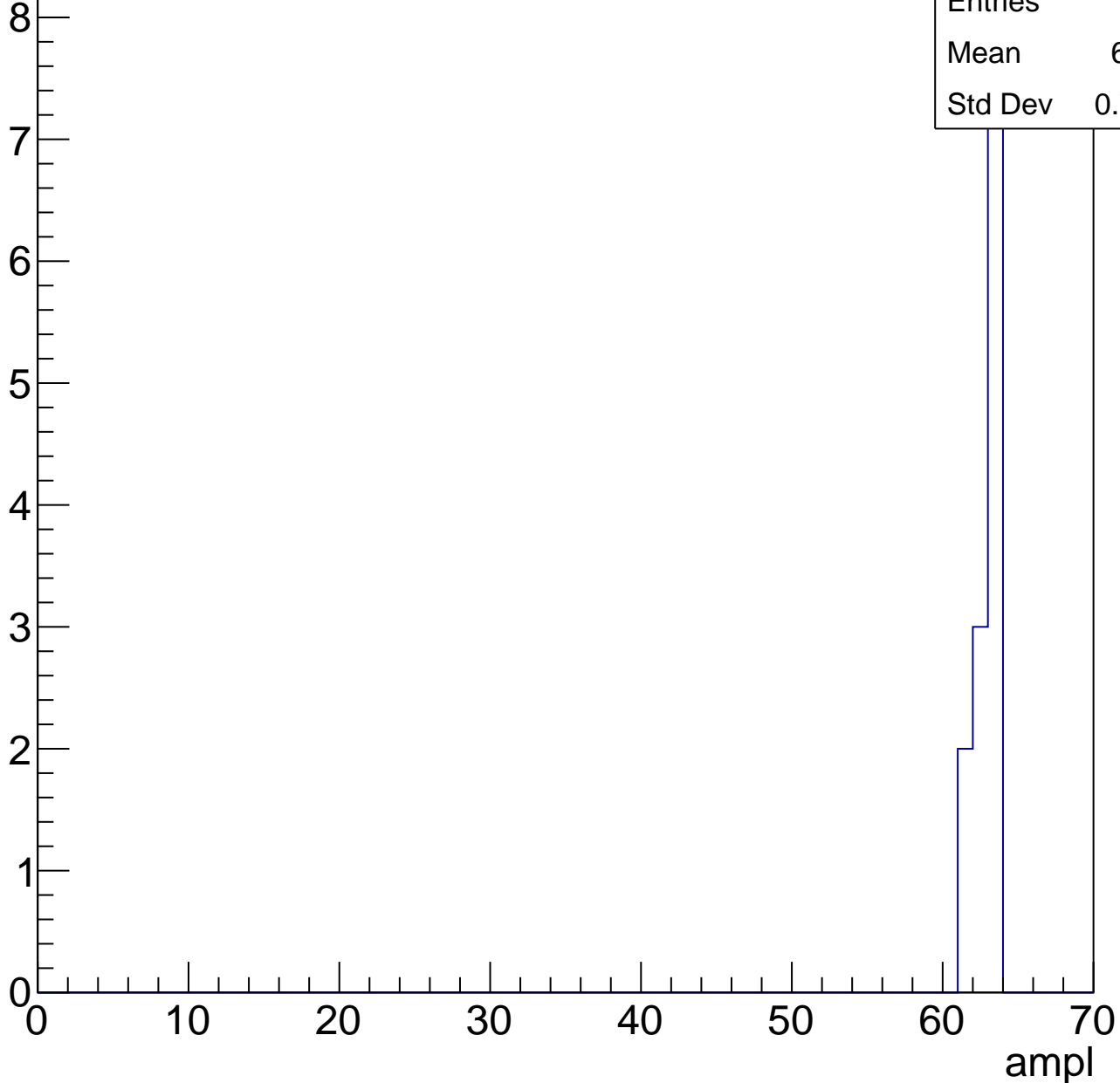


# B0L101S, U26-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	13
Mean	62.46
Std Dev	0.7458





# B0L101S, U26-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	149
Mean	28.86
Std Dev	4.53

**Gaus mean : 29.1797**

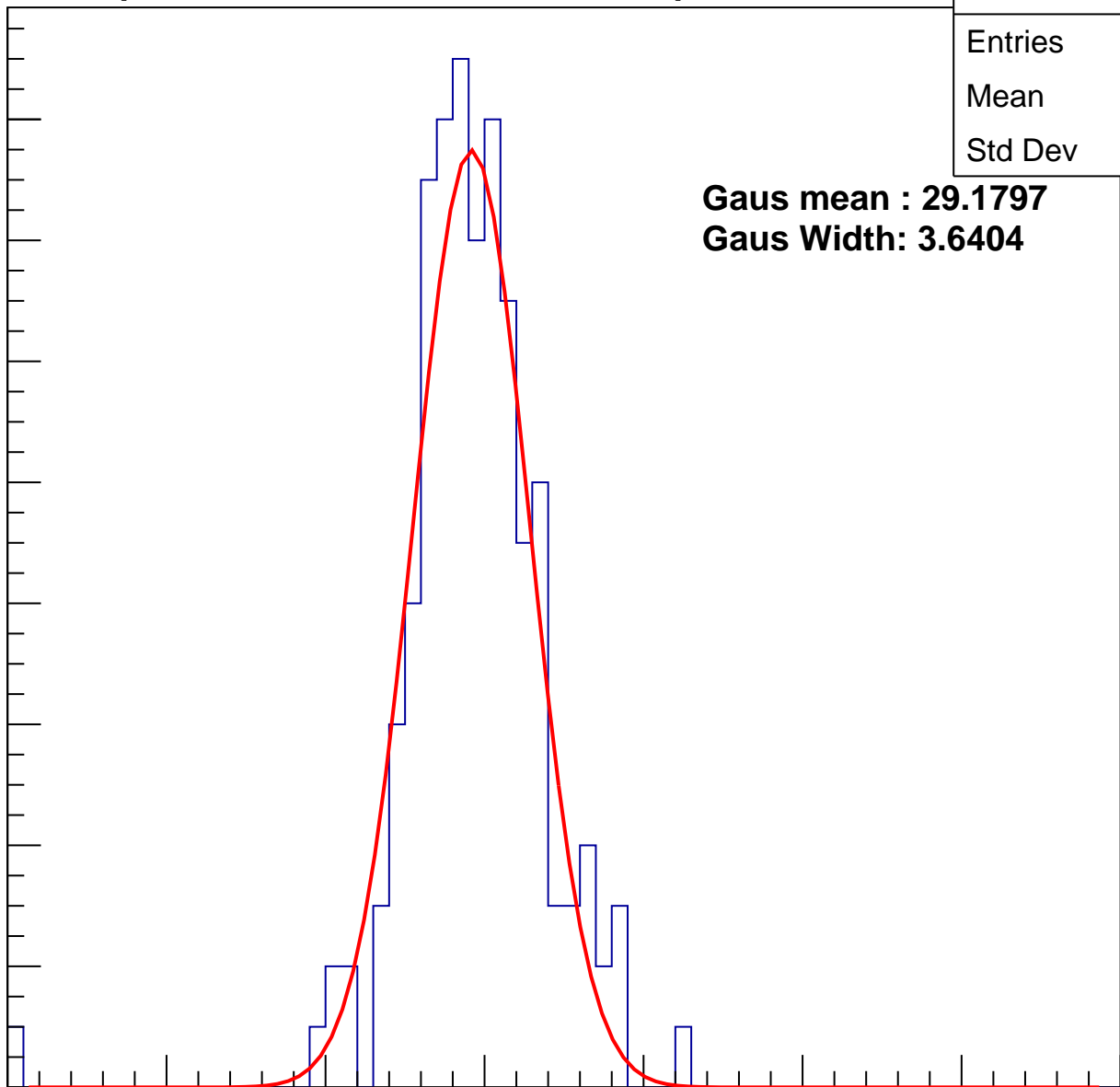
**Gaus Width: 3.6404**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L101S, U26-ch121, adc1

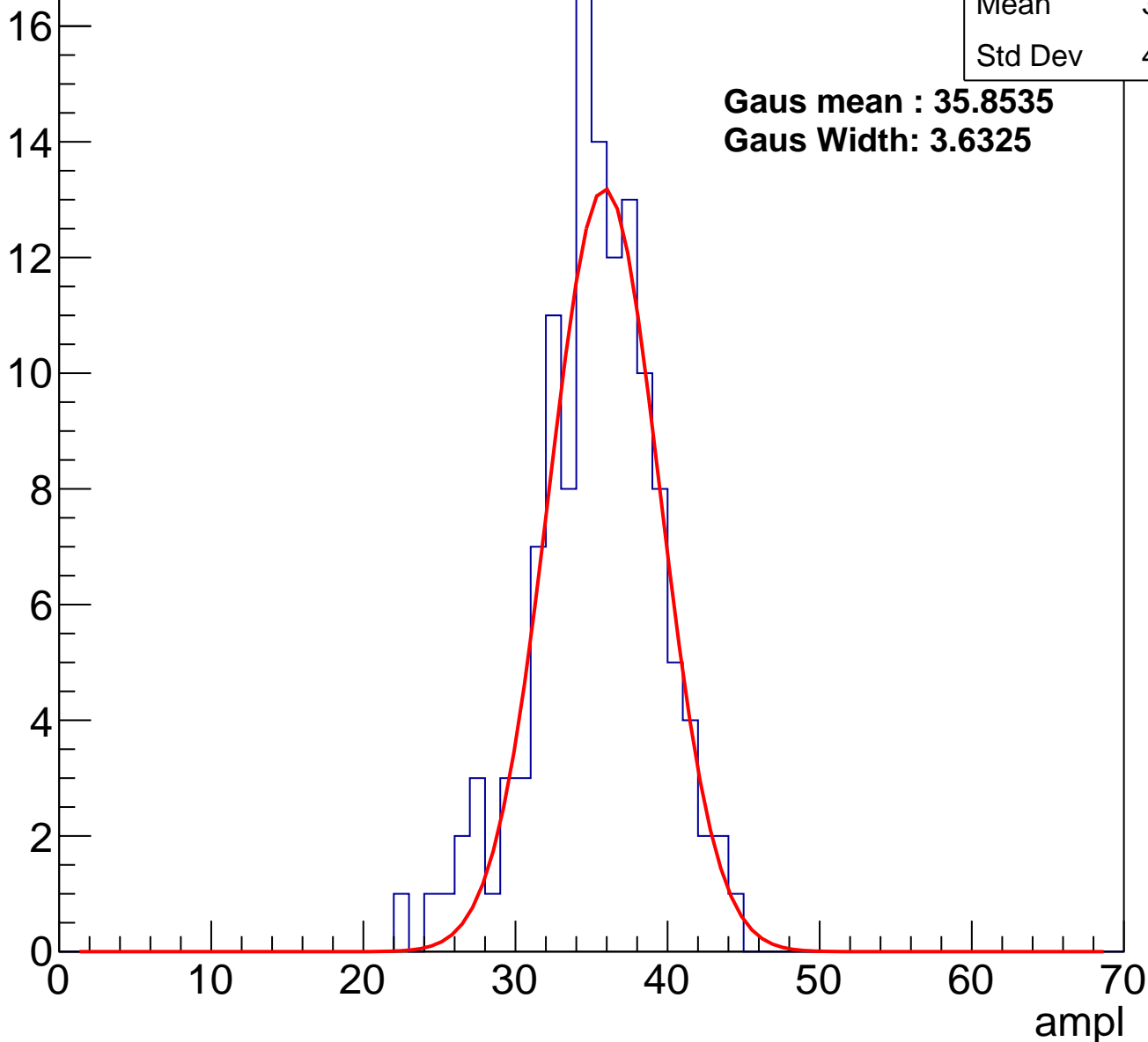
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	129
Mean	34.83
Std Dev	4.025

**Gaus mean : 35.8535**

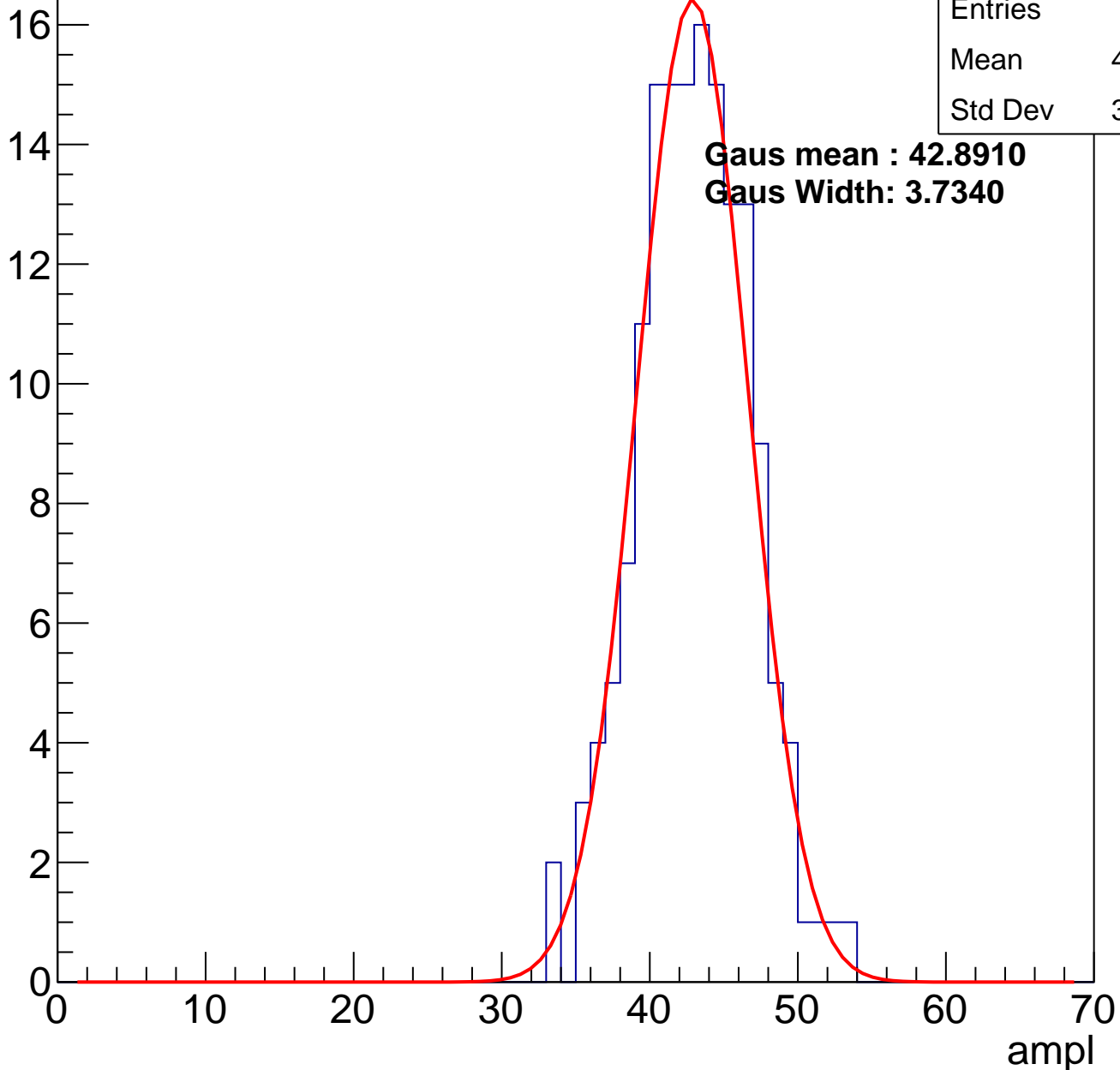
**Gaus Width: 3.6325**



# B0L101S, U26-ch121, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

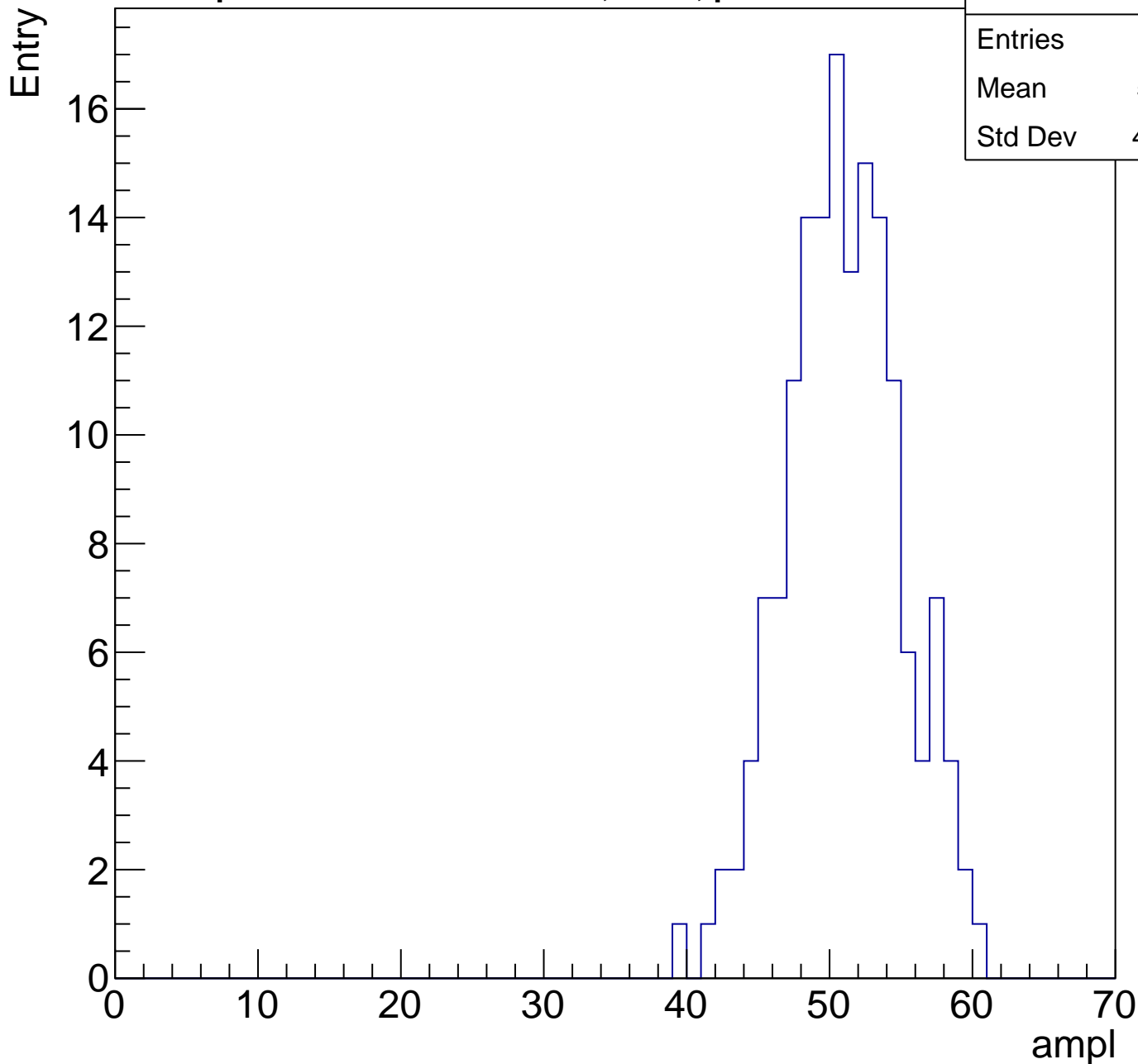
Entry



# B0L101S, U26-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

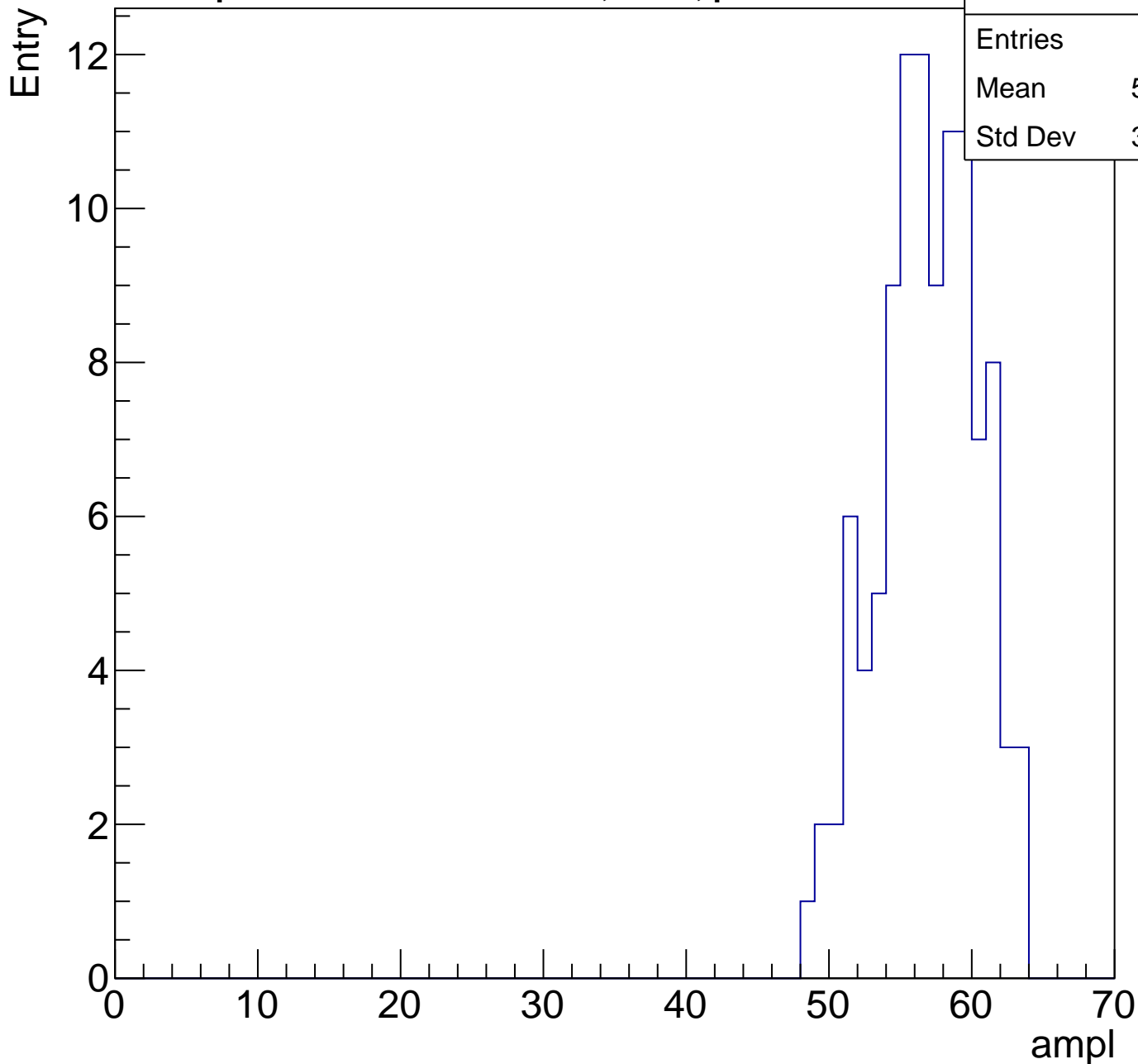
Entries	157
Mean	50.51
Std Dev	4.004



# B0L101S, U26-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	105
Mean	56.44
Std Dev	3.436



# B0L101S, U26-ch121, adc5

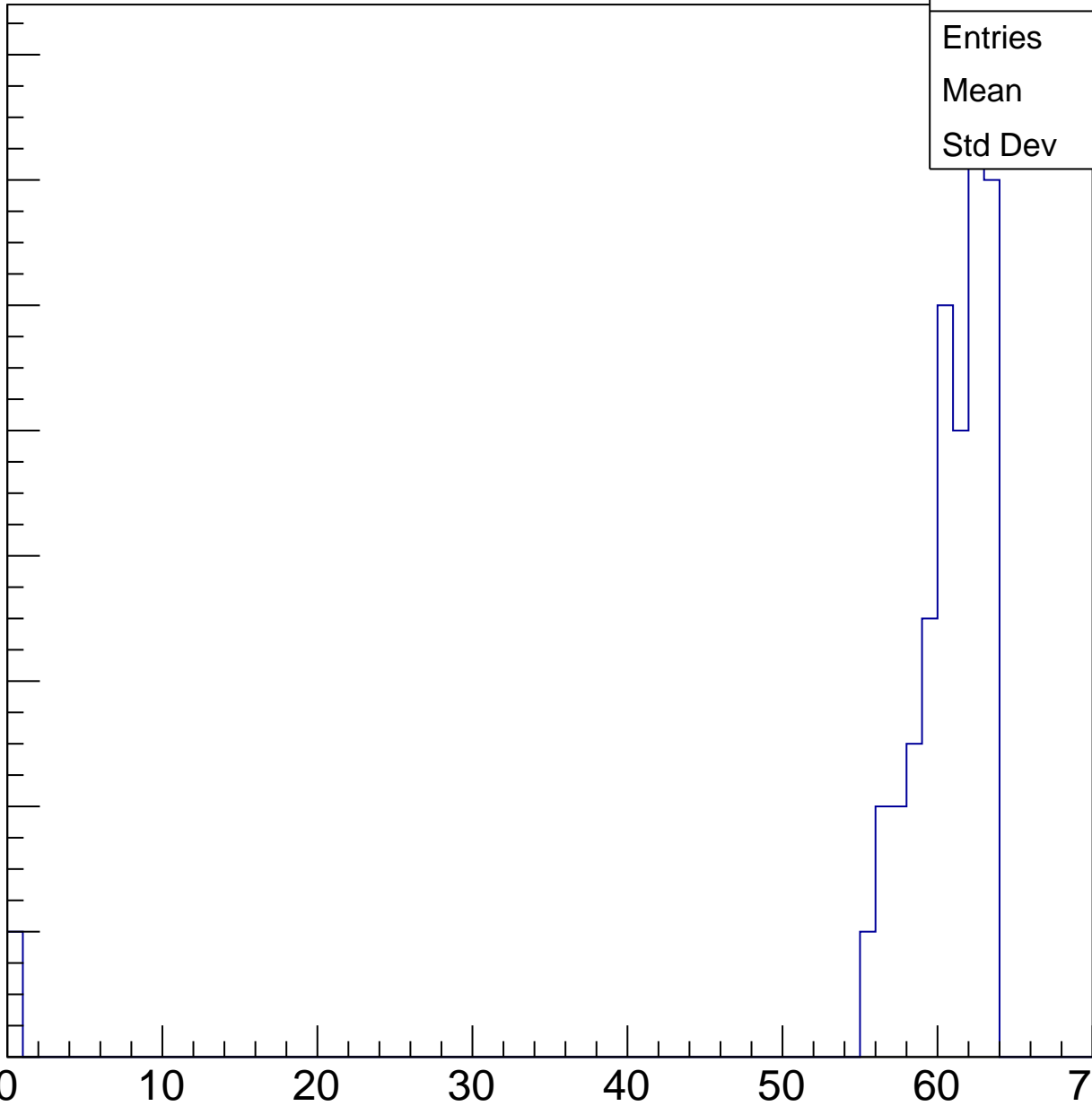
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	76
Mean	58.8
Std Dev	9.913

ampl



# B0L101S, U26-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	2
Mean	8.5
Std Dev	8.5

# B0L101S, U26-ch122, adc0

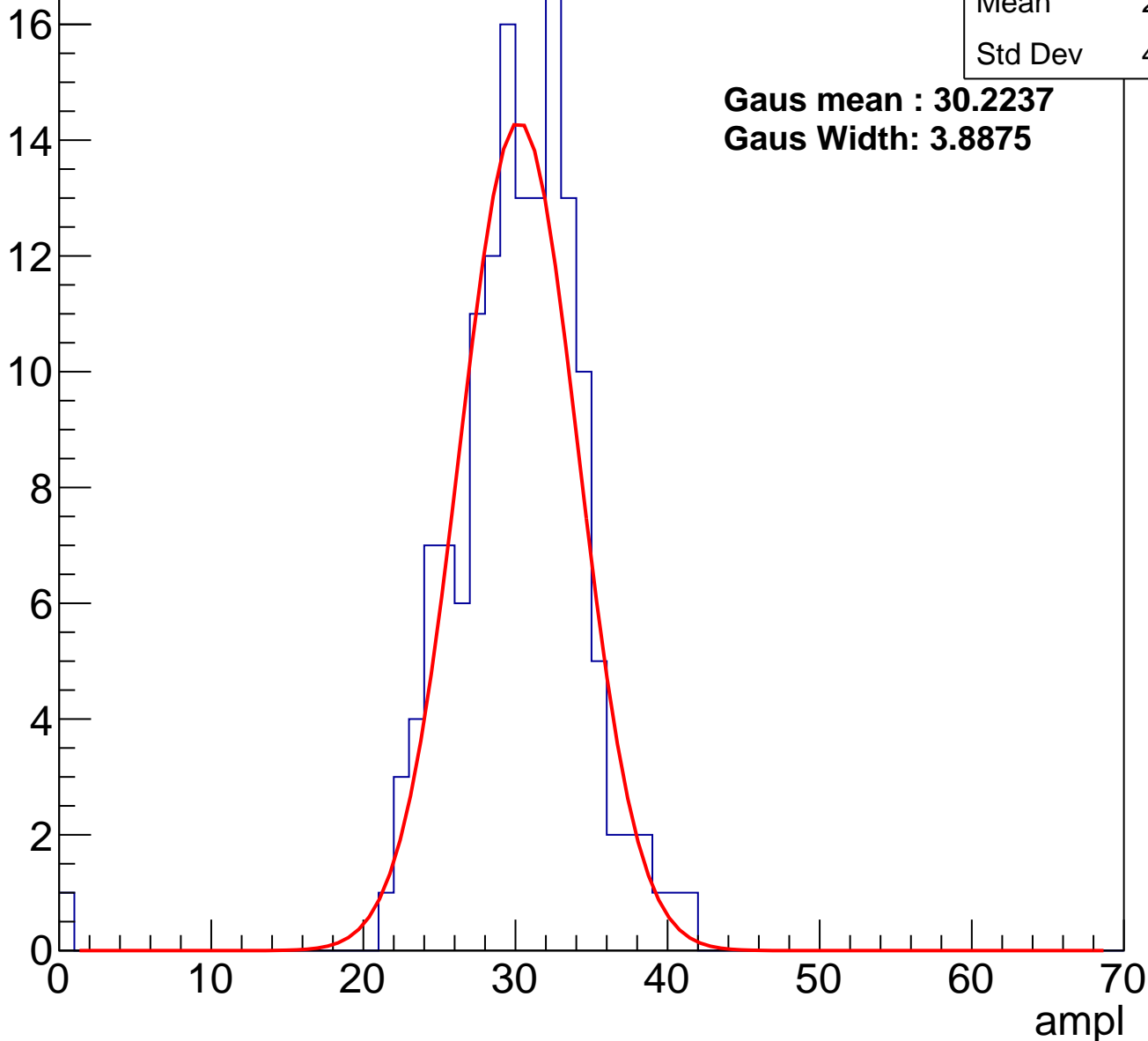
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	148
Mean	29.72
Std Dev	4.596

**Gaus mean : 30.2237**

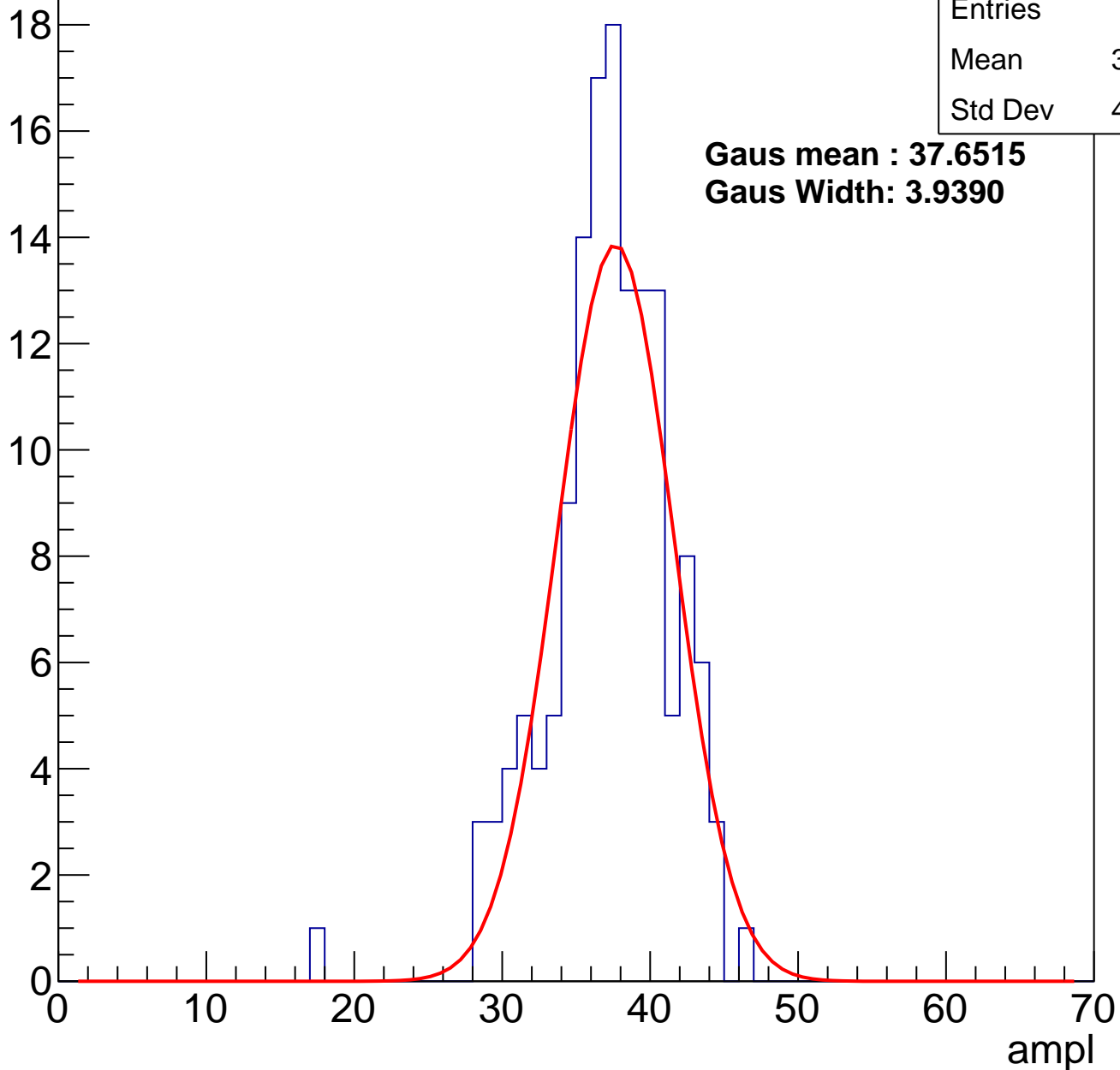
**Gaus Width: 3.8875**



# B0L101S, U26-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch122, adc2

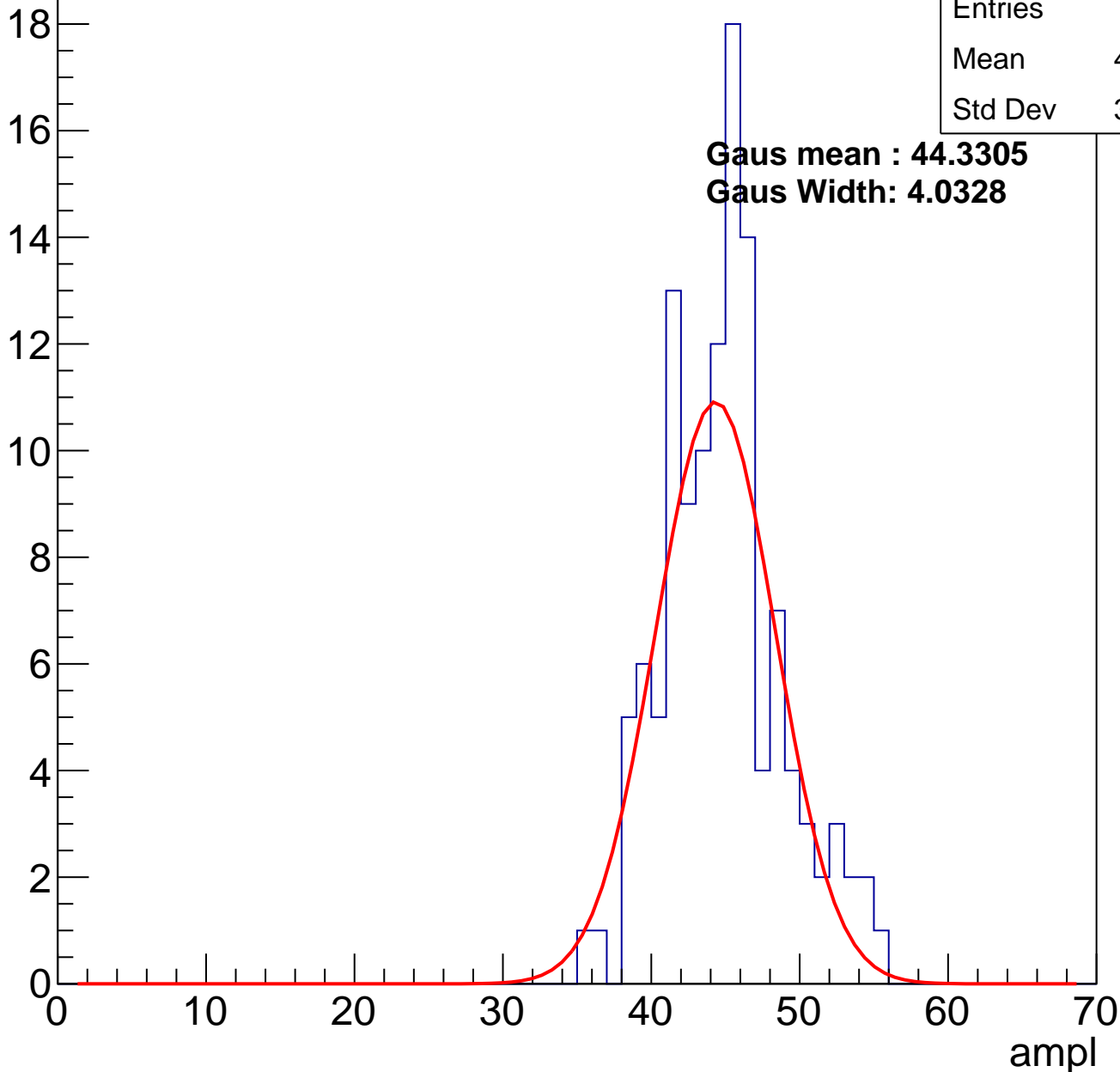
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	44.39
Std Dev	3.949

**Gaus mean : 44.3305**

**Gaus Width: 4.0328**

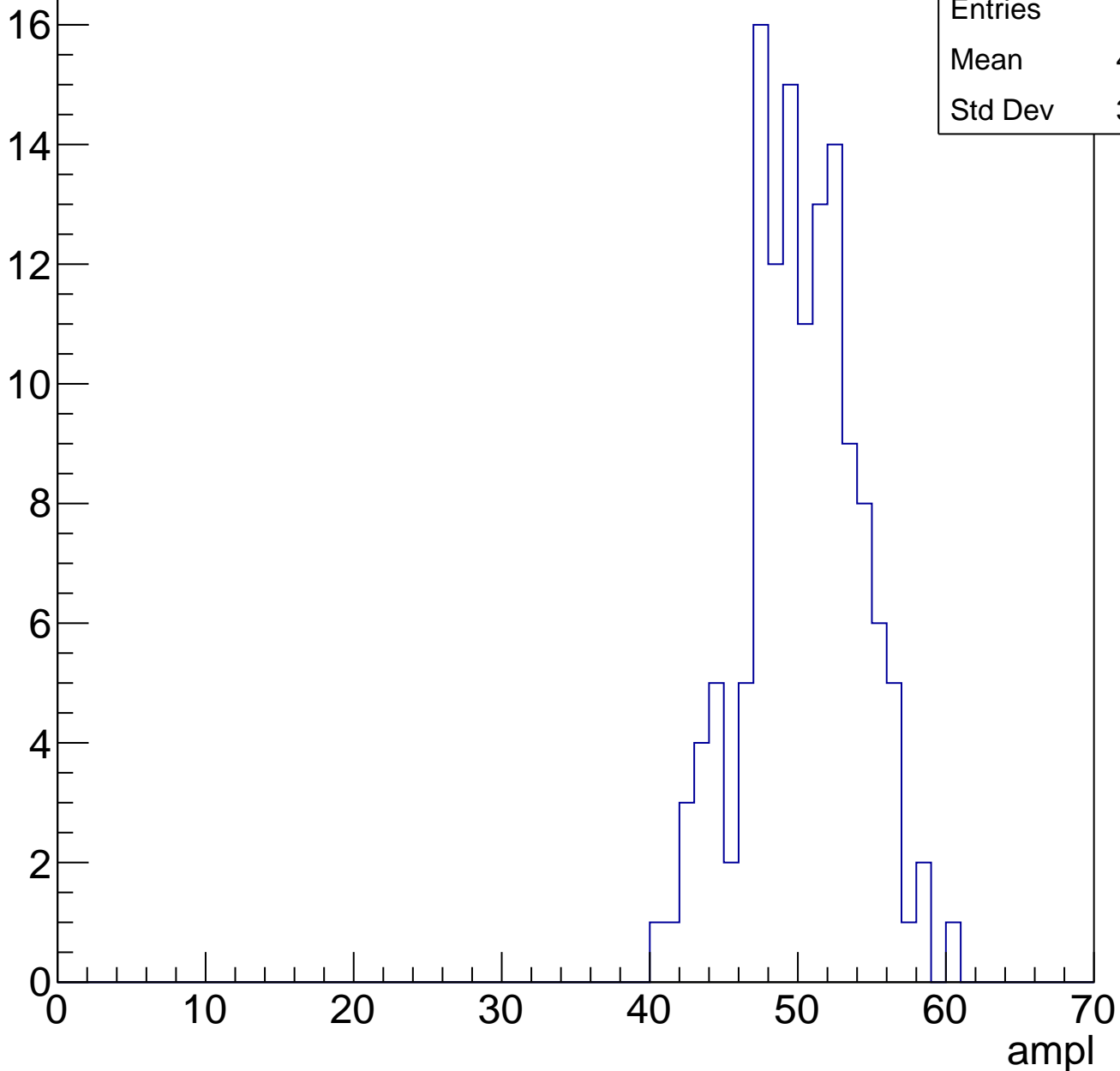
Entry



# B0L101S, U26-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	134
Mean	49.81
Std Dev	3.831

# B0L101S, U26-ch122, adc4

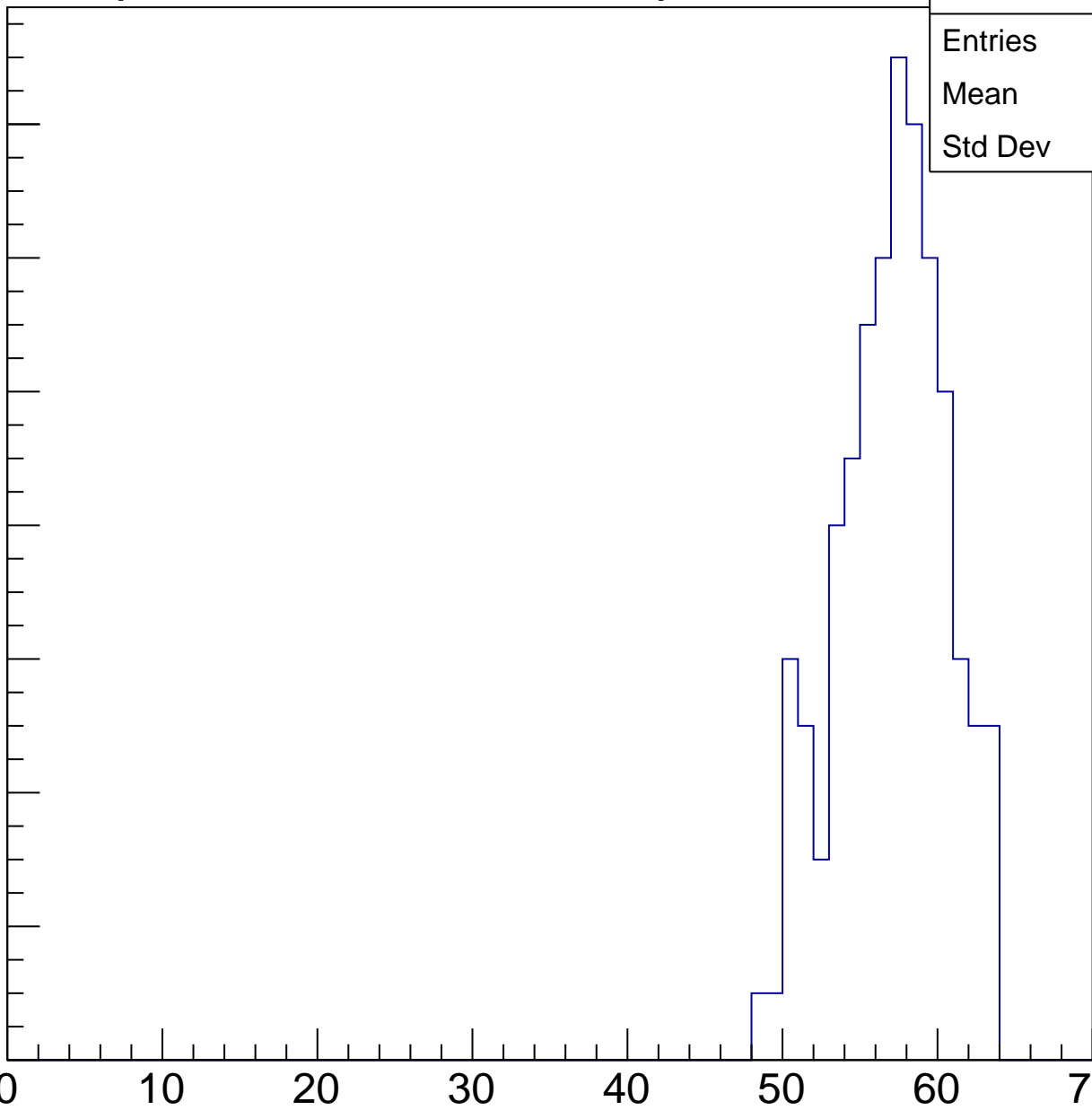
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	123
Mean	56.59
Std Dev	3.482

ampl



# B0L101S, U26-ch122, adc5

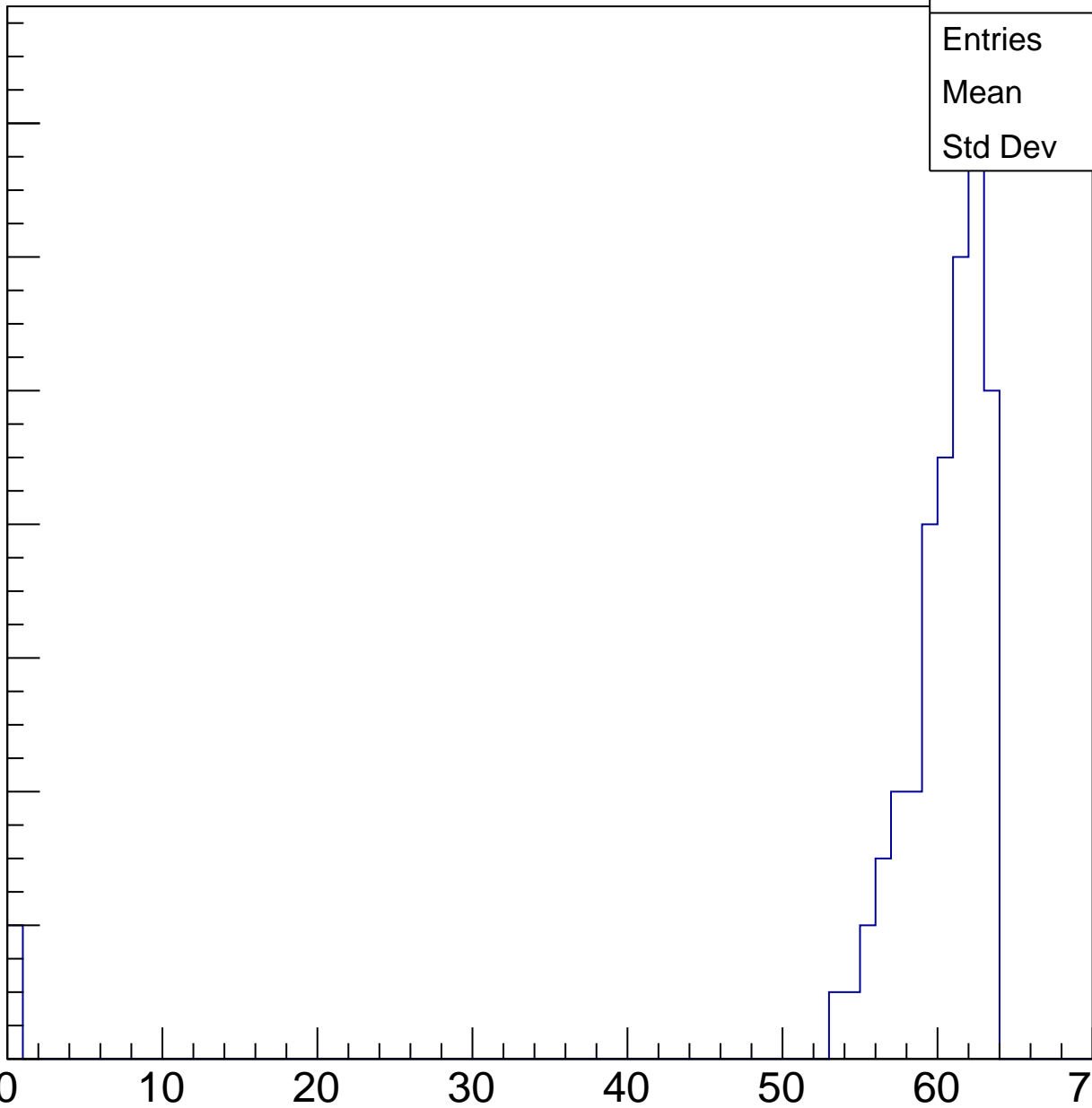
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	71
Mean	58.44
Std Dev	10.23

ampl

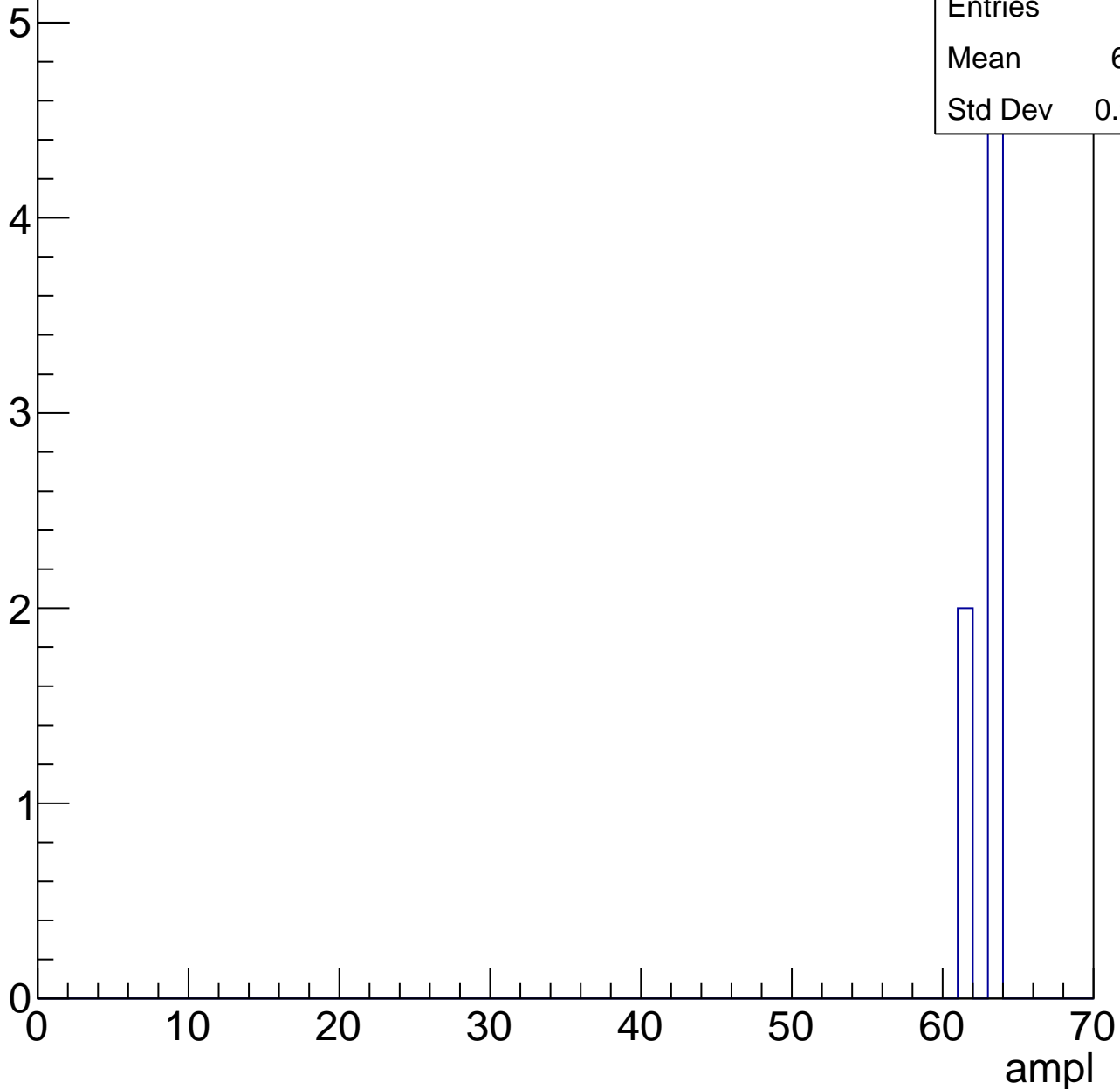


# B0L101S, U26-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.43
Std Dev	0.9035





# B0L101S, U26-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch123, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	161
Mean	28.61
Std Dev	4.677

**Gaus mean : 29.3038**

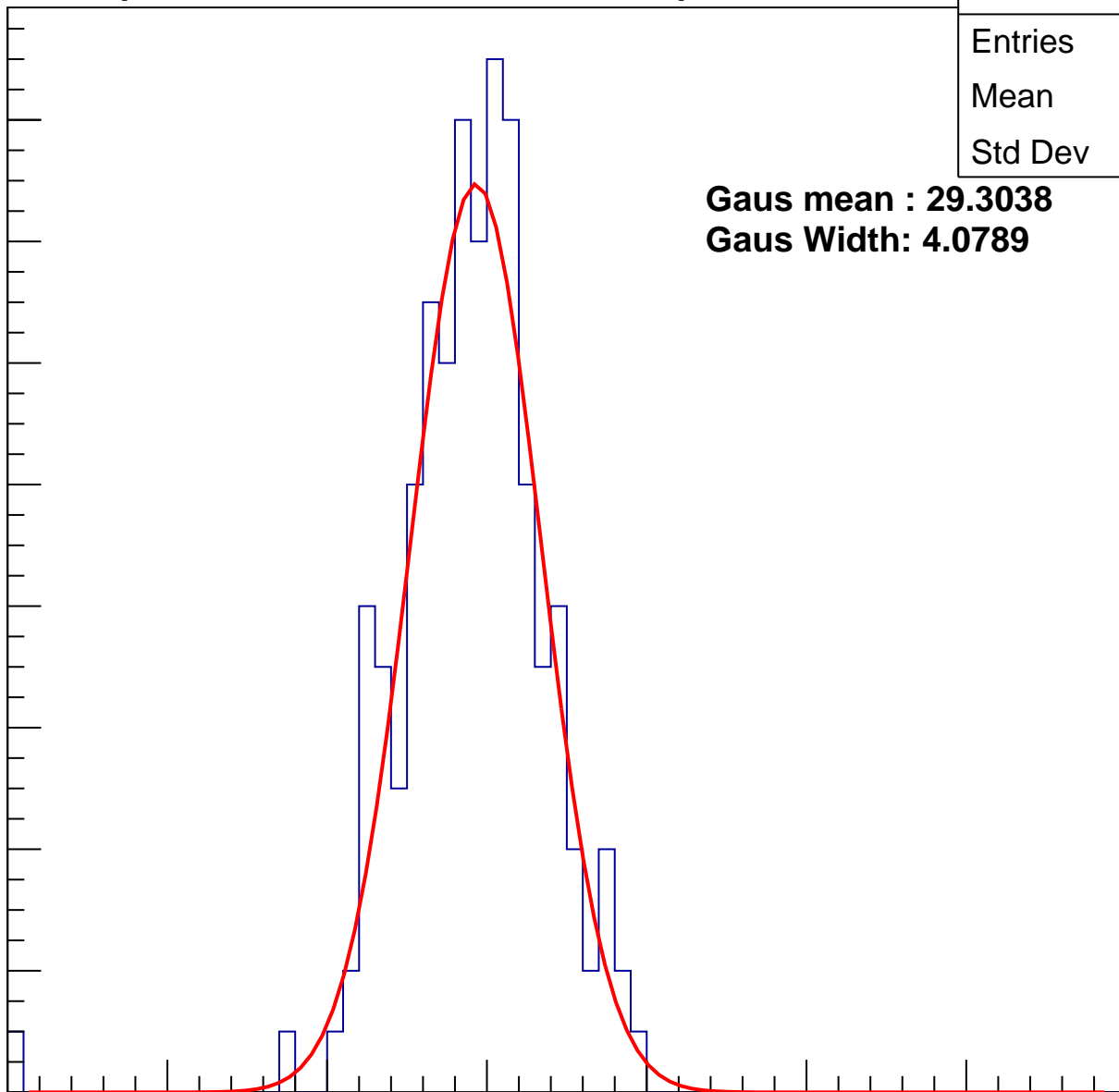
**Gaus Width: 4.0789**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L101S, U26-ch123, adc1

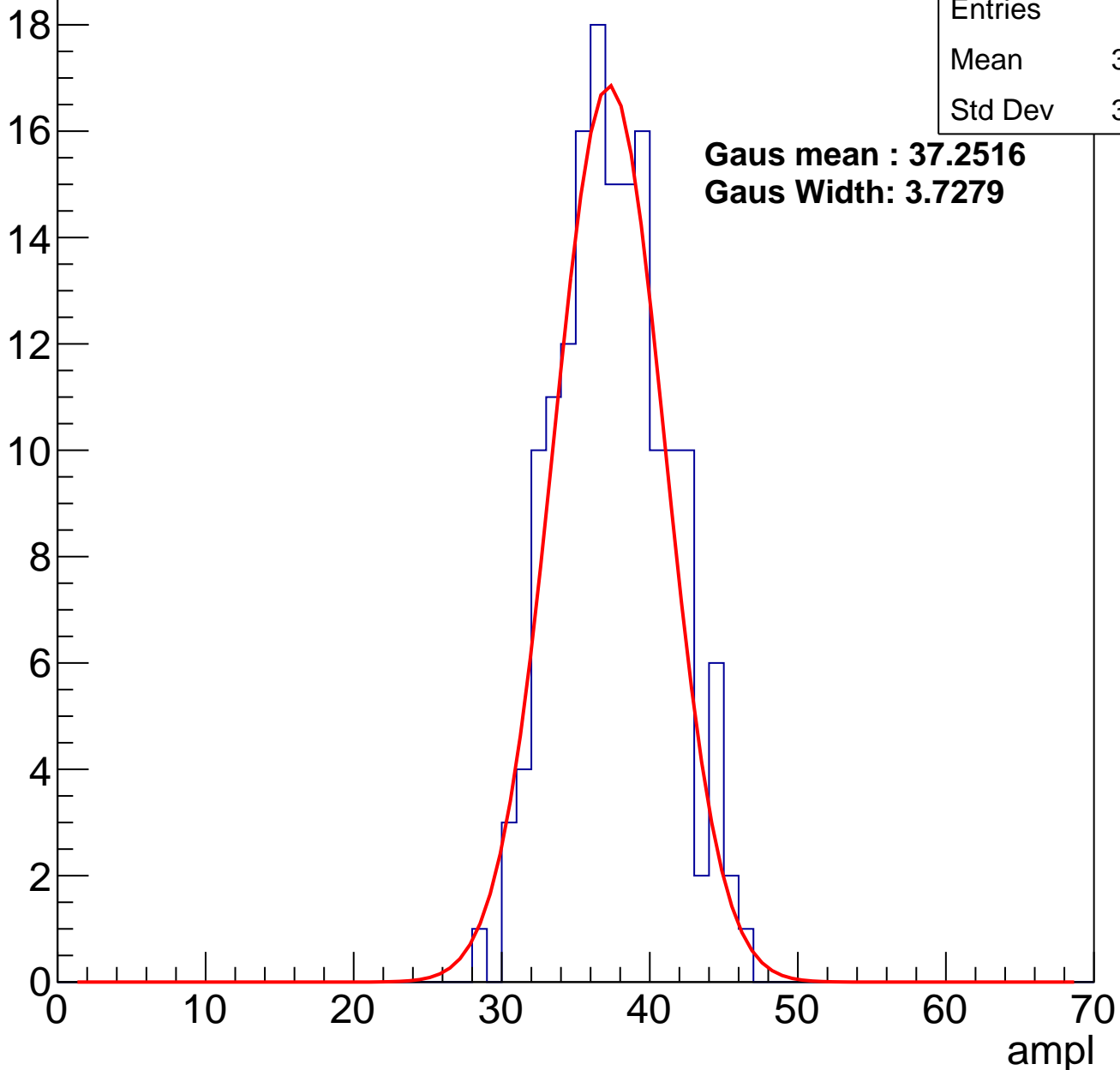
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	162
Mean	37.07
Std Dev	3.635

**Gaus mean : 37.2516**

**Gaus Width: 3.7279**

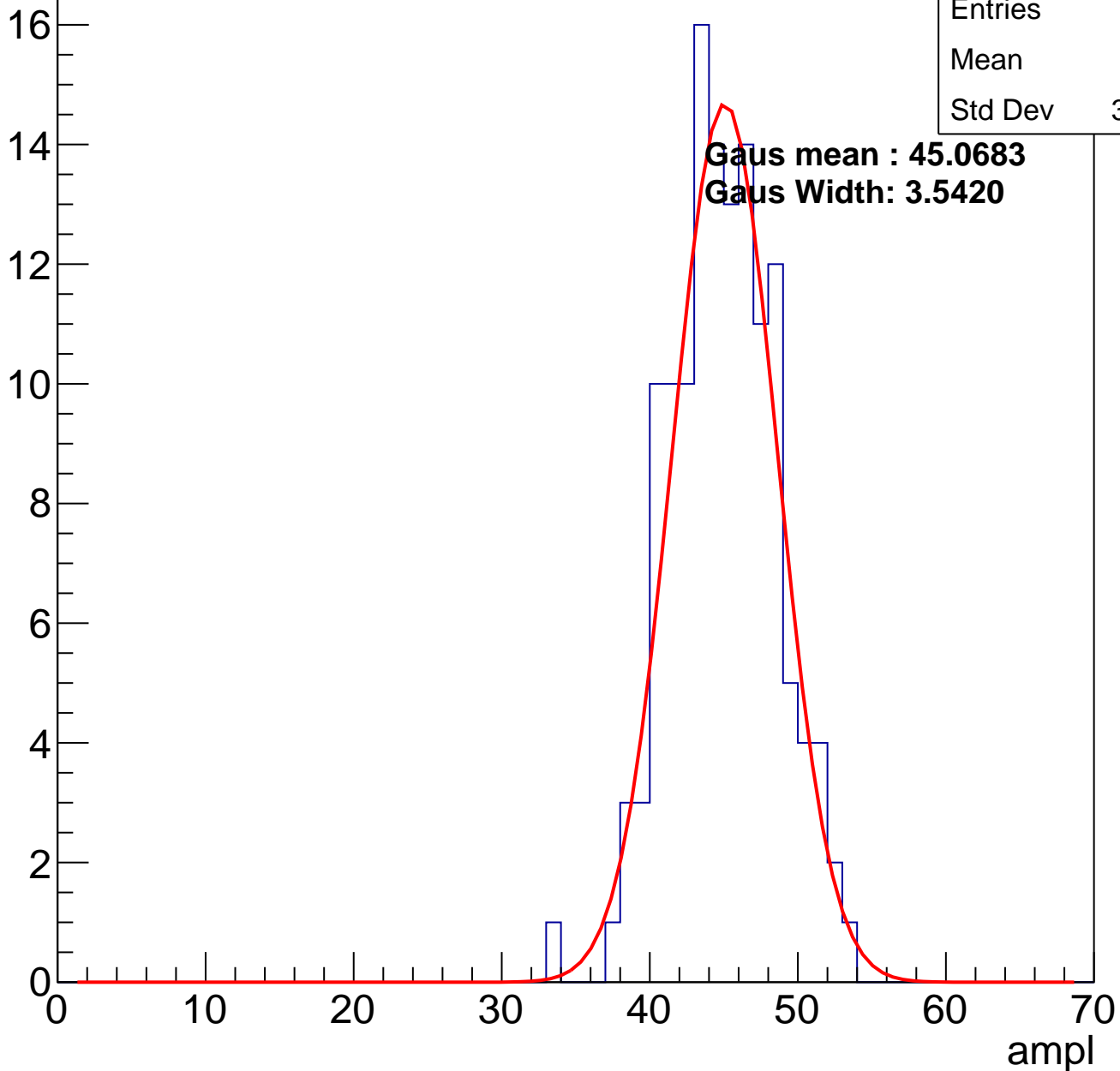
Entry



# B0L101S, U26-ch123, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

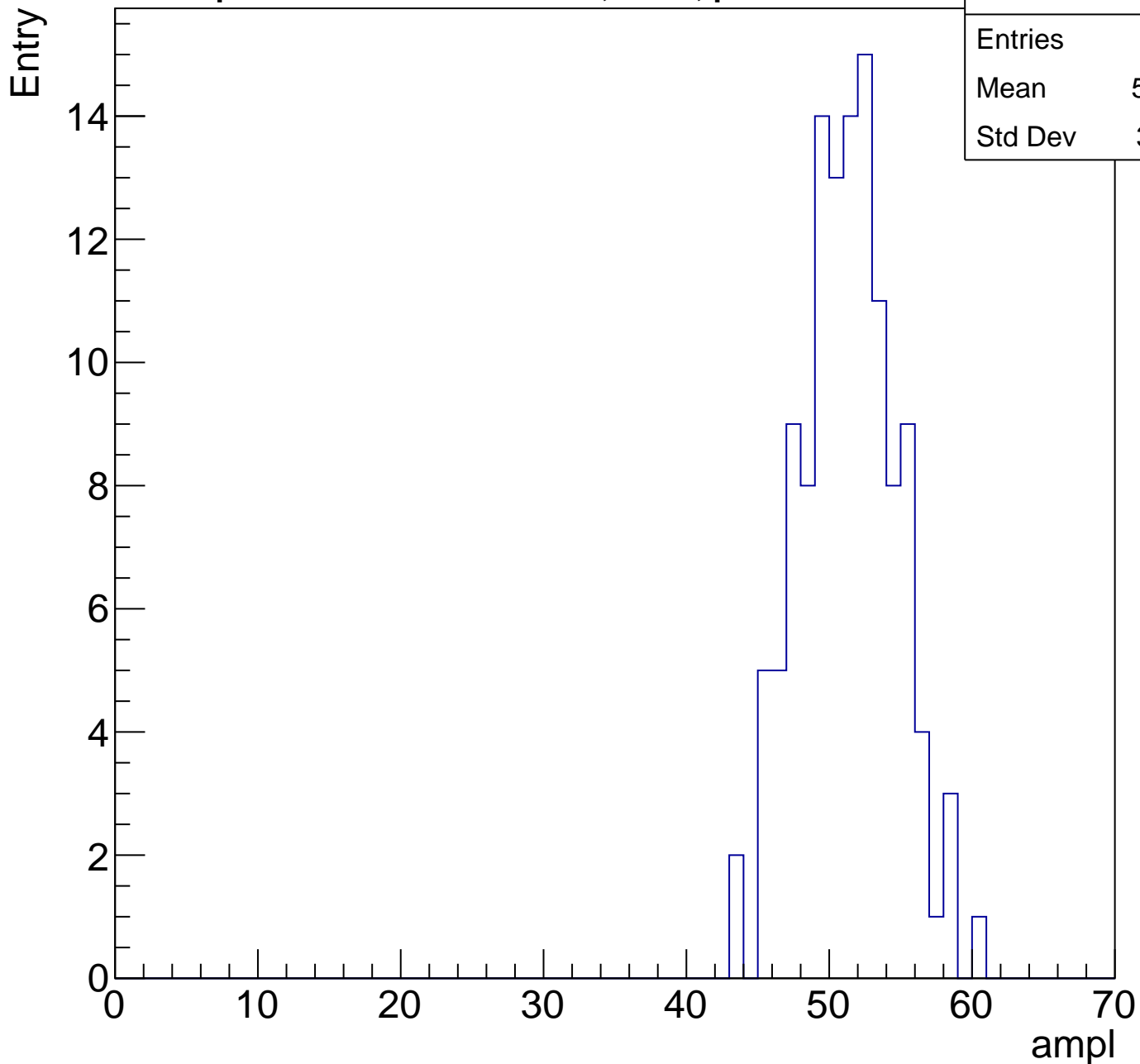
Entry



# B0L101S, U26-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	122
Mean	50.84
Std Dev	3.331



# B0L101S, U26-ch123, adc4

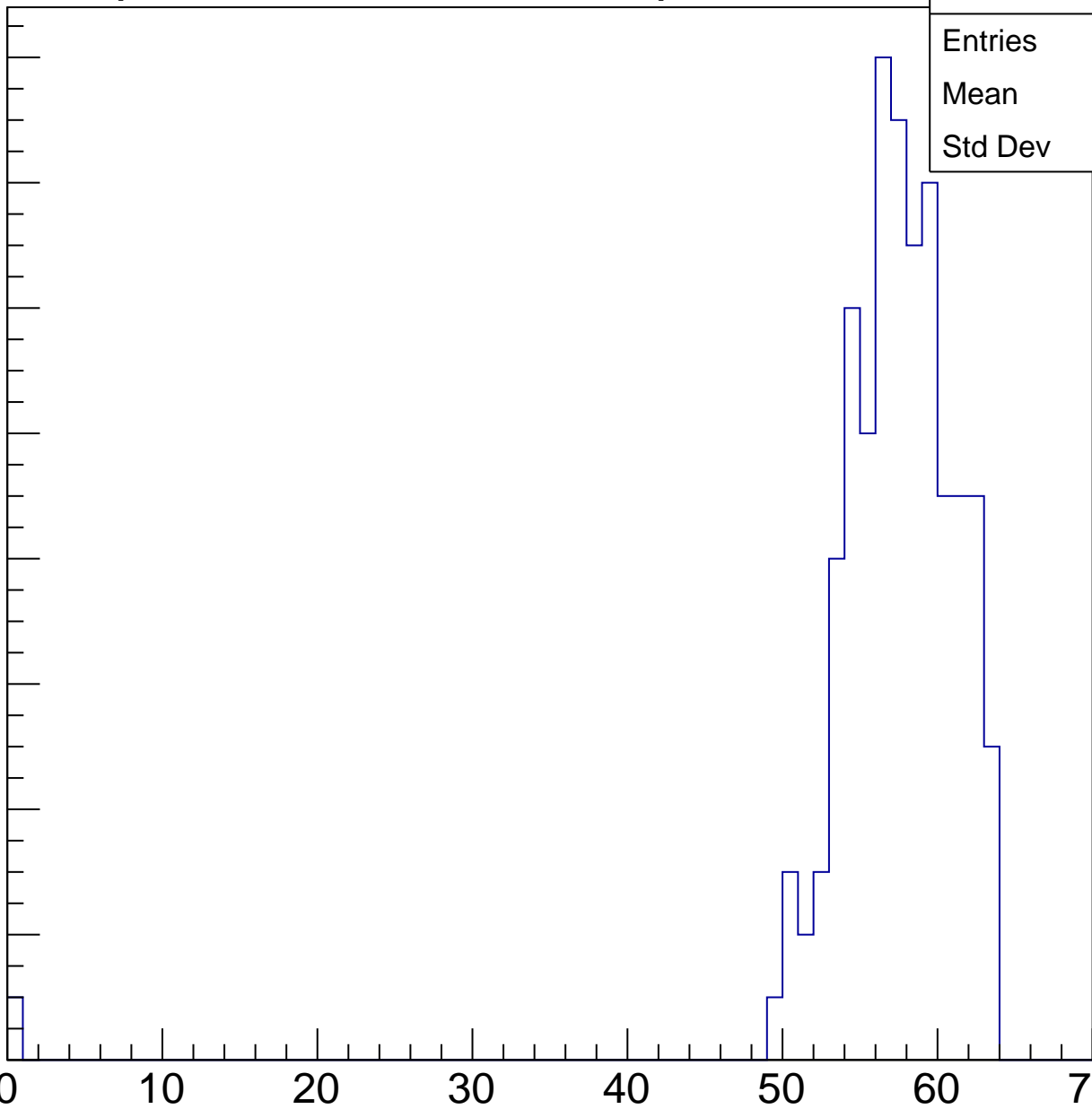
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	130
Mean	56.71
Std Dev	5.94

ampl



# B0L101S, U26-ch123, adc5

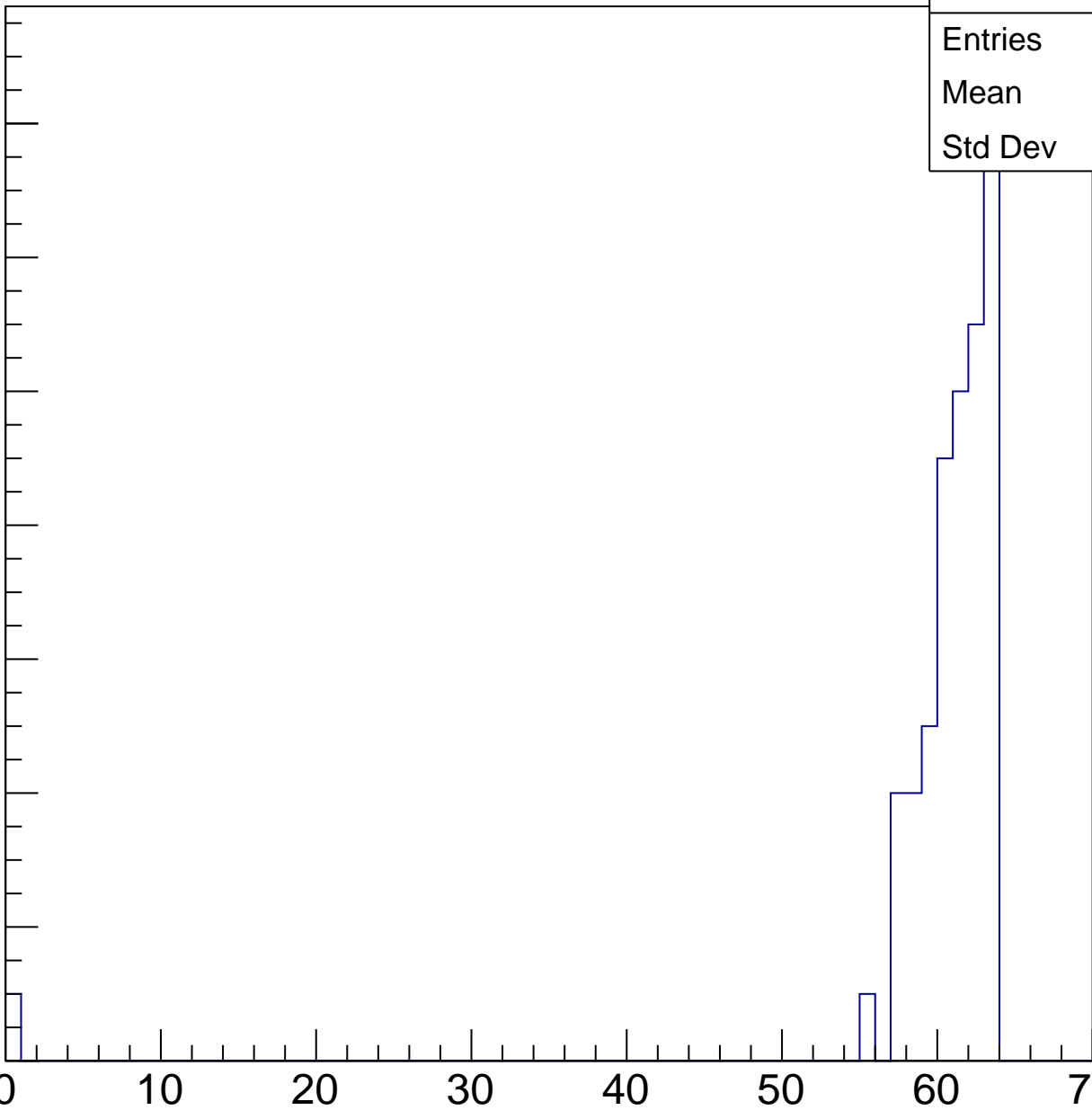
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	60
Mean	59.78
Std Dev	8.029

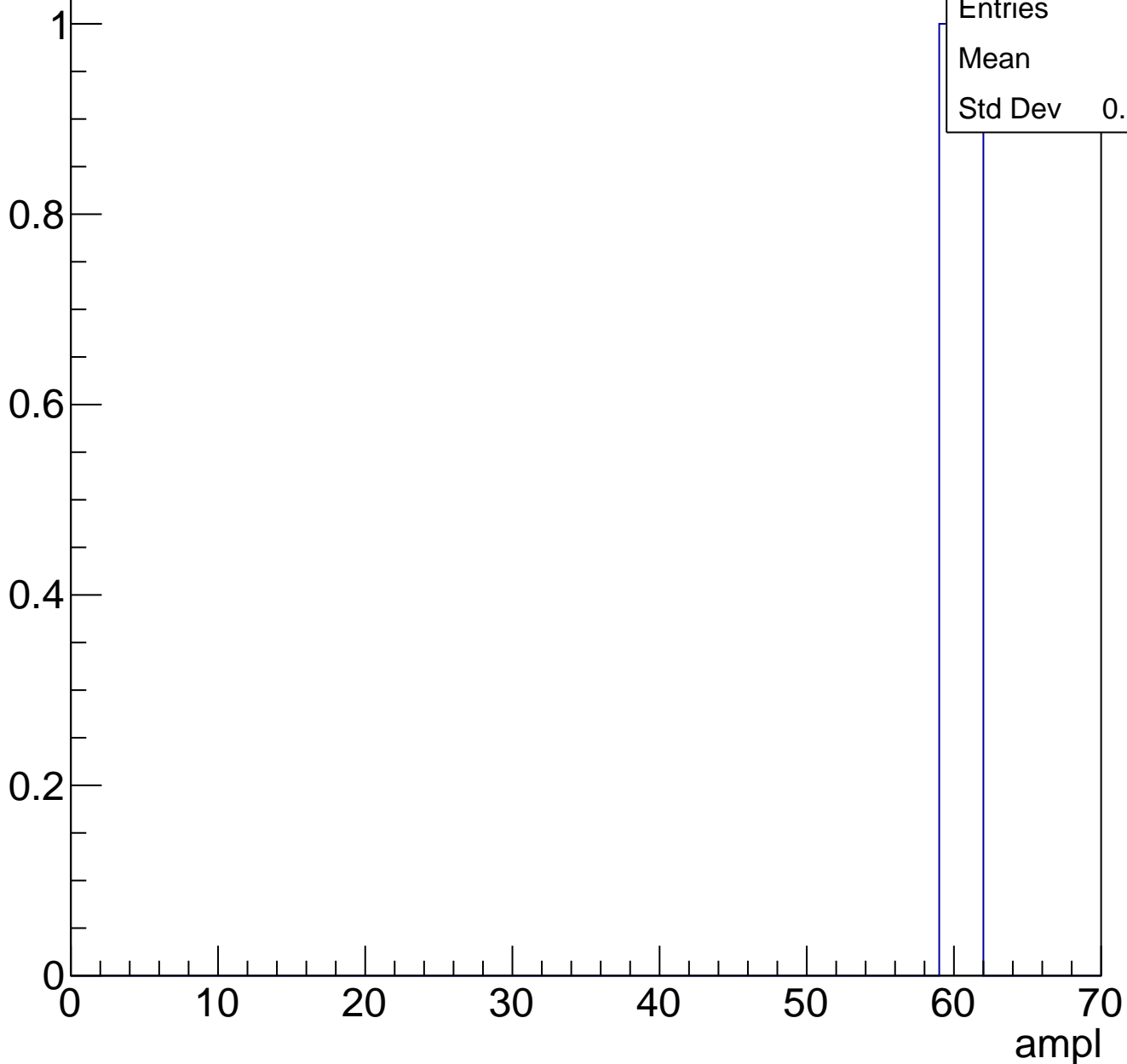
ampl



# B0L101S, U26-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



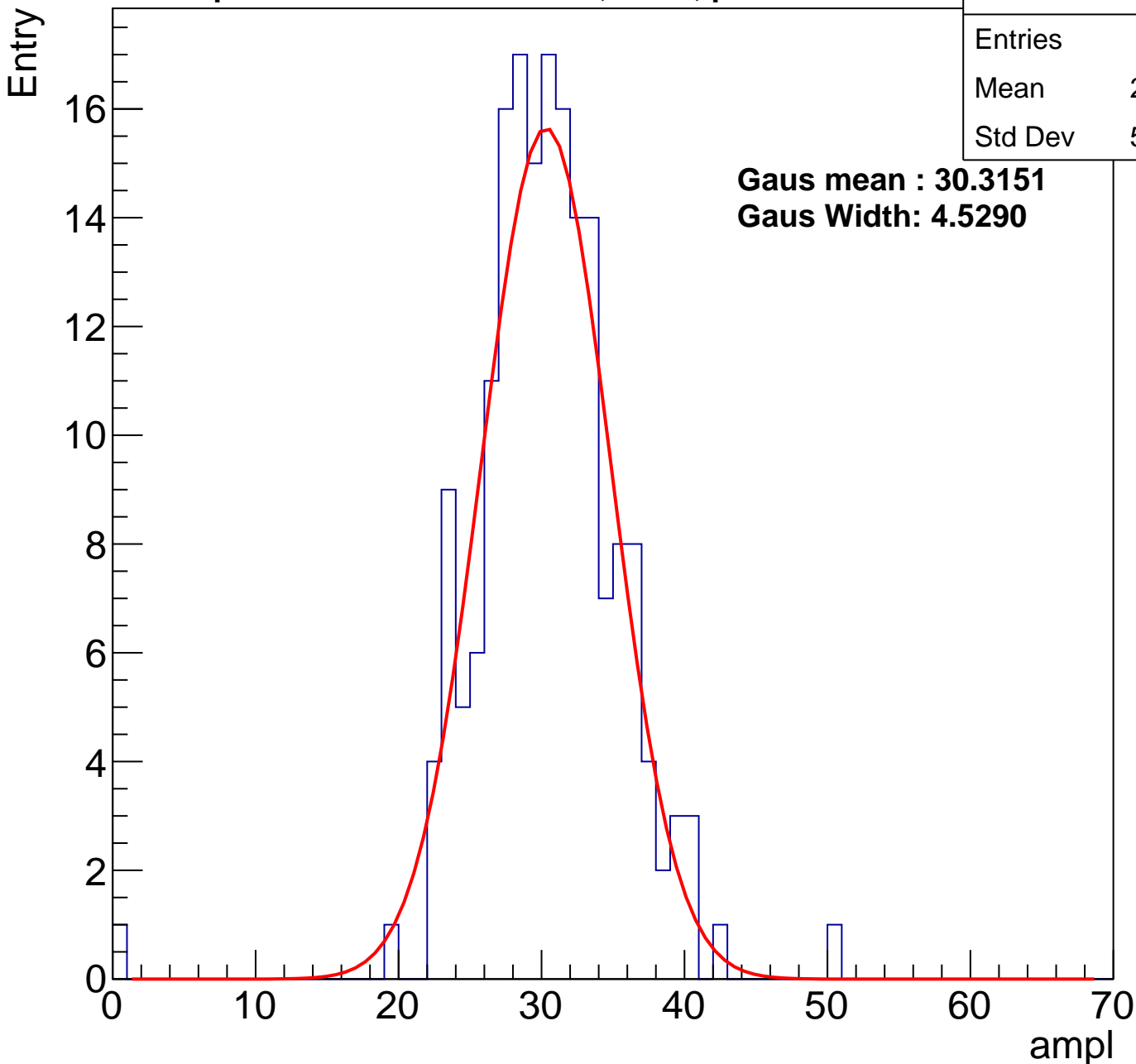
Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	183
Mean	29.99
Std Dev	5.046

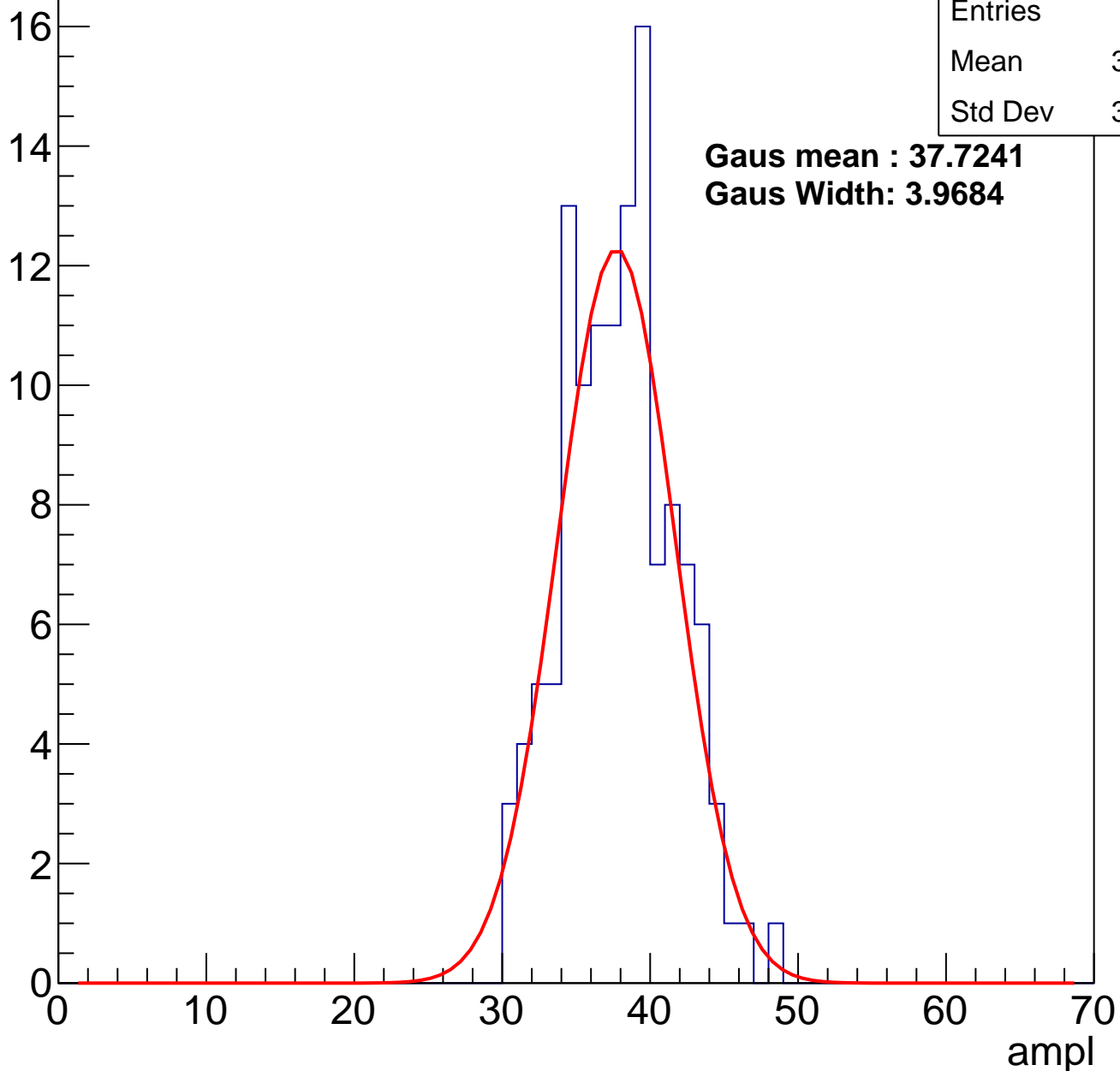
**Gaus mean : 30.3151**  
**Gaus Width: 4.5290**



# B0L101S, U26-ch124, adc1

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch124, adc2

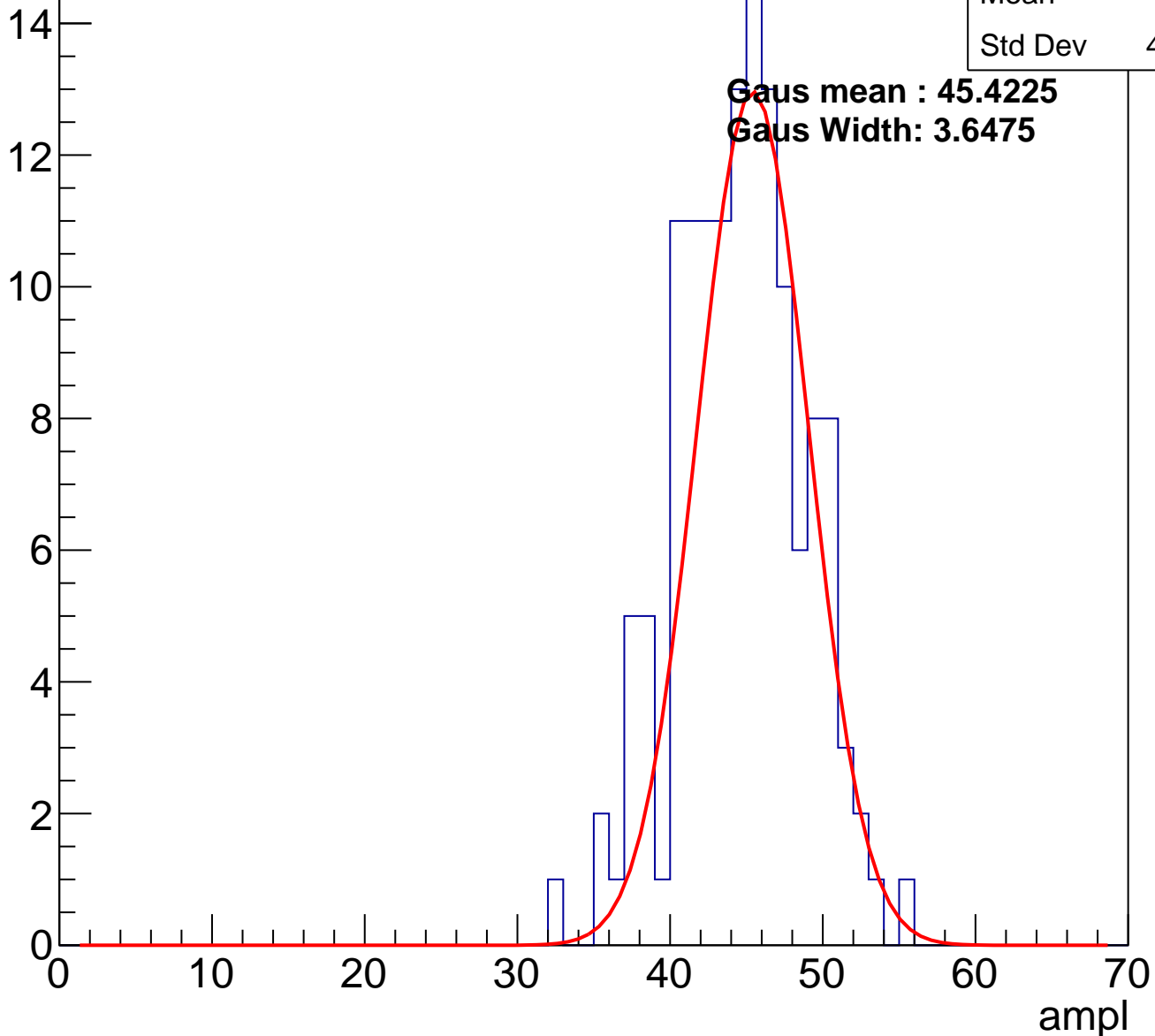
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	44.16
Std Dev	4.124

**Gaus mean : 45.4225**

**Gaus Width: 3.6475**

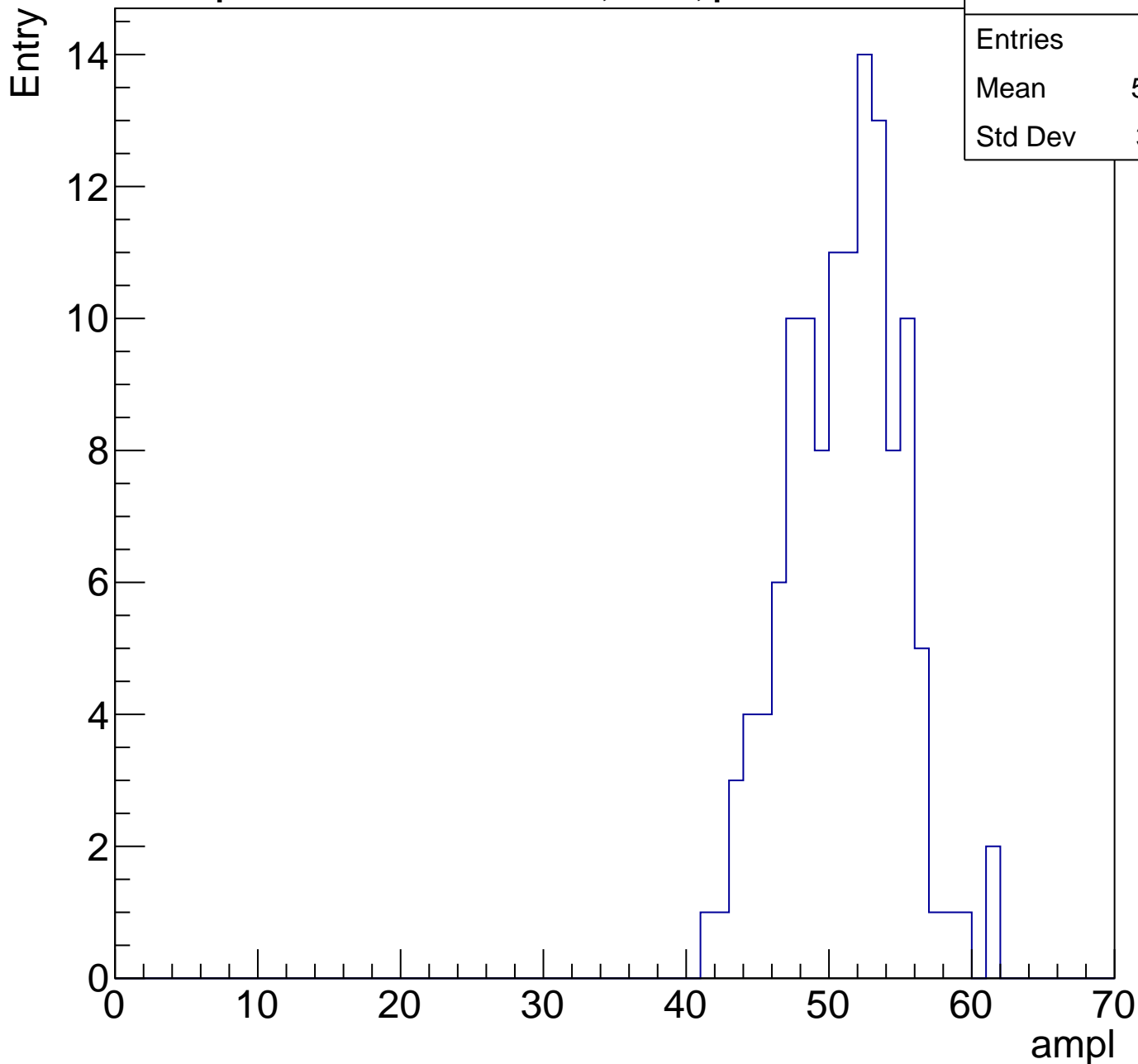
Entry



# B0L101S, U26-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	124
Mean	50.58
Std Dev	3.921



# B0L101S, U26-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	131
Mean	56.34
Std Dev	6.153

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0

10

20

30

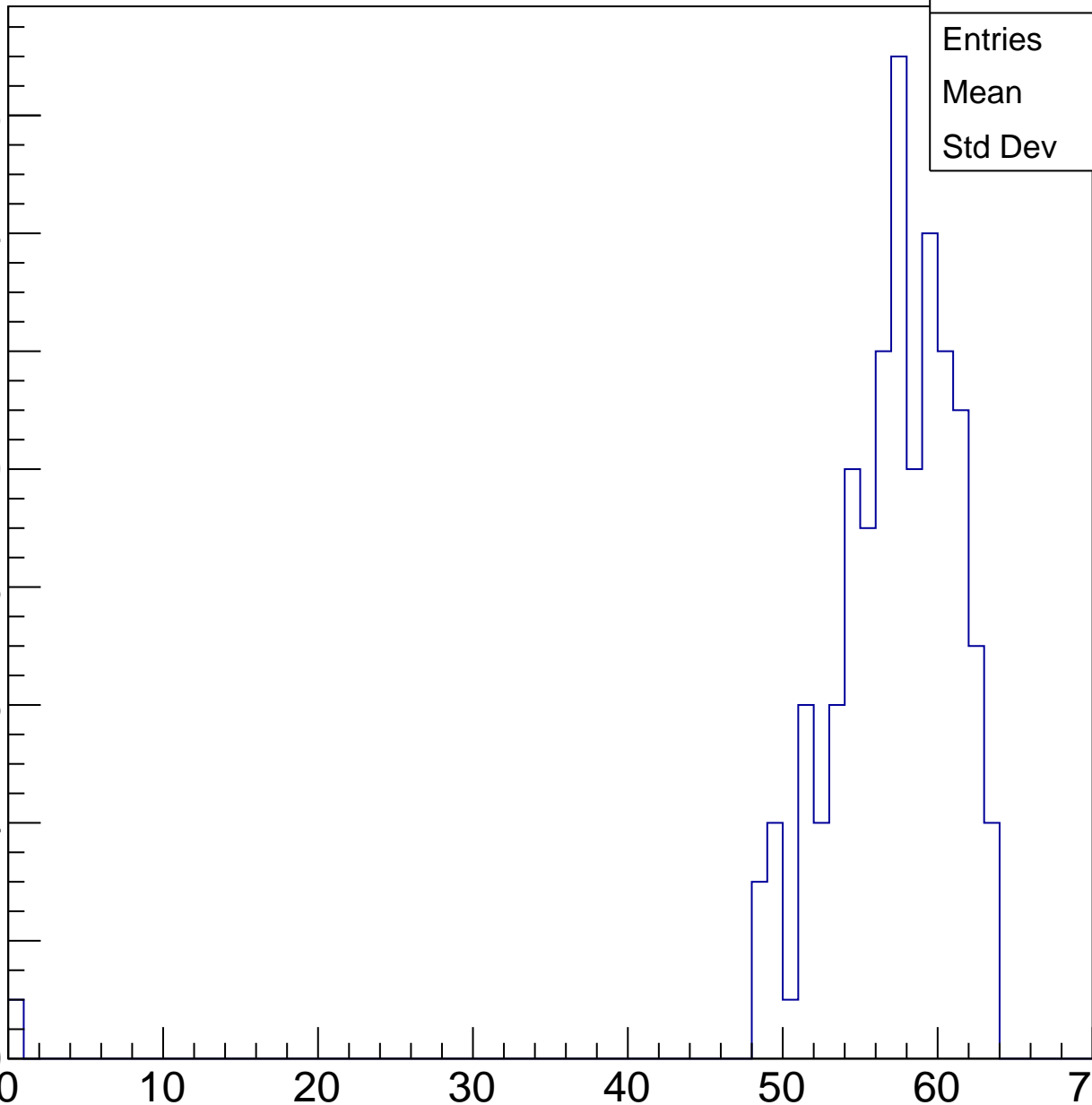
40

50

60

70

ampl



# B0L101S, U26-ch124, adc5

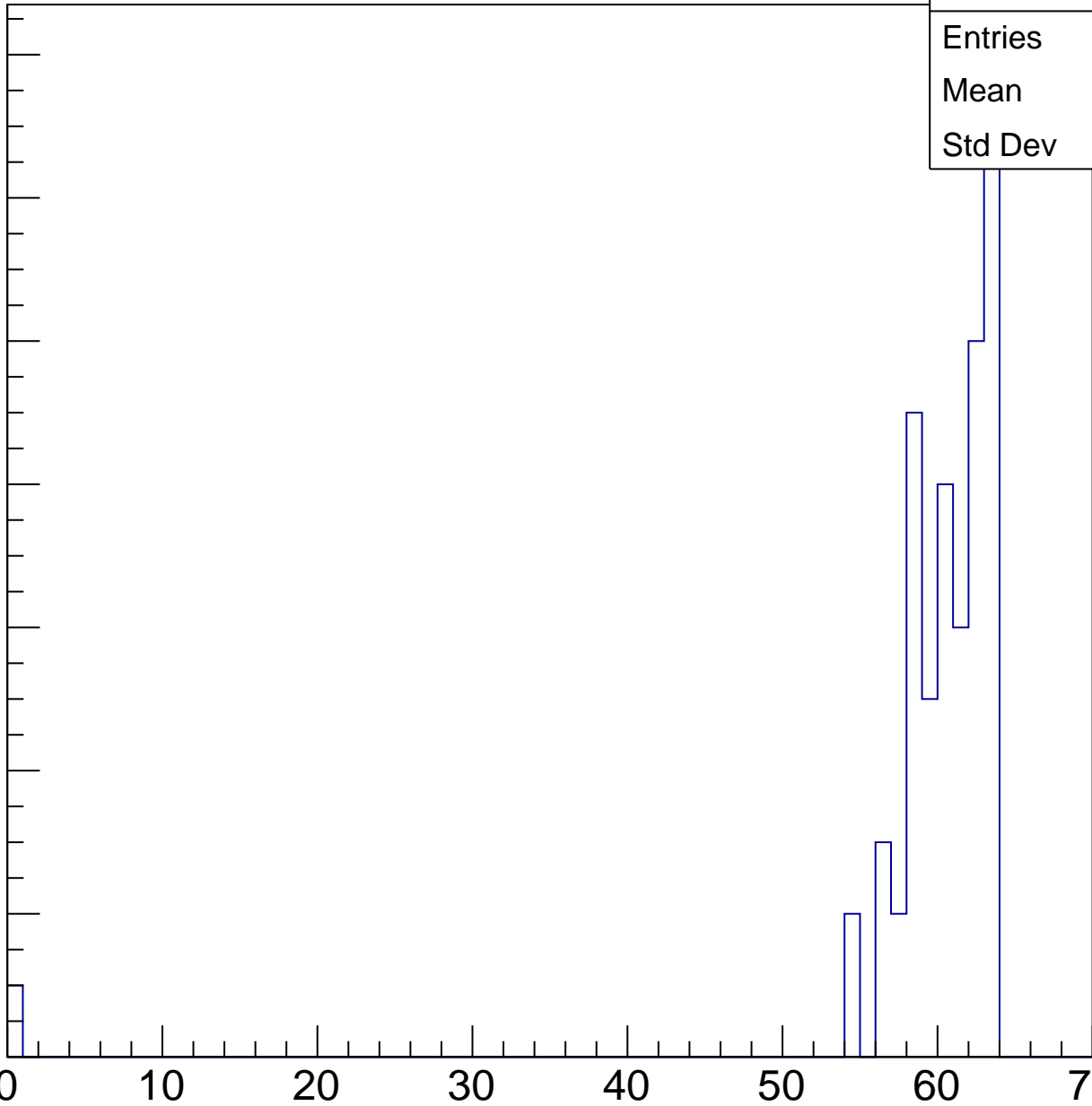
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

14  
12  
10  
8  
6  
4  
2  
0

Entries	60
Mean	59.25
Std Dev	8.08

ampl



# B0L101S, U26-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

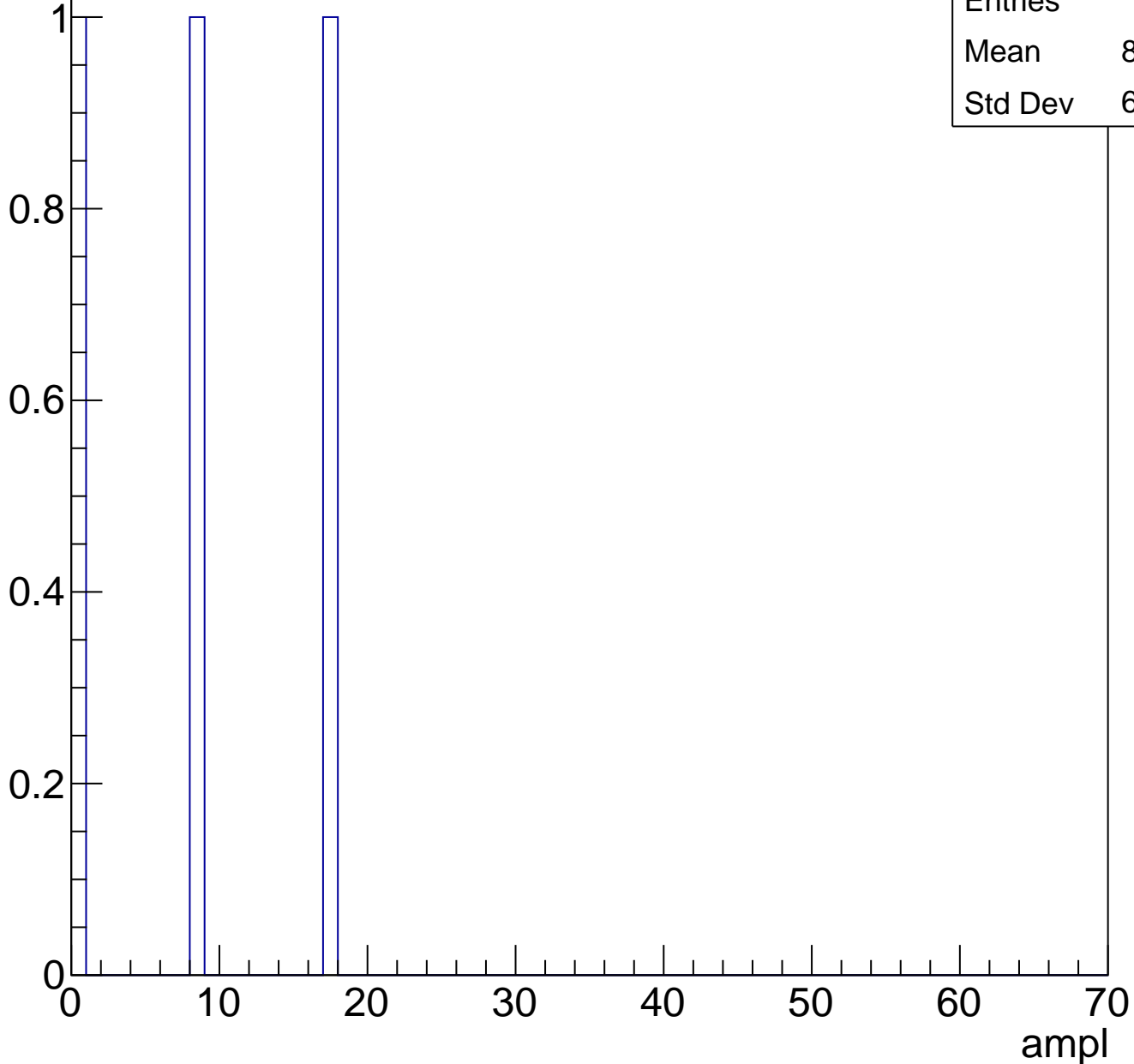




# B0L101S, U26-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

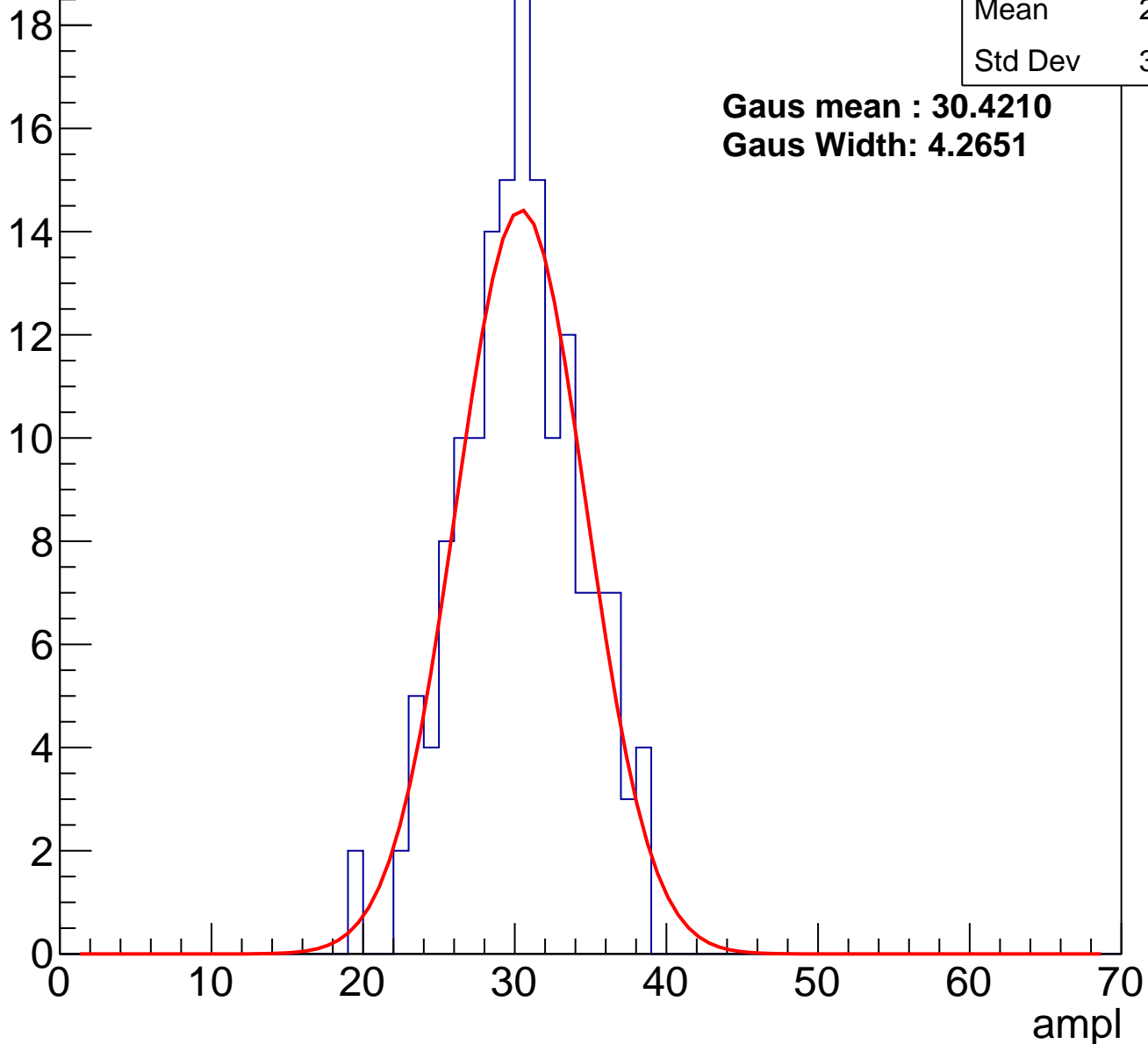
Entry



# B0L101S, U26-ch125, adc0

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	154
Mean	29.86
Std Dev	3.933

# B0L101S, U26-ch125, adc1

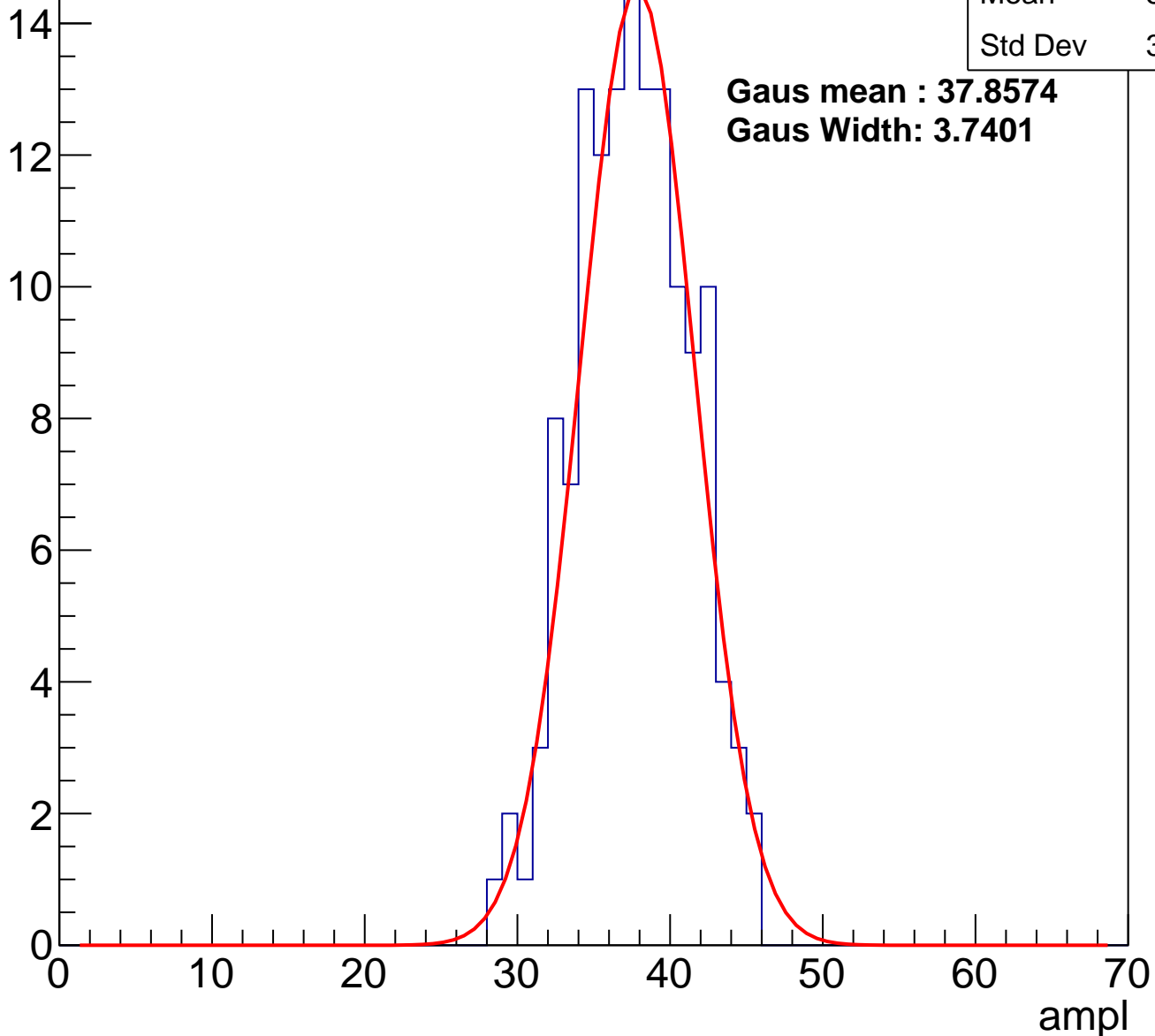
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	139
Mean	37.16
Std Dev	3.619

**Gaus mean : 37.8574**

**Gaus Width: 3.7401**

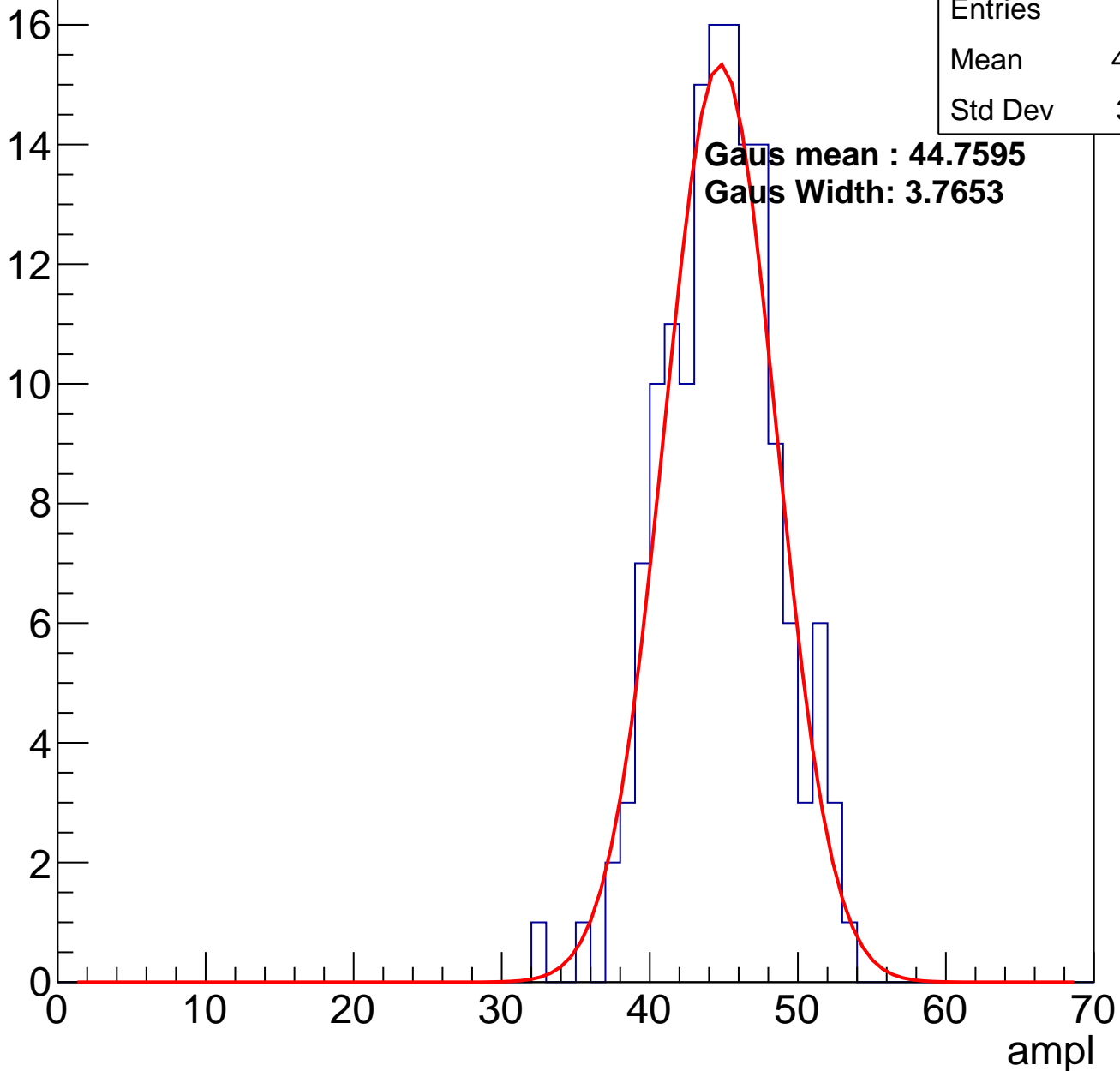
Entry



# B0L101S, U26-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

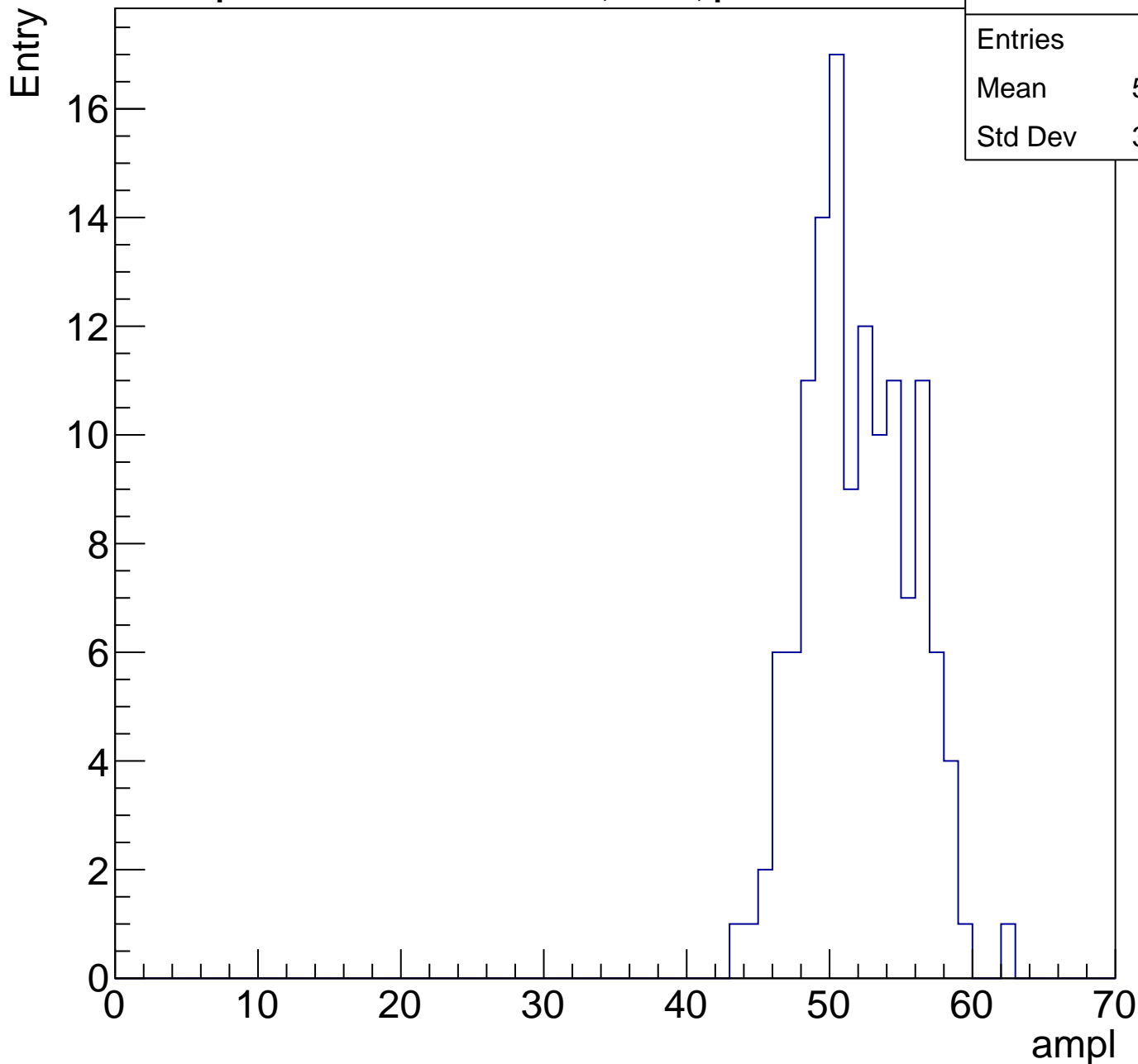
Entry



# B0L101S, U26-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

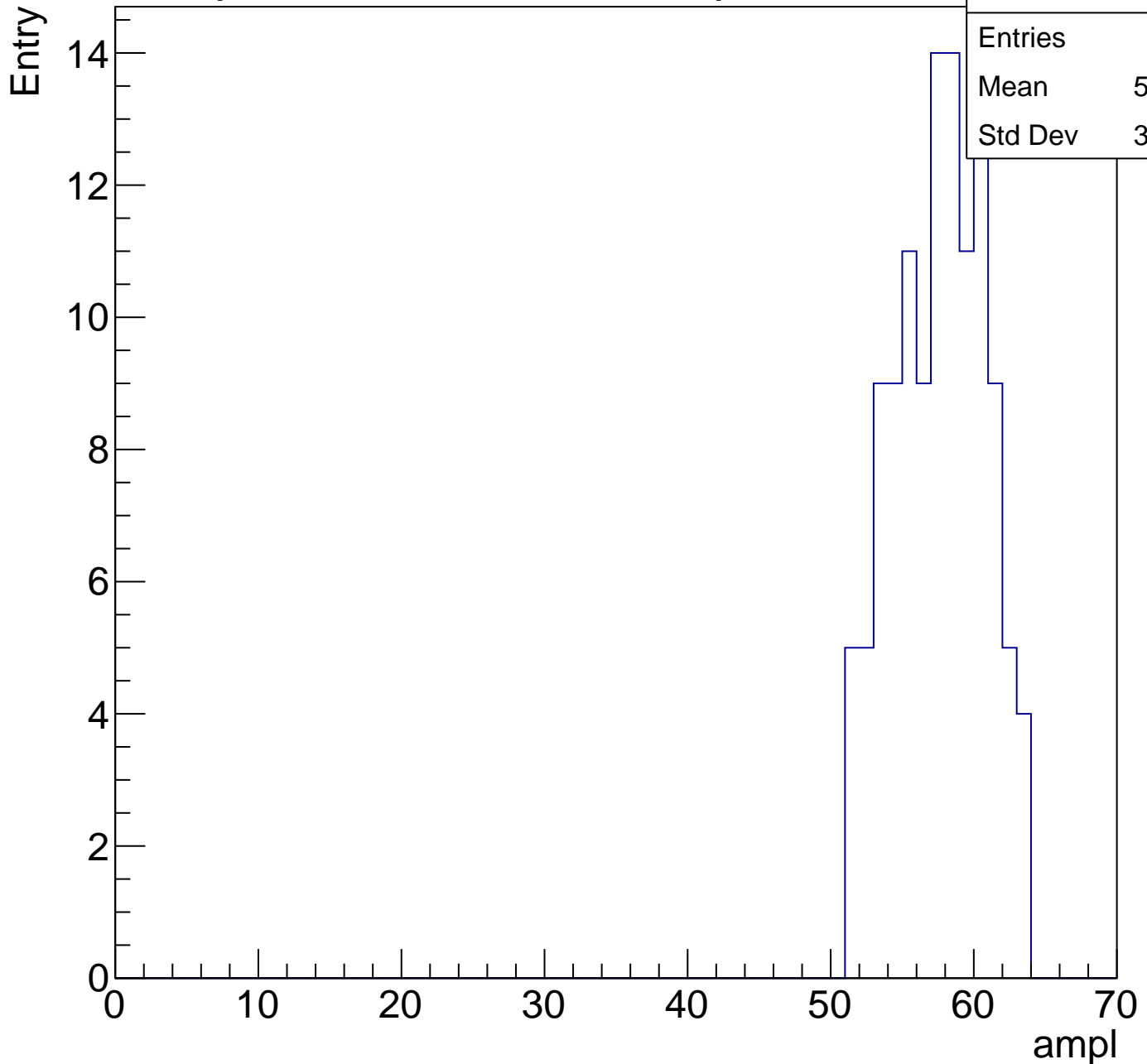
Entries	130
Mean	51.55
Std Dev	3.614



# B0L101S, U26-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	118
Mean	57.09
Std Dev	3.149



# B0L101S, U26-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

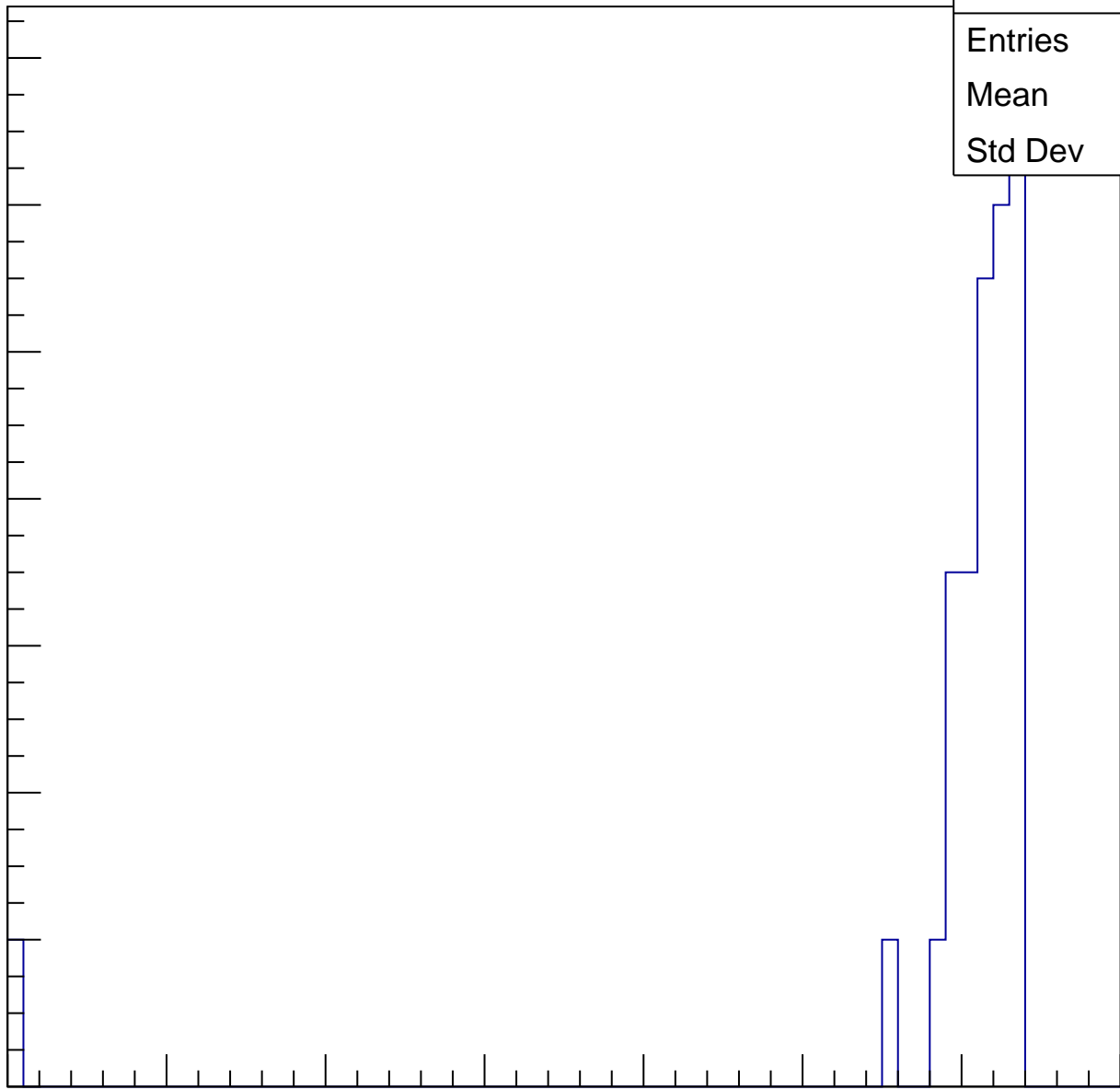
Entries	57
Mean	58.88
Std Dev	11.38

Entry

14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

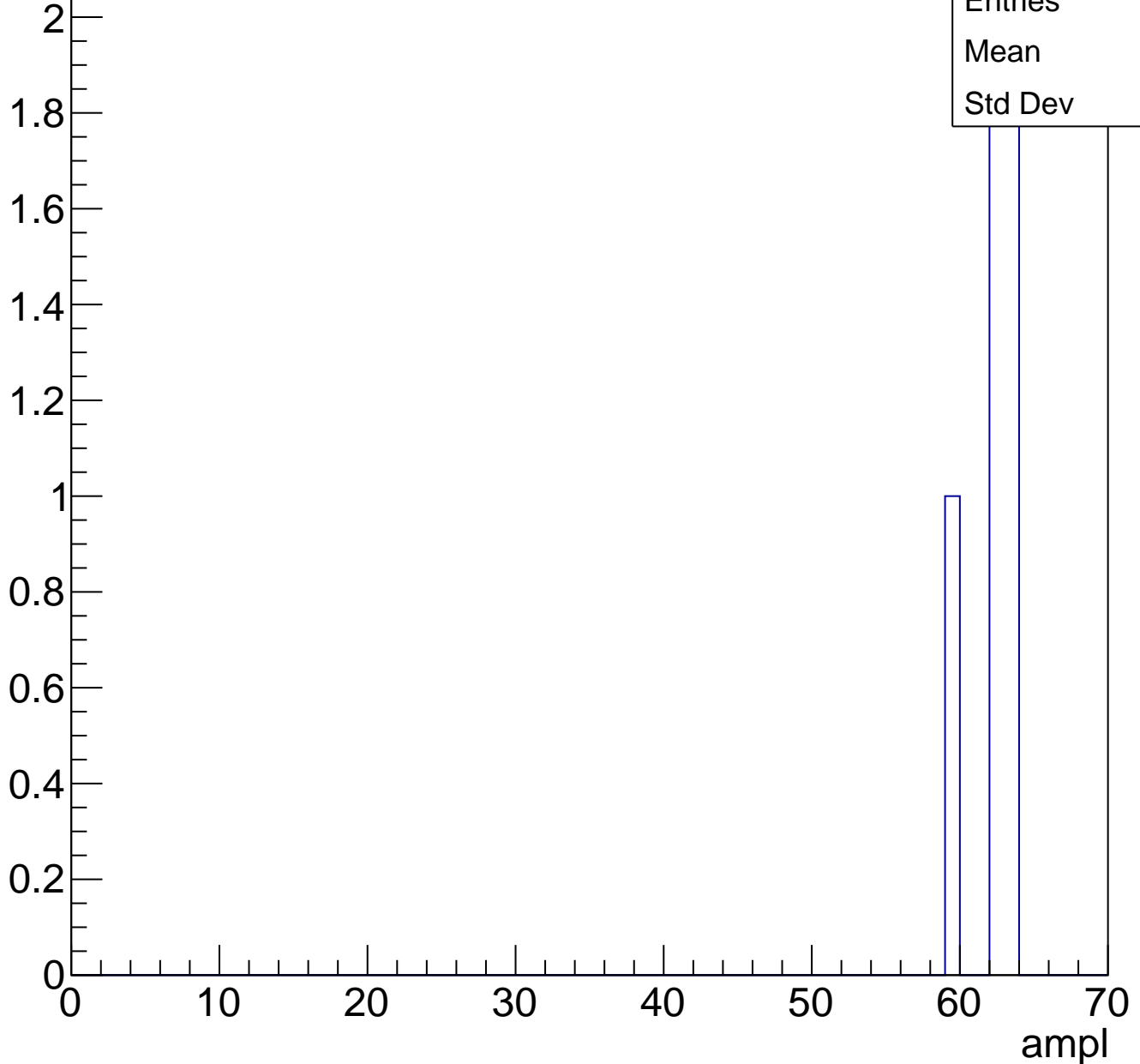
ampl



# B0L101S, U26-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L101S, U26-ch126, adc0

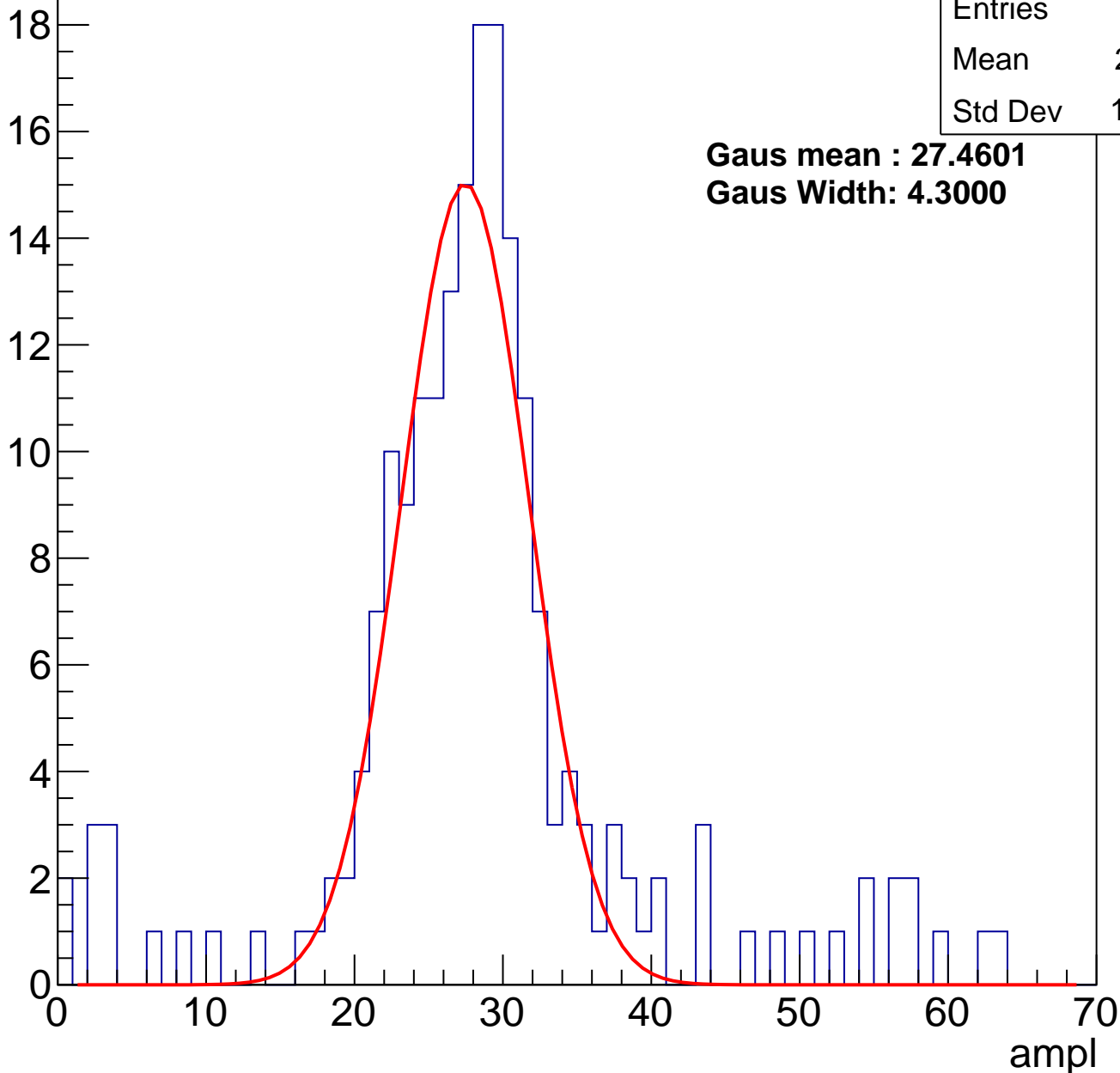
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	201
Mean	28.01
Std Dev	10.23

**Gaus mean : 27.4601**

**Gaus Width: 4.3000**

Entry



# B0L101S, U26-ch126, adc1

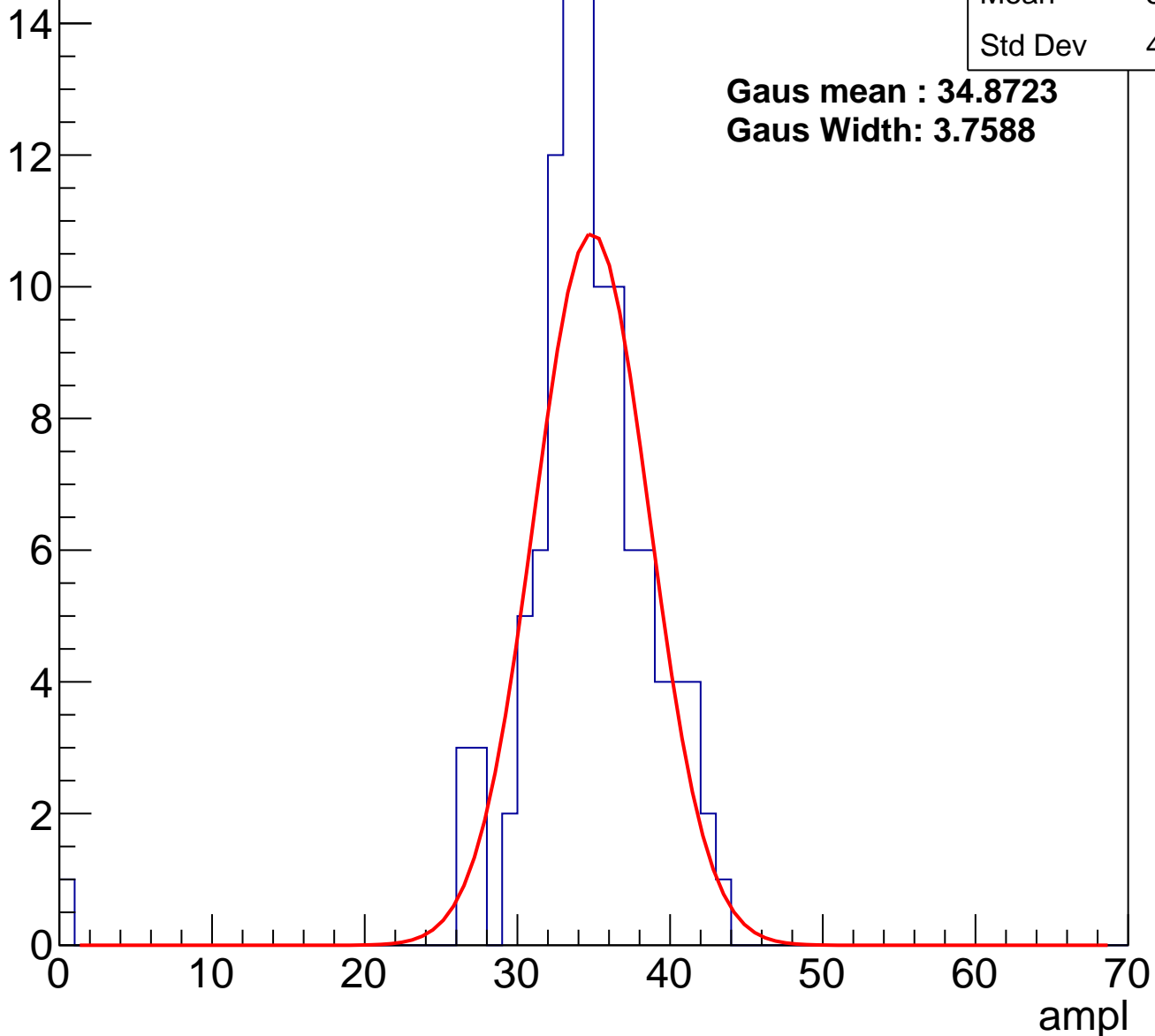
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	109
Mean	34.03
Std Dev	4.875

**Gaus mean : 34.8723**

**Gaus Width: 3.7588**

Entry



# B0L101S, U26-ch126, adc2

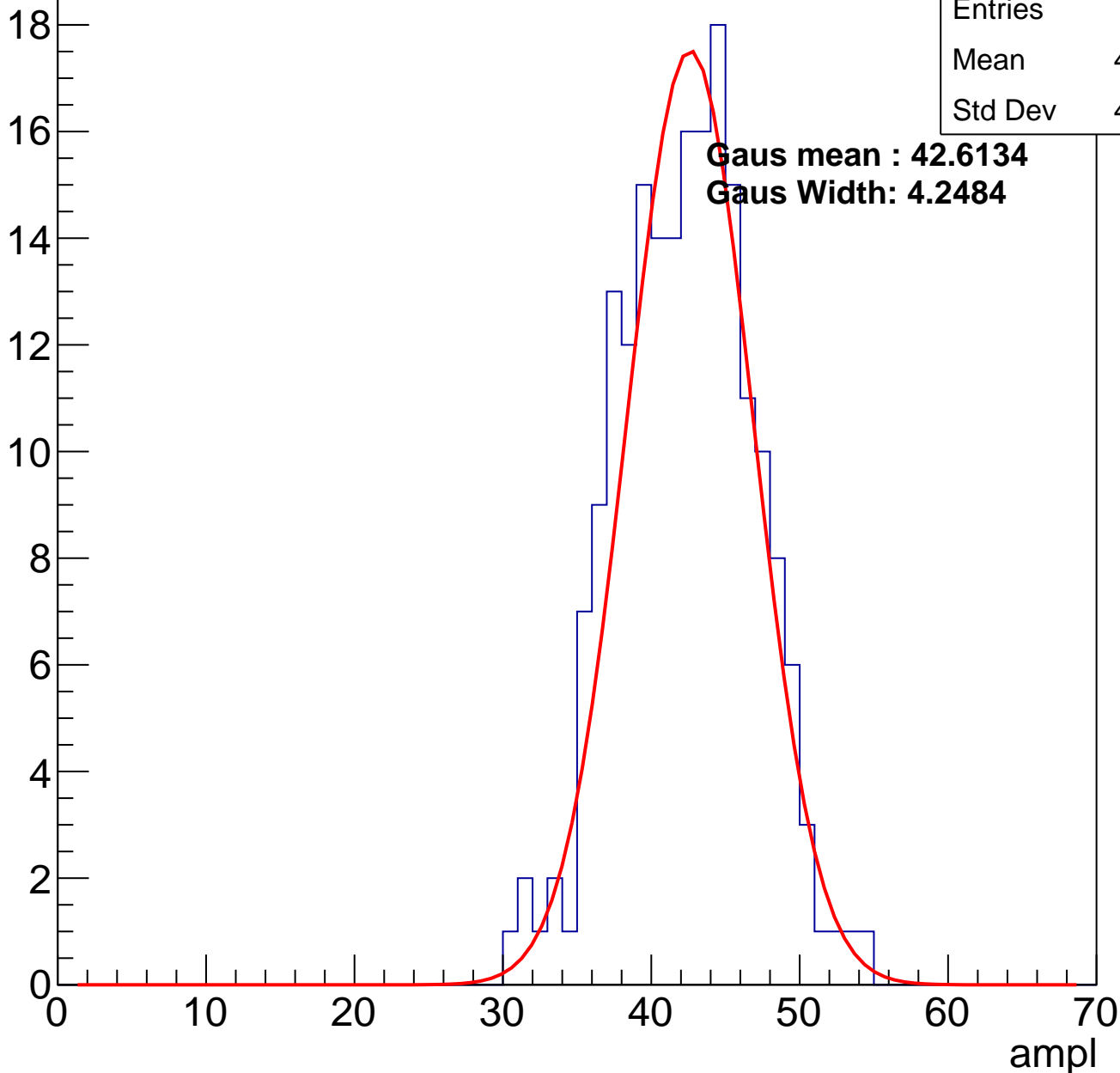
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	198
Mean	41.87
Std Dev	4.476

**Gaus mean : 42.6134**

**Gaus Width: 4.2484**

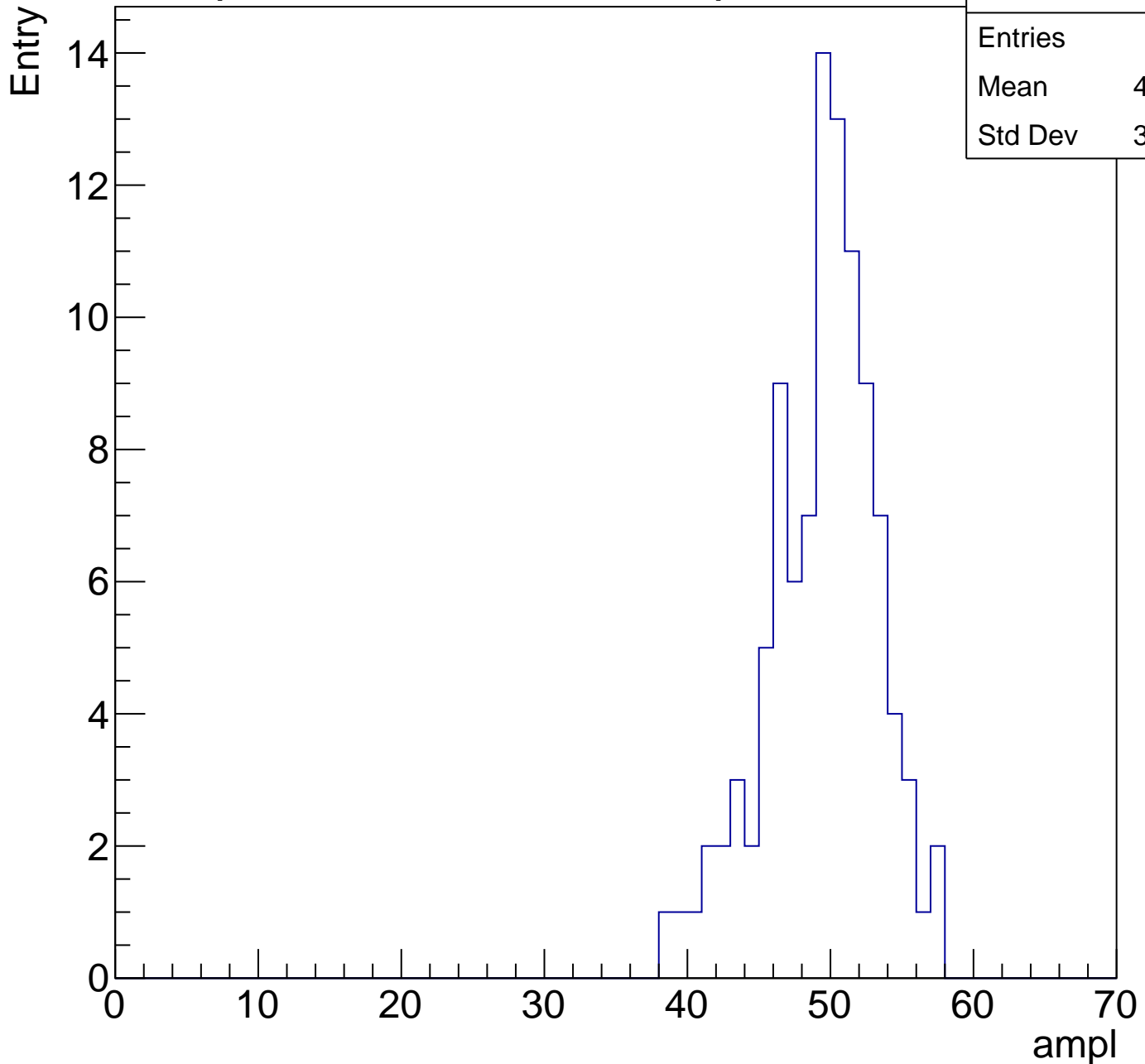
Entry



# B0L101S, U26-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

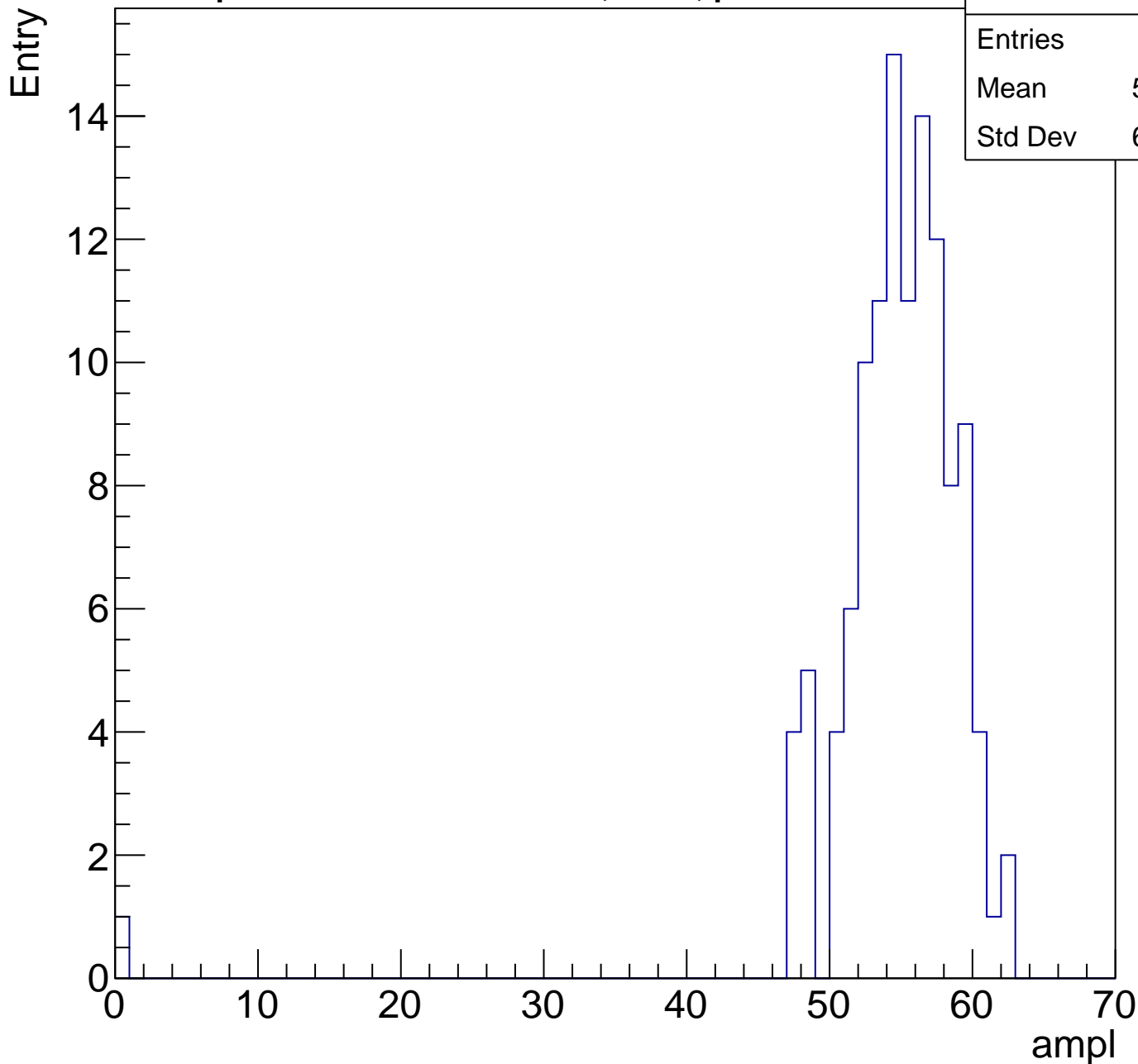
Entries	103
Mean	48.97
Std Dev	3.833



# B0L101S, U26-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	117
Mean	54.19
Std Dev	6.066



# B0L101S, U26-ch126, adc5

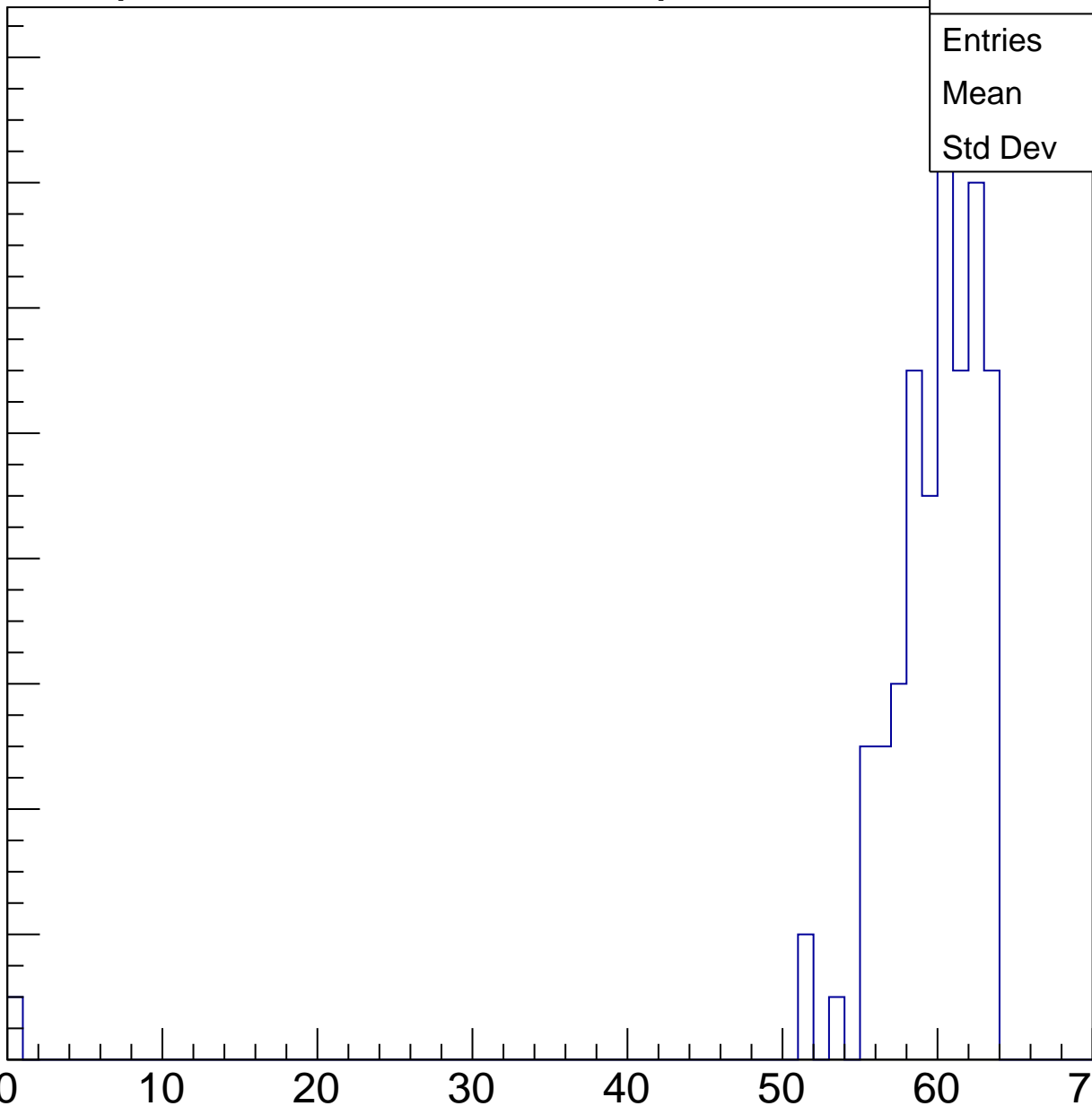
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	92
Mean	58.84
Std Dev	6.728

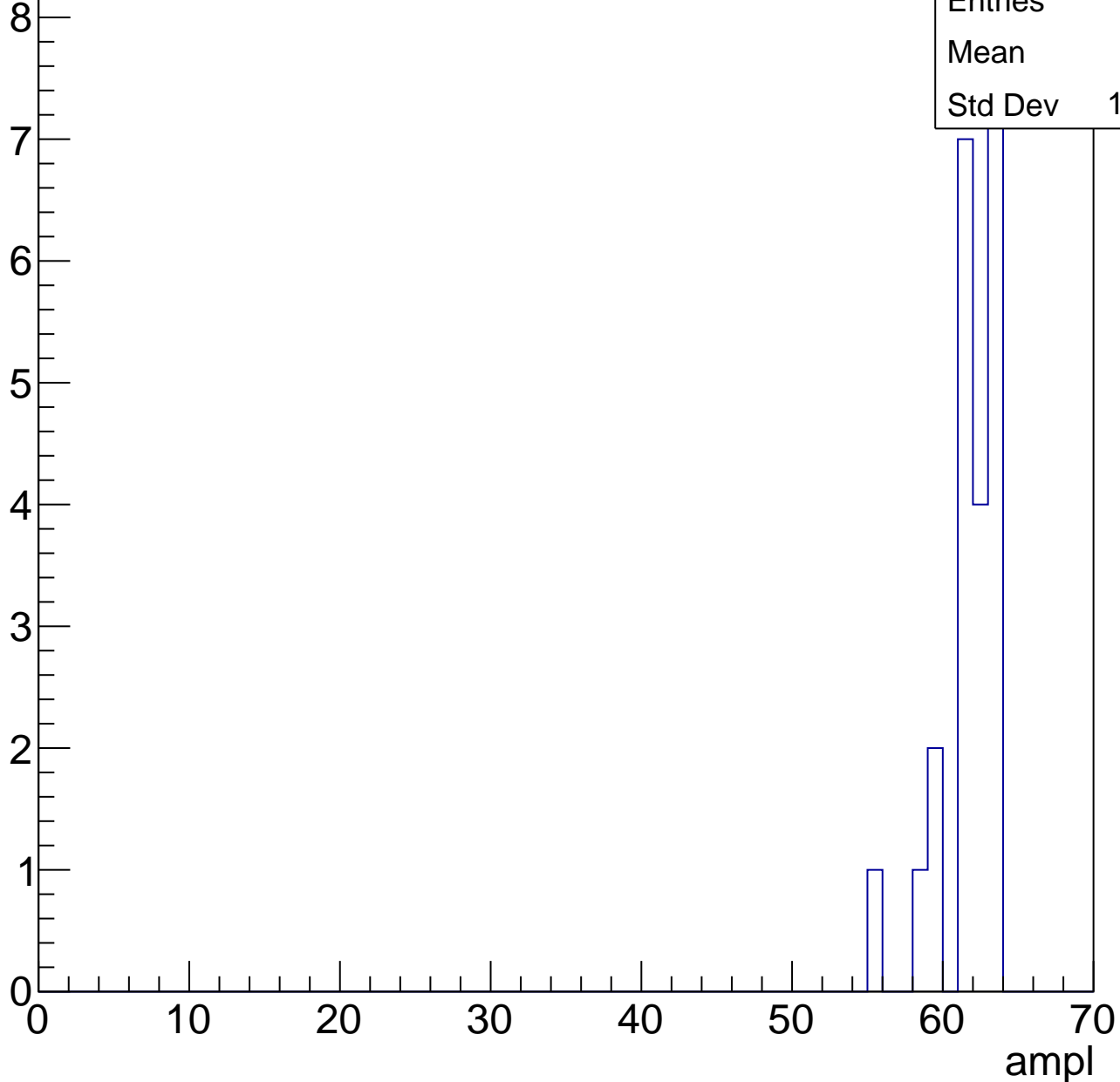
ampl



# B0L101S, U26-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry





# B0L101S, U26-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



# B0L101S, U26-ch127, adc0

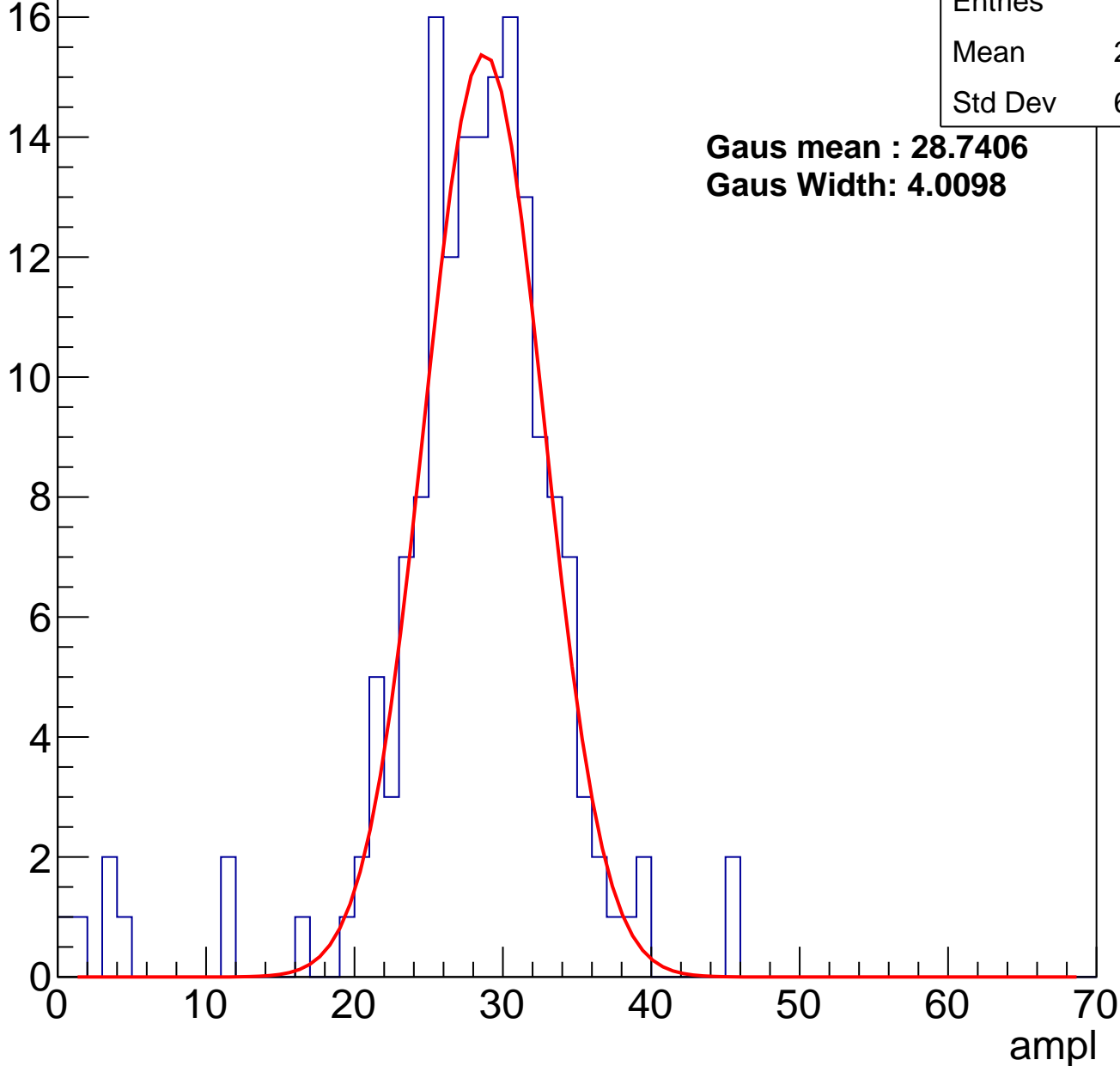
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	169
Mean	27.47
Std Dev	6.505

**Gaus mean : 28.7406**

**Gaus Width: 4.0098**

Entry



# B0L101S, U26-ch127, adc1

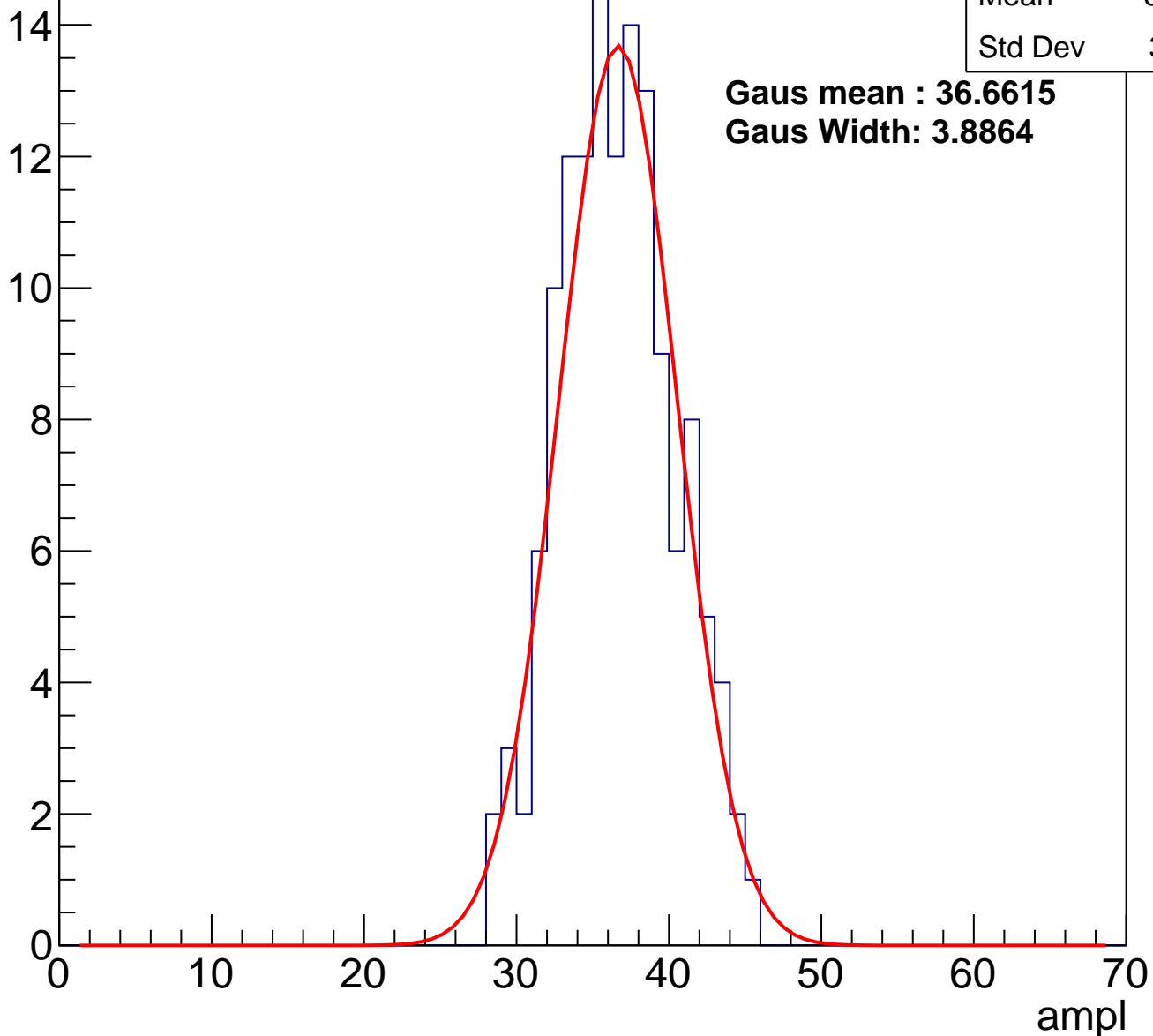
calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	136
Mean	36.15
Std Dev	3.691

**Gaus mean : 36.6615**

**Gaus Width: 3.8864**

Entry



# B0L101S, U26-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	133
Mean	42.53
Std Dev	3.711

**Gaus mean : 42.8599**

**Gaus Width: 3.8160**

Entry

12

10

8

6

4

2

0

0

10

20

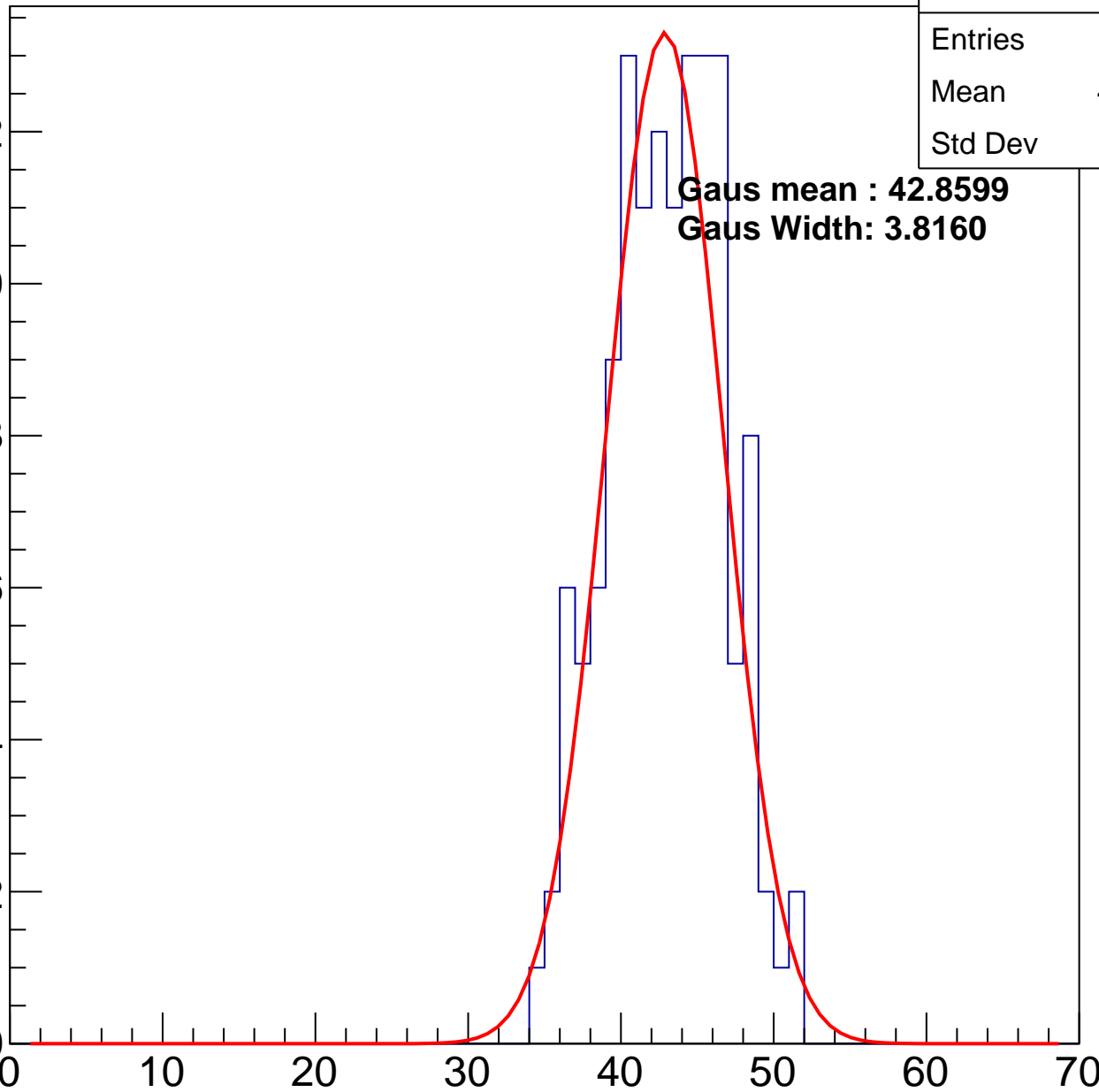
30

40

50

60

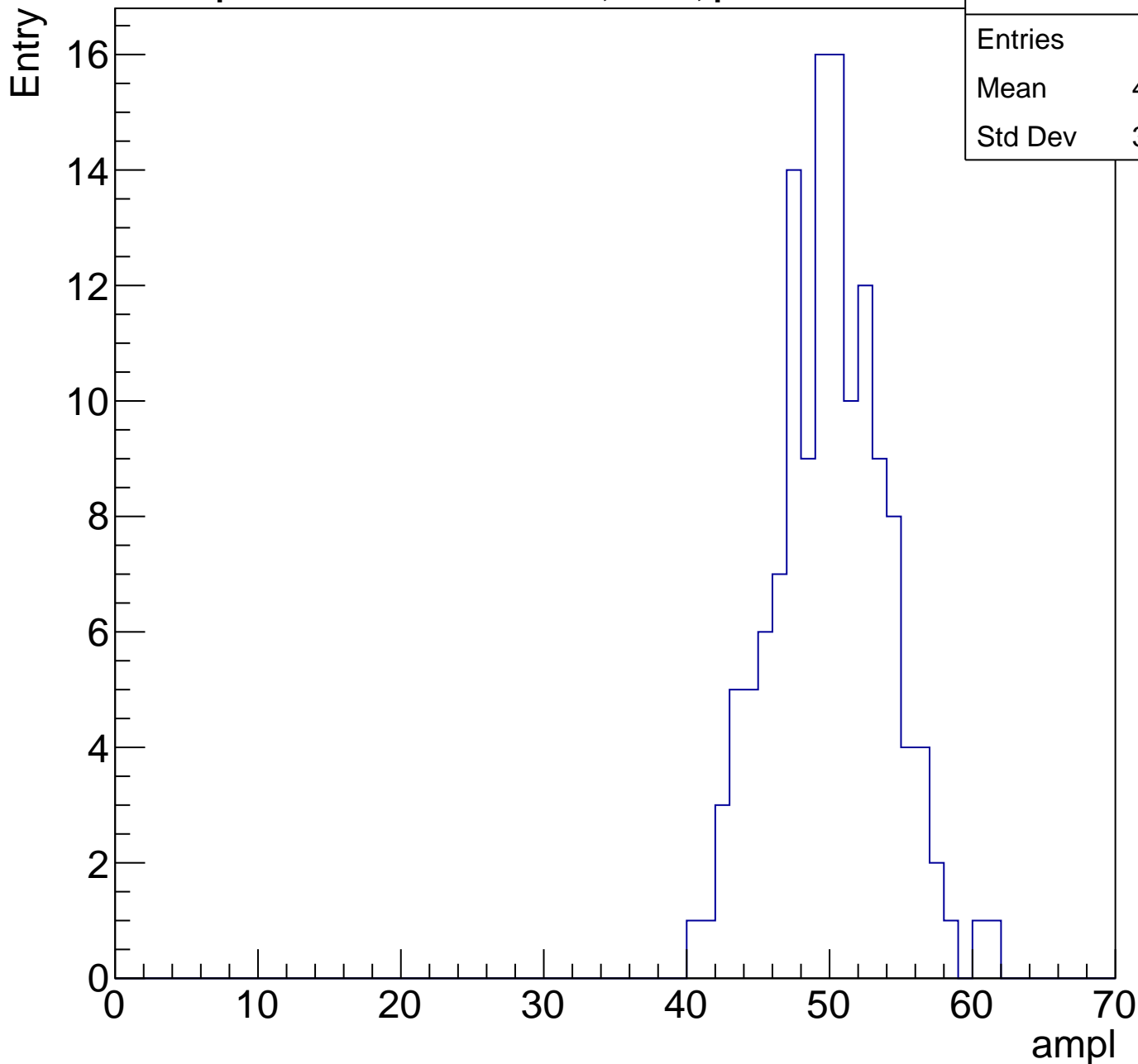
ampl



# B0L101S, U26-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#1, port C1

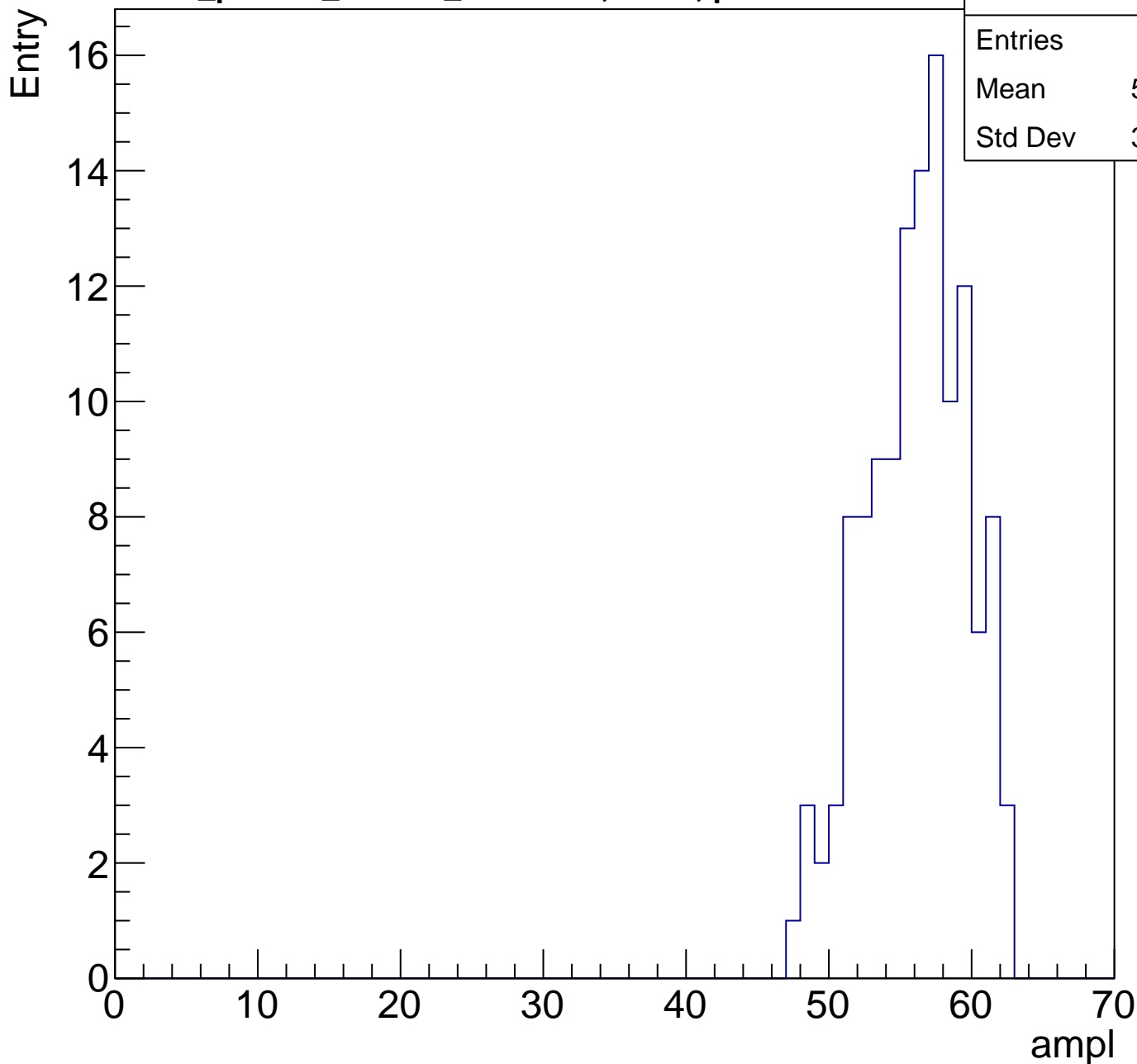
Entries	135
Mean	49.54
Std Dev	3.947



# B0L101S, U26-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#1, port C1

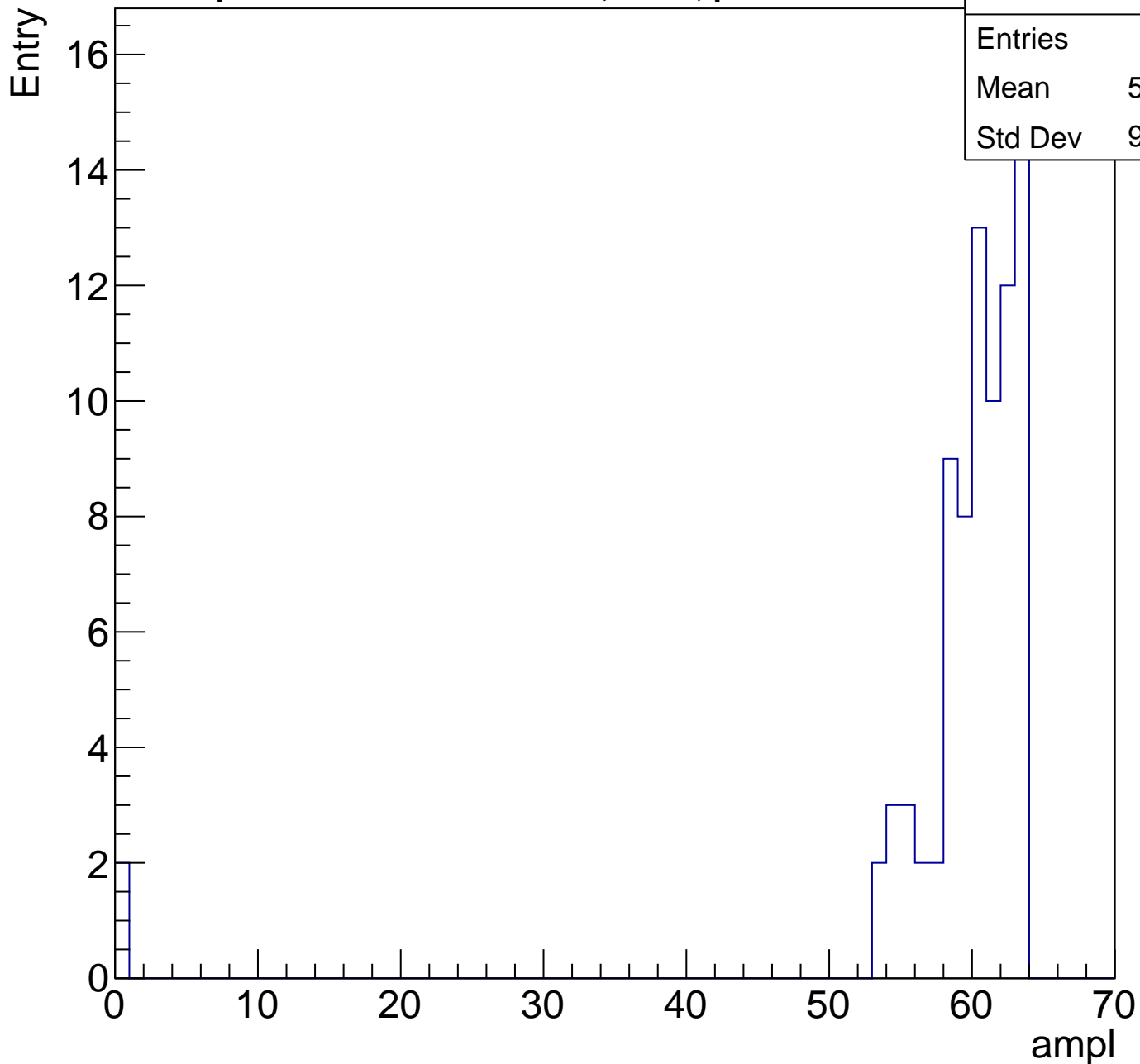
Entries	125
Mean	55.67
Std Dev	3.454



# B0L101S, U26-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entries	82
Mean	58.48
Std Dev	9.619

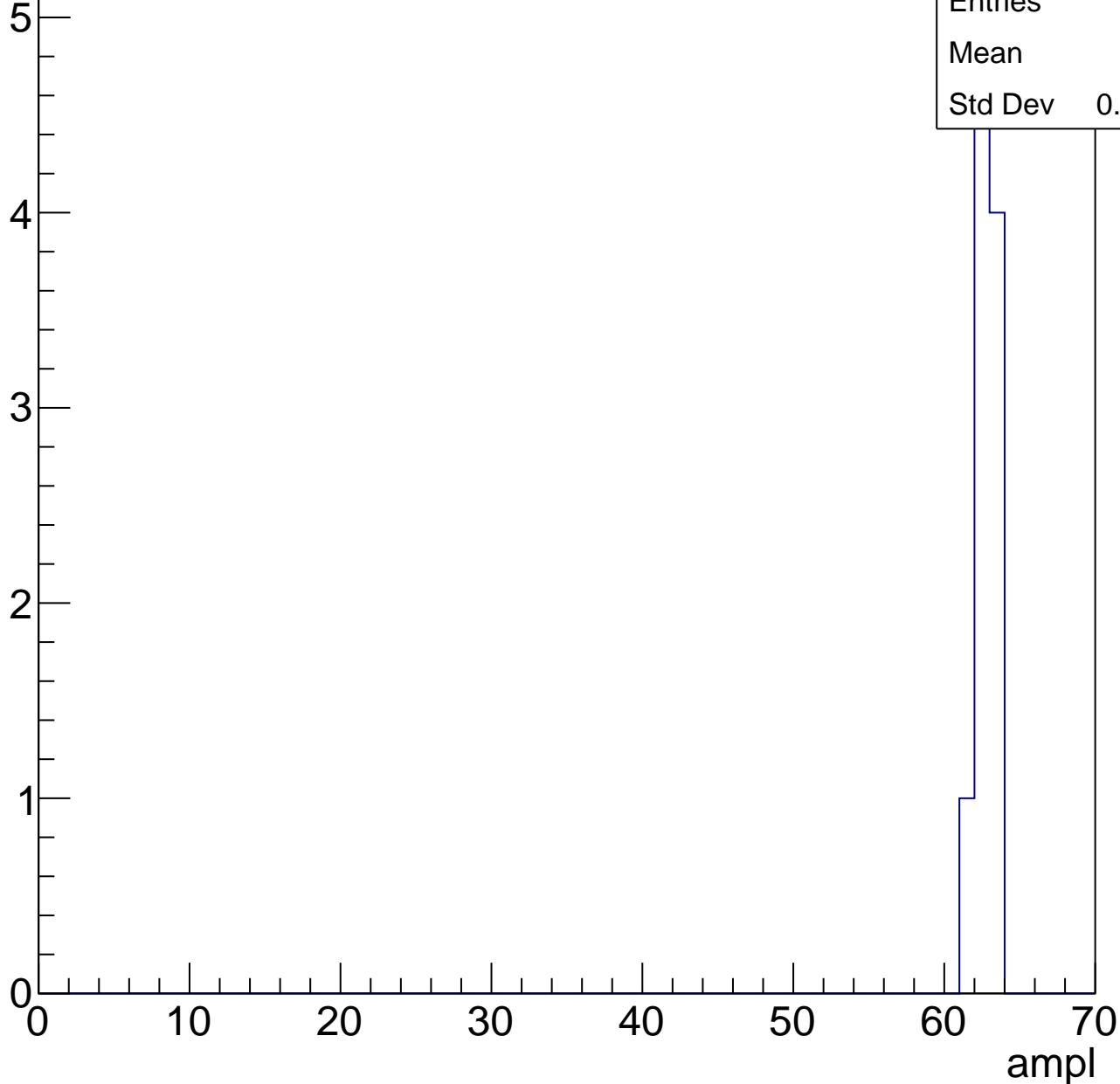


# B0L101S, U26-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.3
Std Dev	0.6403

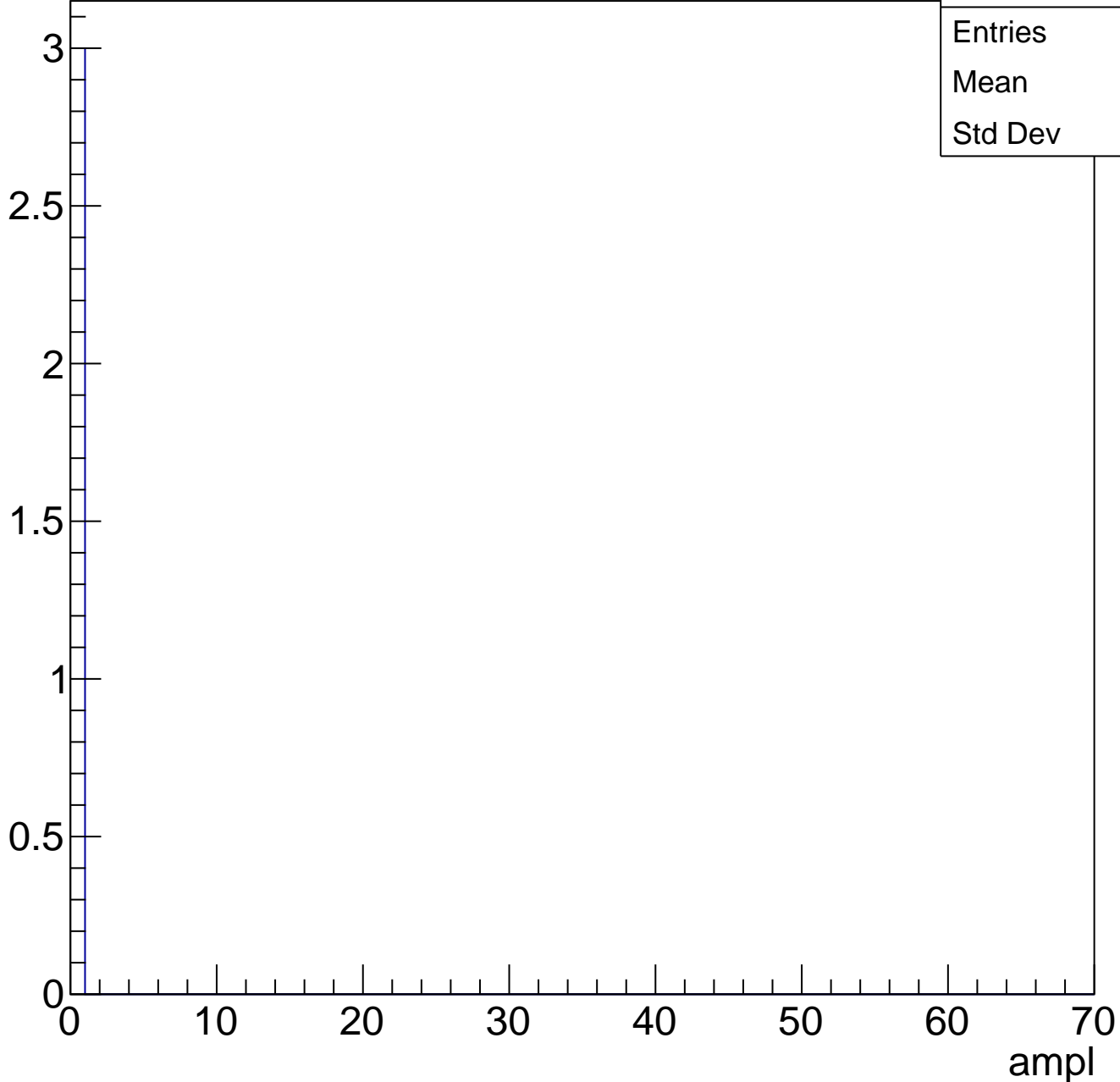




# B0L101S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L101S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#1, port C1

Entry

