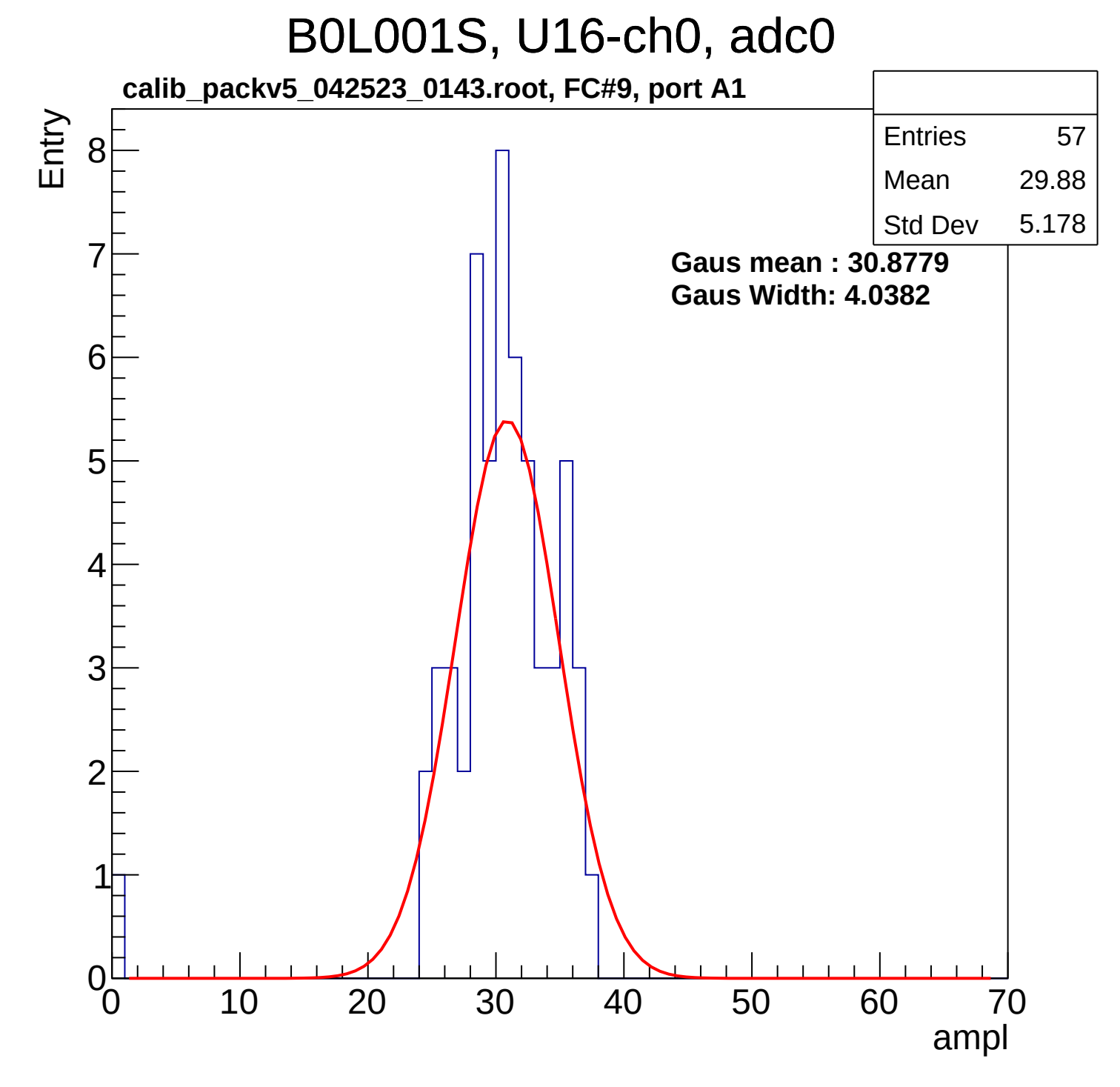
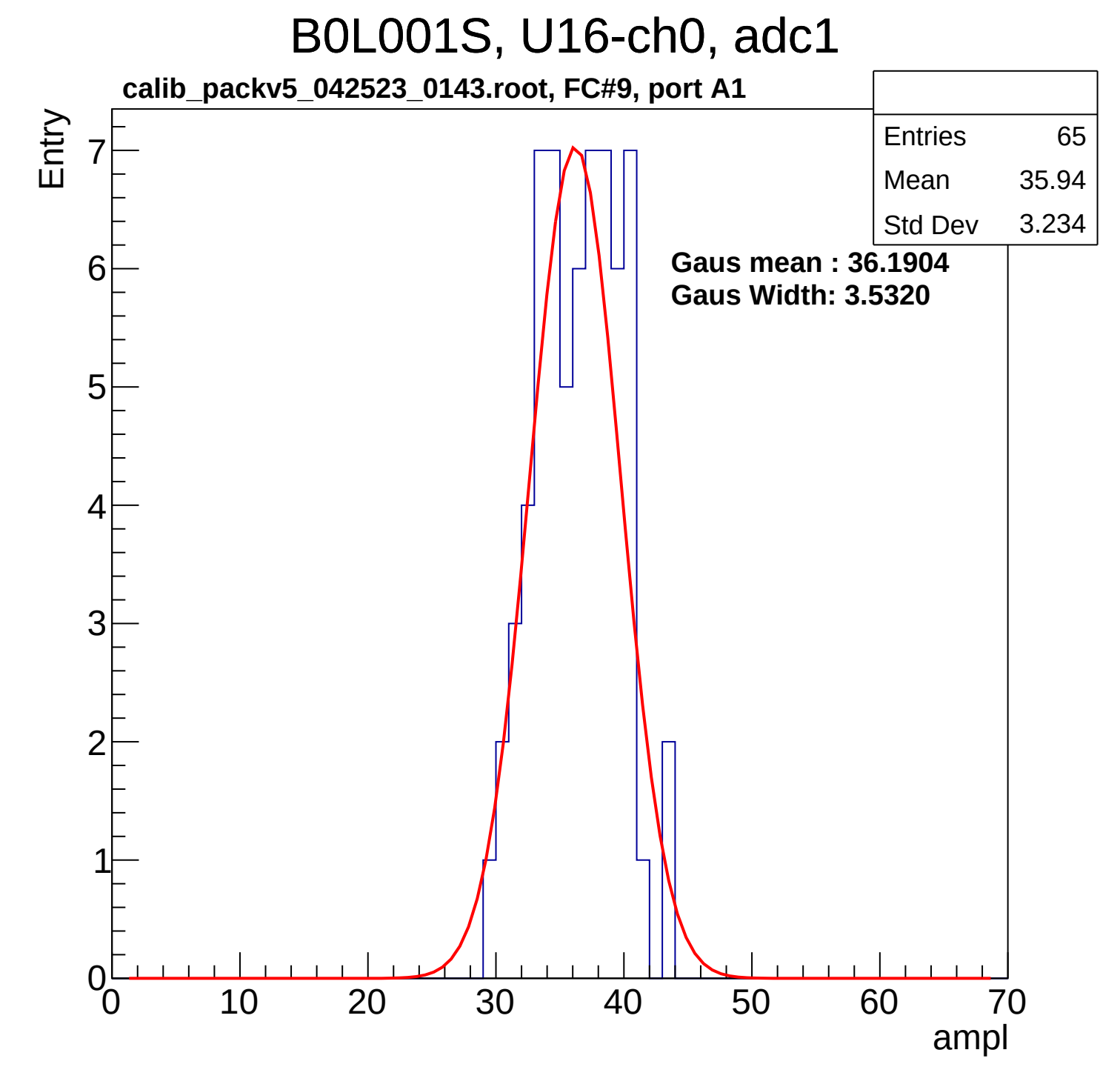
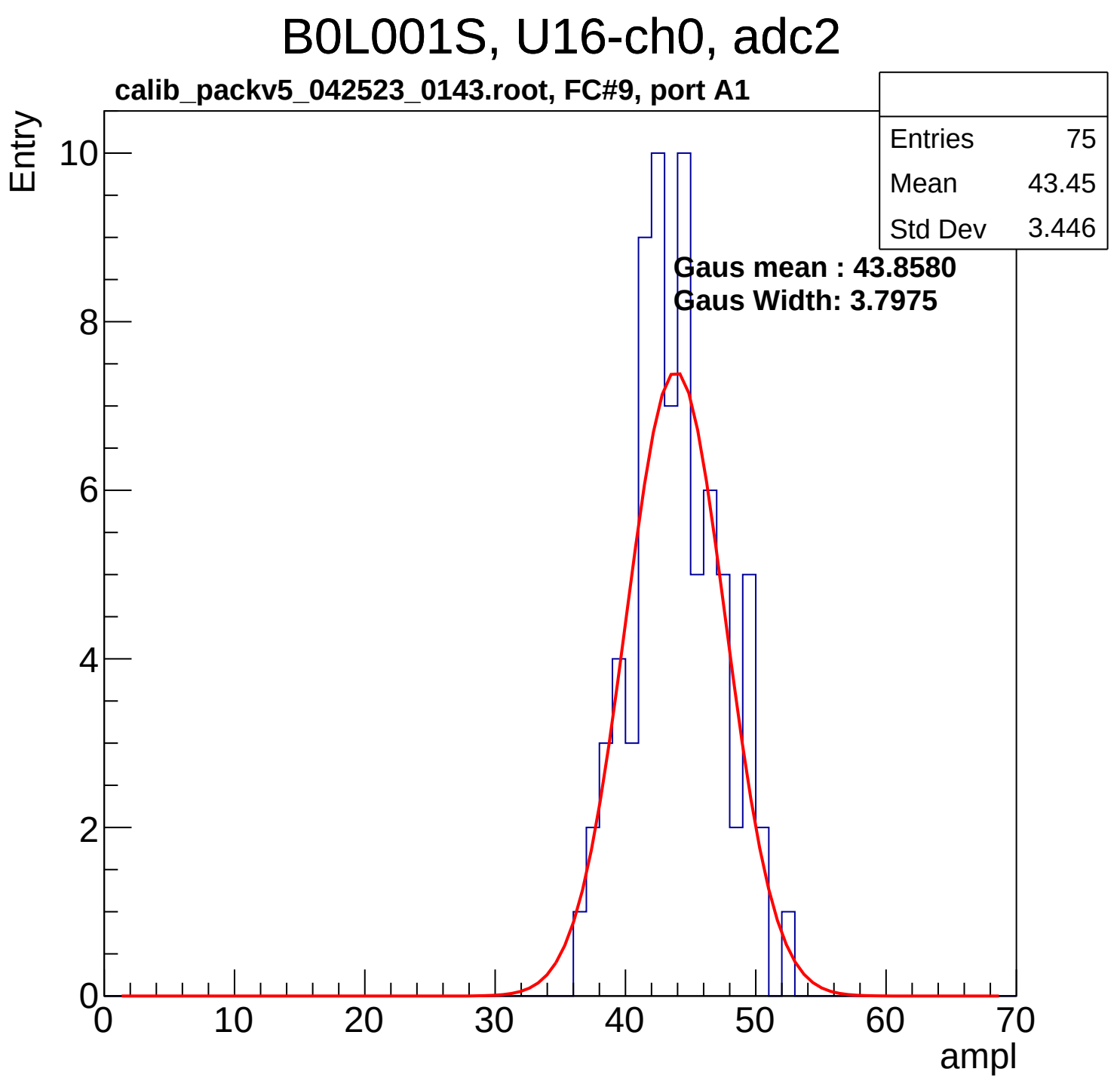
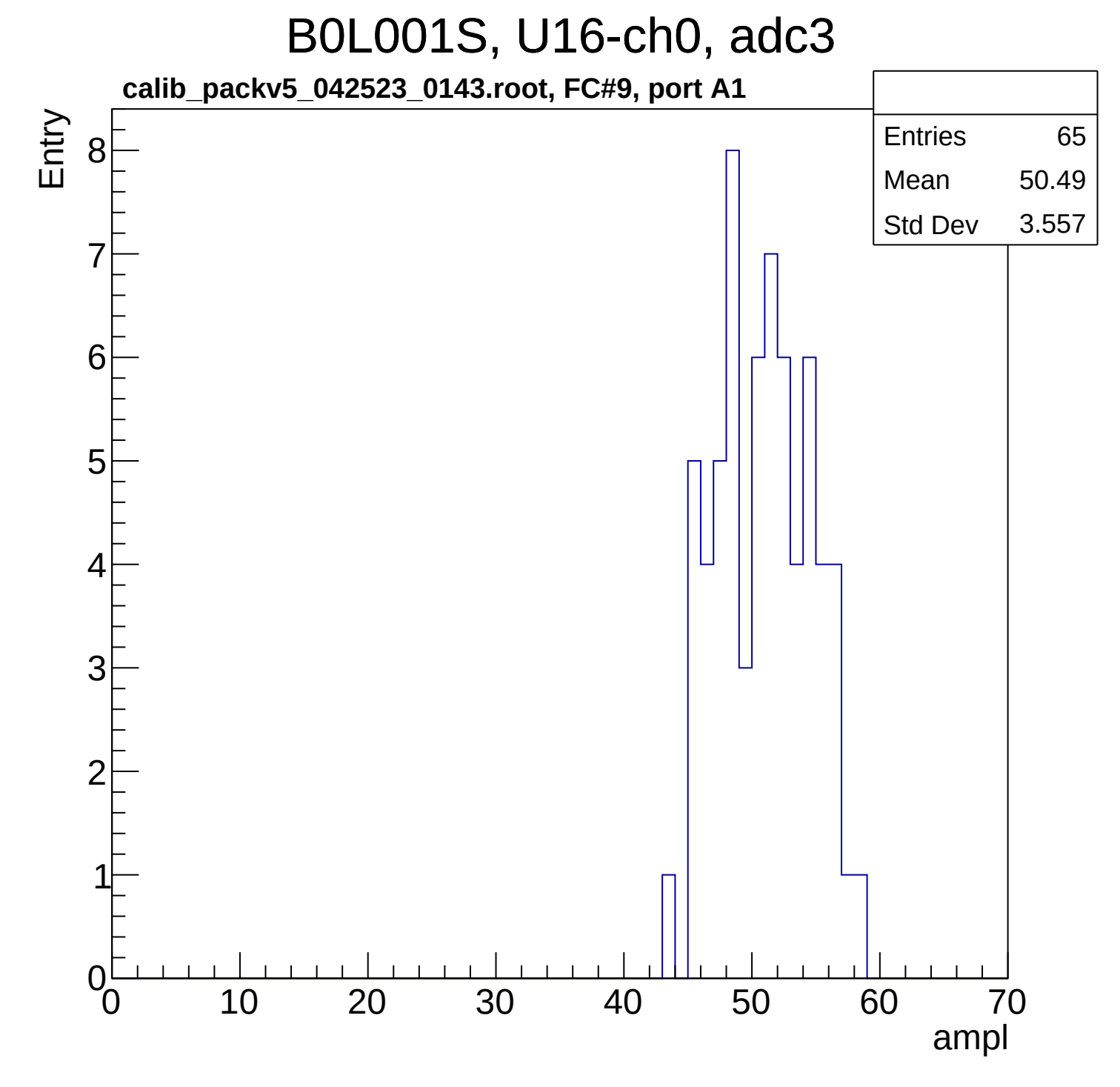
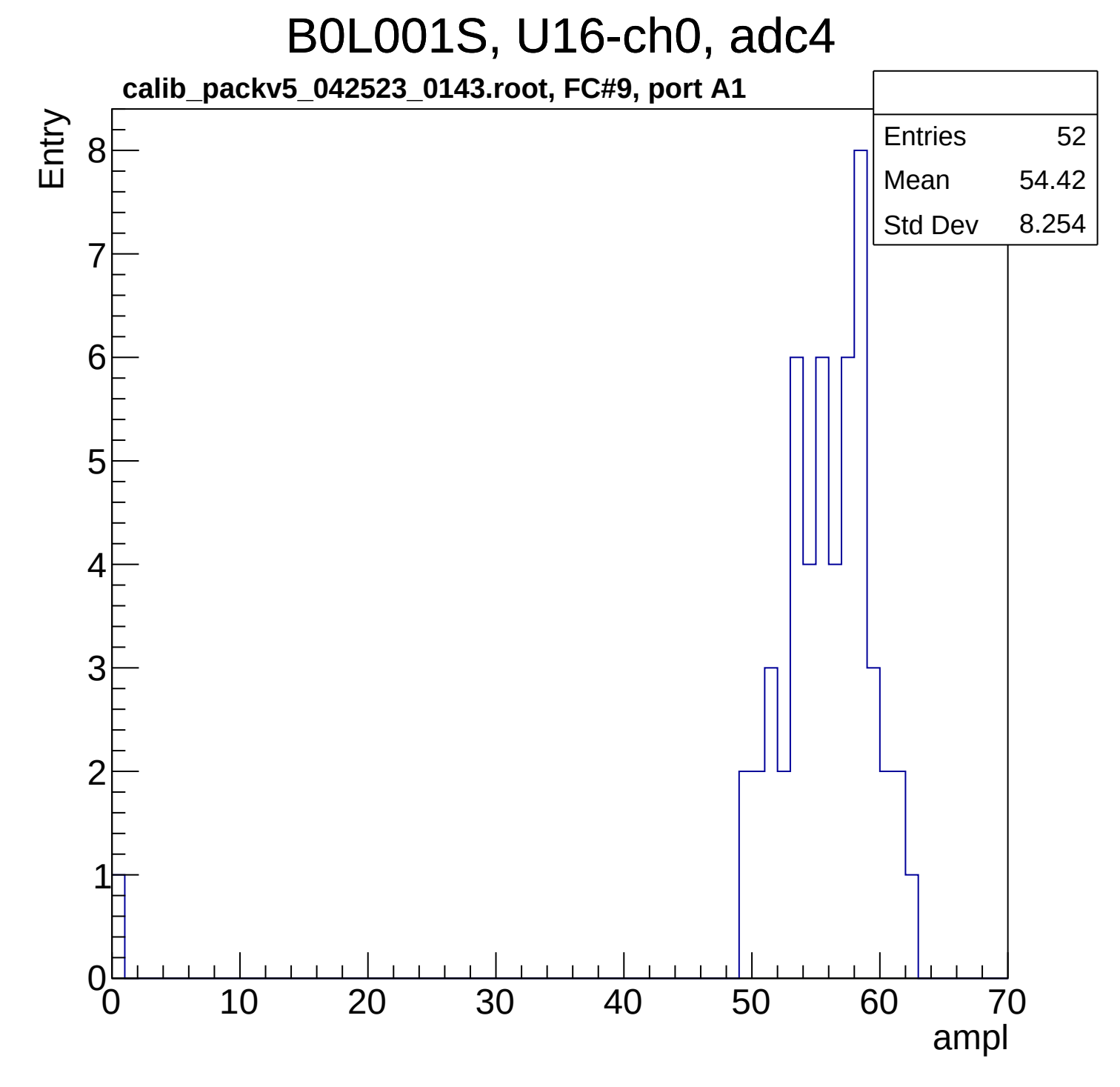
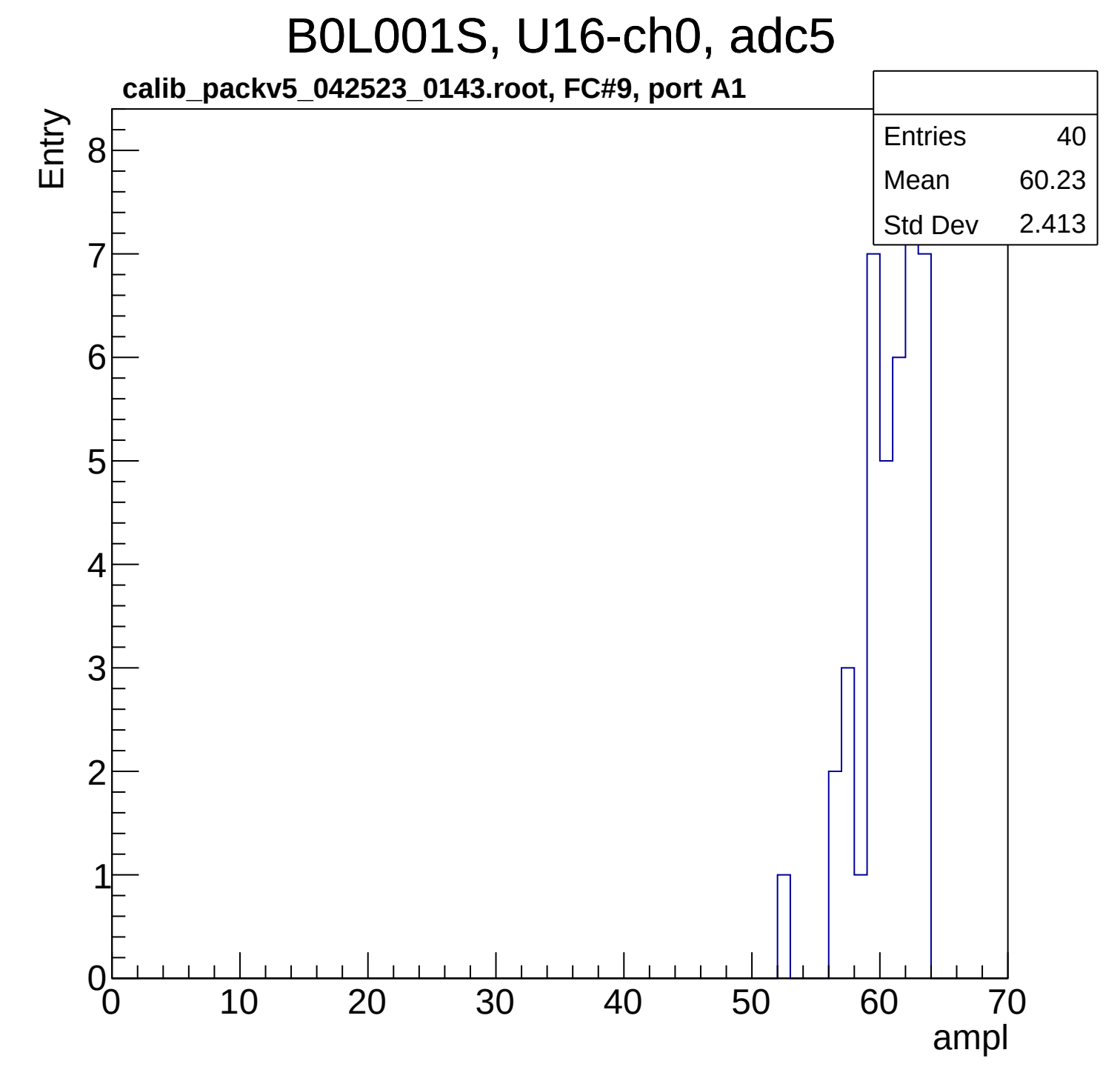

B0L001S, U16-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 29.88
Std Dev 5.178
Gaus mean : 30.8779
Gaus Width: 4.0382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 35.94
Std Dev 3.234
Gaus mean : 36.1904
Gaus Width: 3.5320
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 43.45
Std Dev 3.446
Gaus mean : 43.8580
Gaus Width: 3.7975
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.49
Std Dev 3.557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 54.42
Std Dev 8.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.23
Std Dev 2.413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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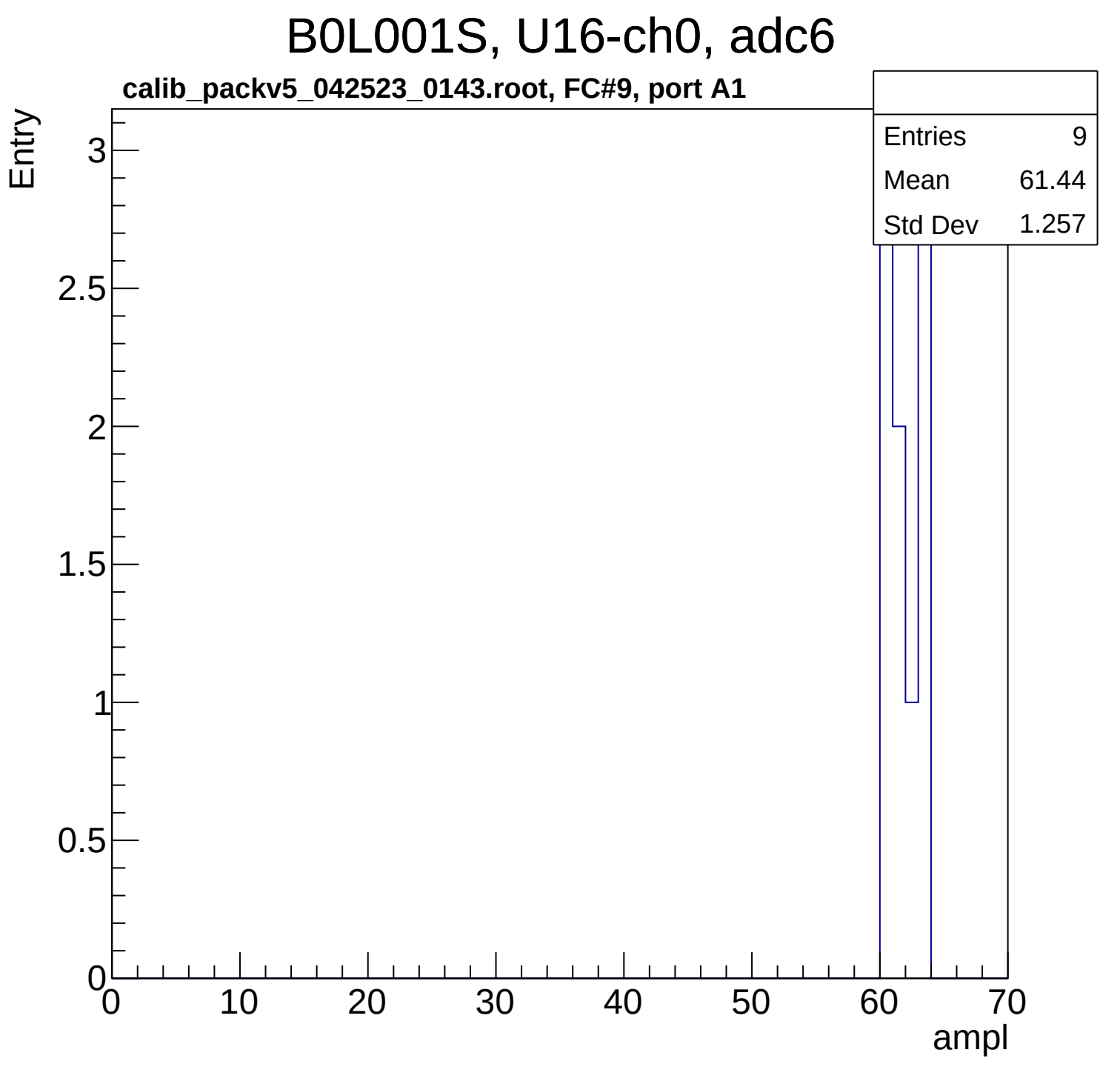
B0L001S, U16-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.44
Std Dev 1.257
Entry
ampl
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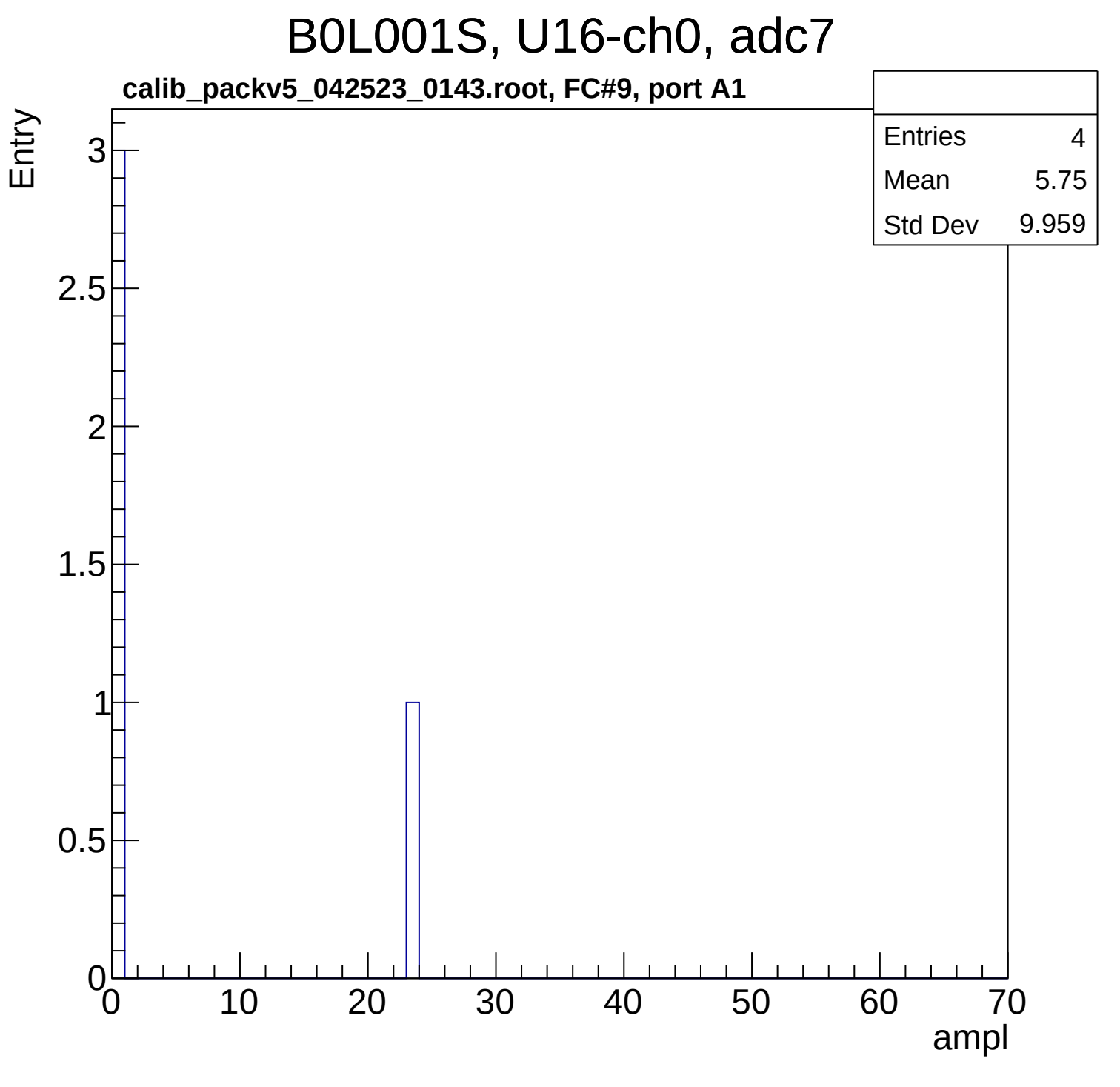
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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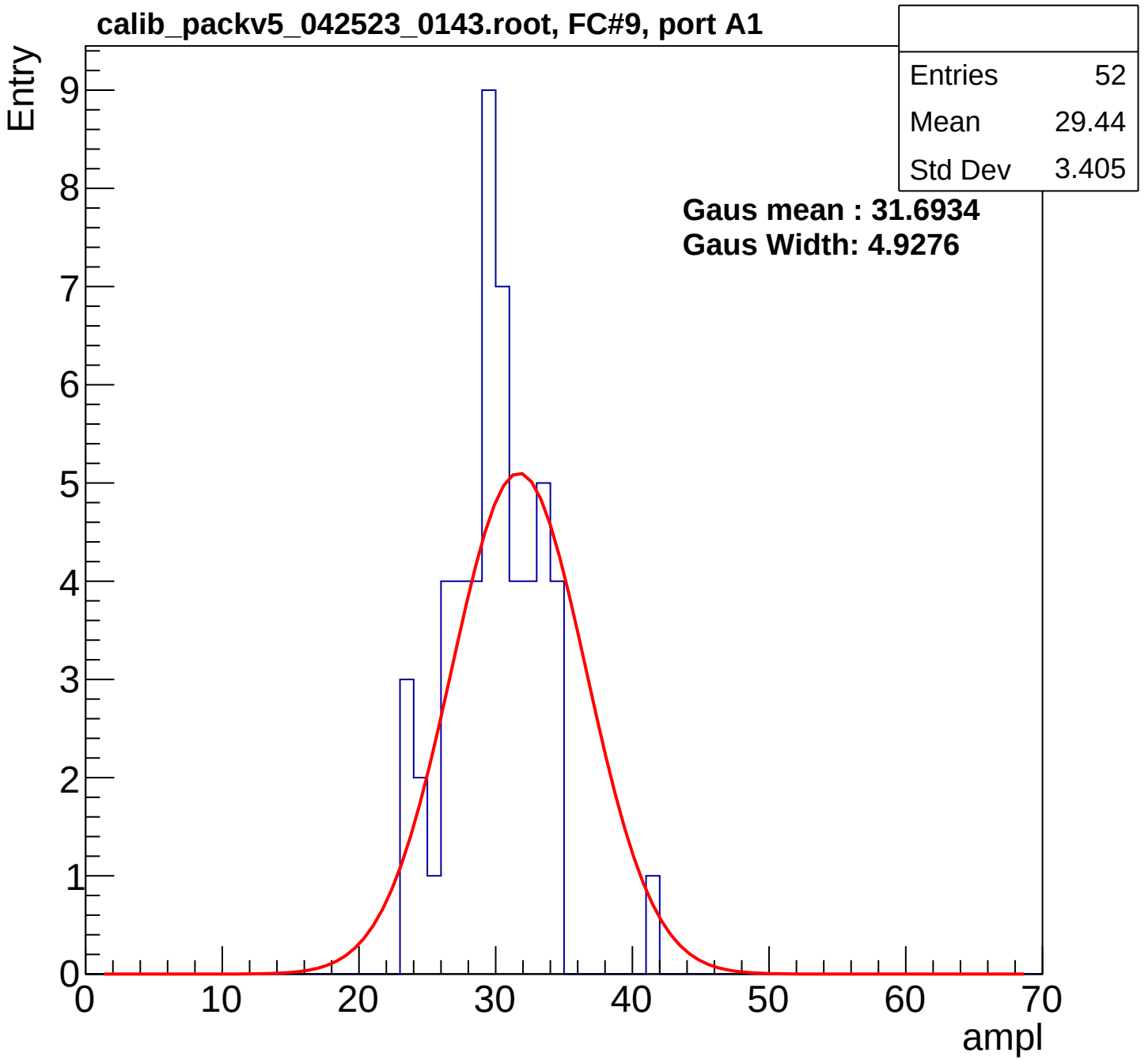
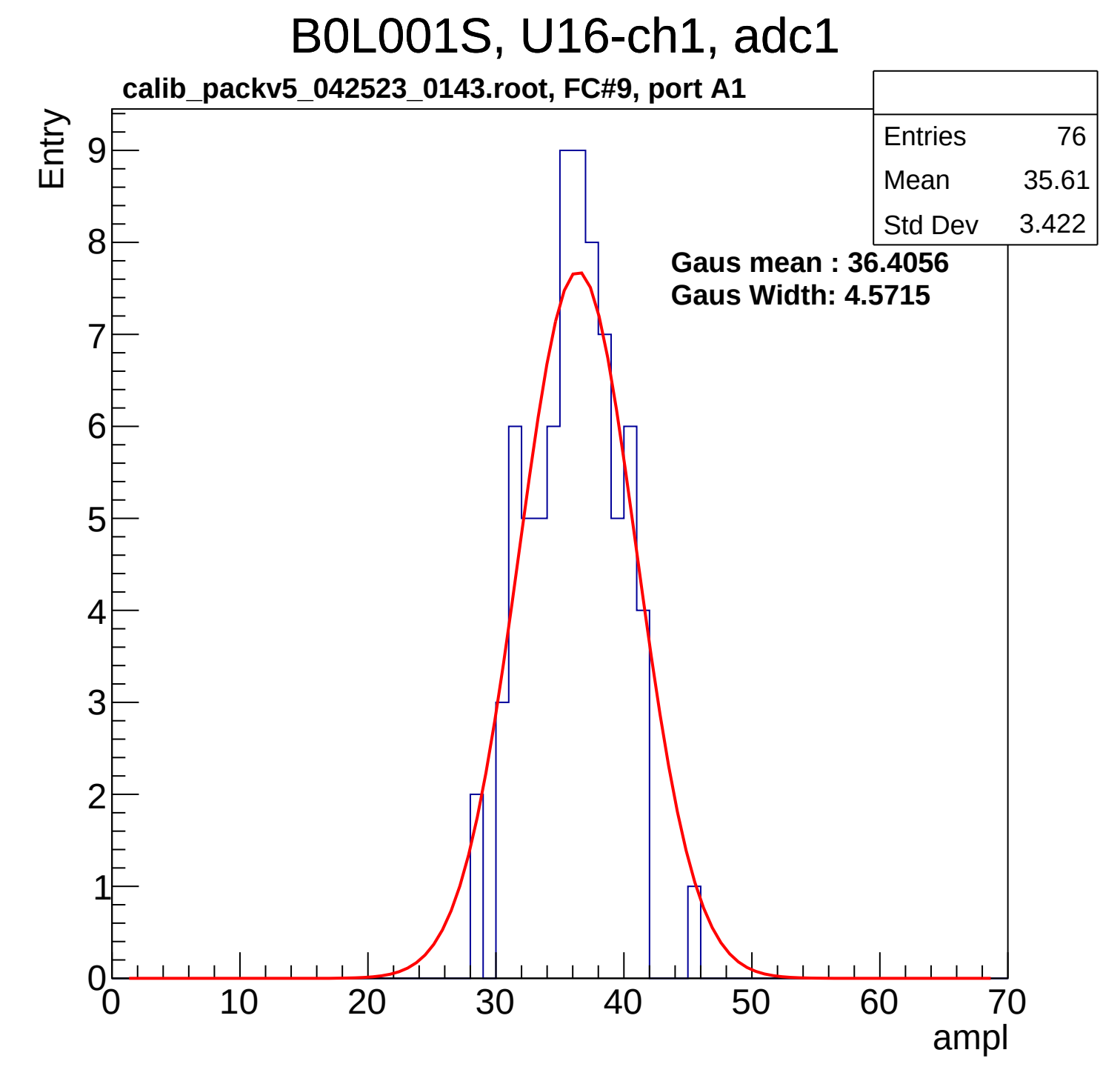

B0L001S, U16-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 29.44
Std Dev 3.405
Gaus mean : 31.6934
Gaus Width: 4.9276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 35.61
Std Dev 3.422
Gaus mean : 36.4056
Gaus Width: 4.5715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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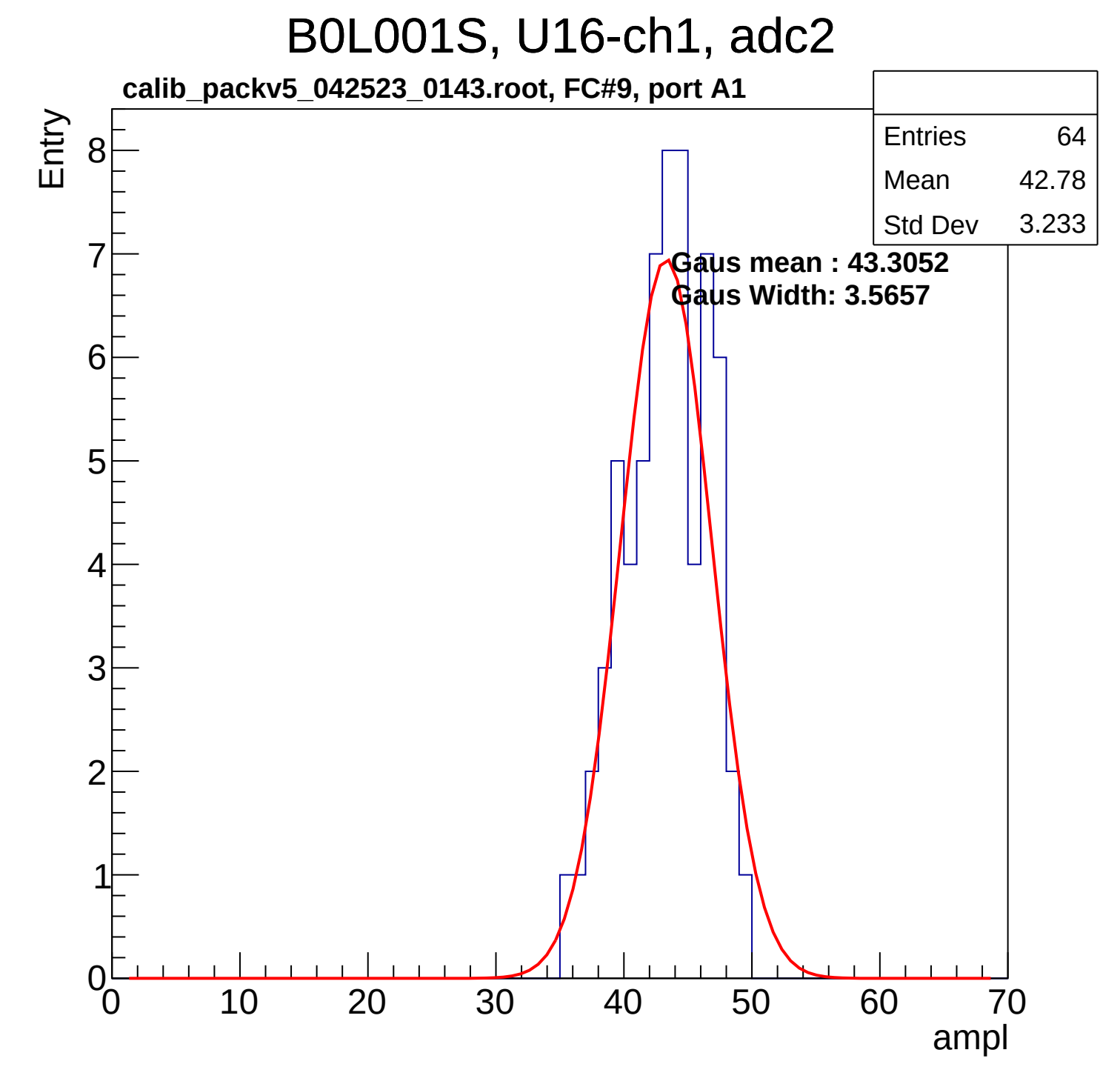
B0L001S, U16-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 42.78
Std Dev 3.233
Gaus mean : 43.3052
Gaus Width: 3.5657
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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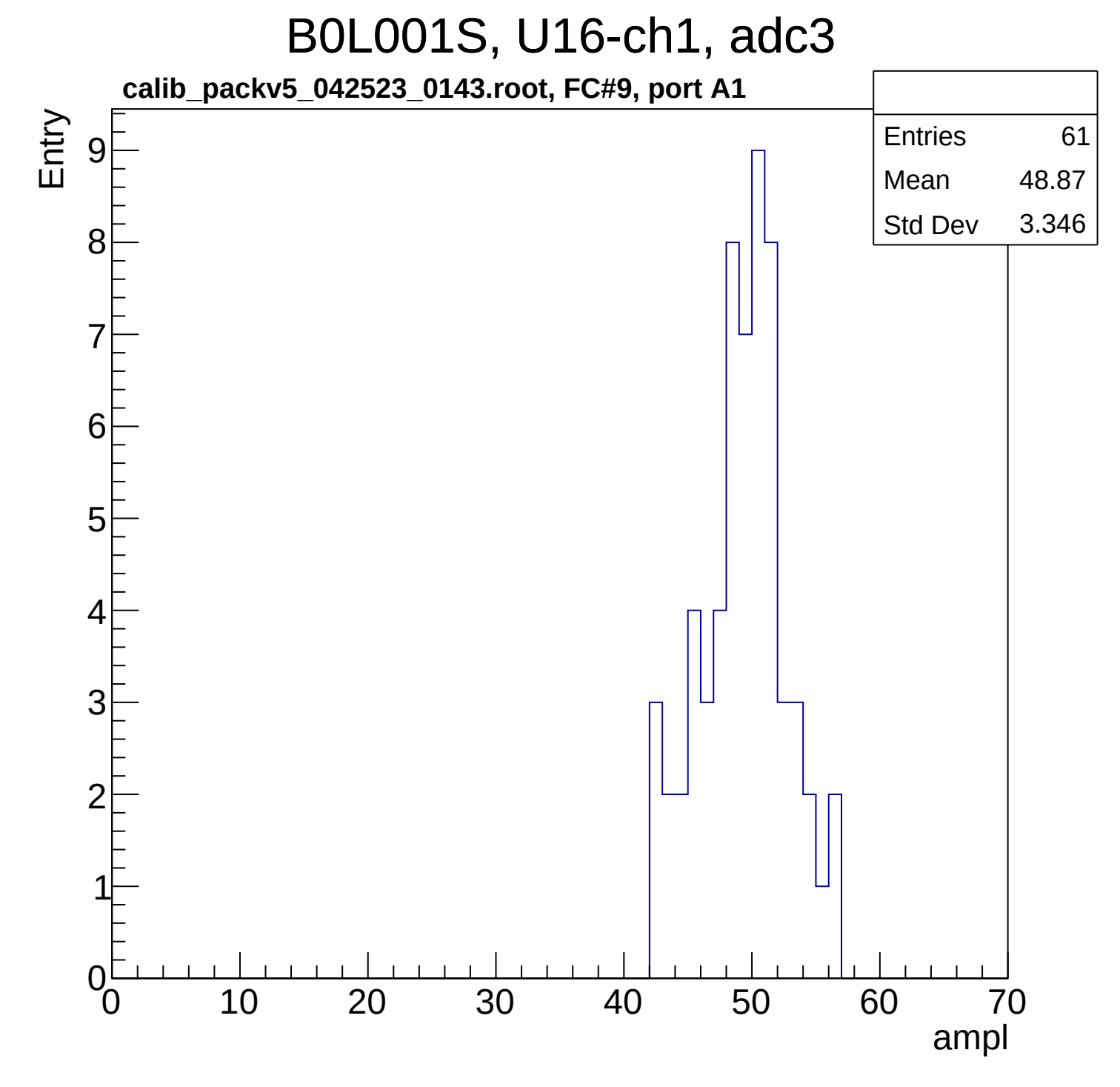
B0L001S, U16-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 48.87
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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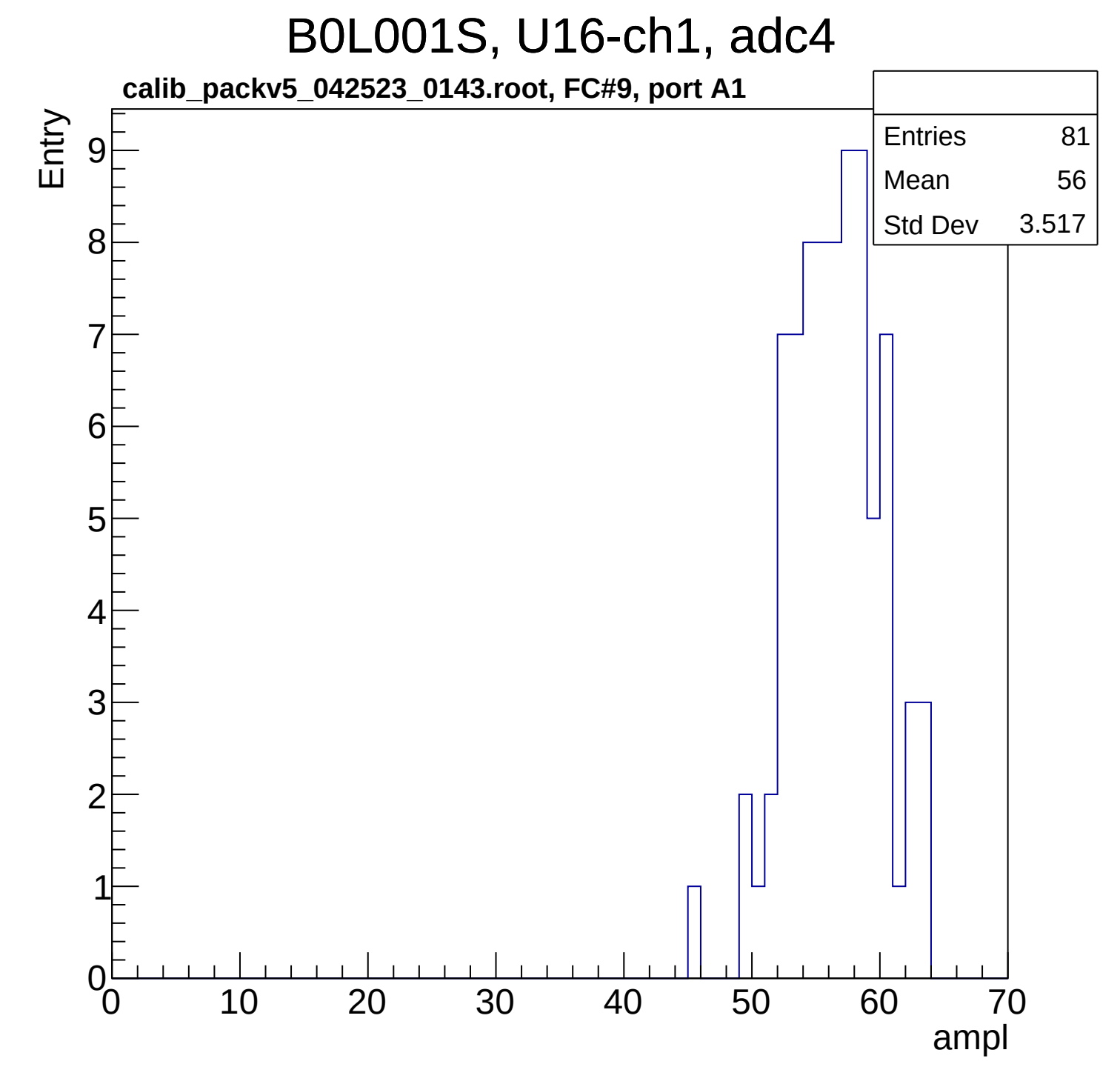
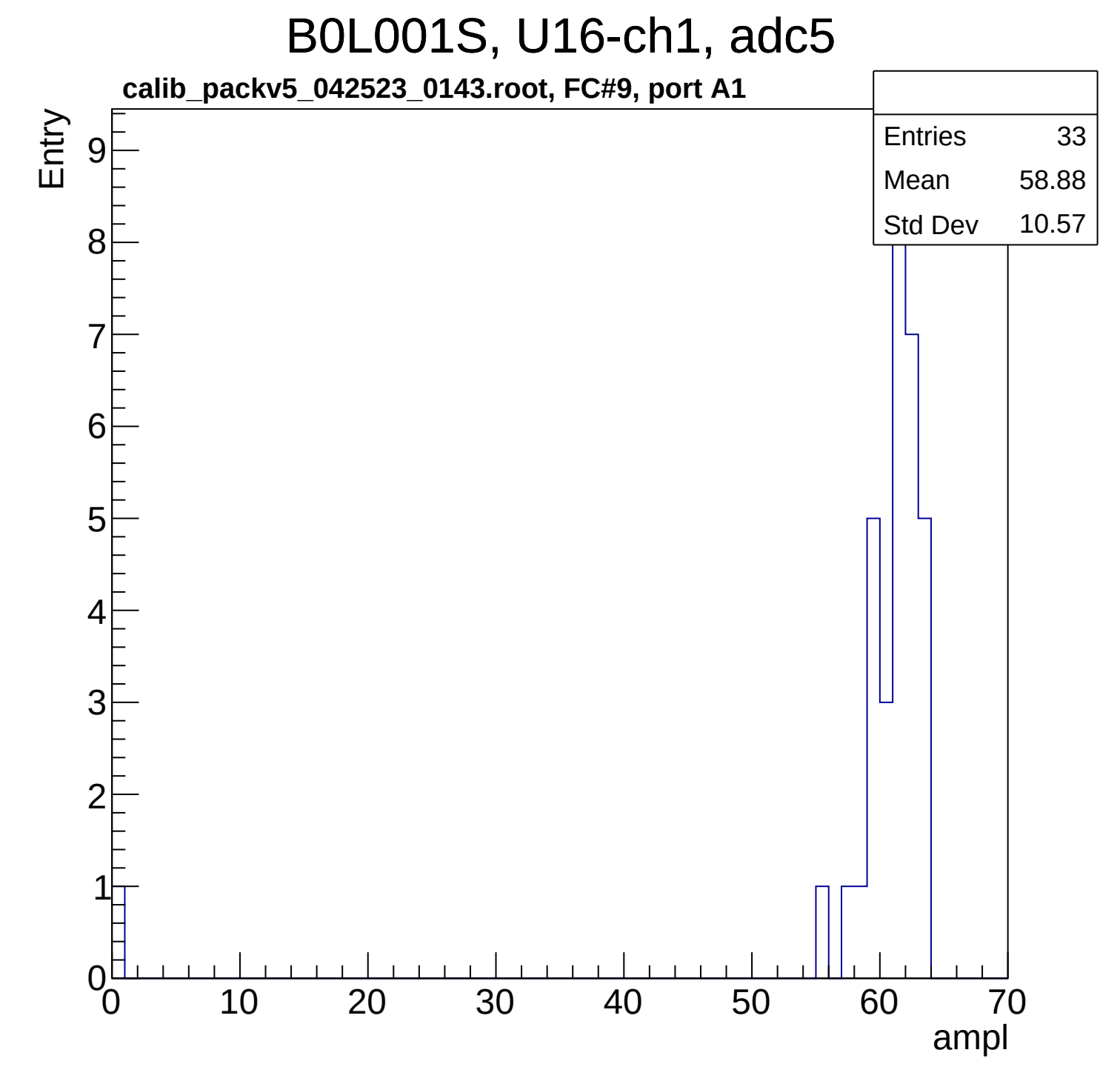
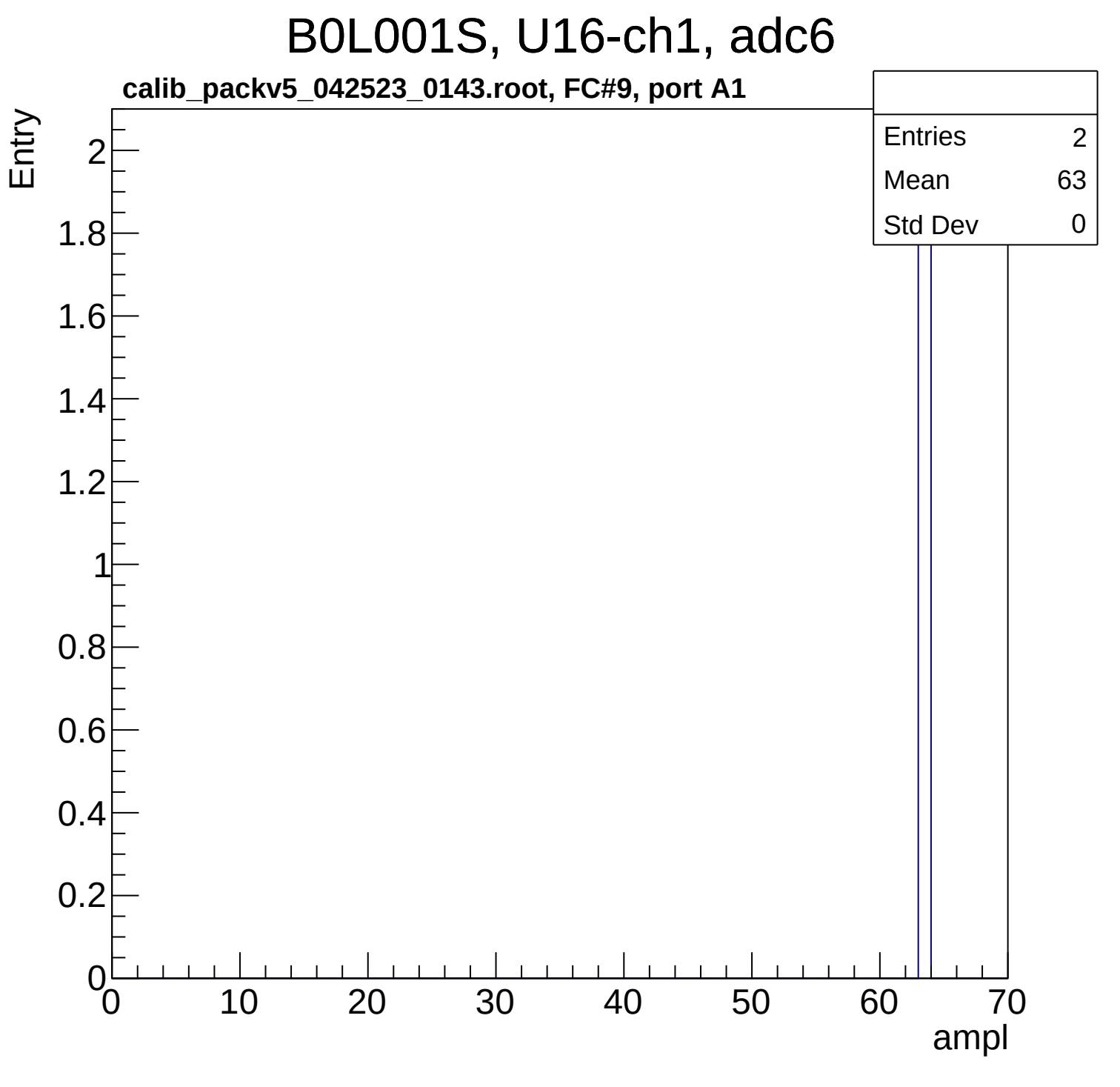
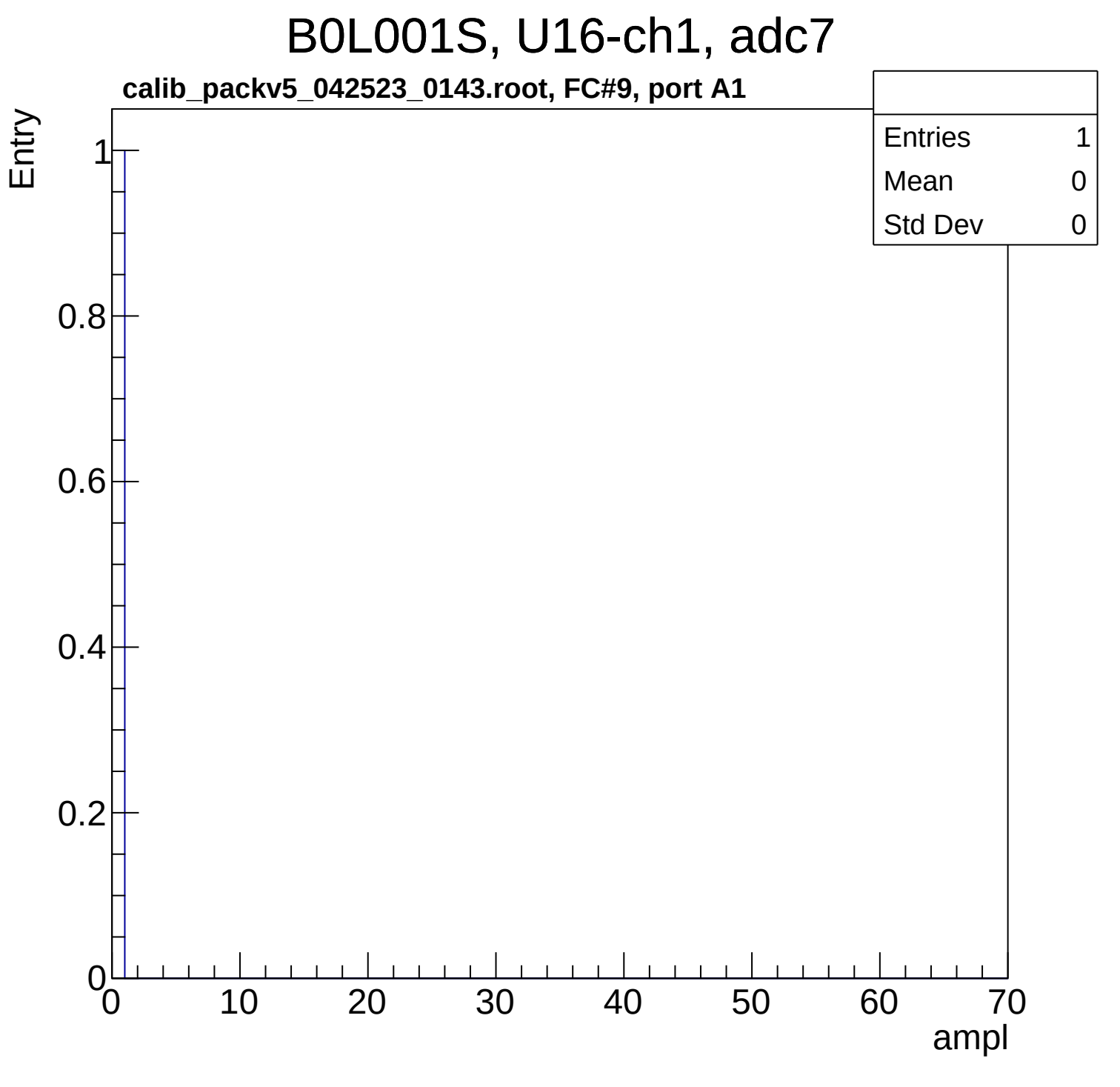
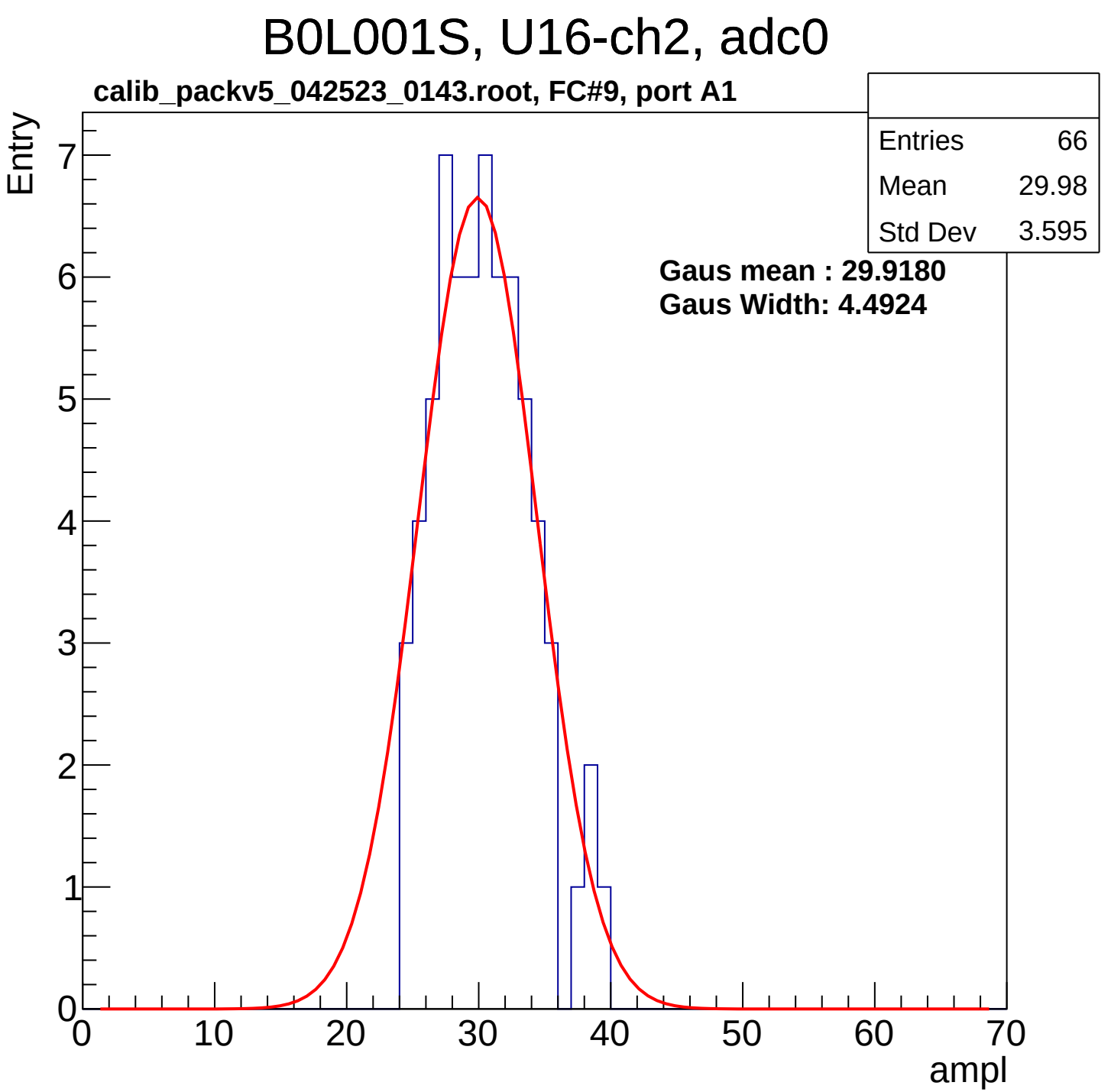
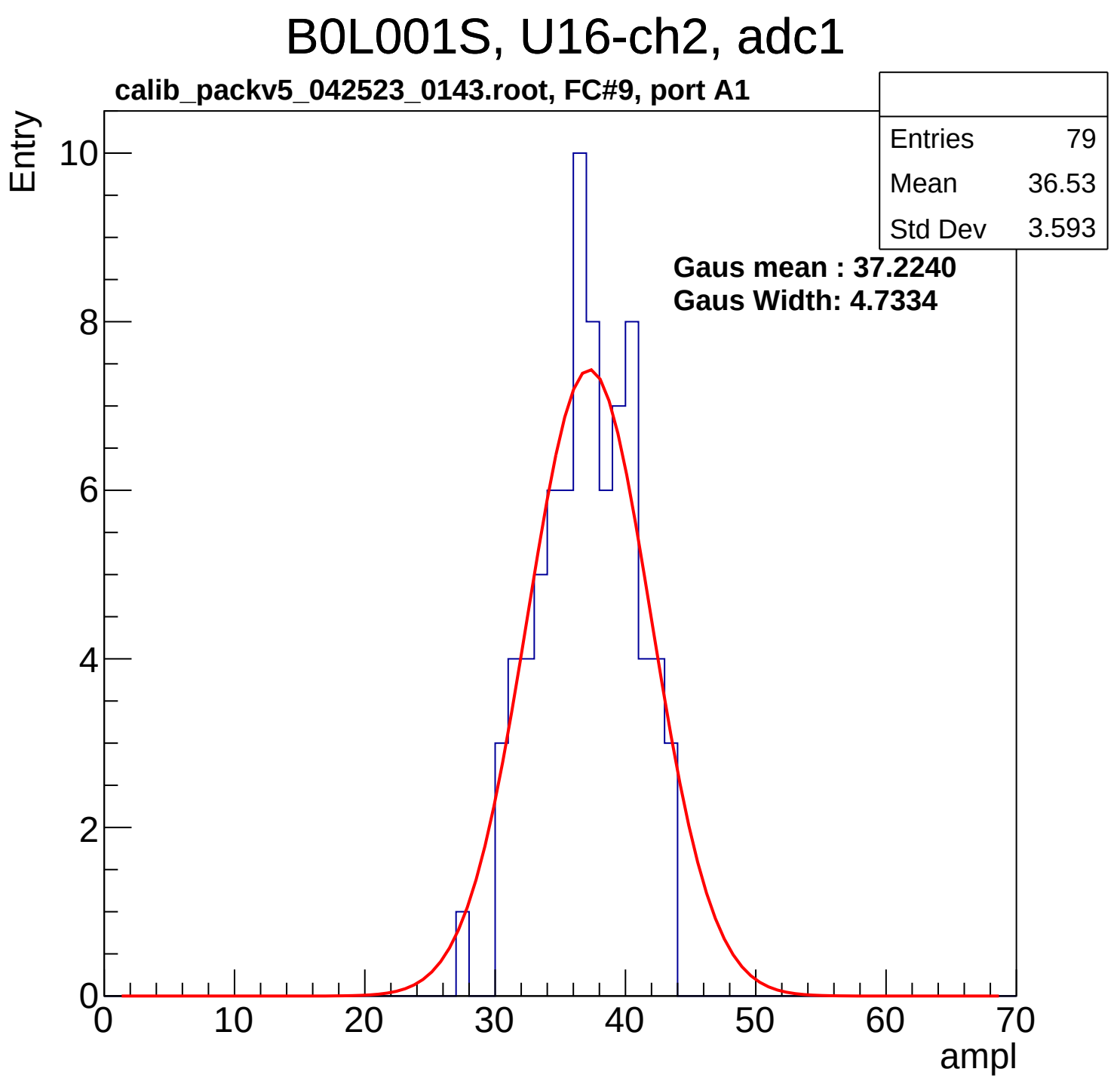

B0L001S, U16-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 56
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

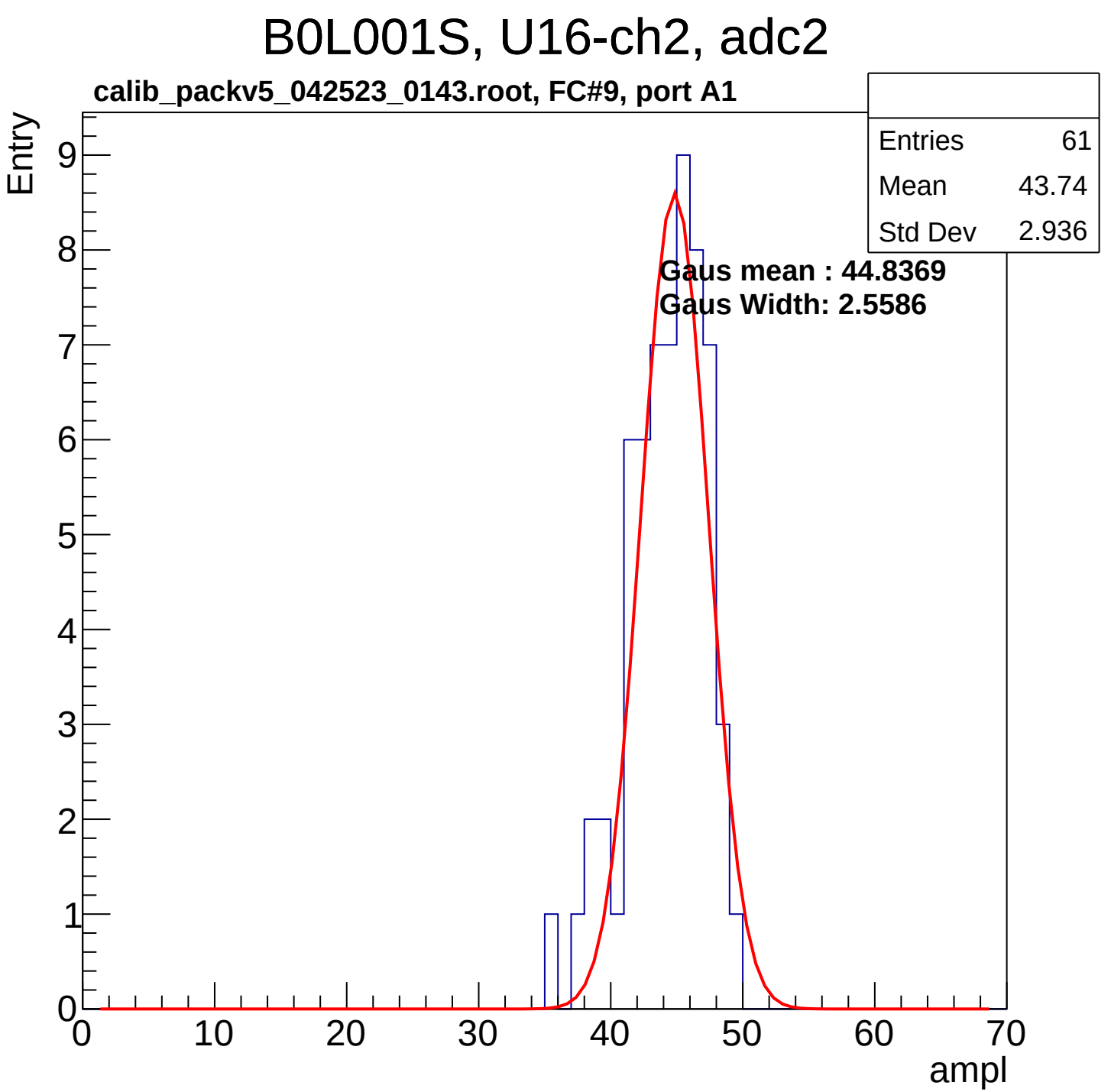

B0L001S, U16-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.88
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.98
Std Dev 3.595
Gaus mean : 29.9180
Gaus Width: 4.4924
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 36.53
Std Dev 3.593
Gaus mean : 37.2240
Gaus Width: 4.7334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.74
Std Dev 2.936
Gaus mean : 44.8369
Gaus Width: 2.5586
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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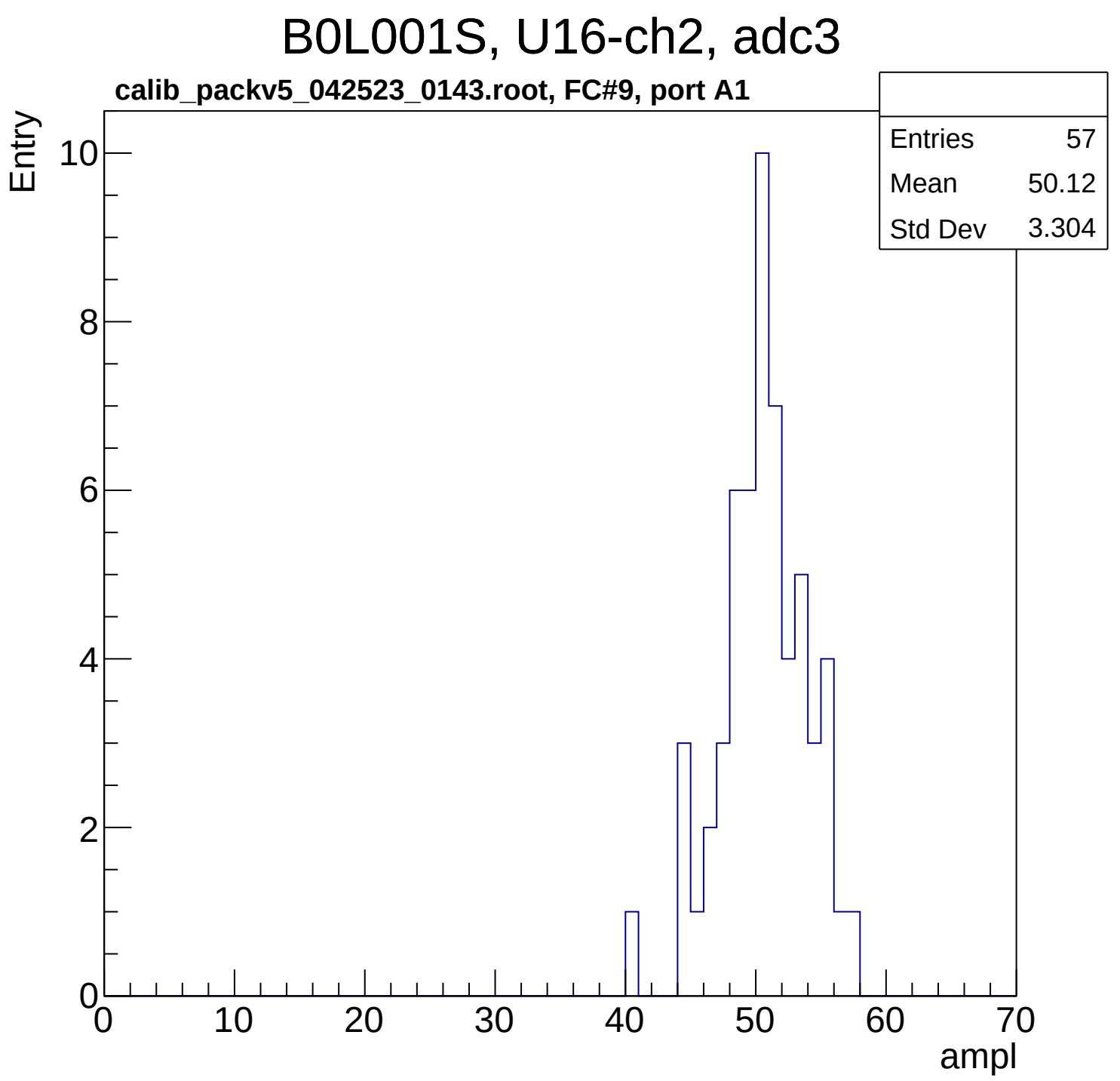
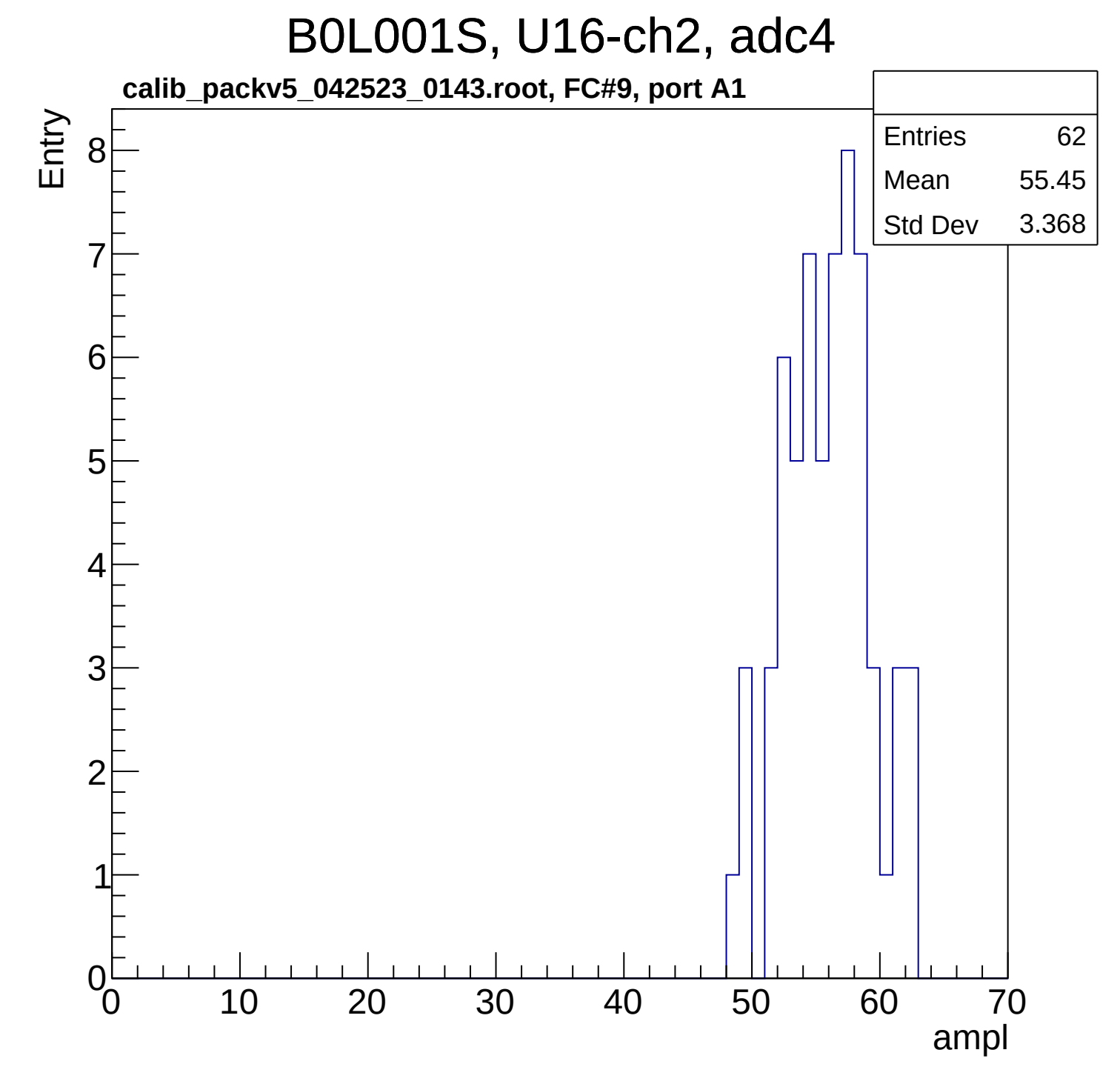

B0L001S, U16-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.12
Std Dev 3.304
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.45
Std Dev 3.368
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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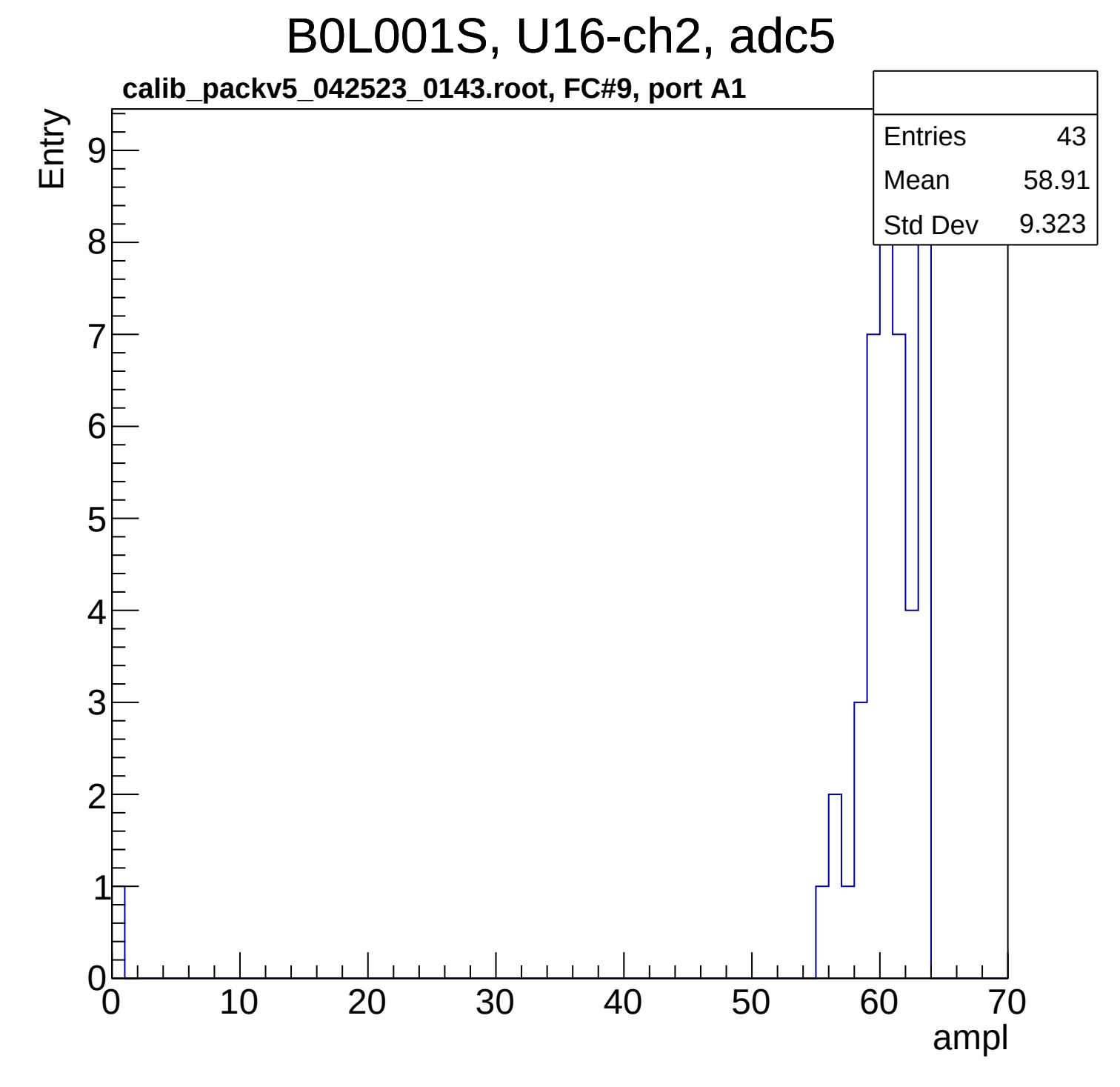
40
50
60
70


B0L001S, U16-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.91
Std Dev 9.323
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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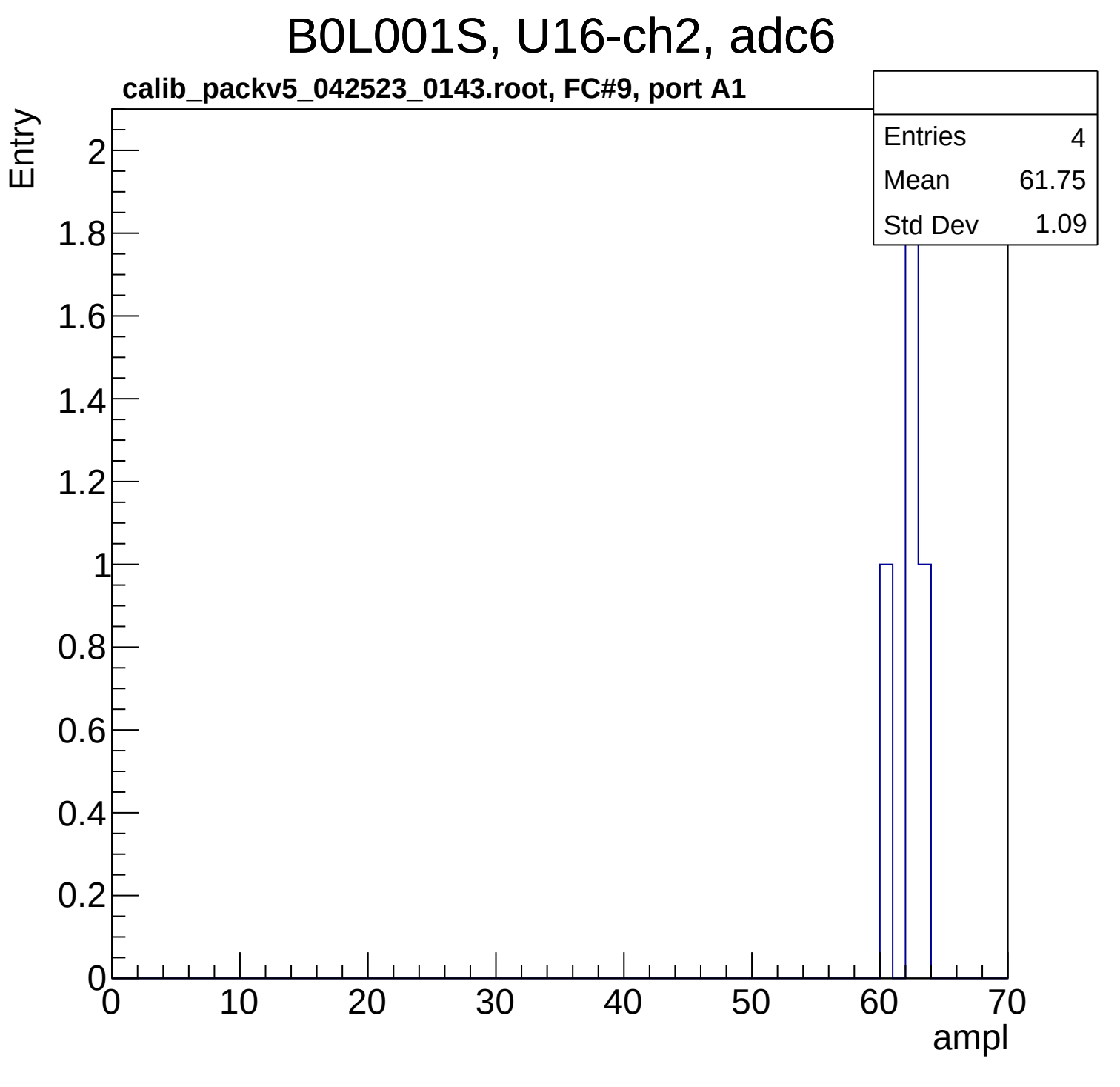
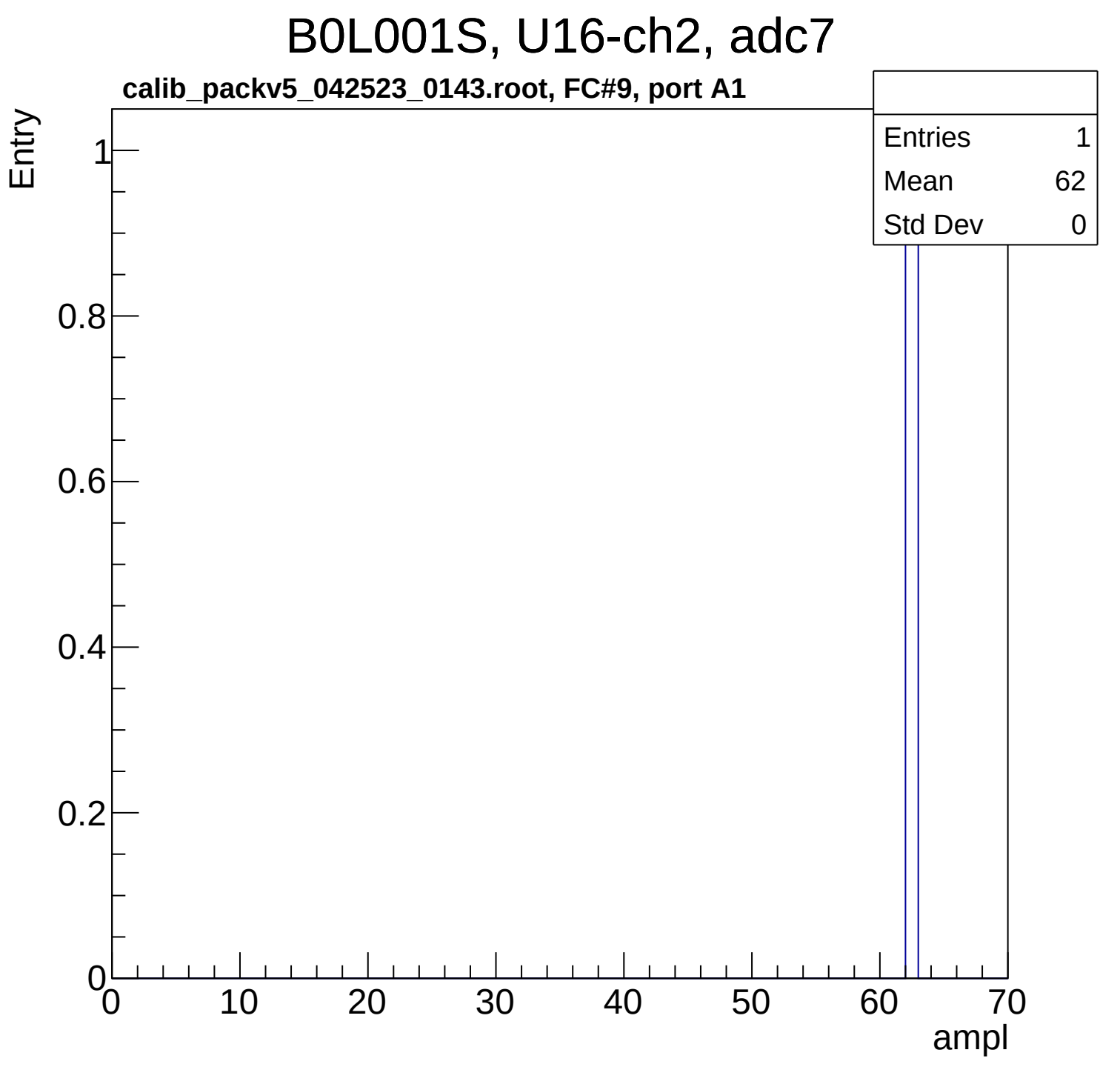
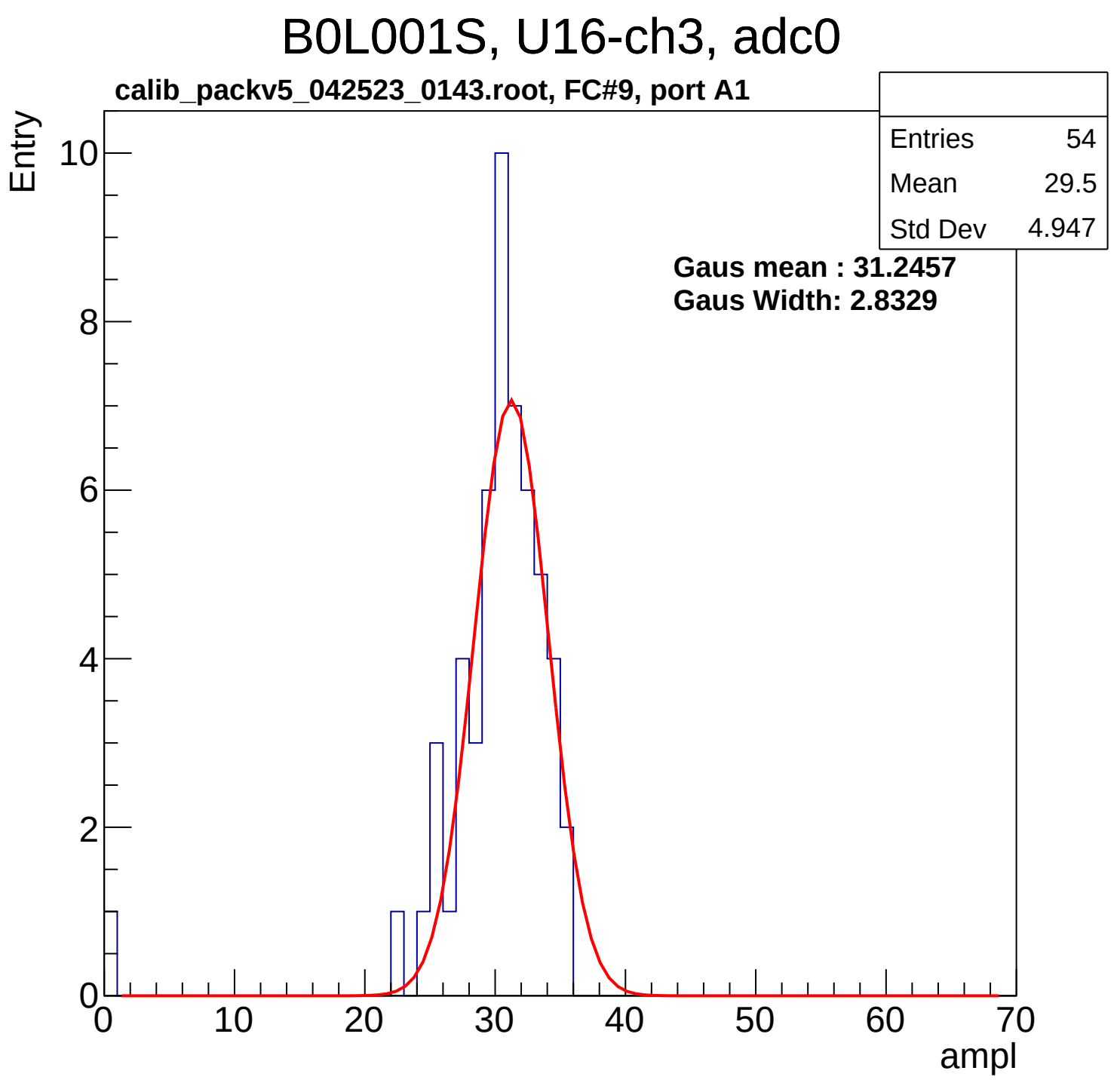
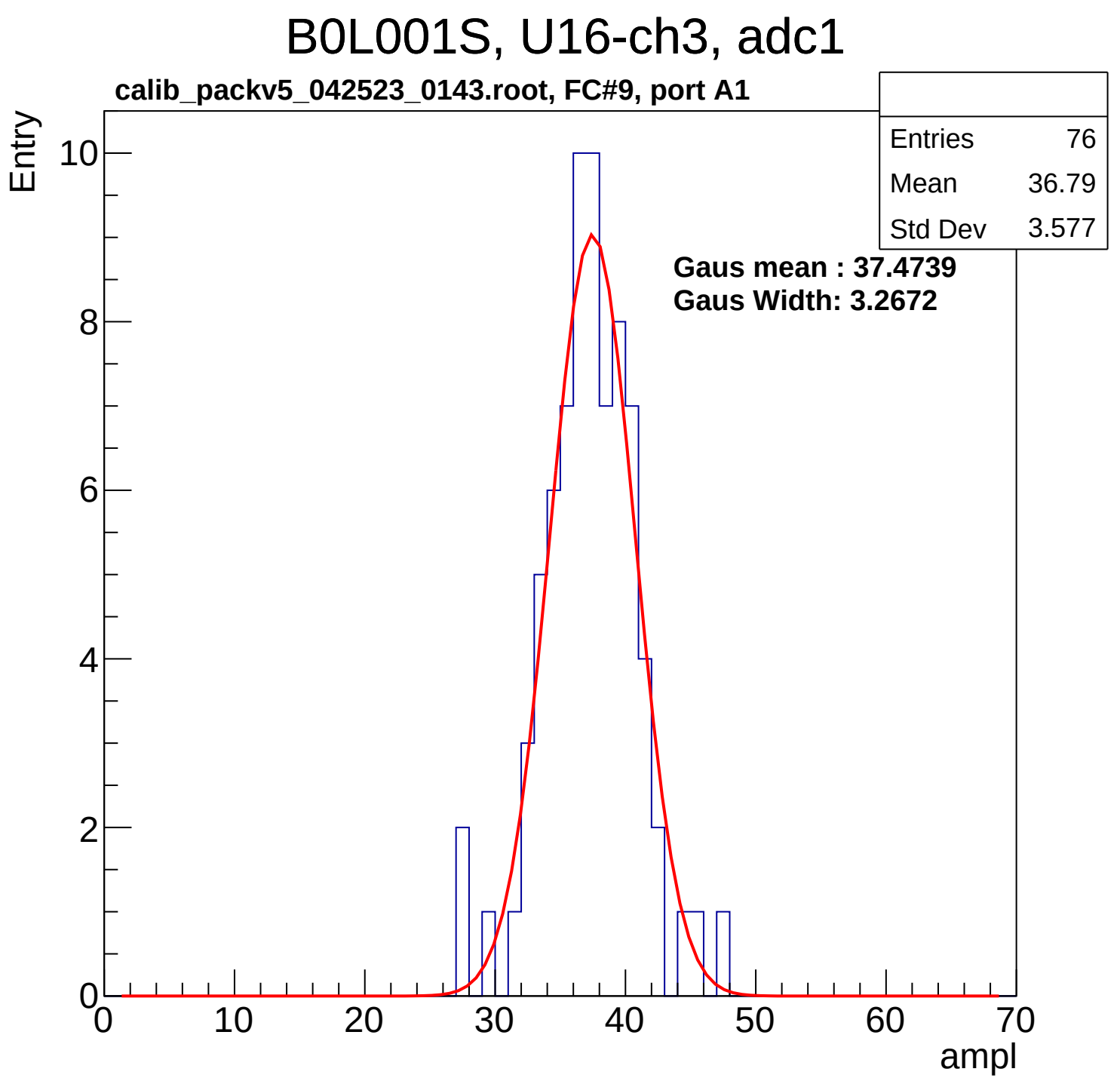
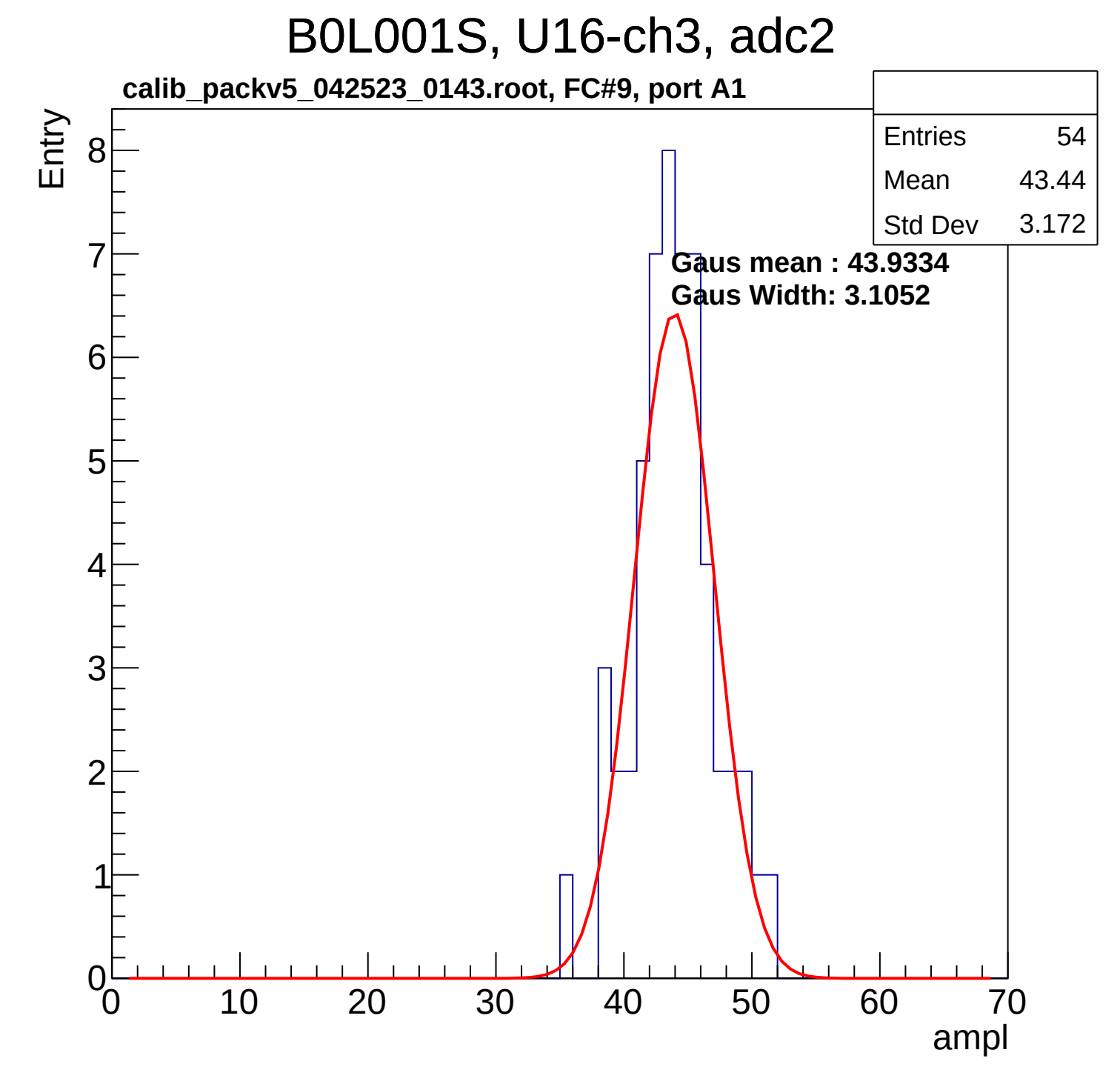
60
70


B0L001S, U16-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 29.5
Std Dev 4.947
Gaus mean : 31.2457
Gaus Width: 2.8329
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.79
Std Dev 3.577
Gaus mean : 37.4739
Gaus Width: 3.2672
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.44
Std Dev 3.172
Gaus mean : 43.9334
Gaus Width: 3.1052
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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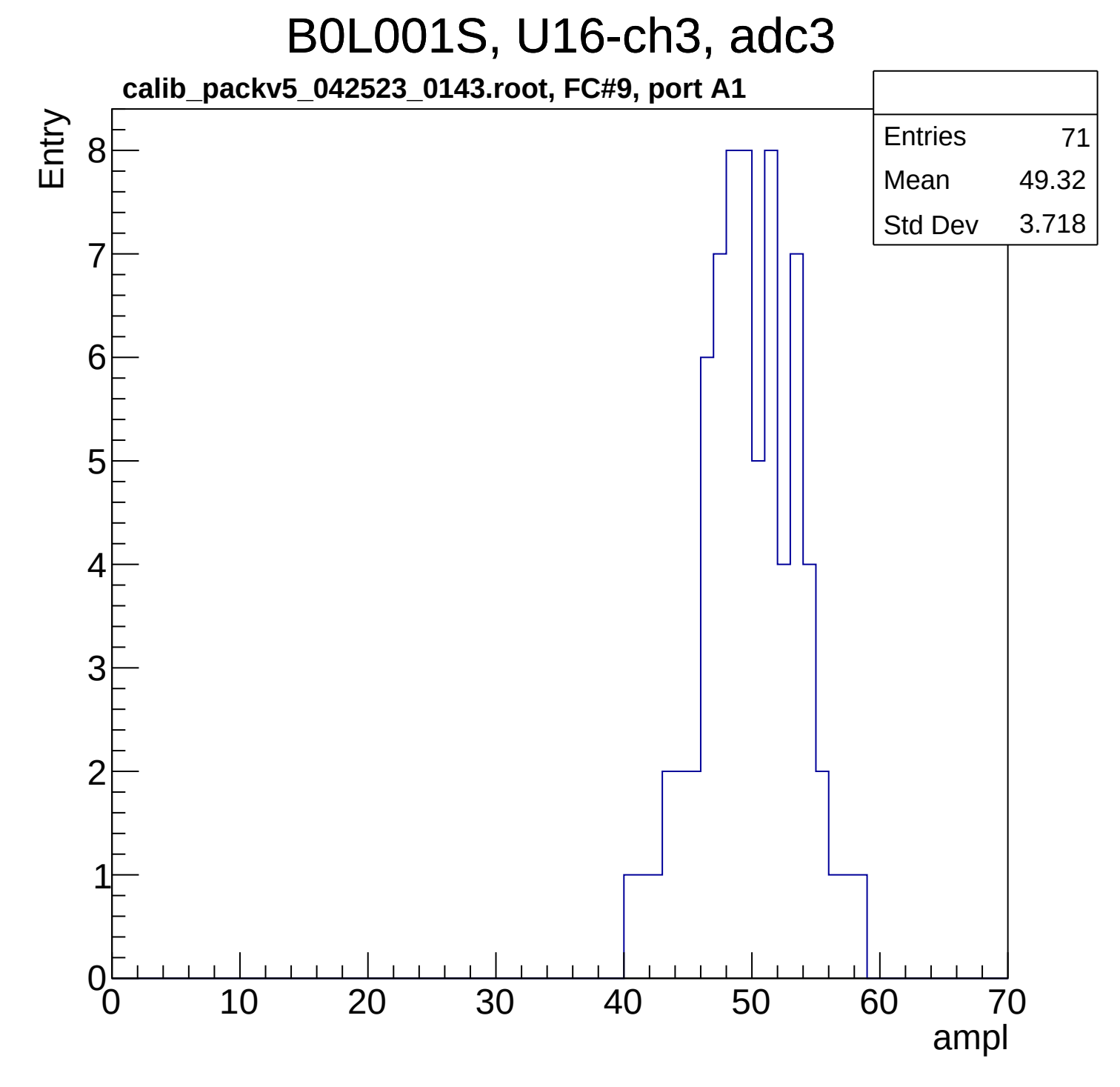
B0L001S, U16-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.32
Std Dev 3.718
Entry
ampl
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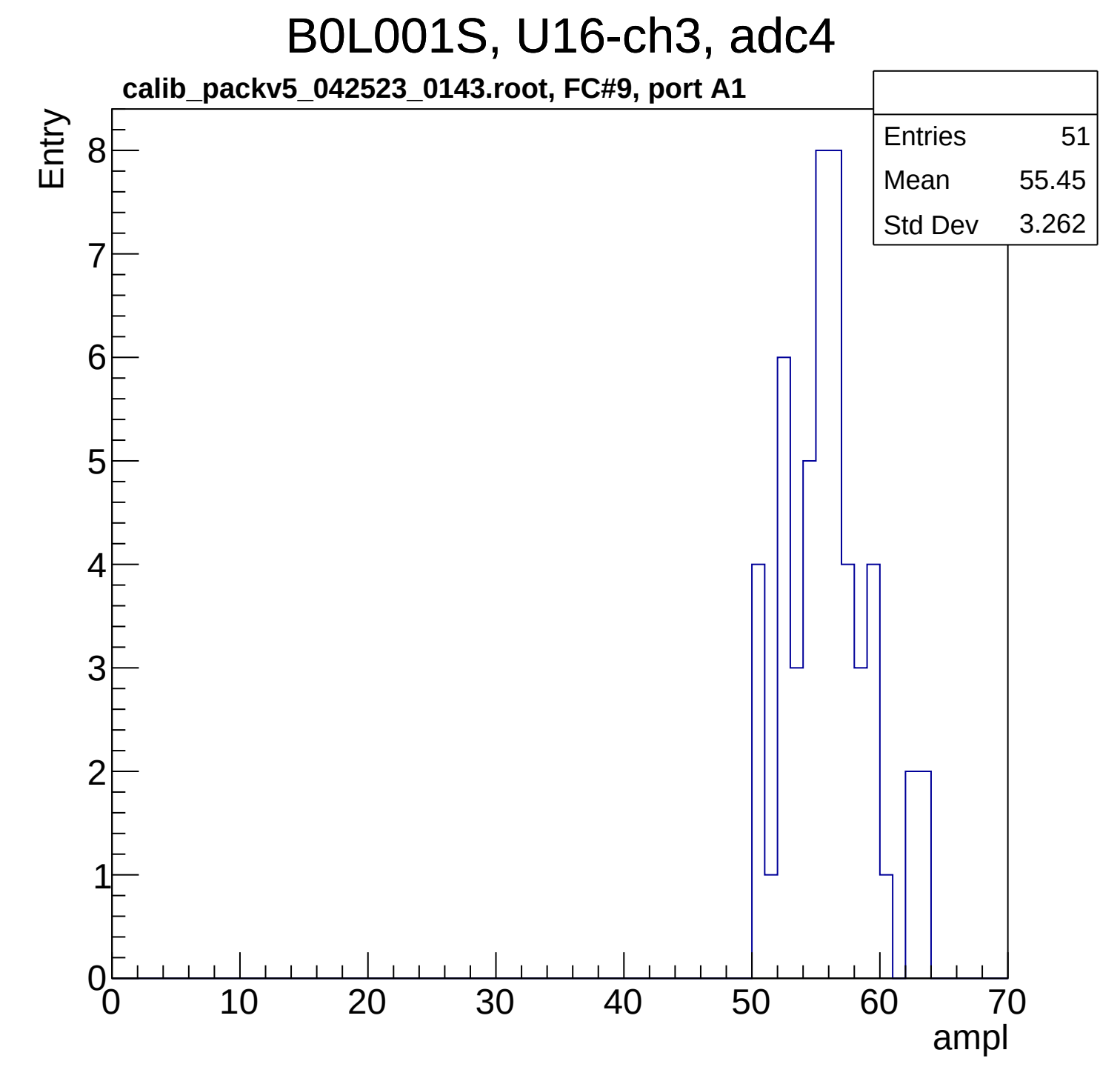
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.45
Std Dev 3.262
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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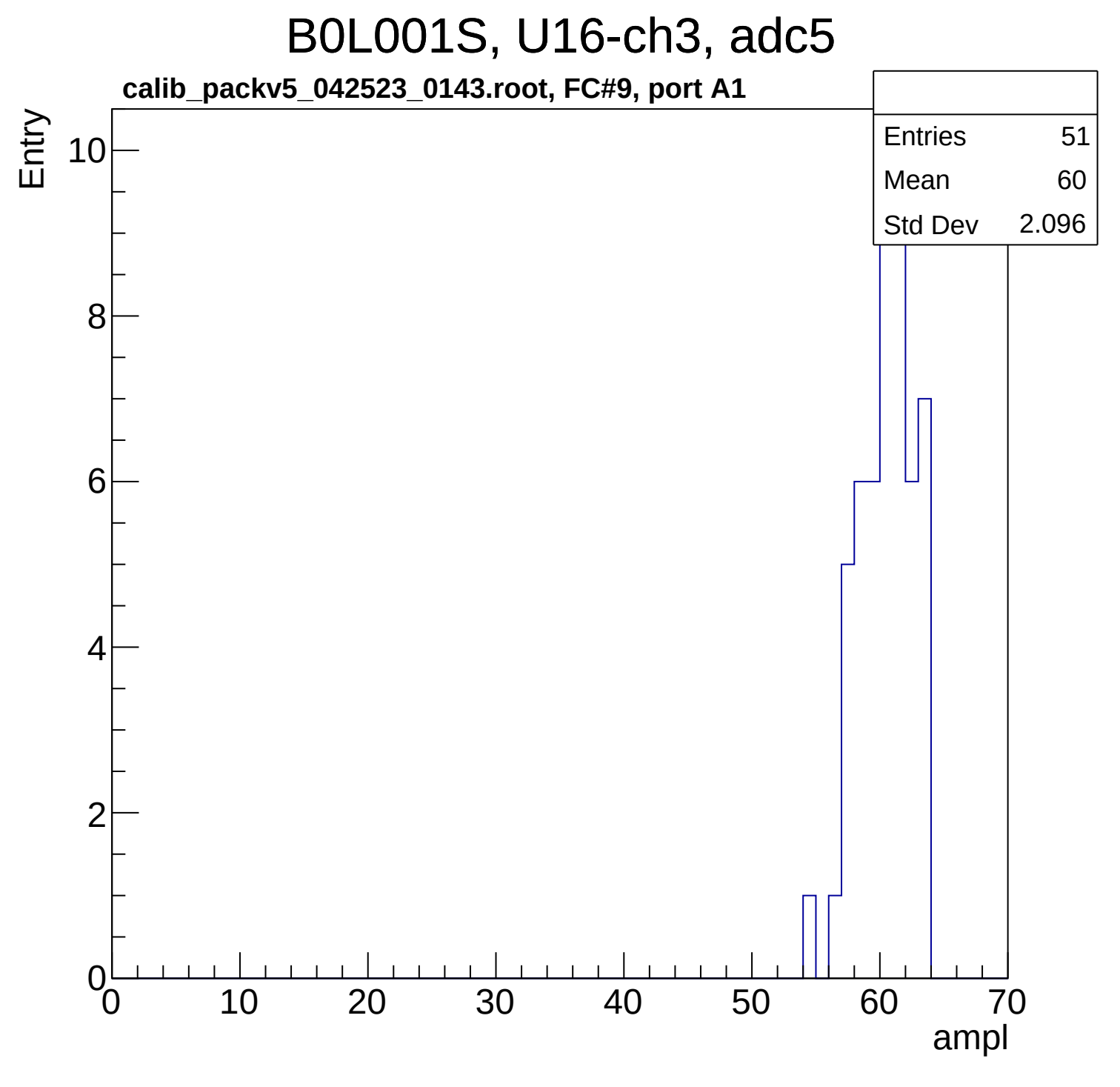
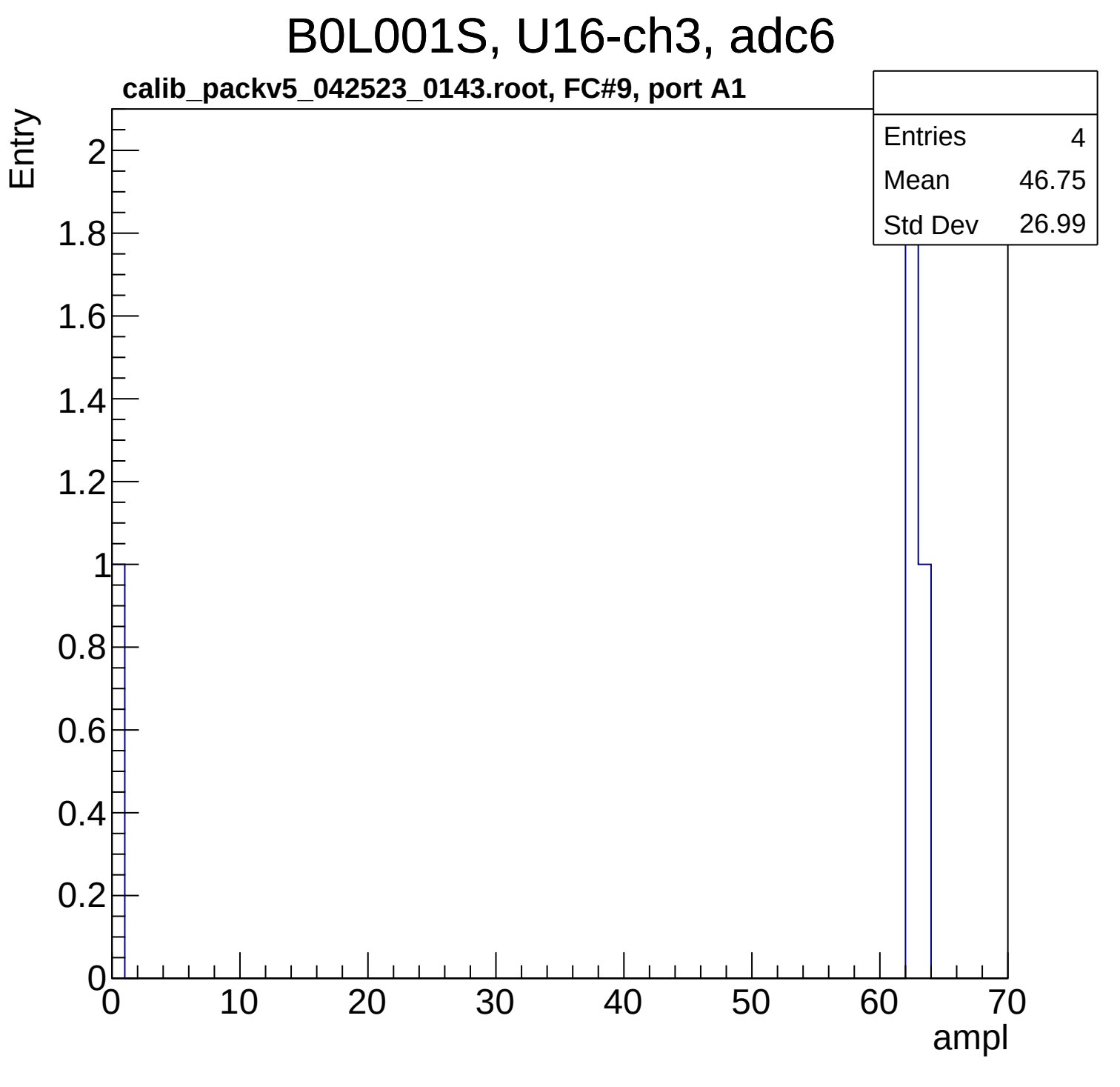

B0L001S, U16-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 60
Std Dev 2.096
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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60
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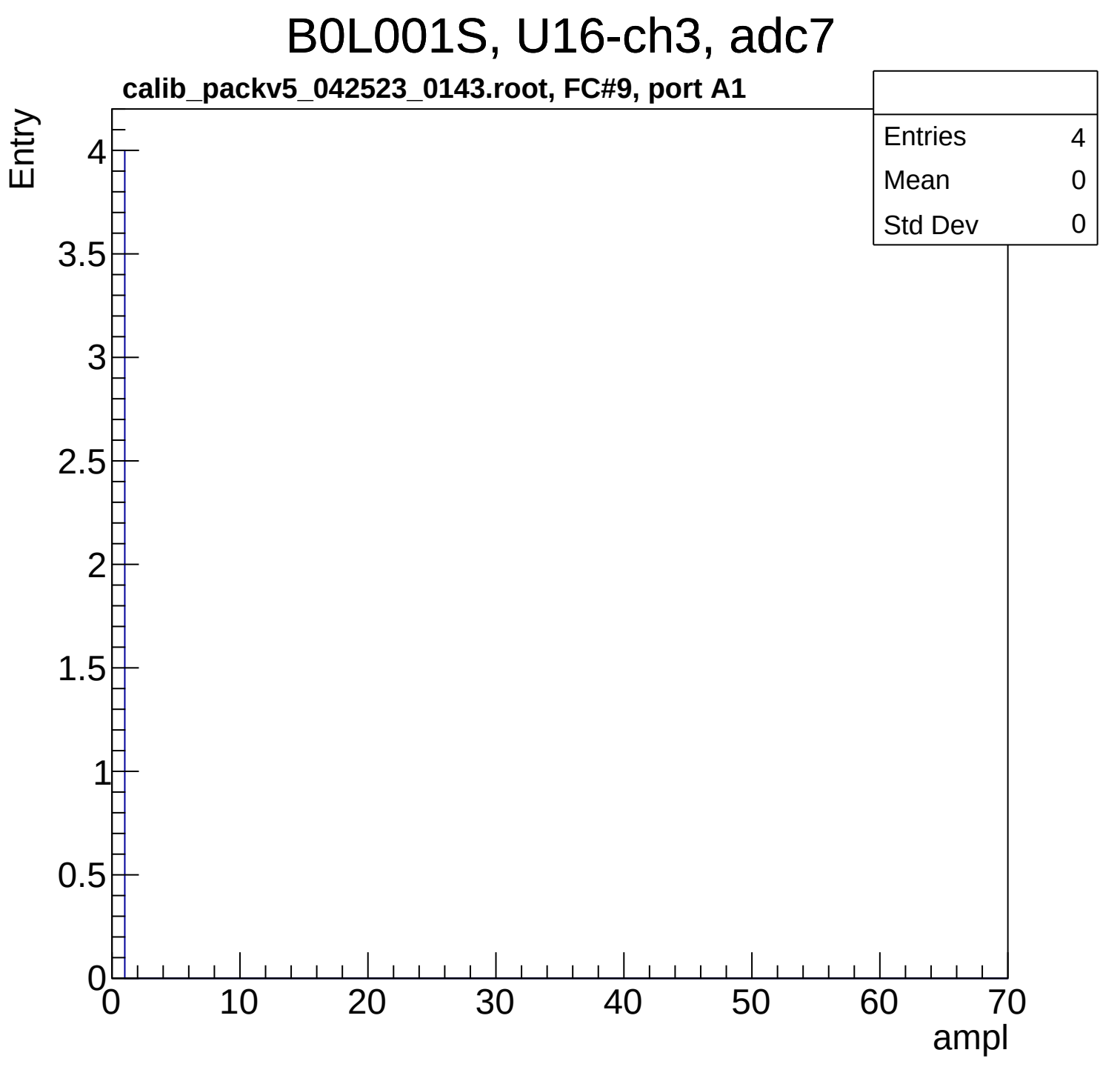
B0L001S, U16-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
4
3.5
3
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1.5
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10
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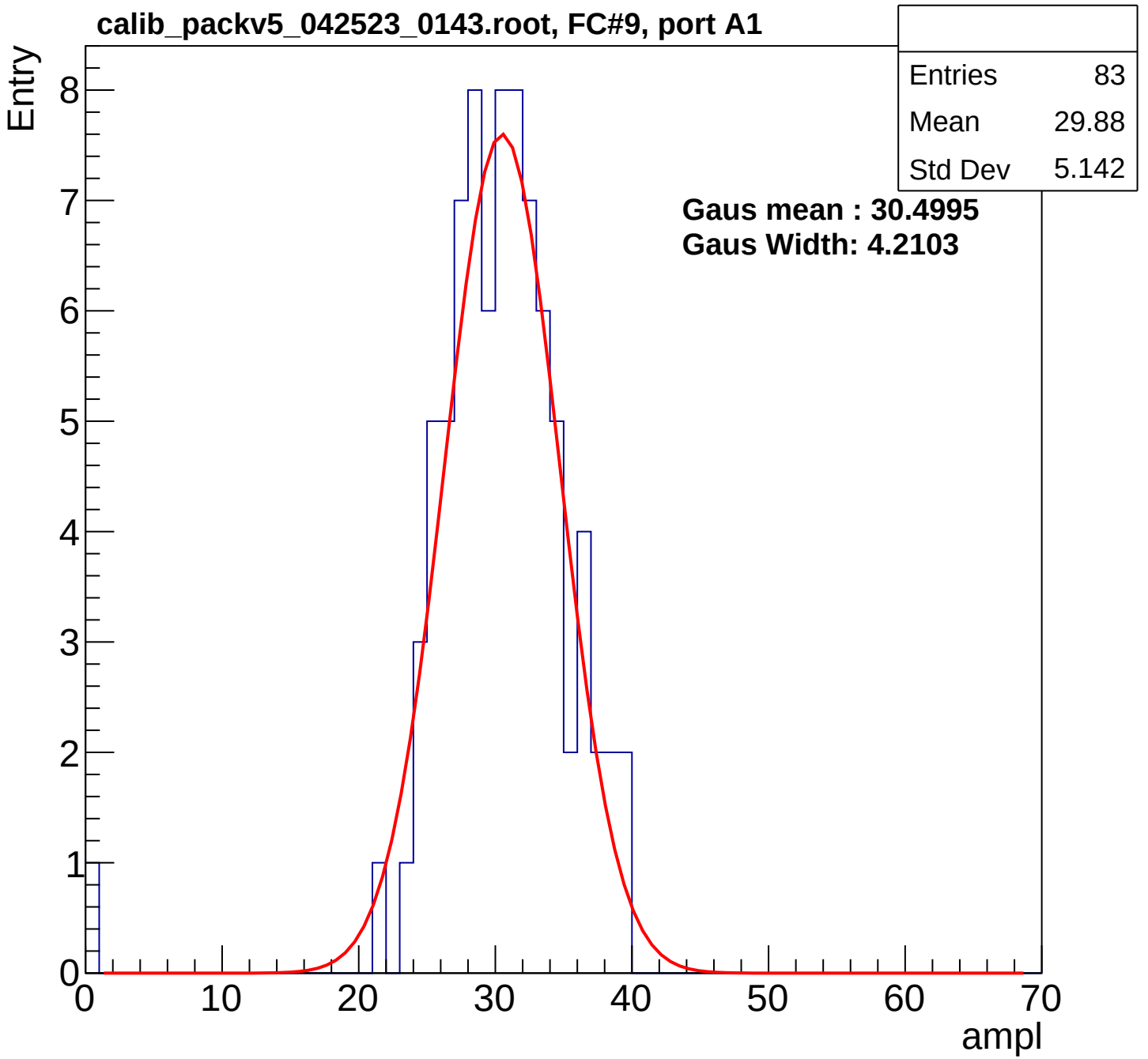
ampl


B0L001S, U16-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 29.88
Std Dev 5.142
Gaus mean : 30.4995
Gaus Width: 4.2103
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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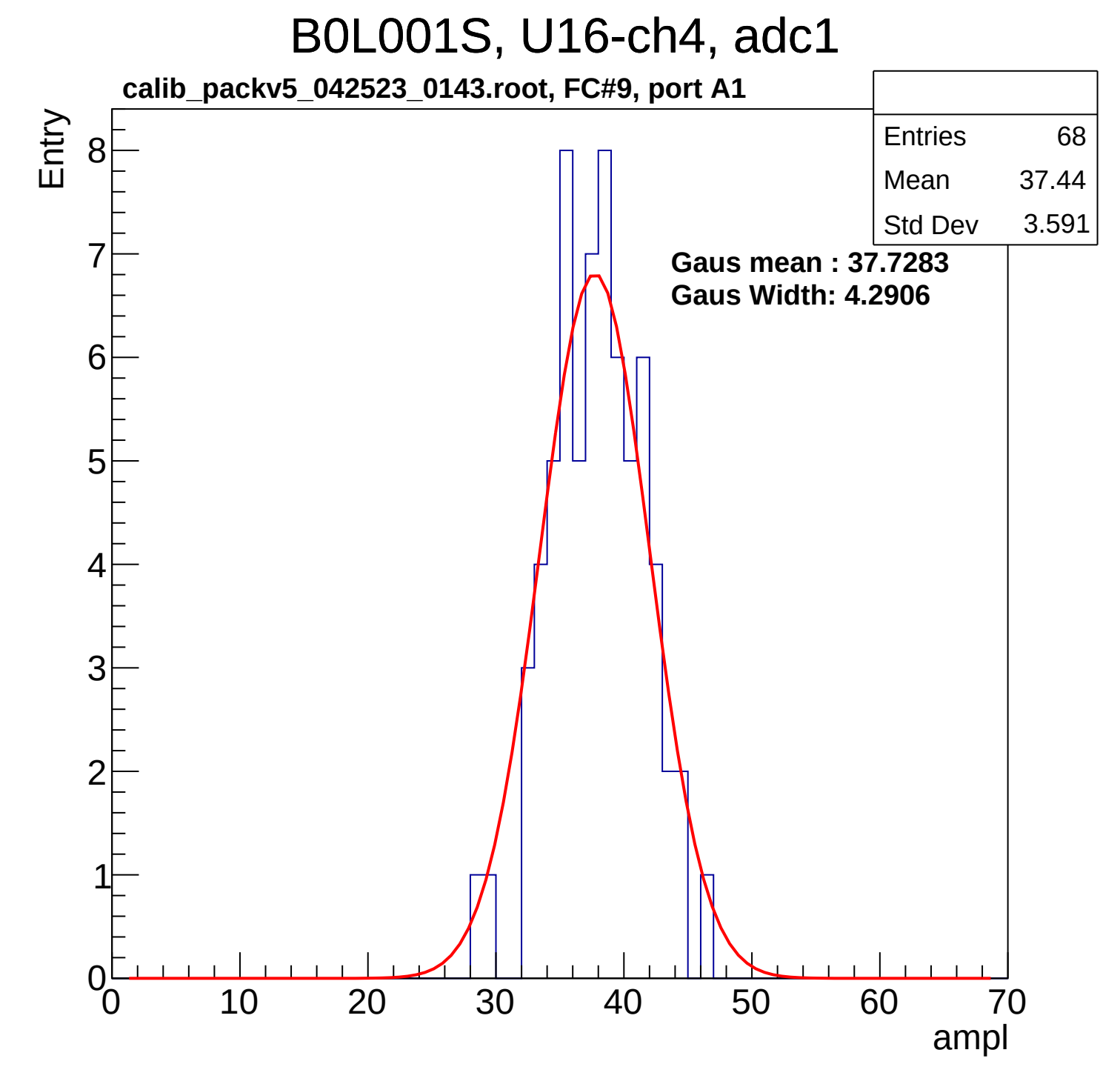
B0L001S, U16-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.44
Std Dev 3.591
Gaus mean : 37.7283
Gaus Width: 4.2906
Entry
ampl
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2
3
4
5
6
7
8
0
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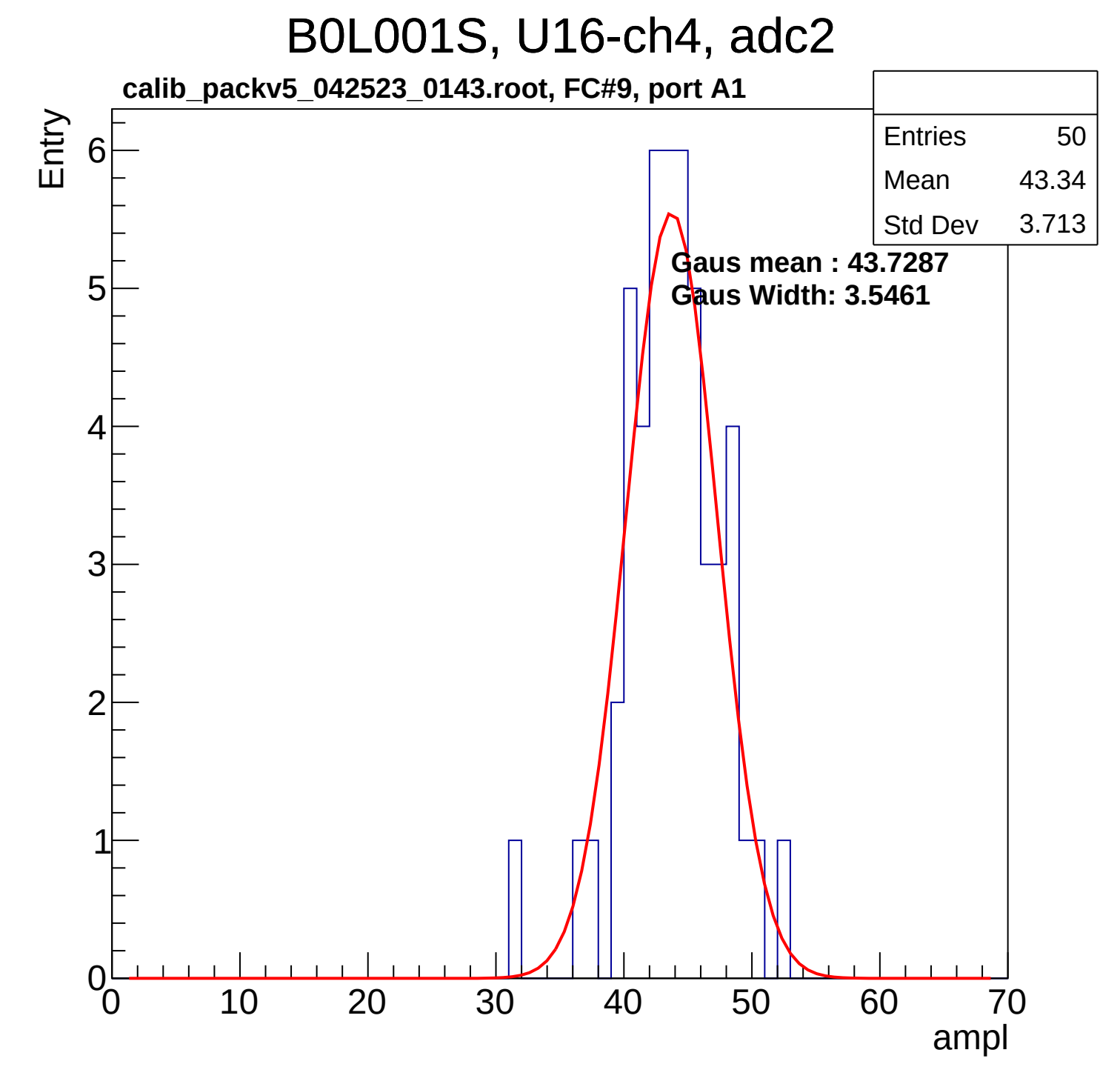
B0L001S, U16-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 43.34
Std Dev 3.713
Gaus mean : 43.7287
Gaus Width: 3.5461
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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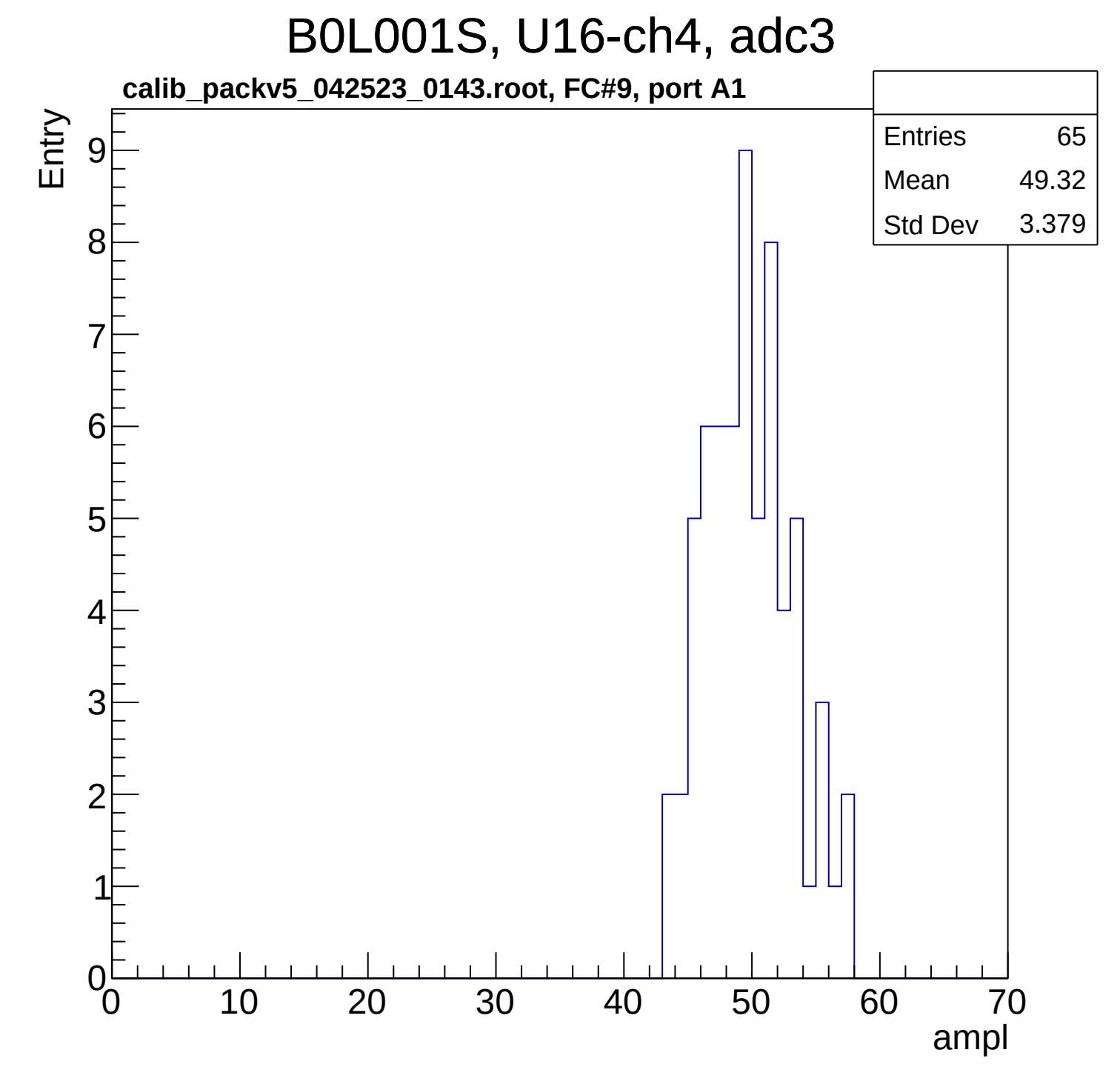
B0L001S, U16-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.32
Std Dev 3.379
Entry
ampl
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2
3
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5
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0
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60
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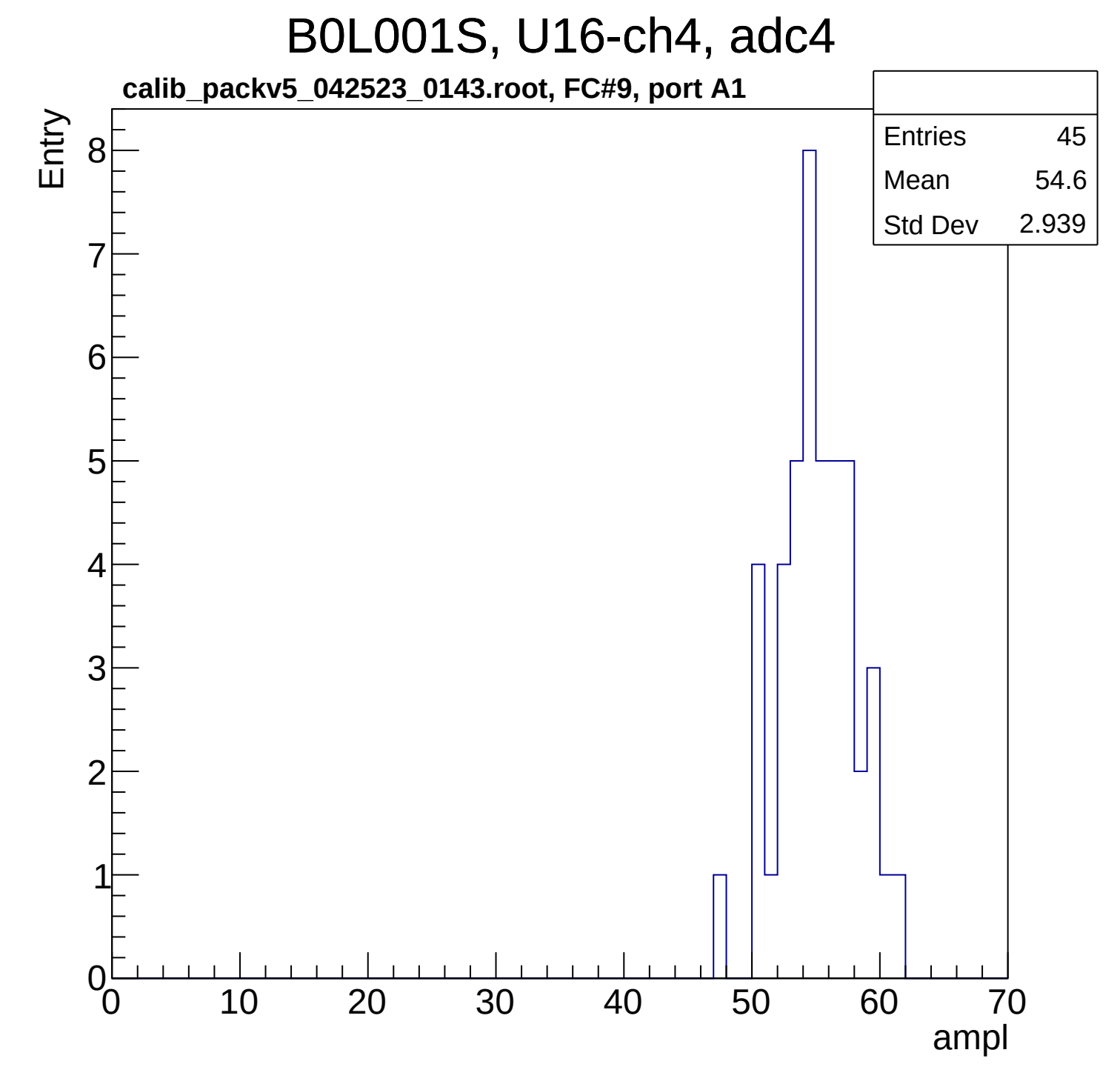
B0L001S, U16-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 54.6
Std Dev 2.939
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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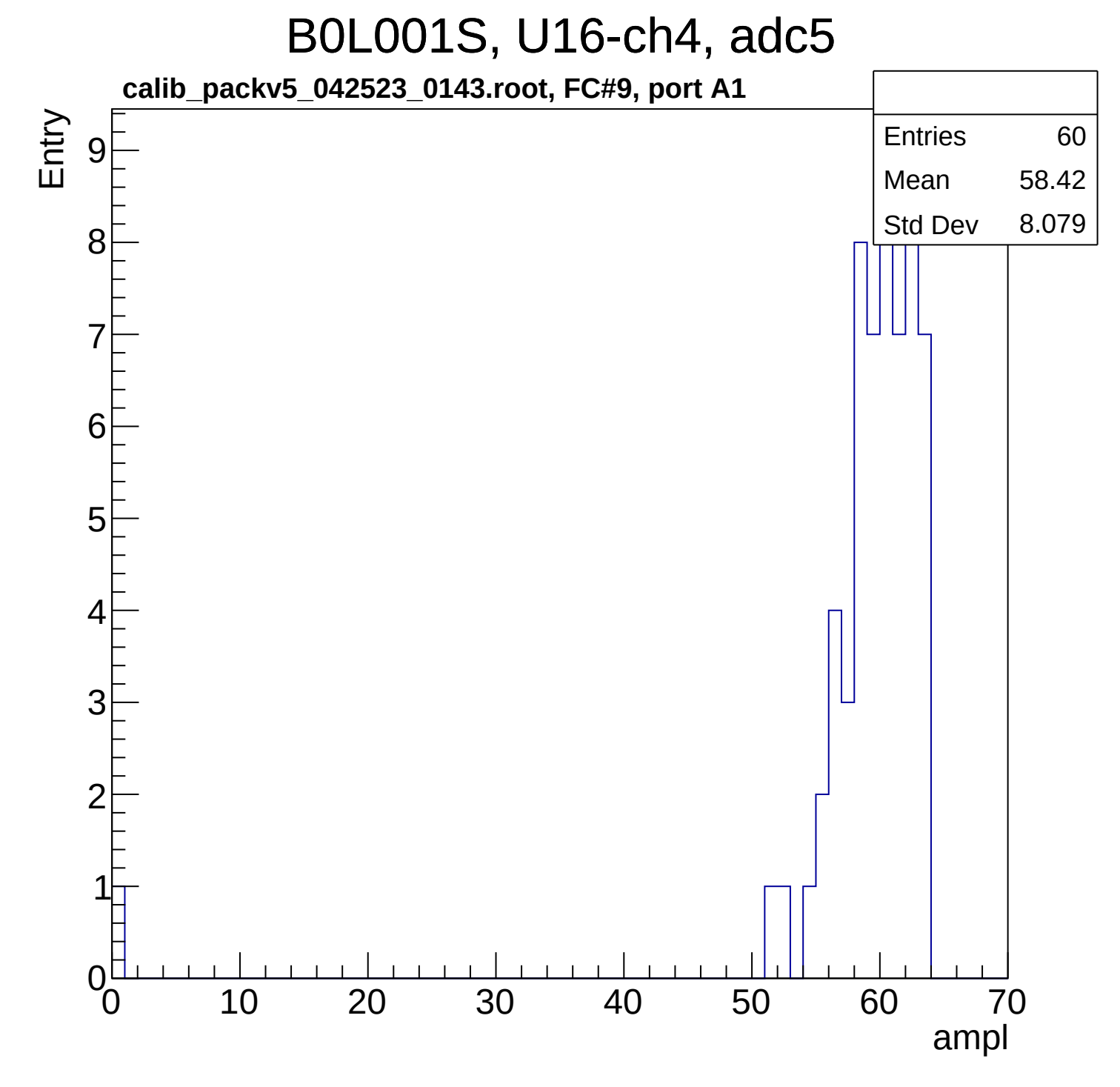
B0L001S, U16-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 58.42
Std Dev 8.079
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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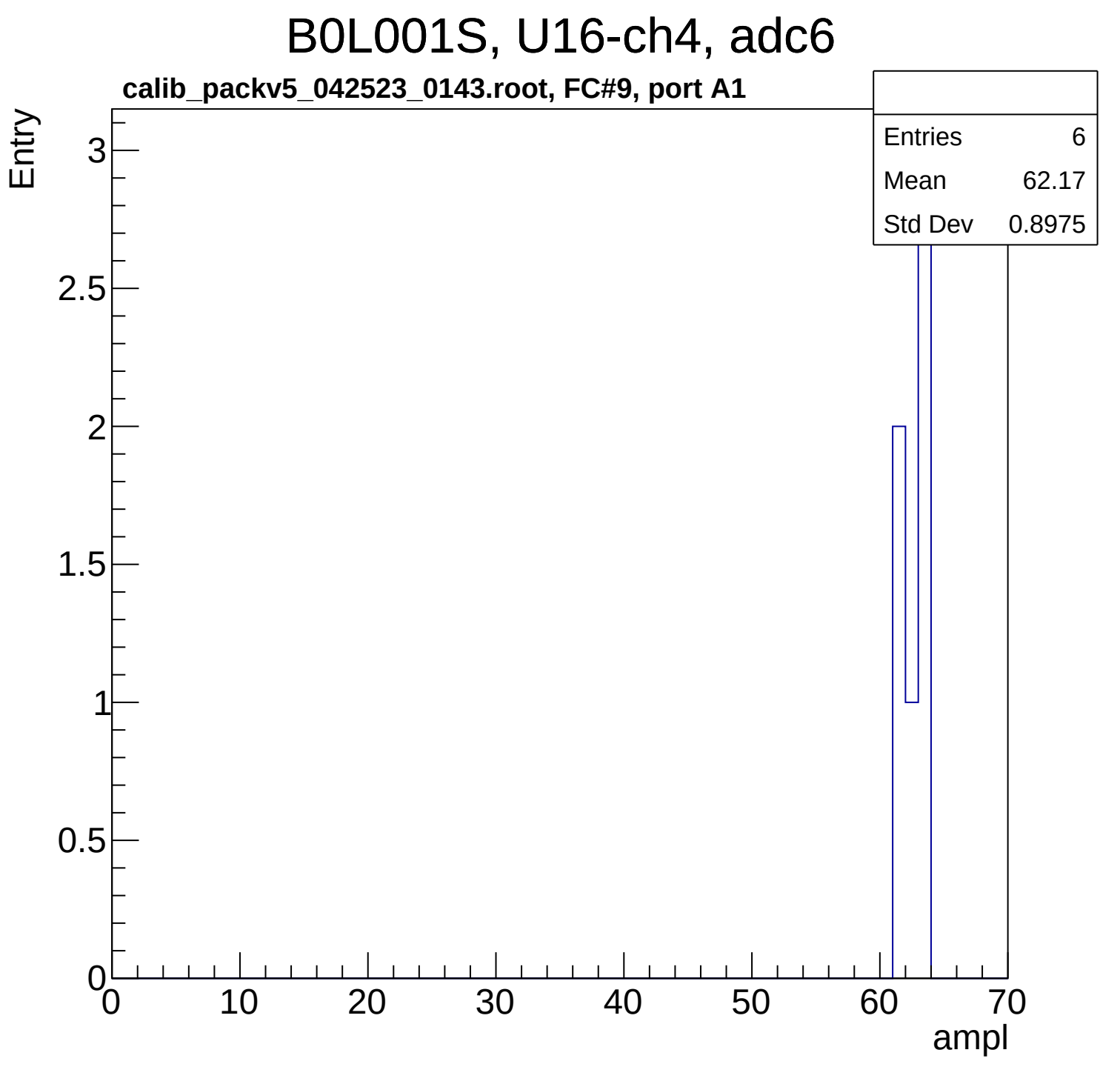
B0L001S, U16-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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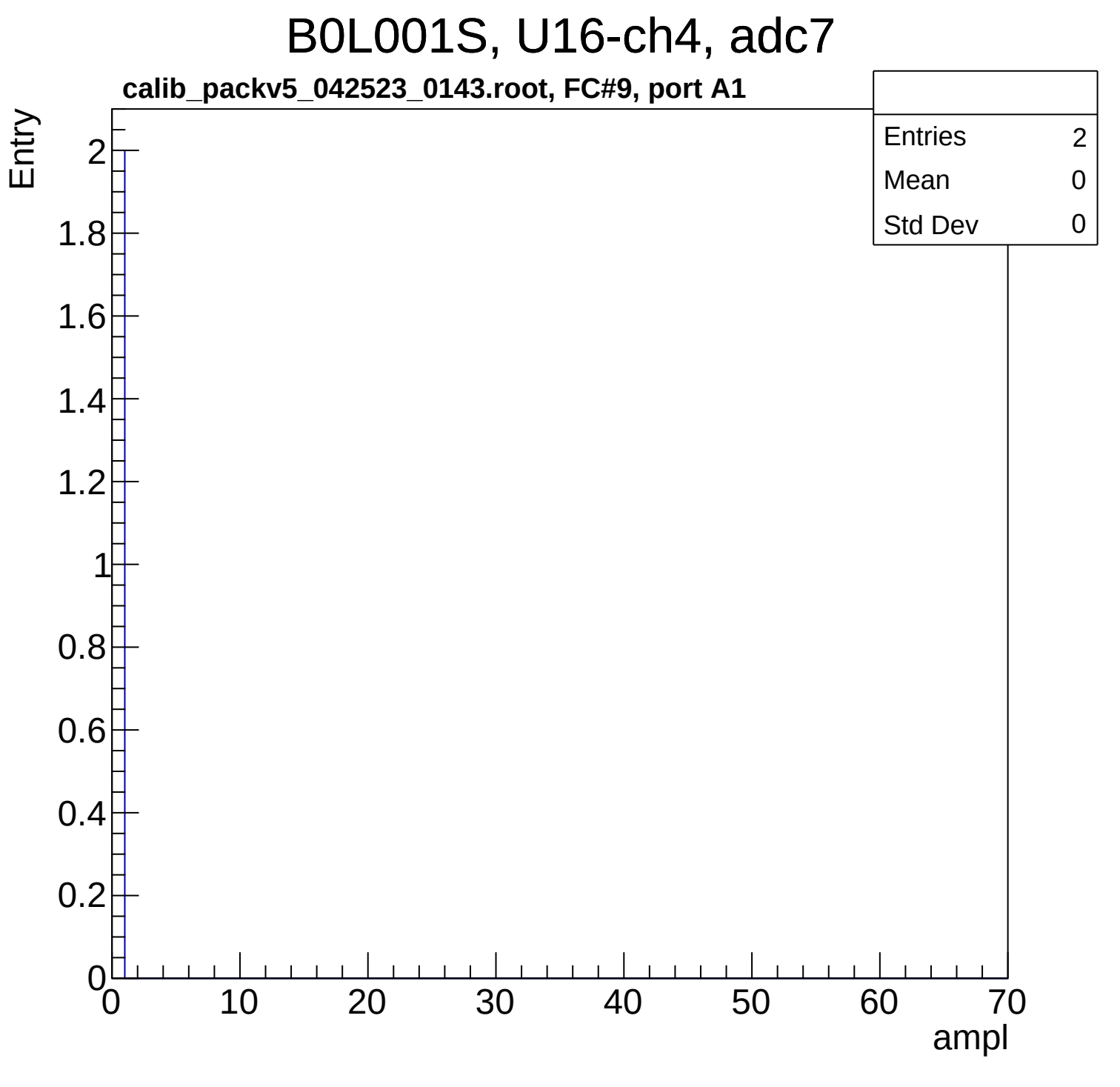
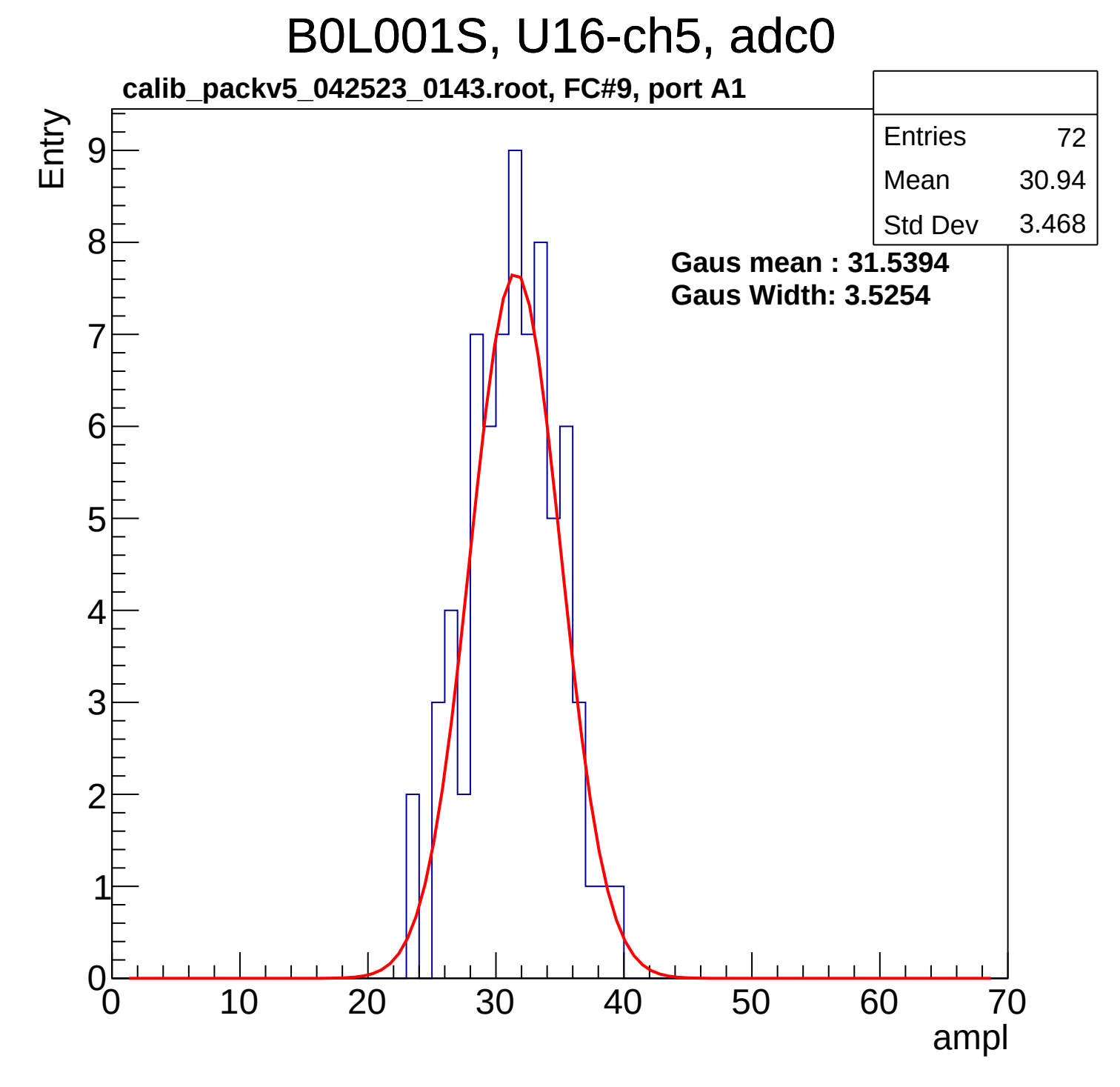

B0L001S, U16-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.94
Std Dev 3.468
Gaus mean : 31.5394
Gaus Width: 3.5254
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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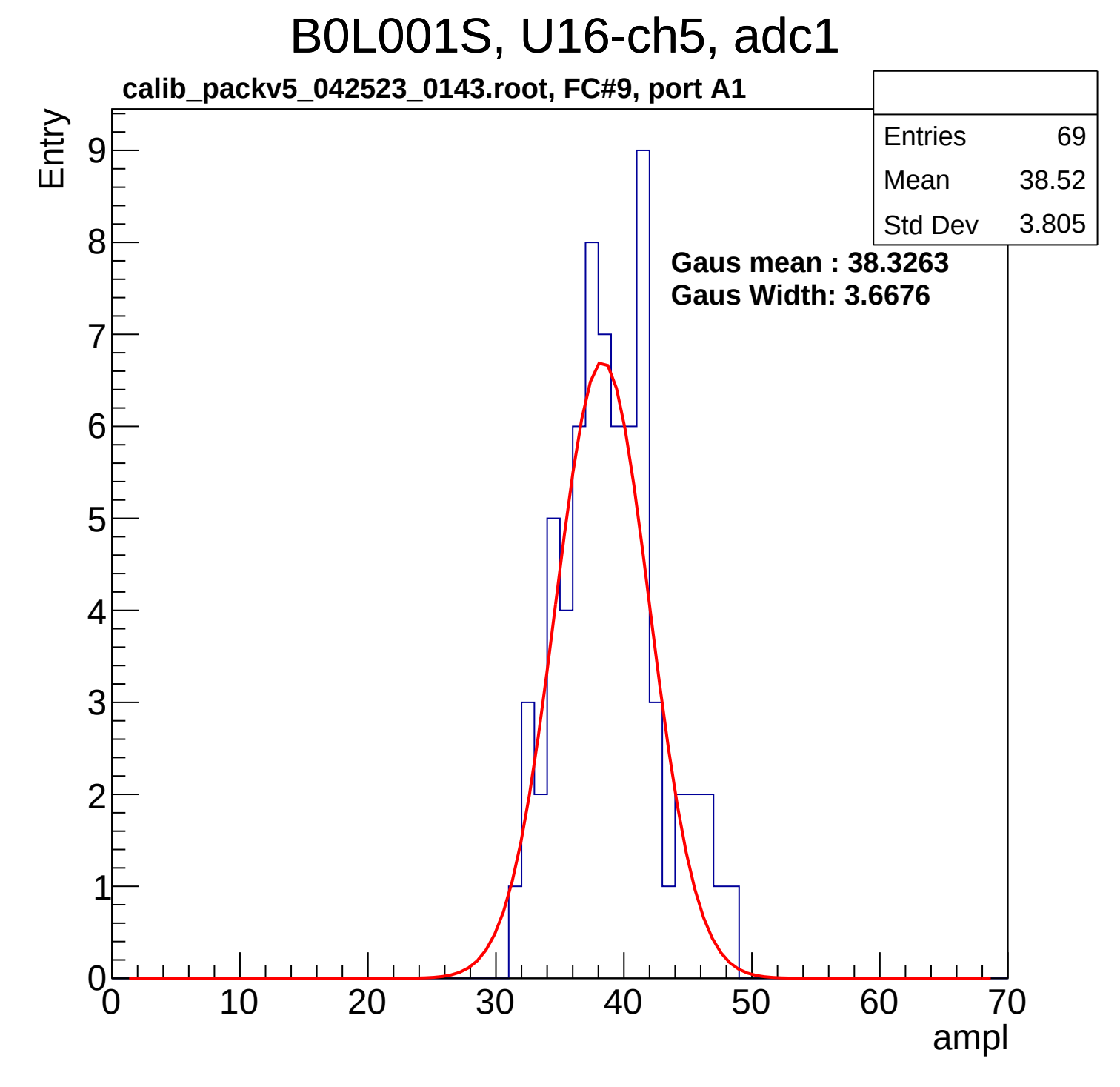
B0L001S, U16-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.52
Std Dev 3.805
Gaus mean : 38.3263
Gaus Width: 3.6676
Entry
ampl
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3
4
5
6
7
8
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0
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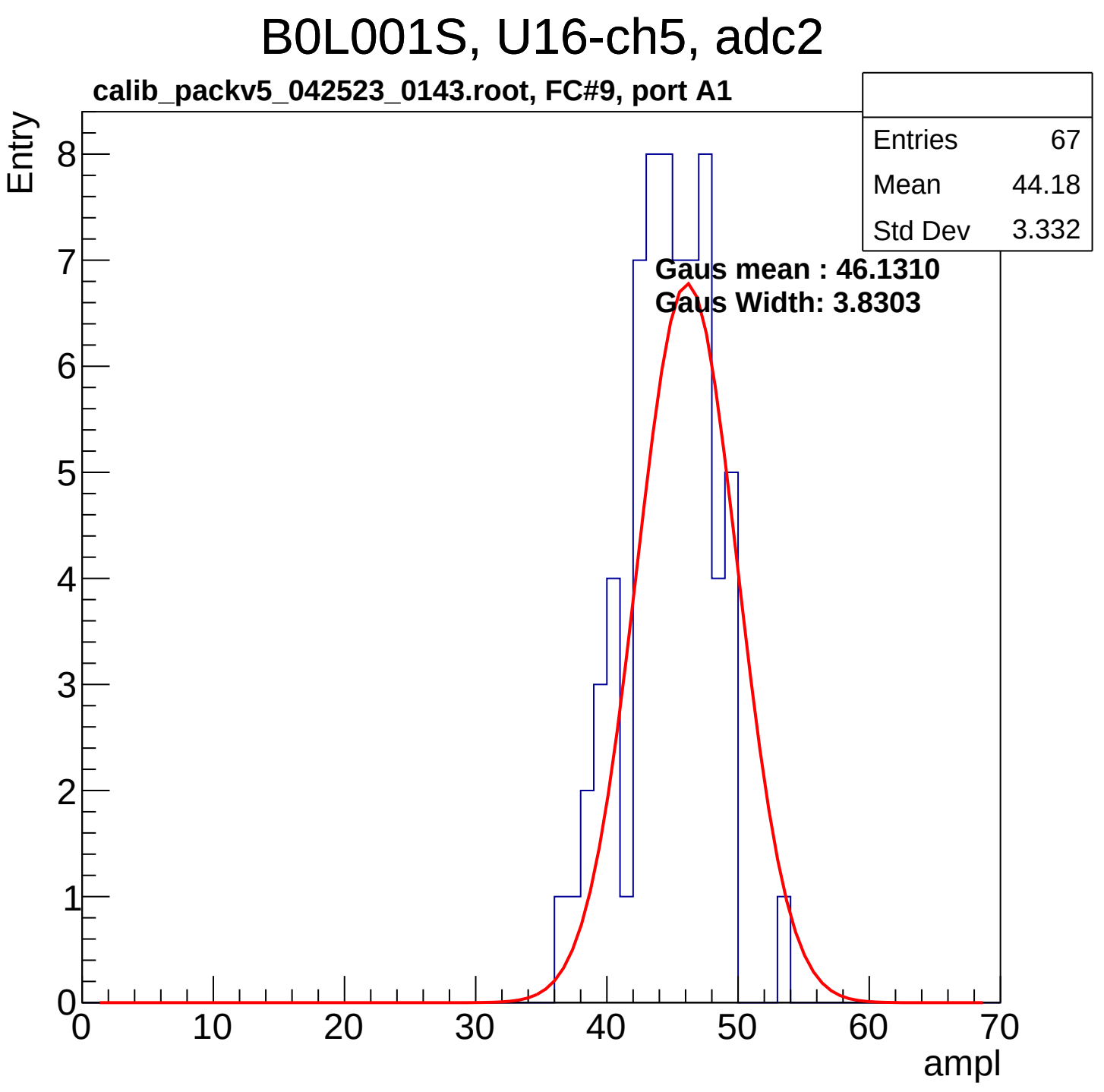
B0L001S, U16-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.18
Std Dev 3.332
Gaus mean : 46.1310
Gaus Width: 3.8303
Entry
ampl
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1
2
3
4
5
6
7
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0
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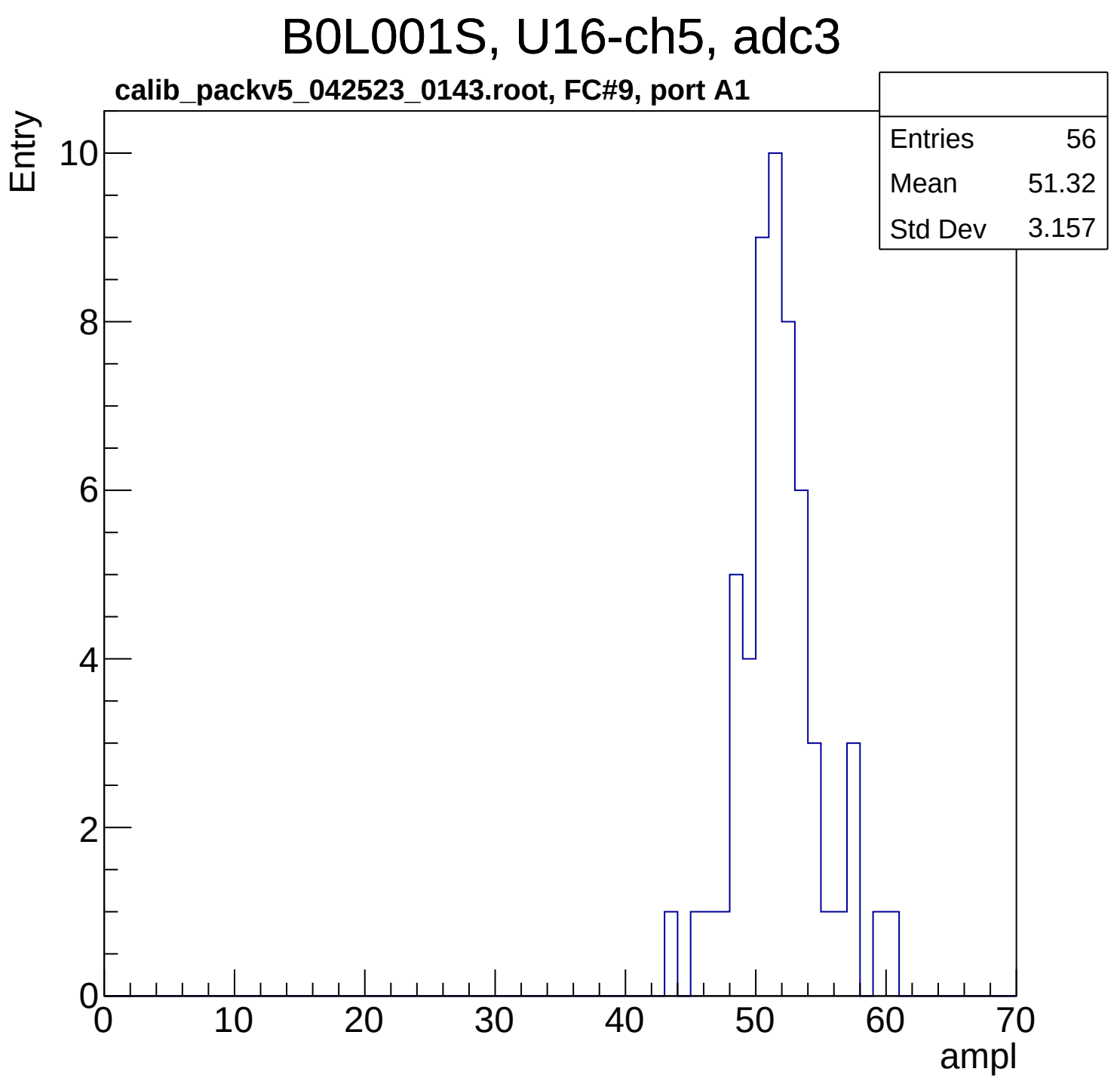
B0L001S, U16-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 51.32
Std Dev 3.157
Entry
ampl
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2
4
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8
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0
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20
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40
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60
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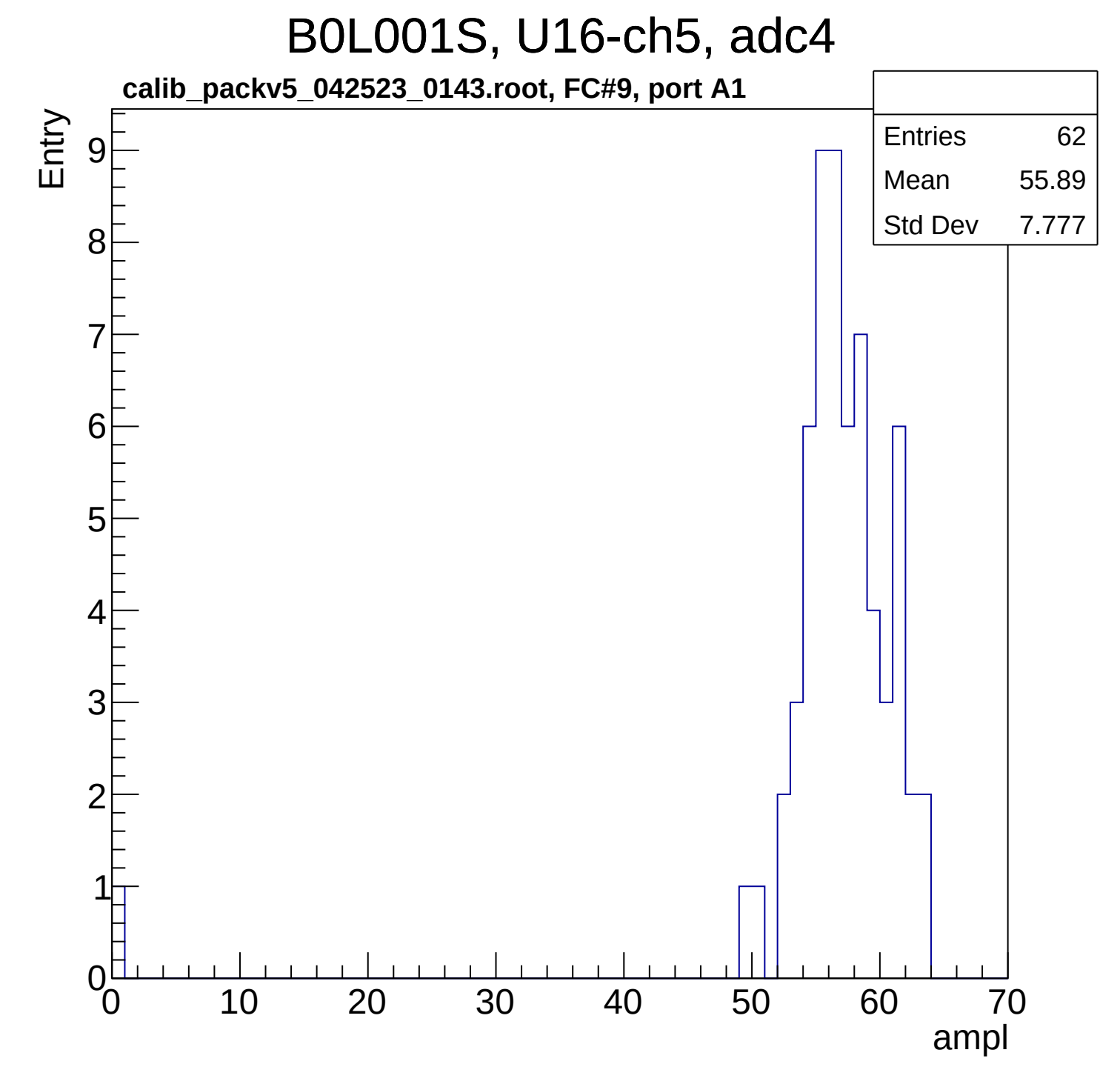
B0L001S, U16-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.89
Std Dev 7.777
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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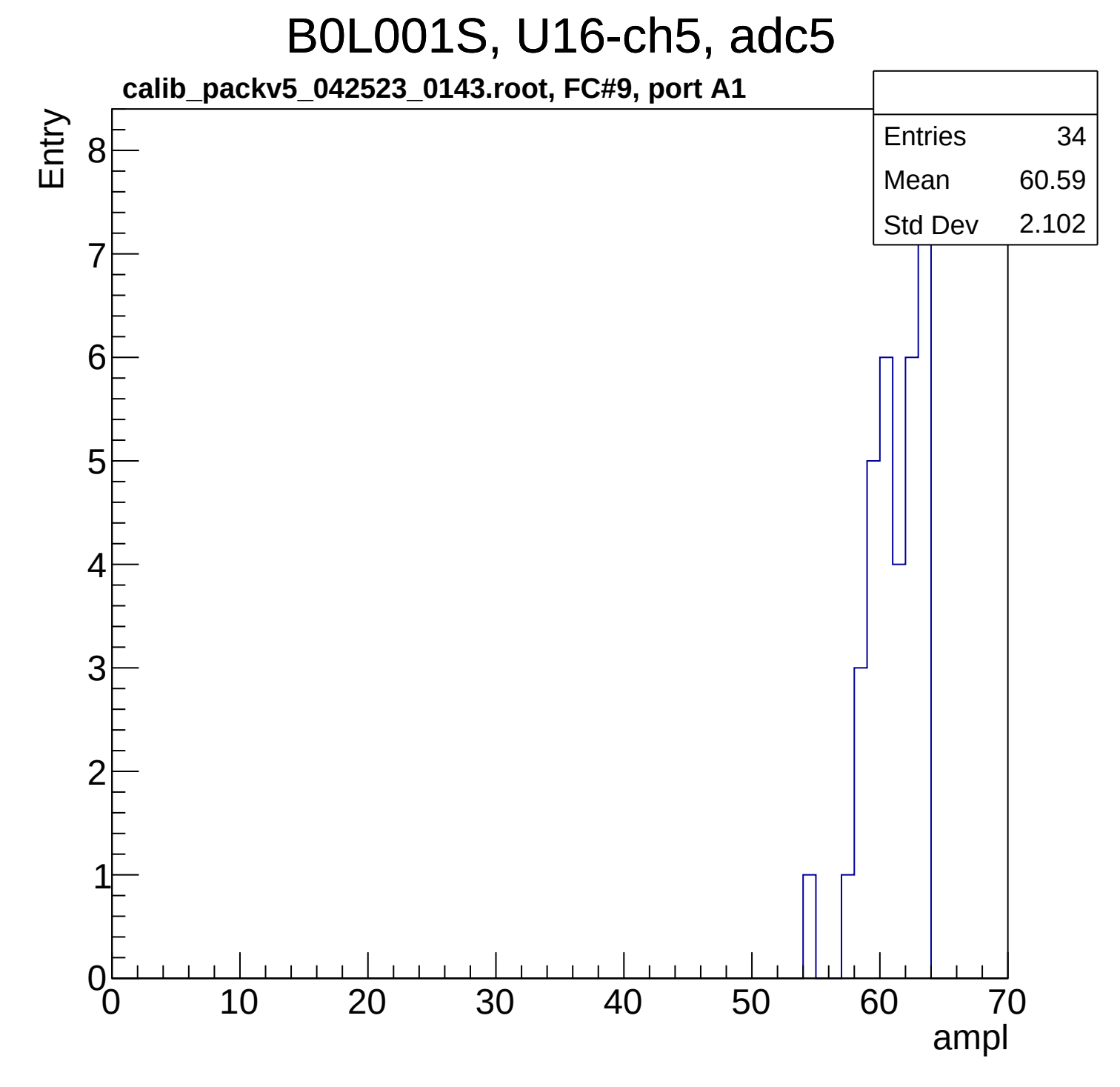
B0L001S, U16-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.59
Std Dev 2.102
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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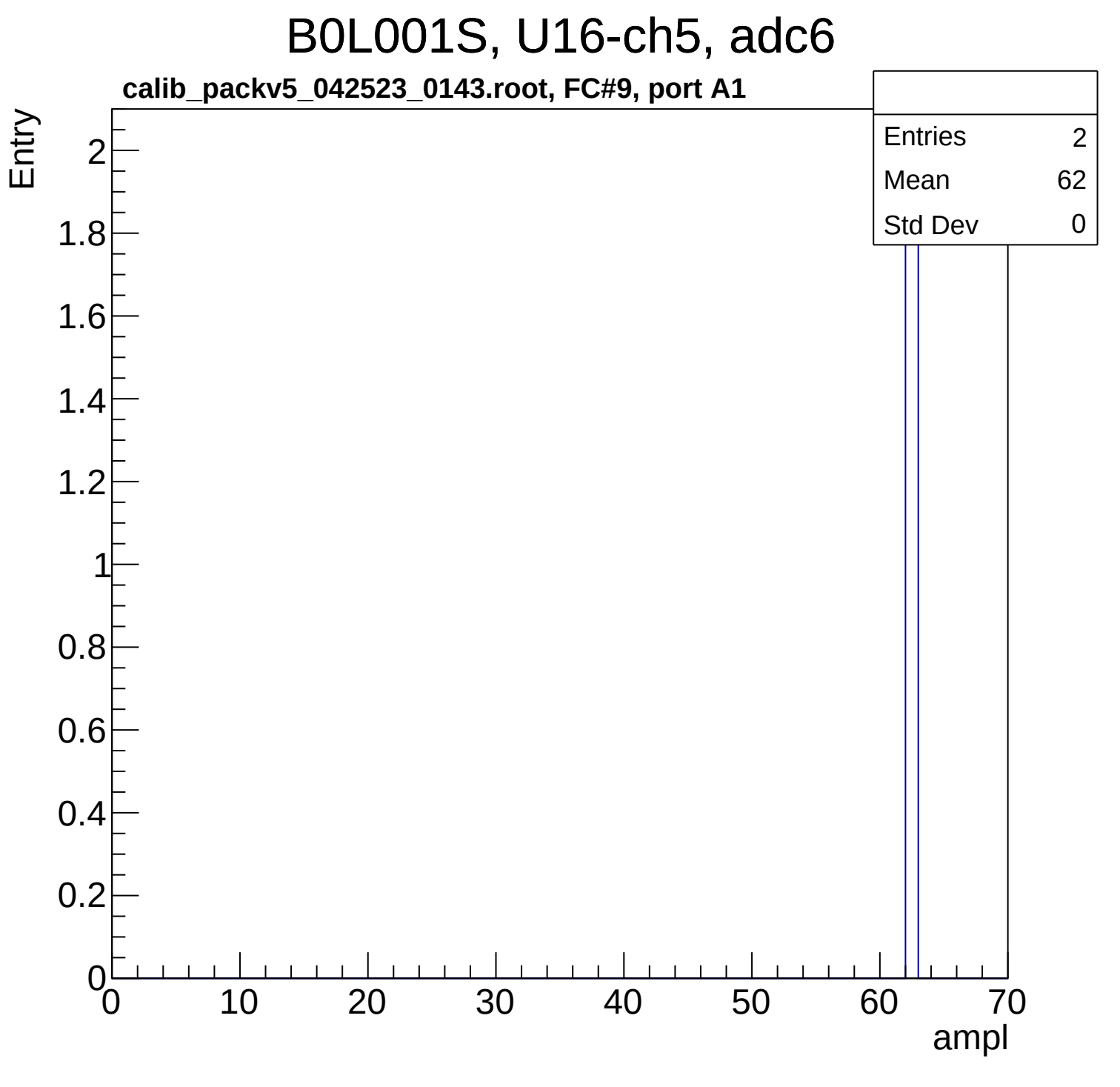
B0L001S, U16-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
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60
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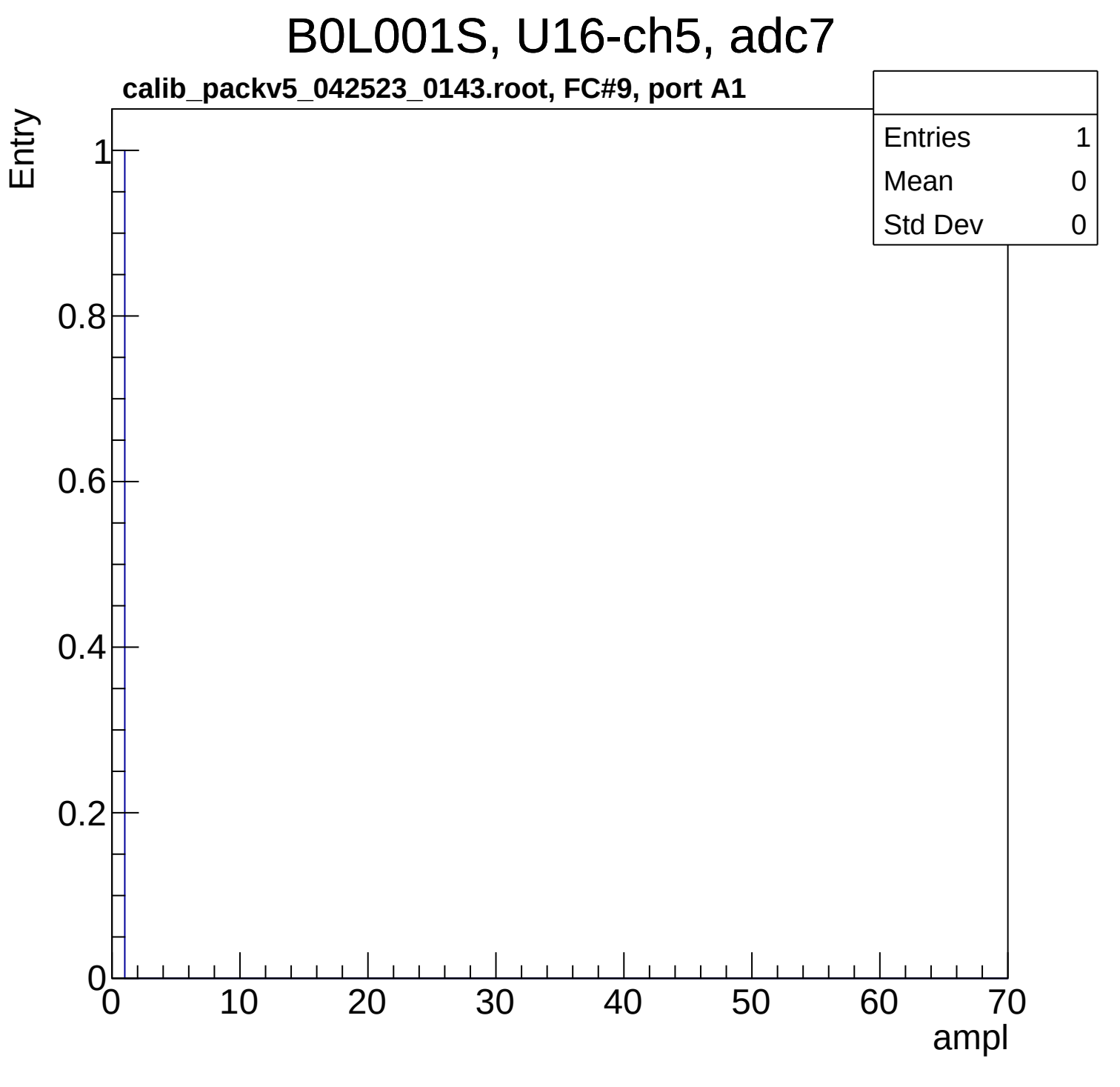
B0L001S, U16-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
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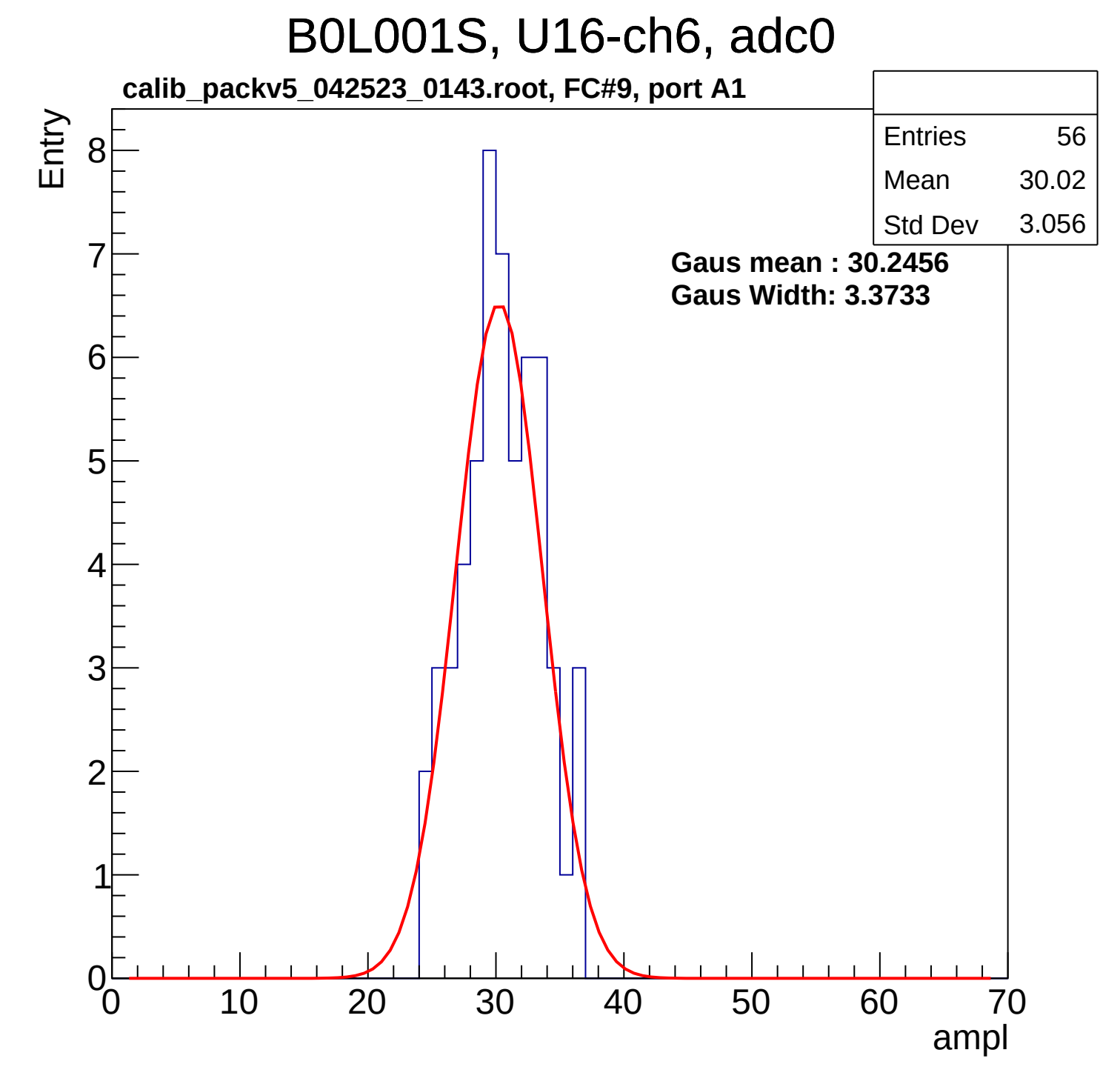
50
60
70
ampl


B0L001S, U16-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.02
Std Dev 3.056
Gaus mean : 30.2456
Gaus Width: 3.3733
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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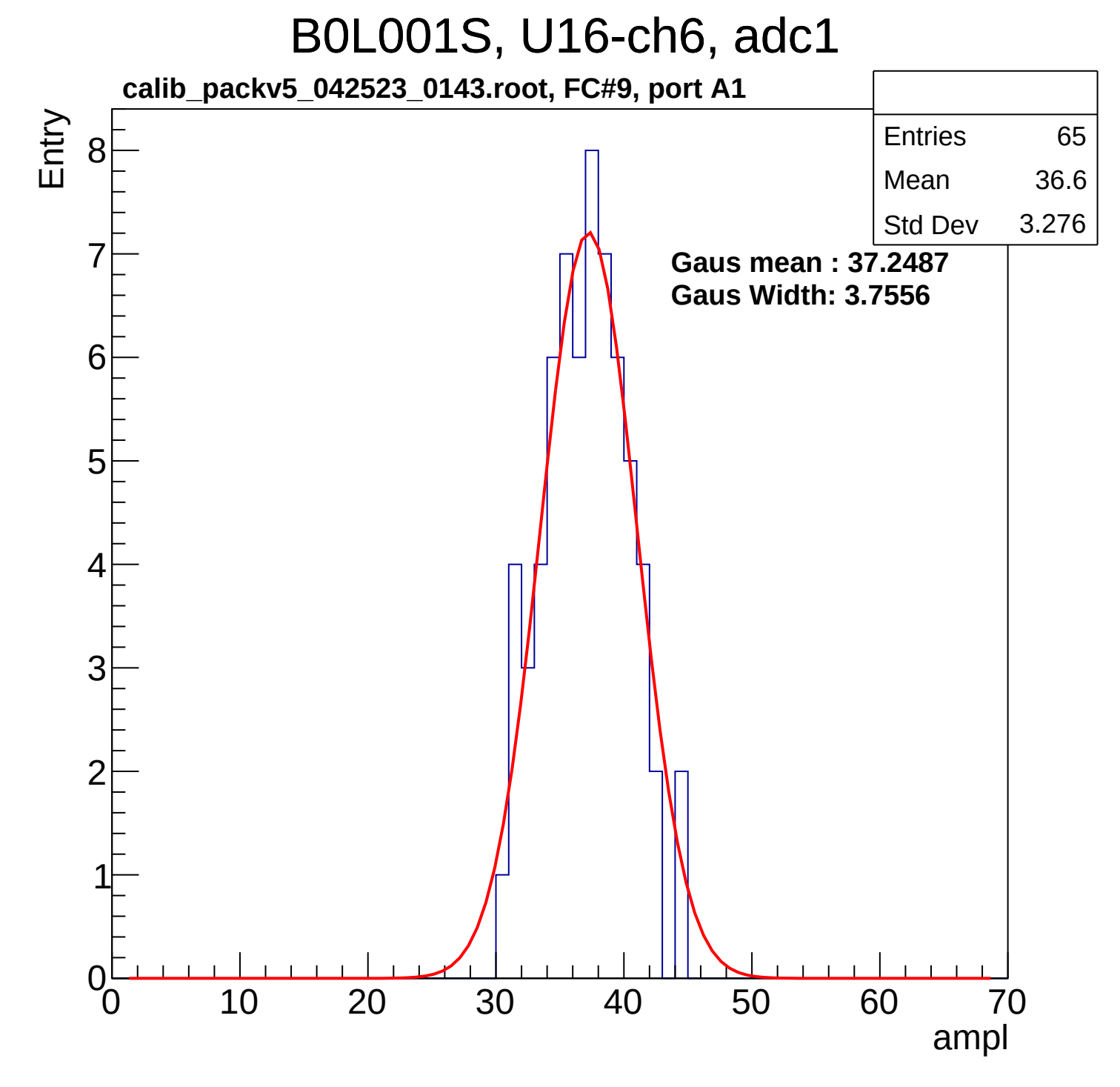
B0L001S, U16-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.6
Std Dev 3.276
Gaus mean : 37.2487
Gaus Width: 3.7556
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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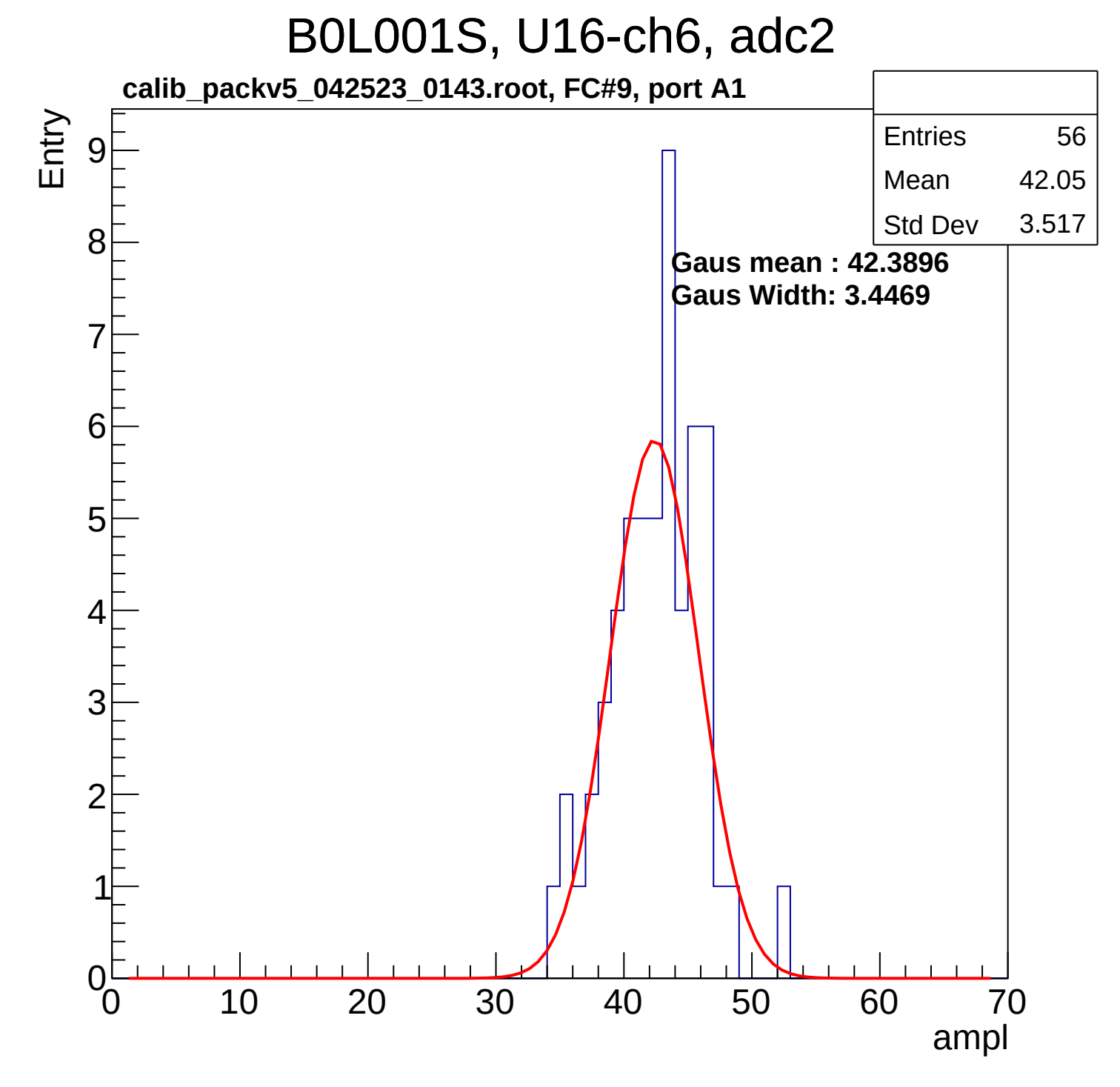
B0L001S, U16-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.05
Std Dev 3.517
Gaus mean : 42.3896
Gaus Width: 3.4469
Entry
ampl
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4
5
6
7
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0
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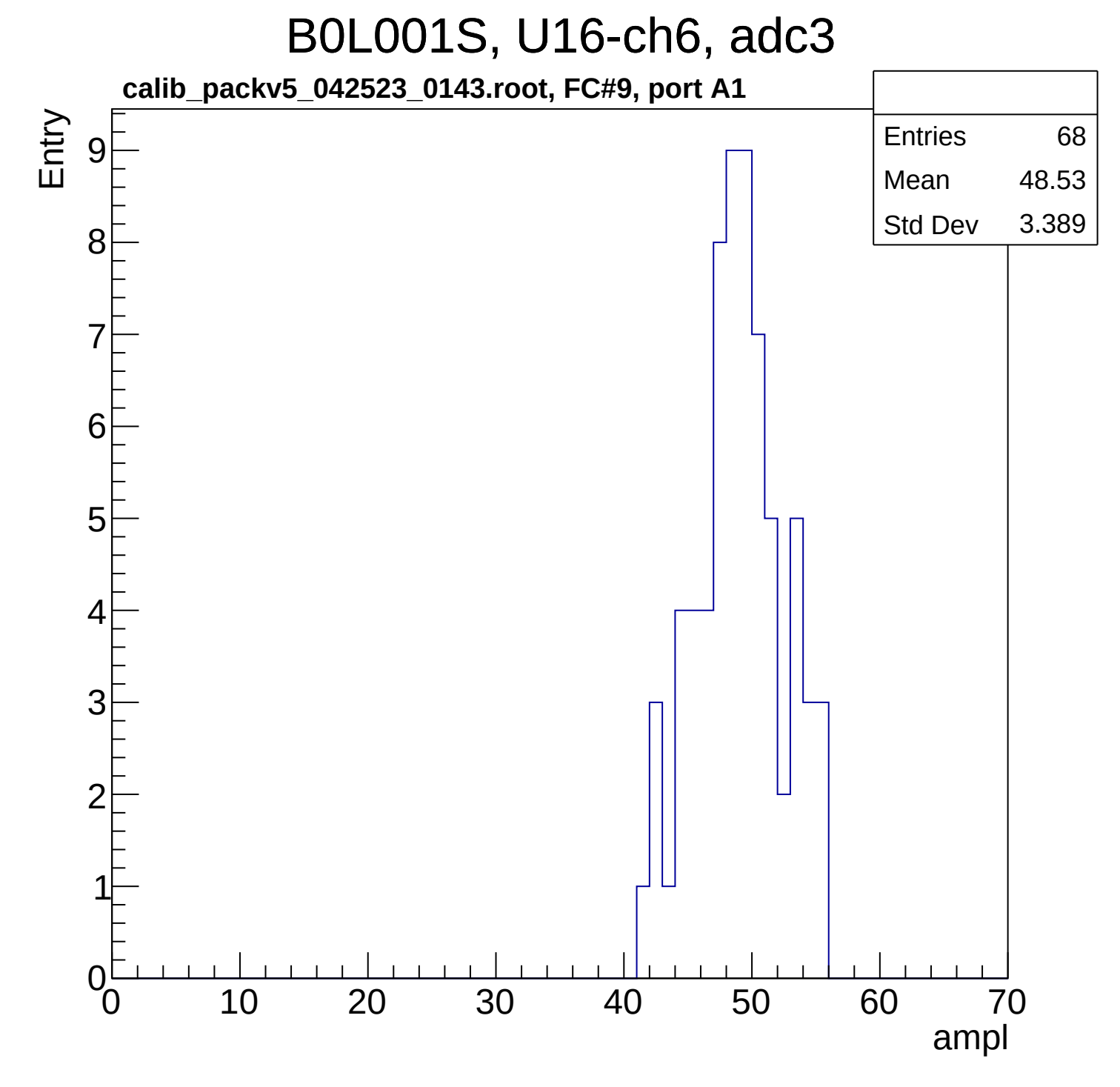
B0L001S, U16-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.53
Std Dev 3.389
Entry
ampl
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7
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0
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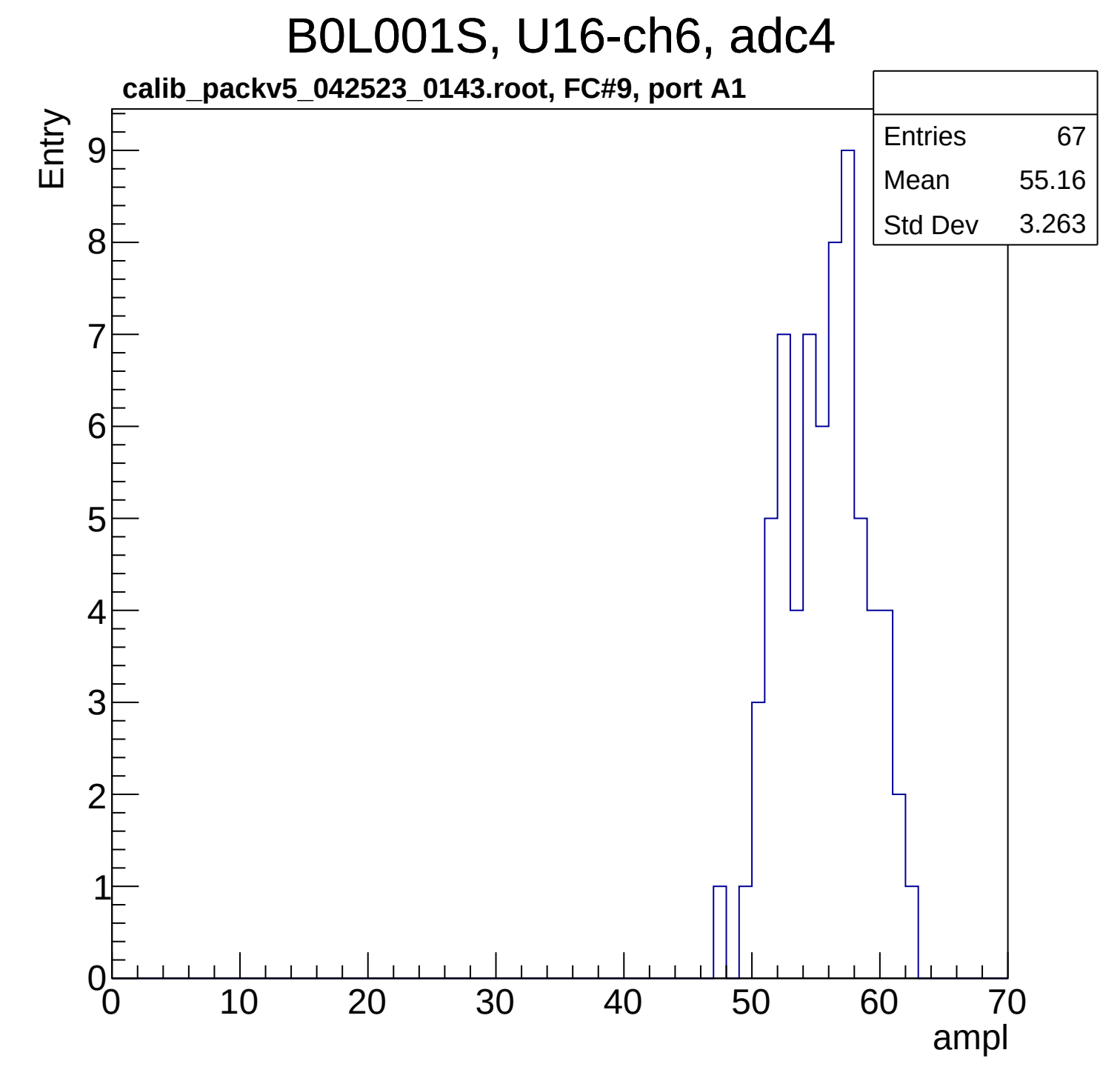
B0L001S, U16-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.16
Std Dev 3.263
Entry
ampl
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7
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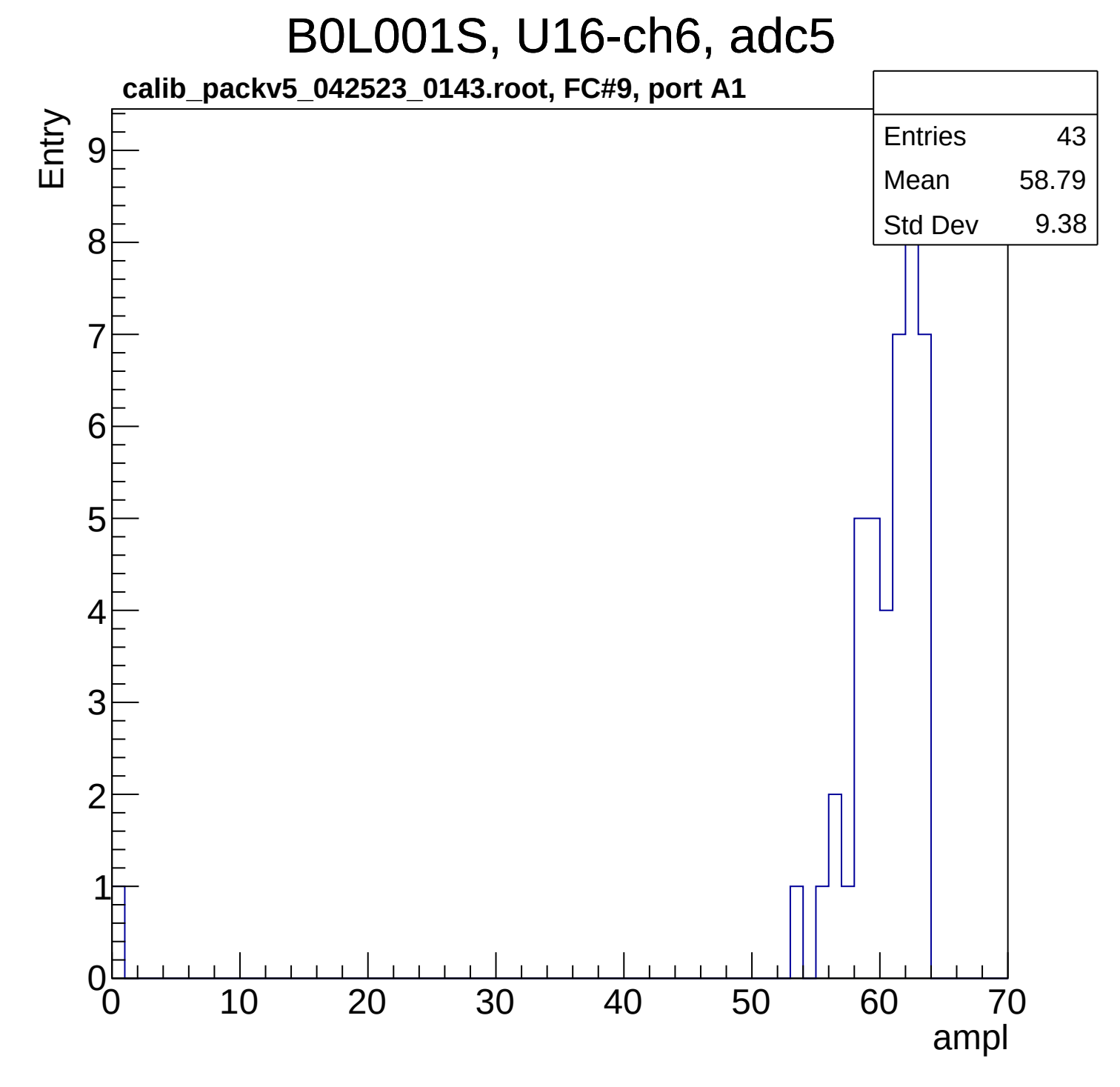
B0L001S, U16-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.79
Std Dev 9.38
Entry
ampl
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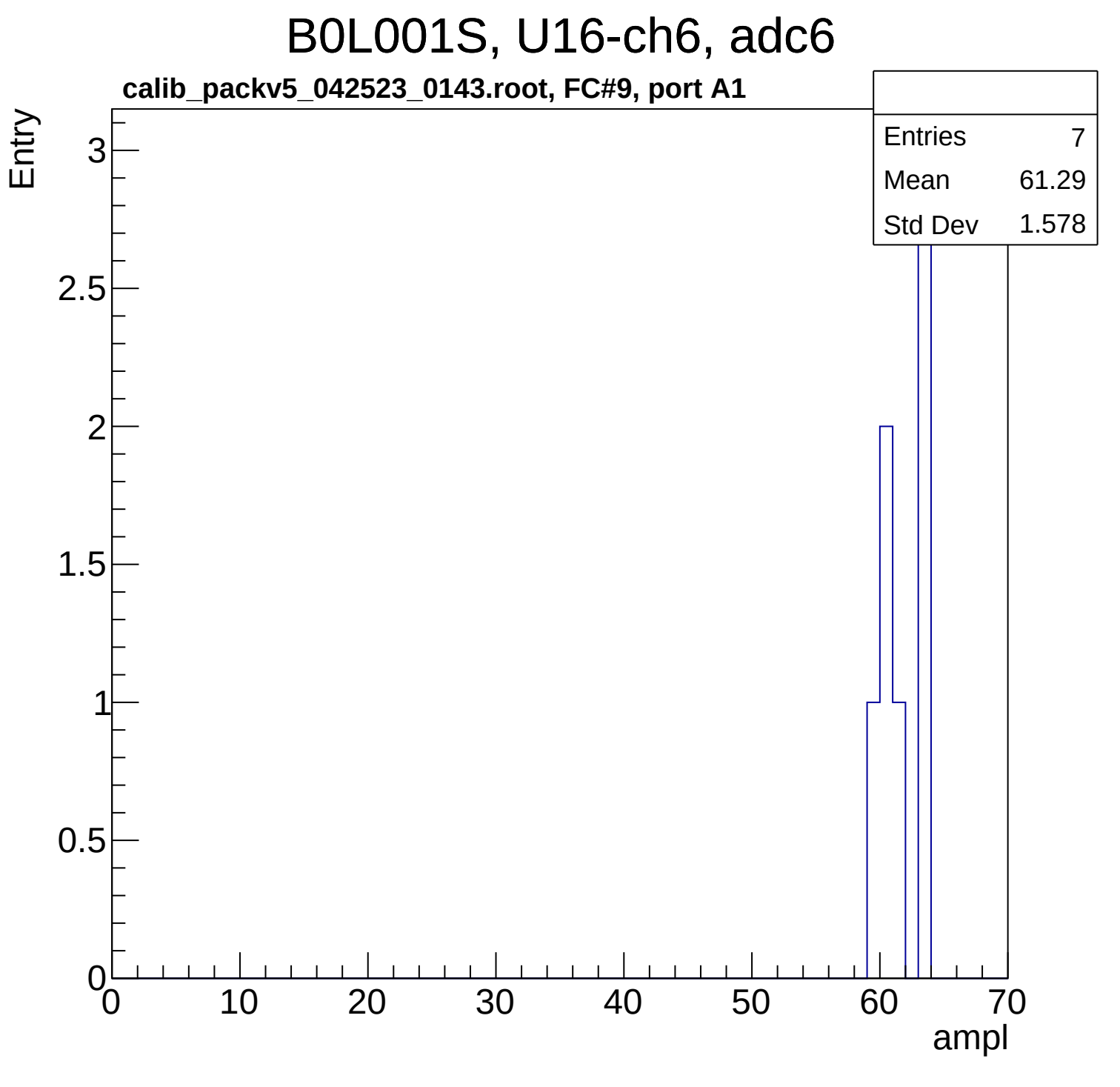
B0L001S, U16-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.29
Std Dev 1.578
Entry
ampl
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0
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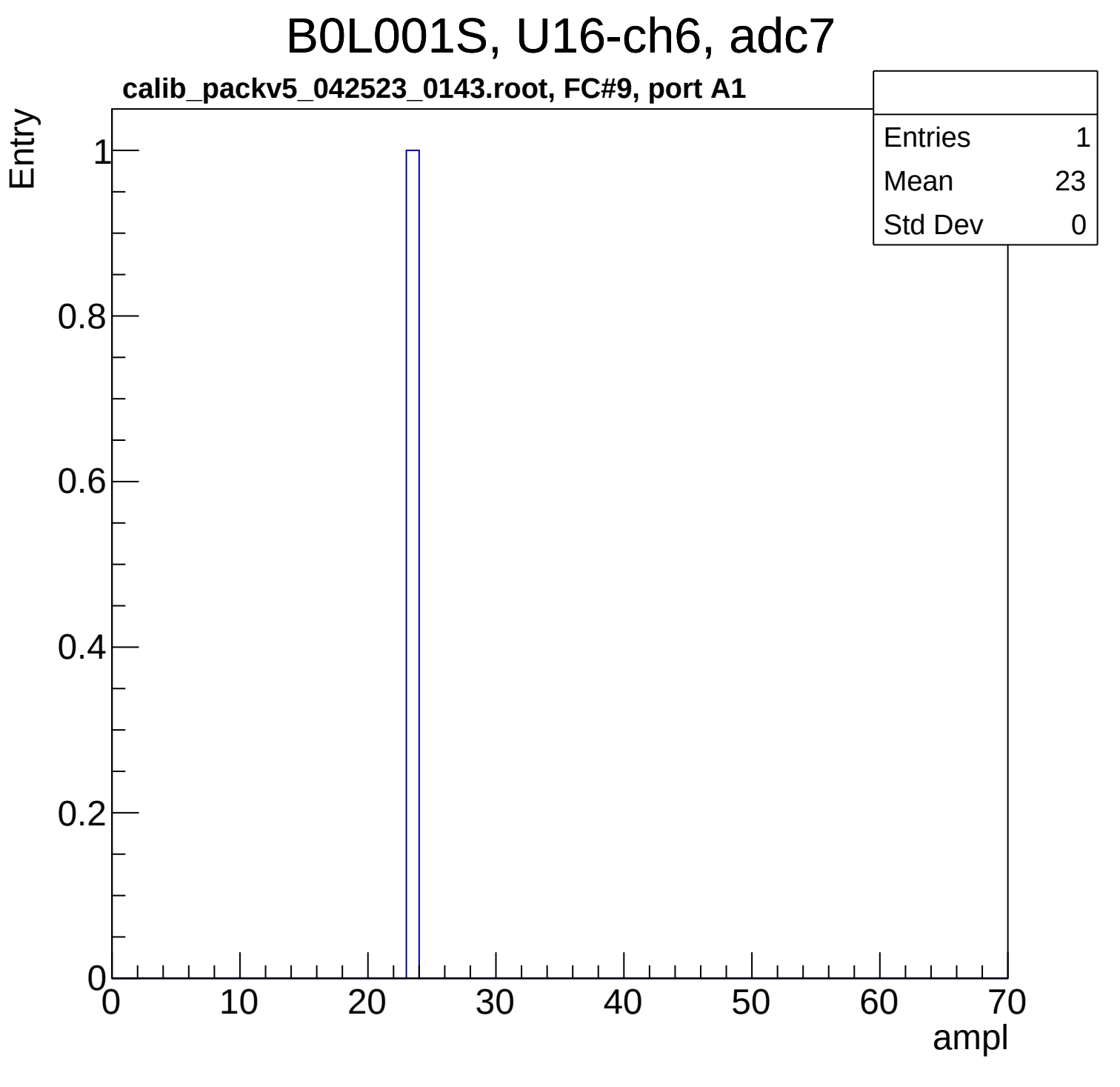
B0L001S, U16-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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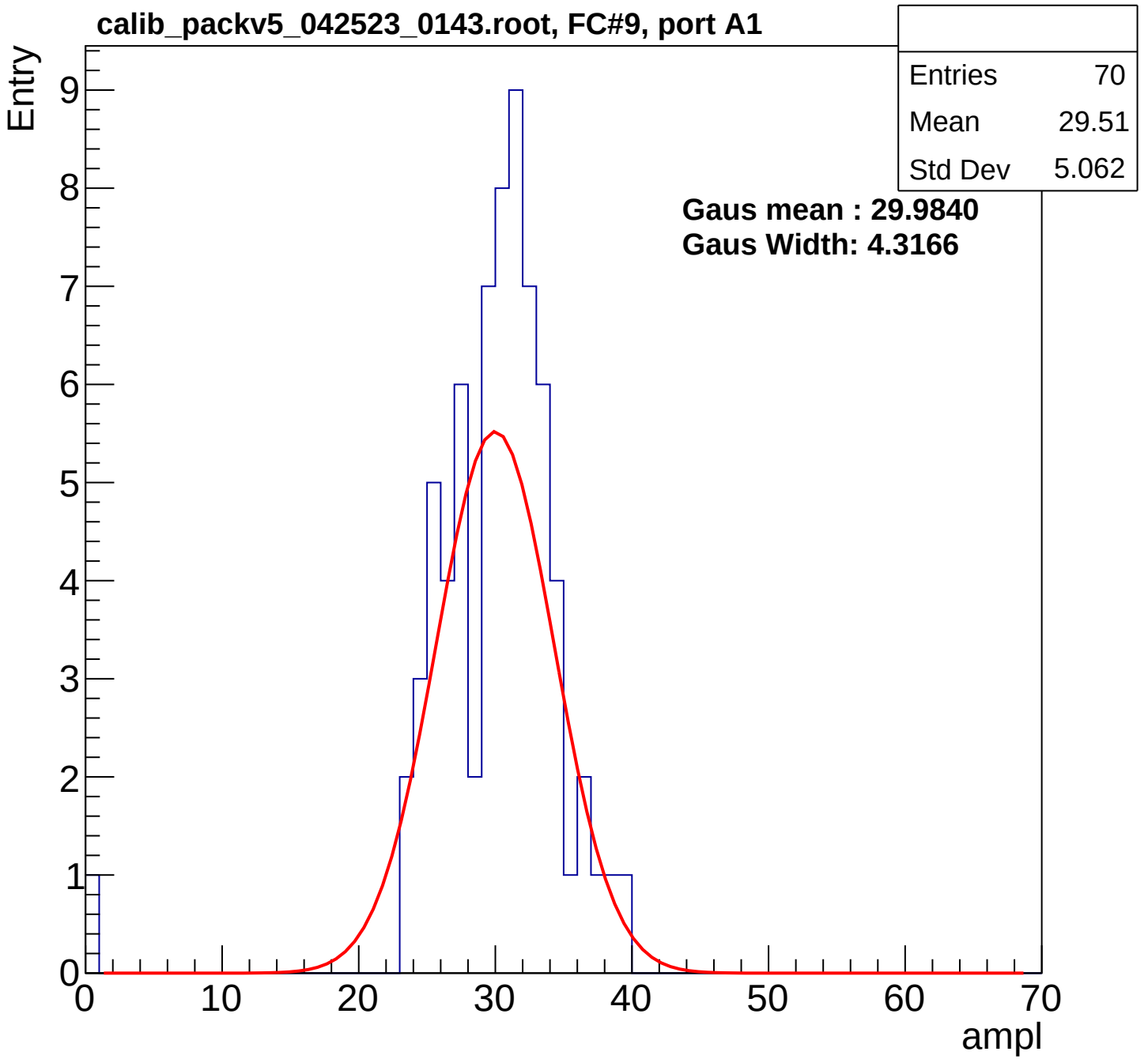
50
60
70
ampl


B0L001S, U16-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.51
Std Dev 5.062
Gaus mean : 29.9840
Gaus Width: 4.3166
Entry
ampl
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2
3
4
5
6
7
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0
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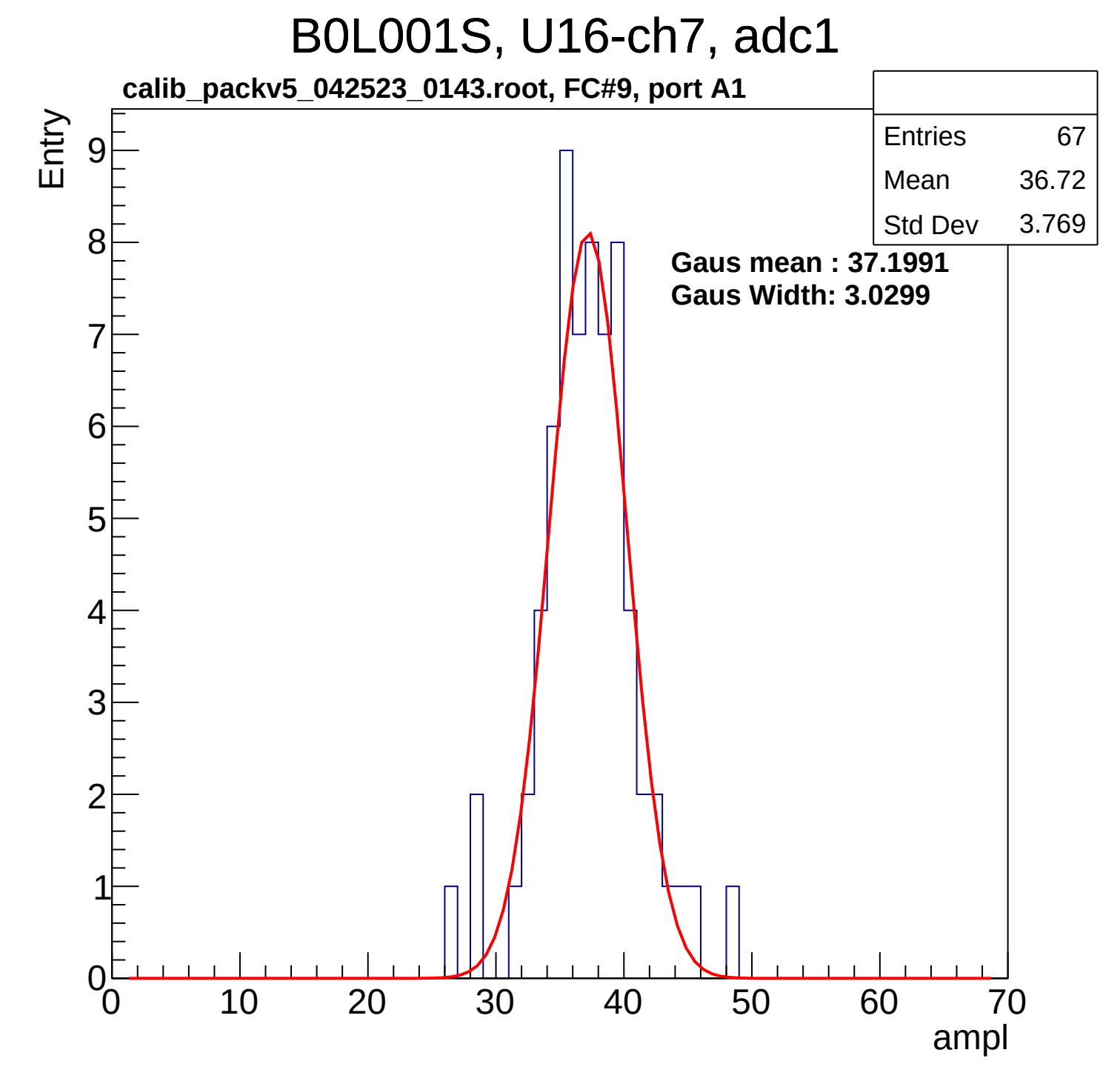
B0L001S, U16-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.72
Std Dev 3.769
Gaus mean : 37.1991
Gaus Width: 3.0299
Entry
ampl
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2
3
4
5
6
7
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0
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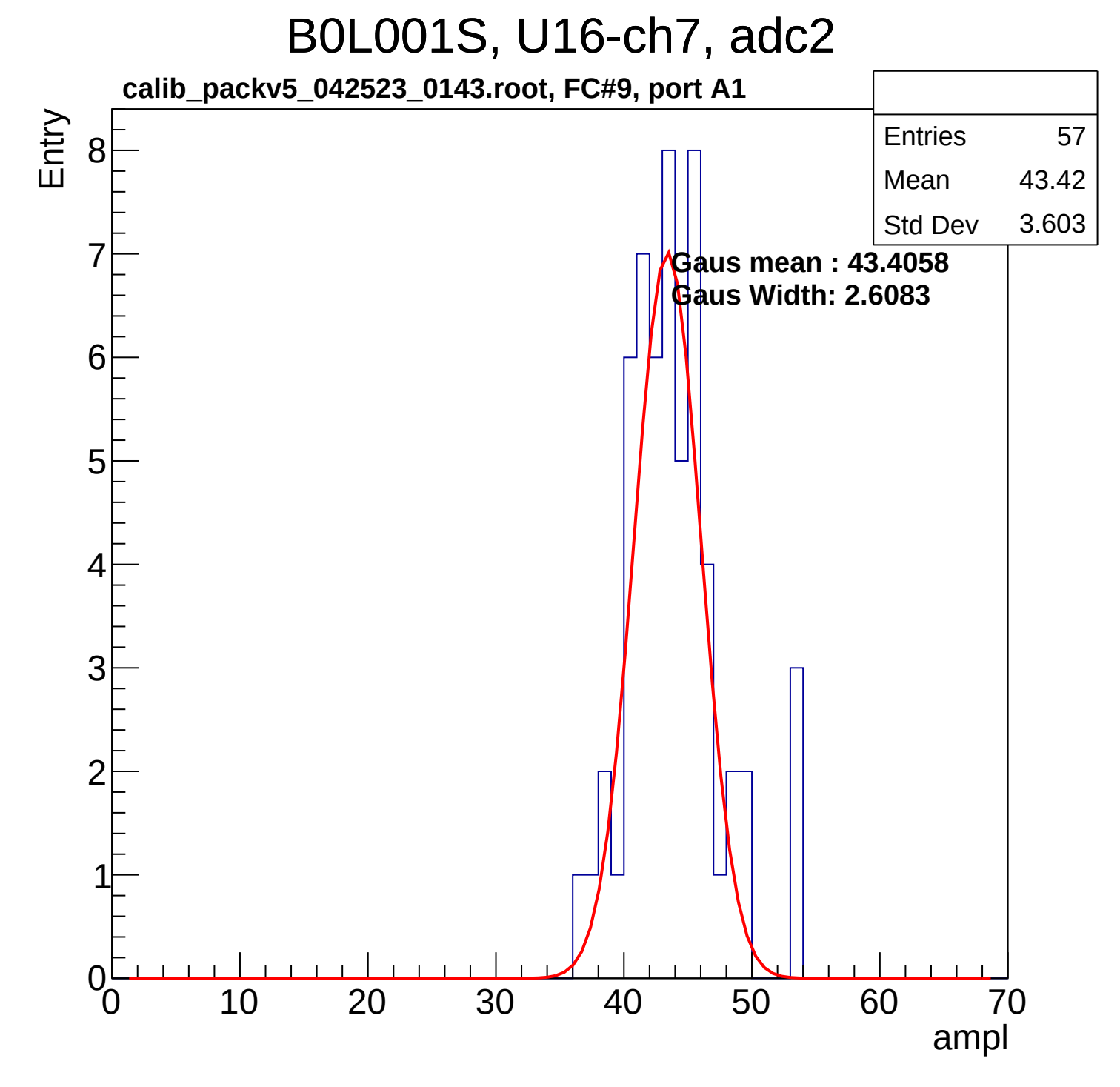
B0L001S, U16-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 43.42
Std Dev 3.603
Gaus mean : 43.4058
Gaus Width: 2.6083
Entry
ampl
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2
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5
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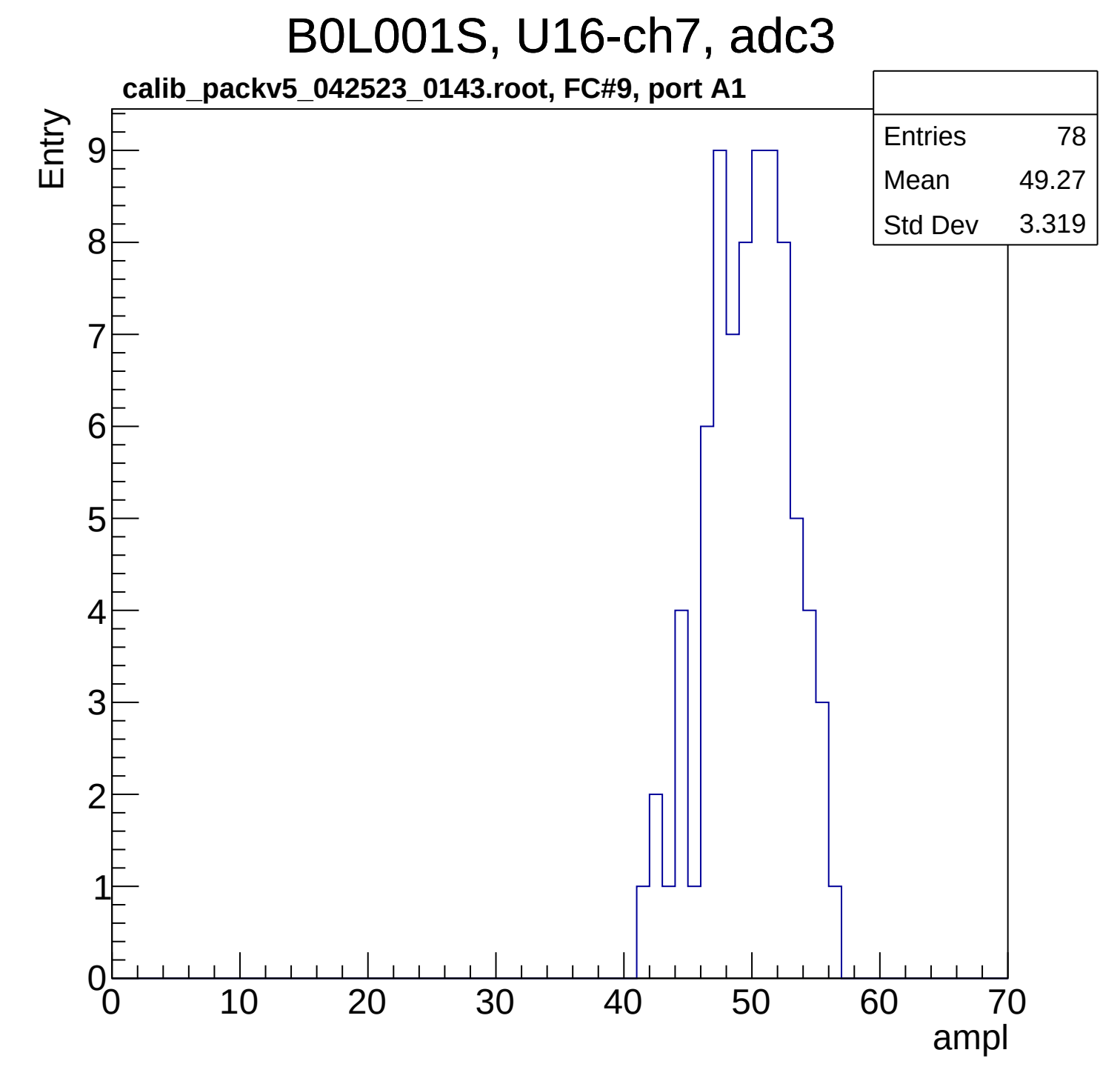
B0L001S, U16-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 49.27
Std Dev 3.319
Entry
ampl
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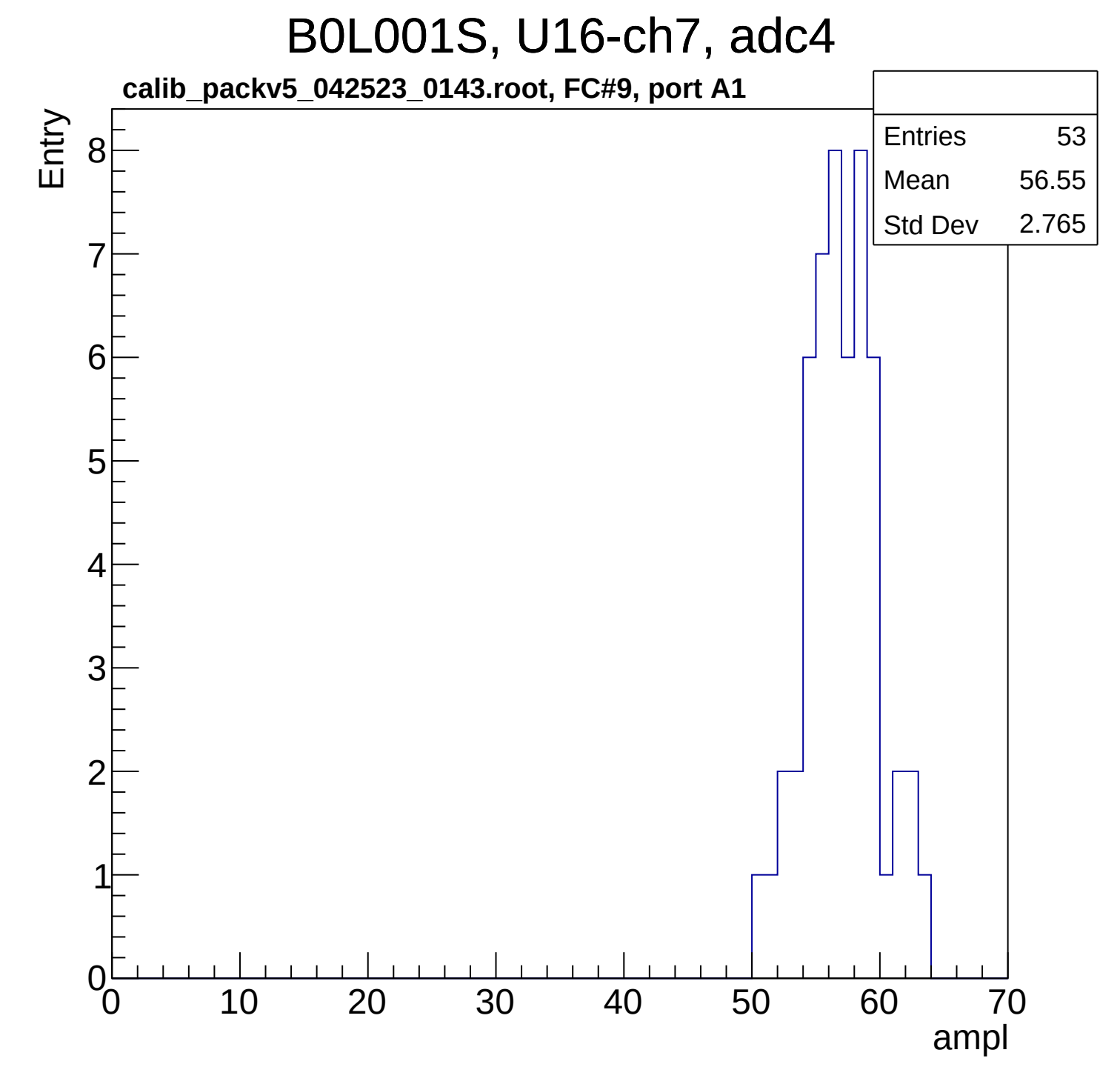
B0L001S, U16-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.55
Std Dev 2.765
Entry
ampl
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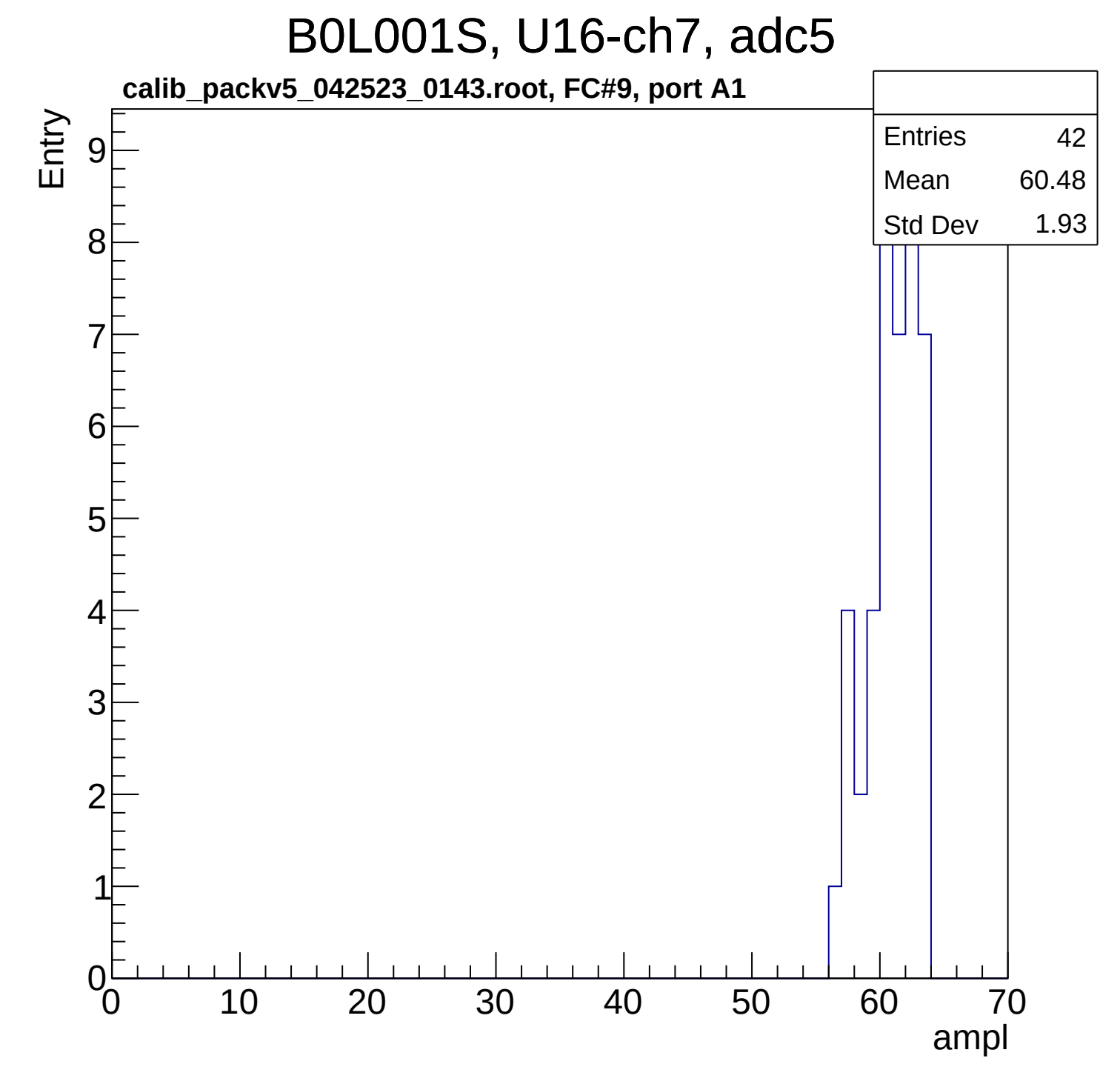
B0L001S, U16-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.48
Std Dev 1.93
Entry
ampl
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2
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5
6
7
8
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60
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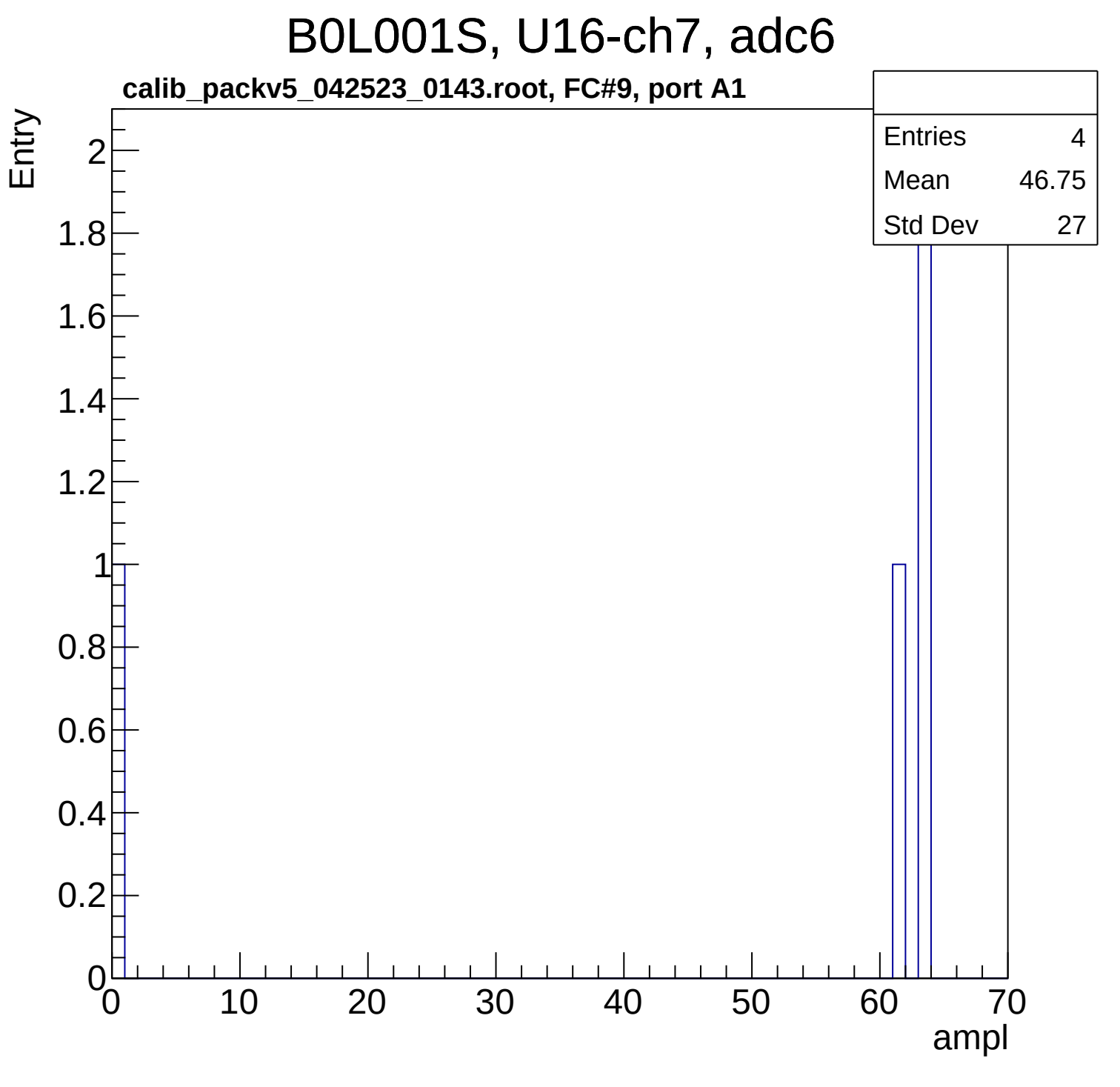
B0L001S, U16-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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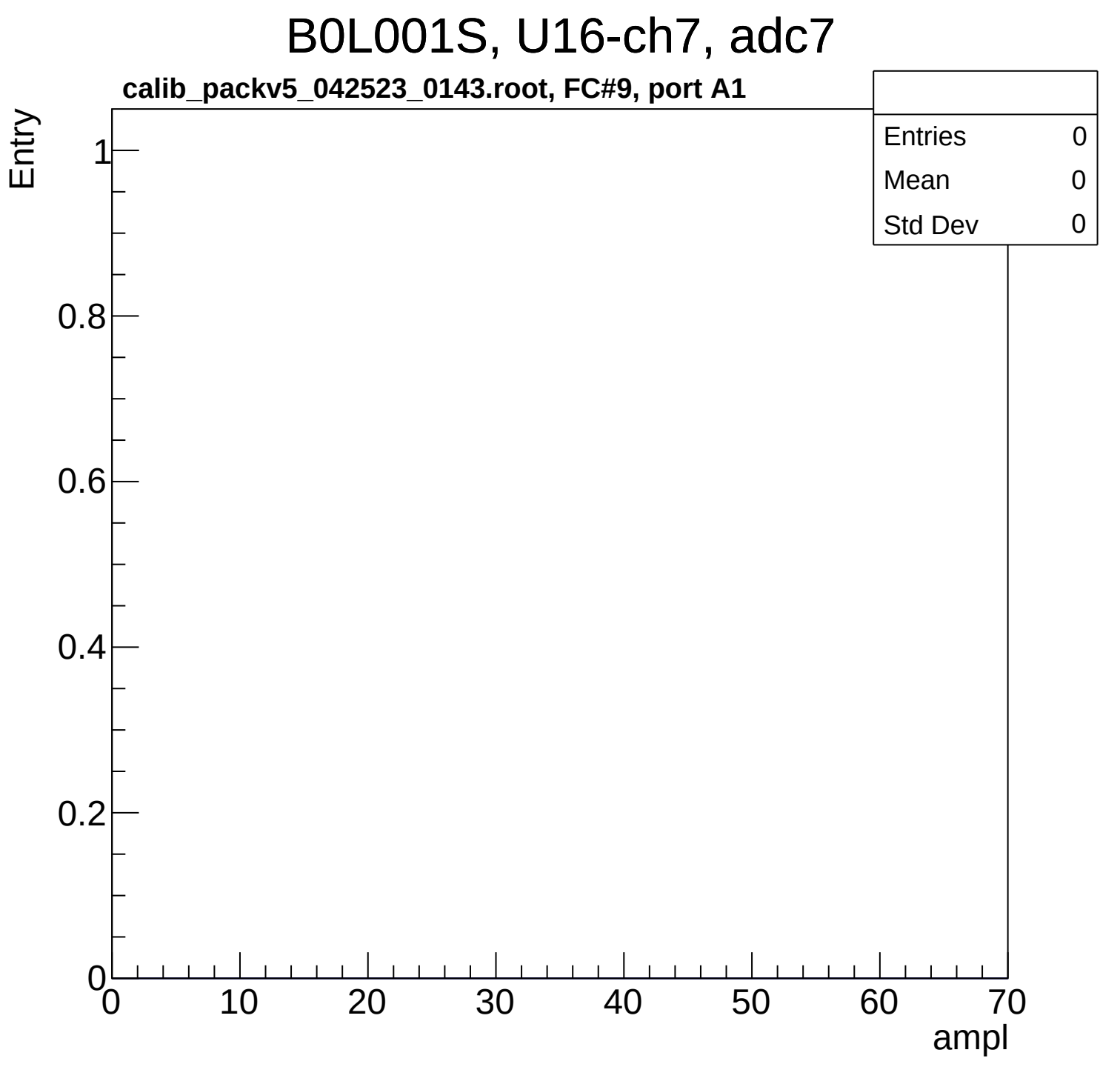
B0L001S, U16-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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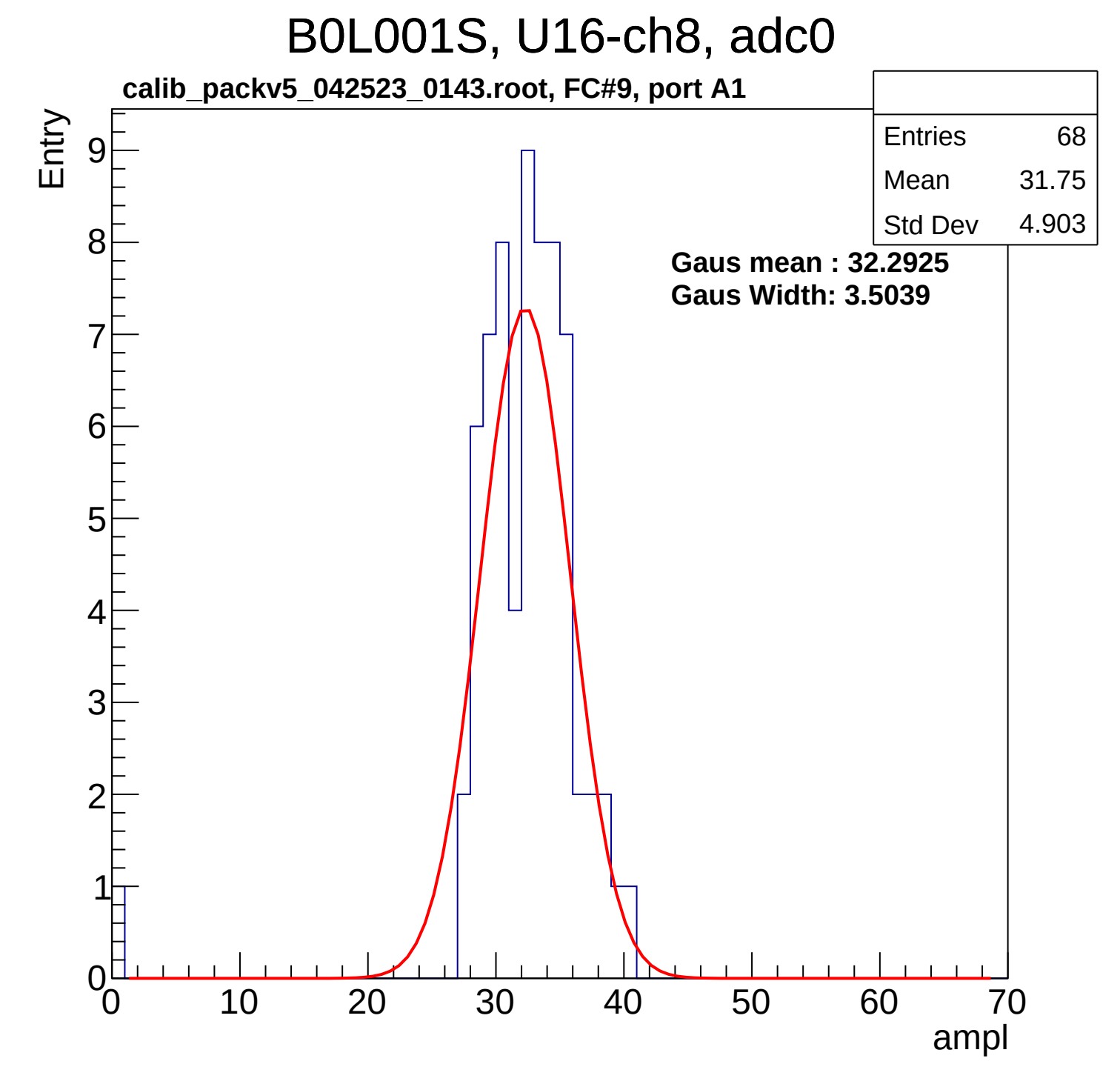
40
50
60
70
ampl


B0L001S, U16-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.75
Std Dev 4.903
Gaus mean : 32.2925
Gaus Width: 3.5039
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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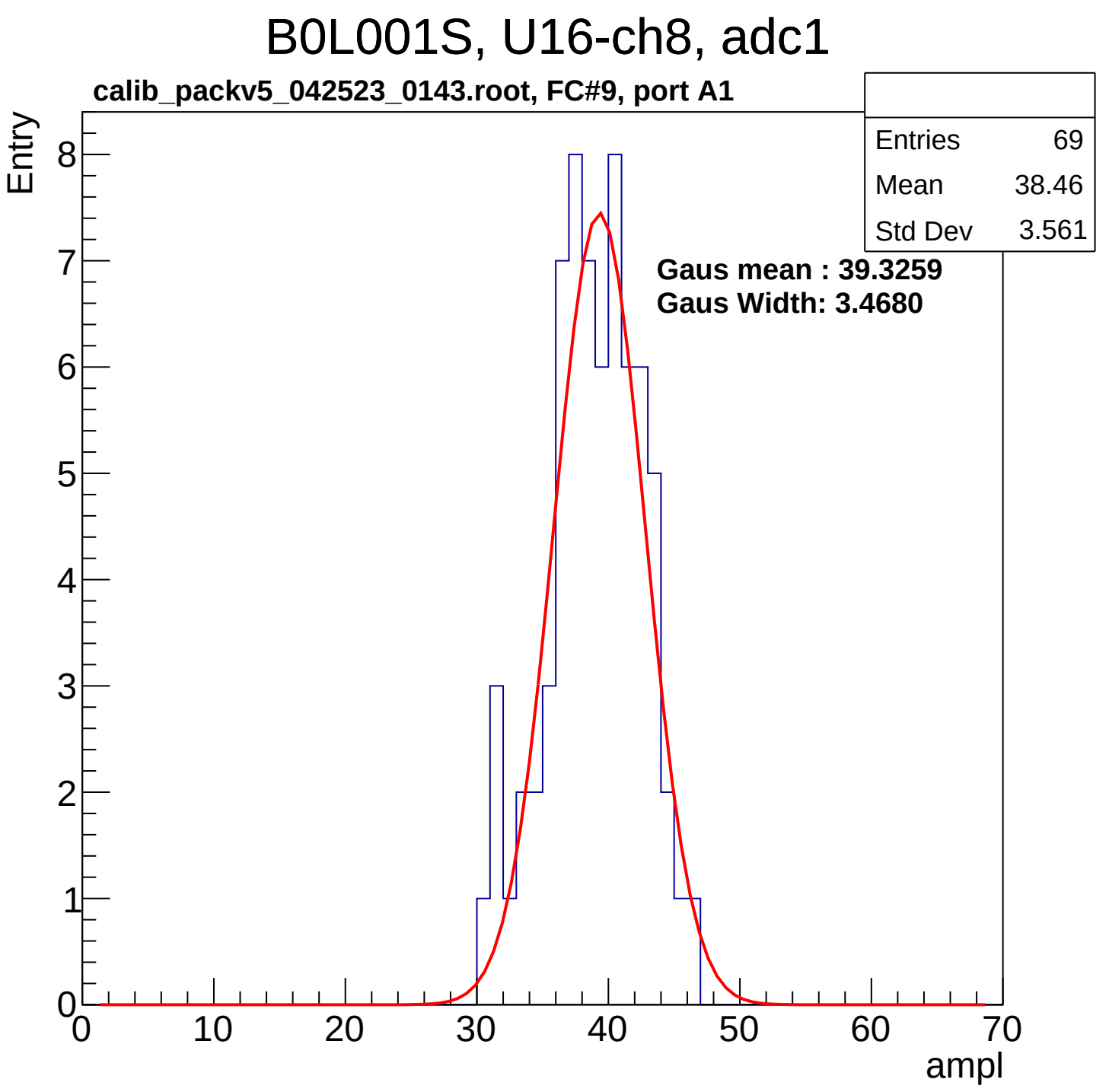
B0L001S, U16-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.46
Std Dev 3.561
Gaus mean : 39.3259
Gaus Width: 3.4680
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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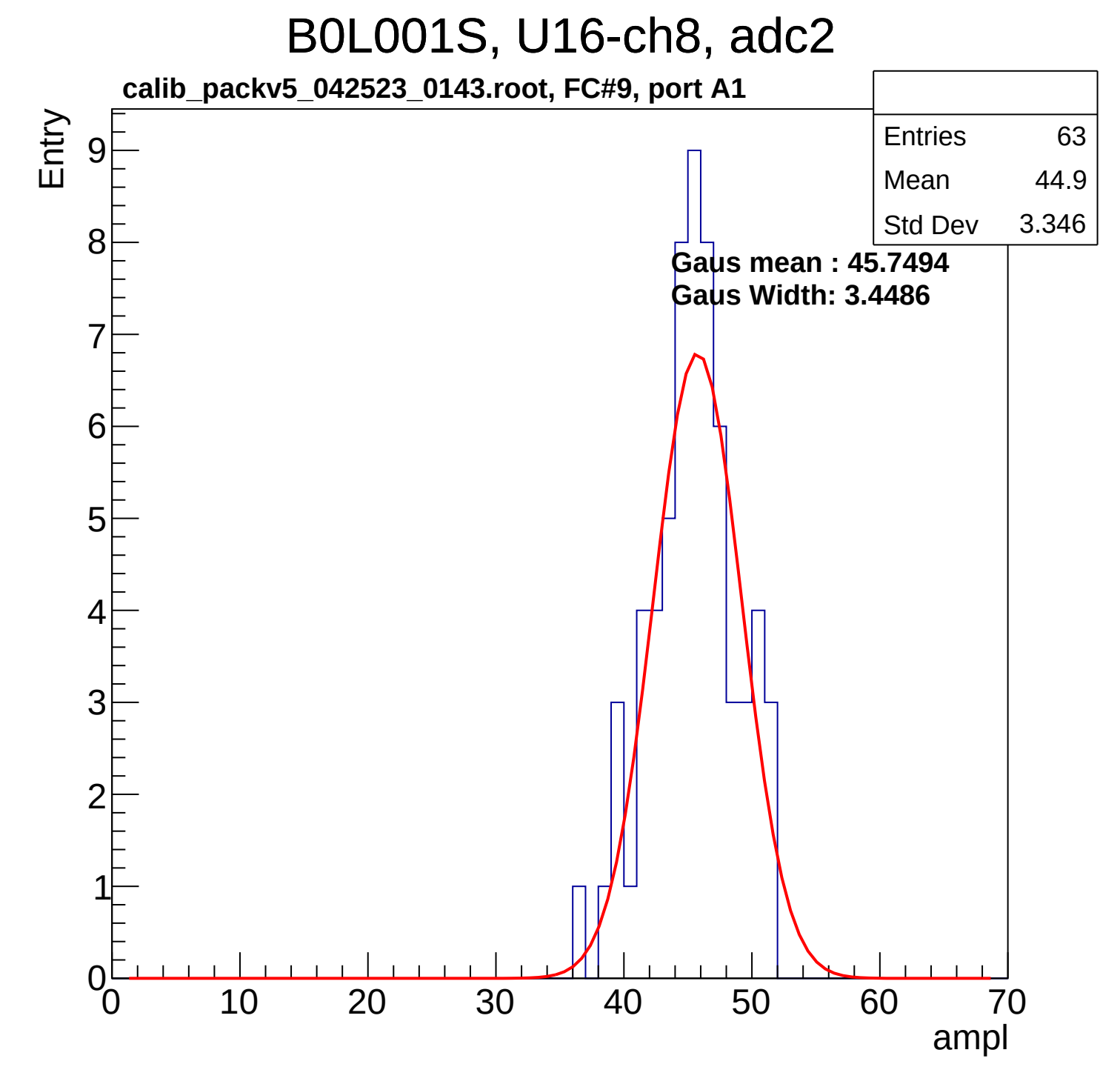
B0L001S, U16-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.9
Std Dev 3.346
Gaus mean : 45.7494
Gaus Width: 3.4486
Entry
ampl
0
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3
4
5
6
7
8
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0
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60
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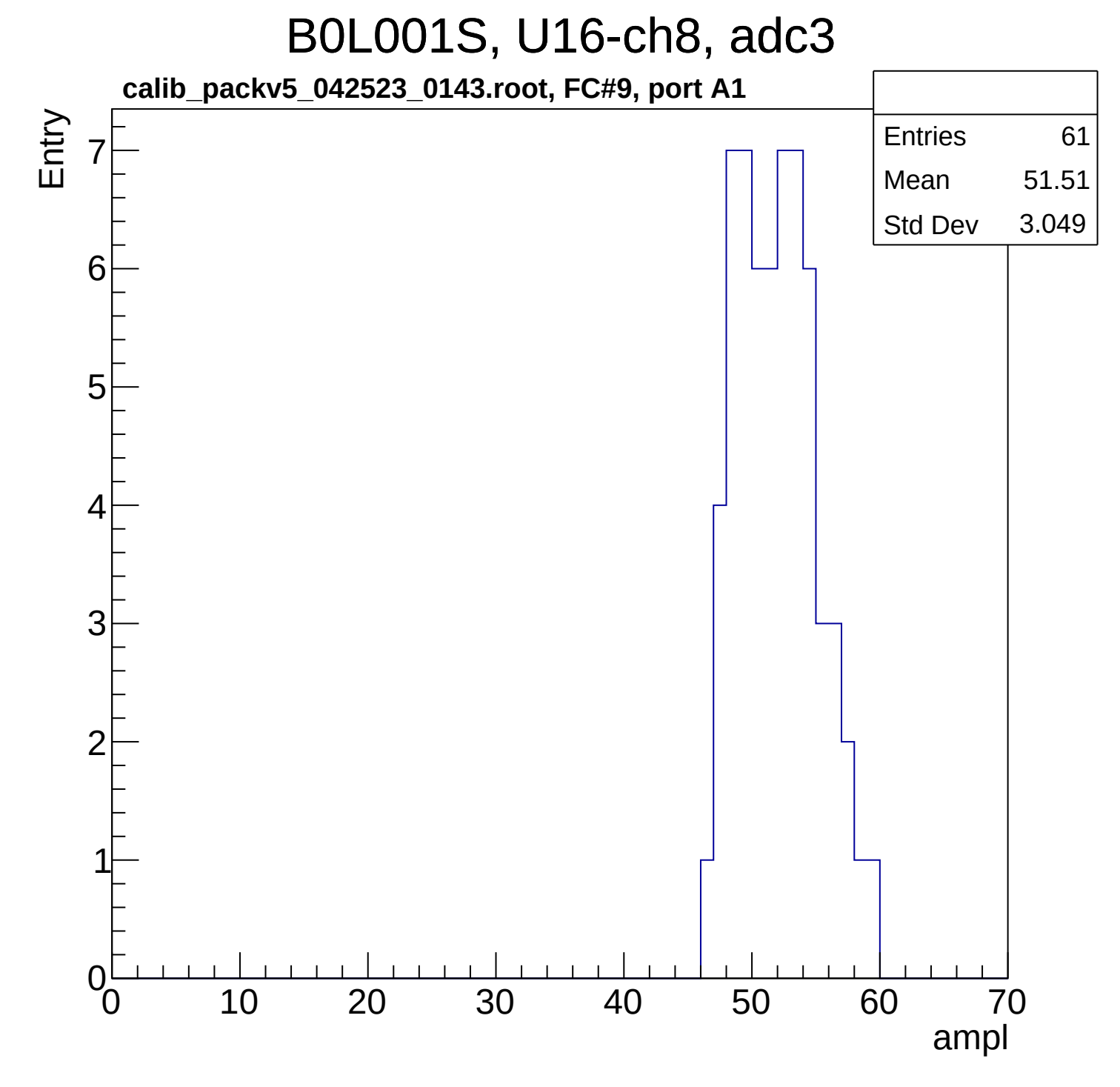
B0L001S, U16-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.51
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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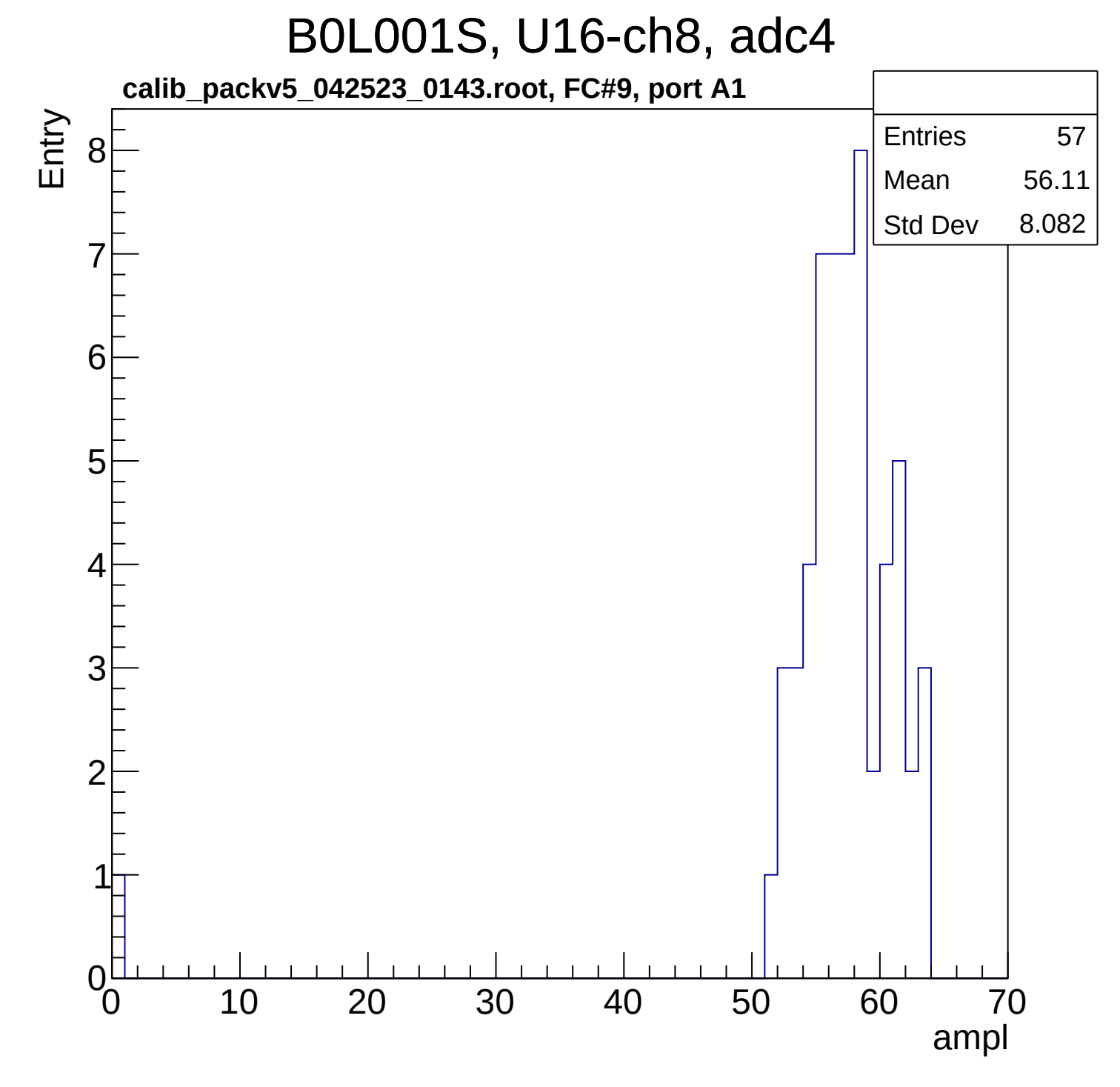
B0L001S, U16-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.11
Std Dev 8.082
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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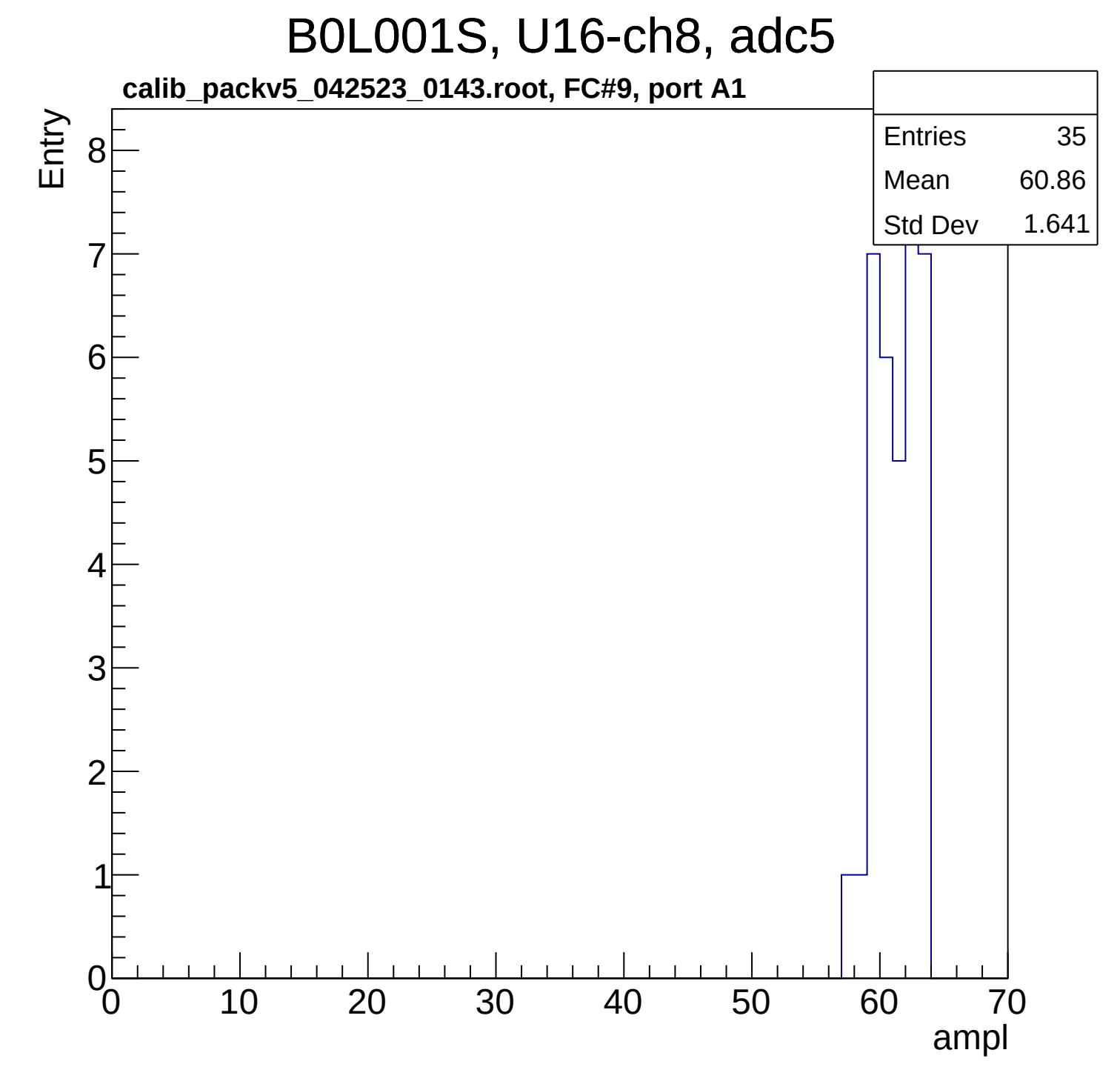
B0L001S, U16-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.86
Std Dev 1.641
Entry
ampl
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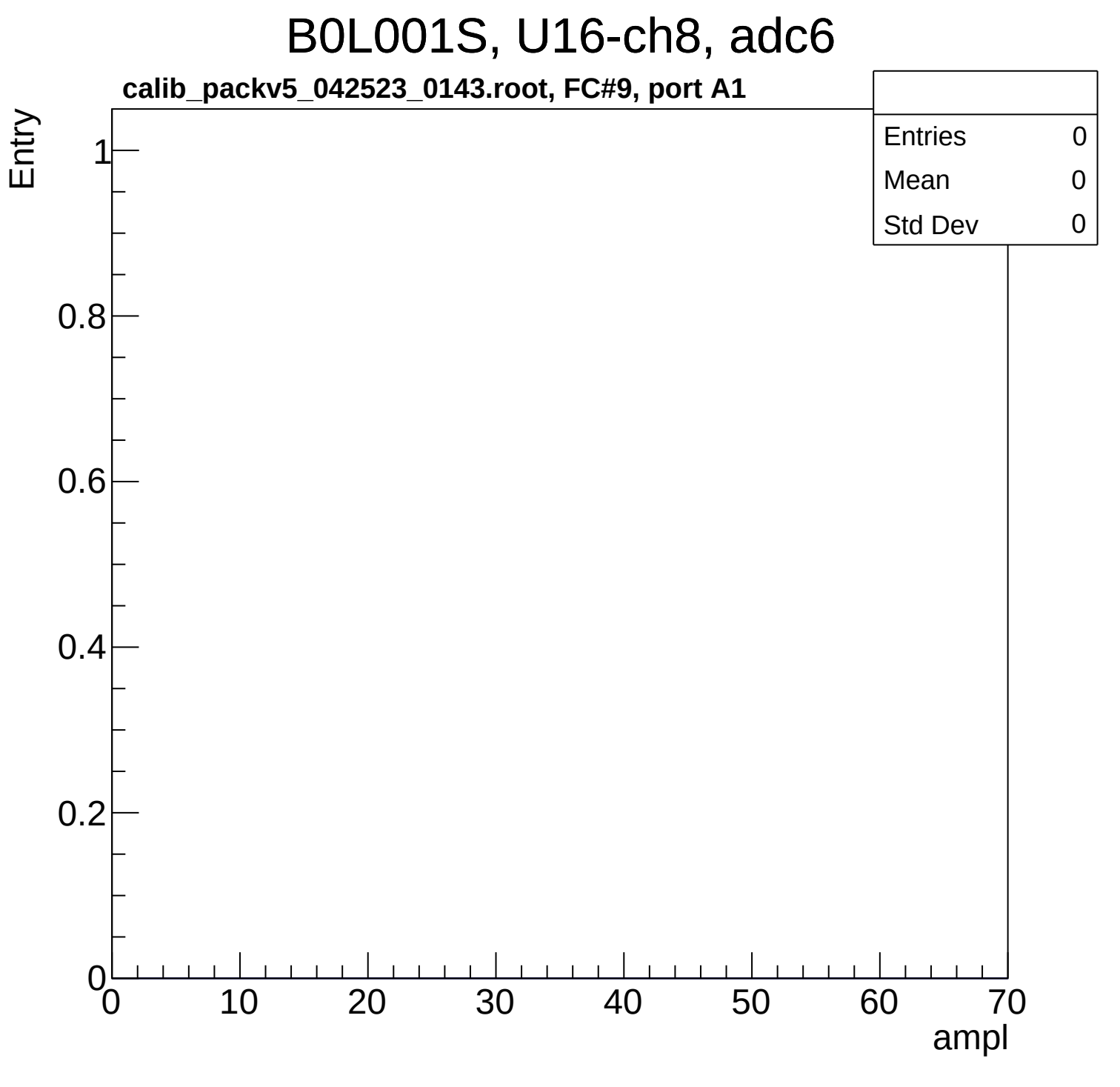
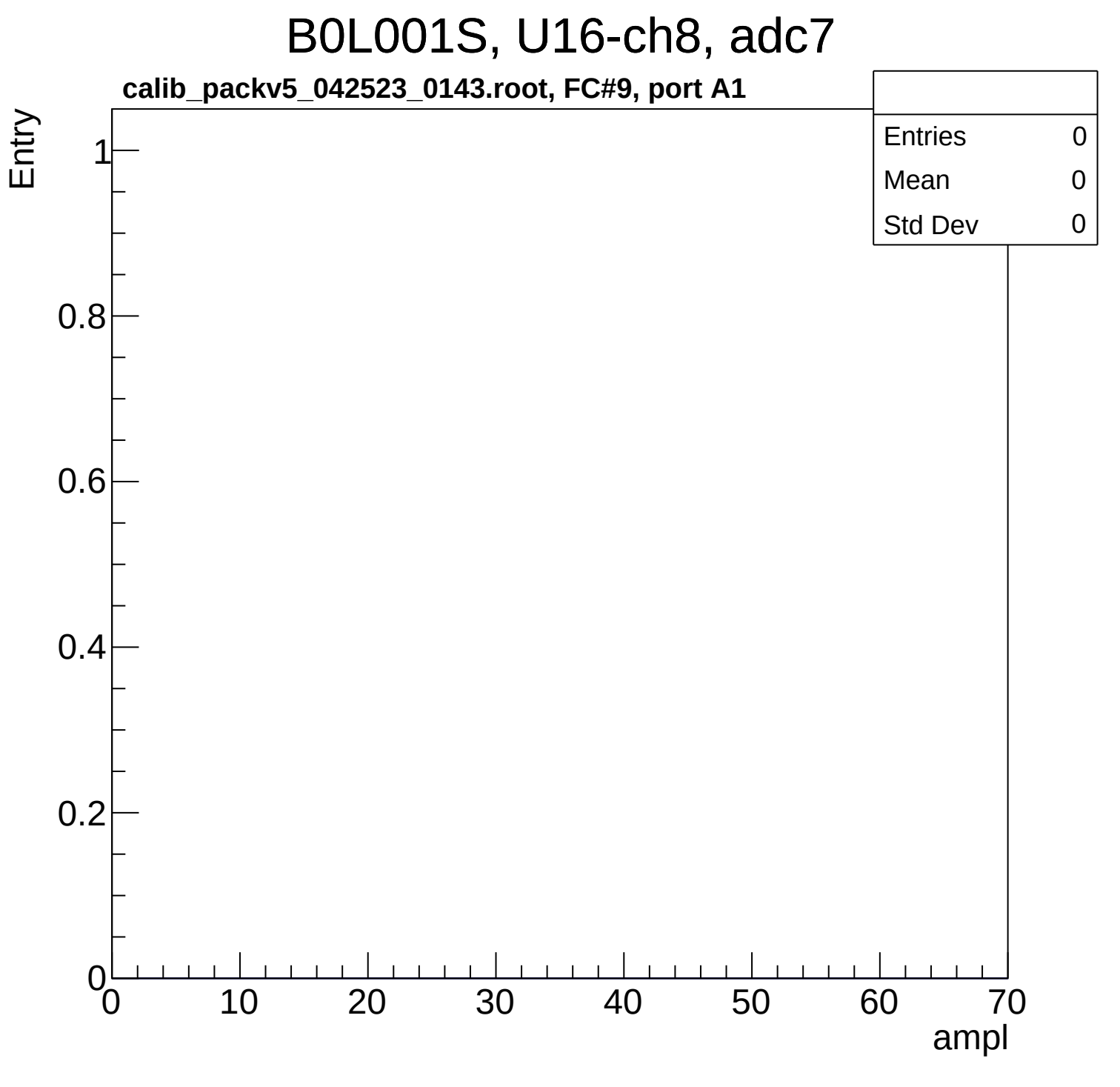
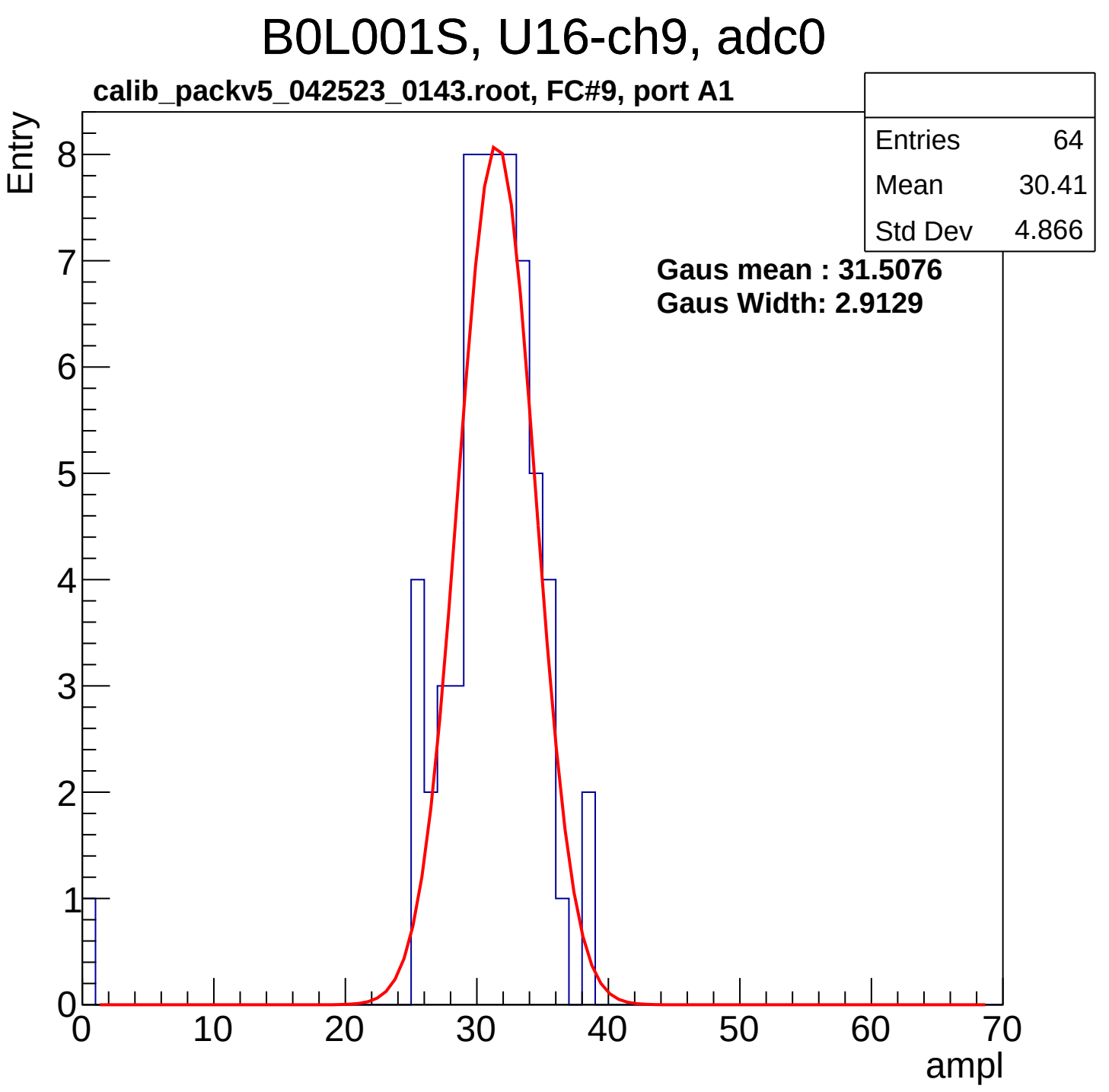
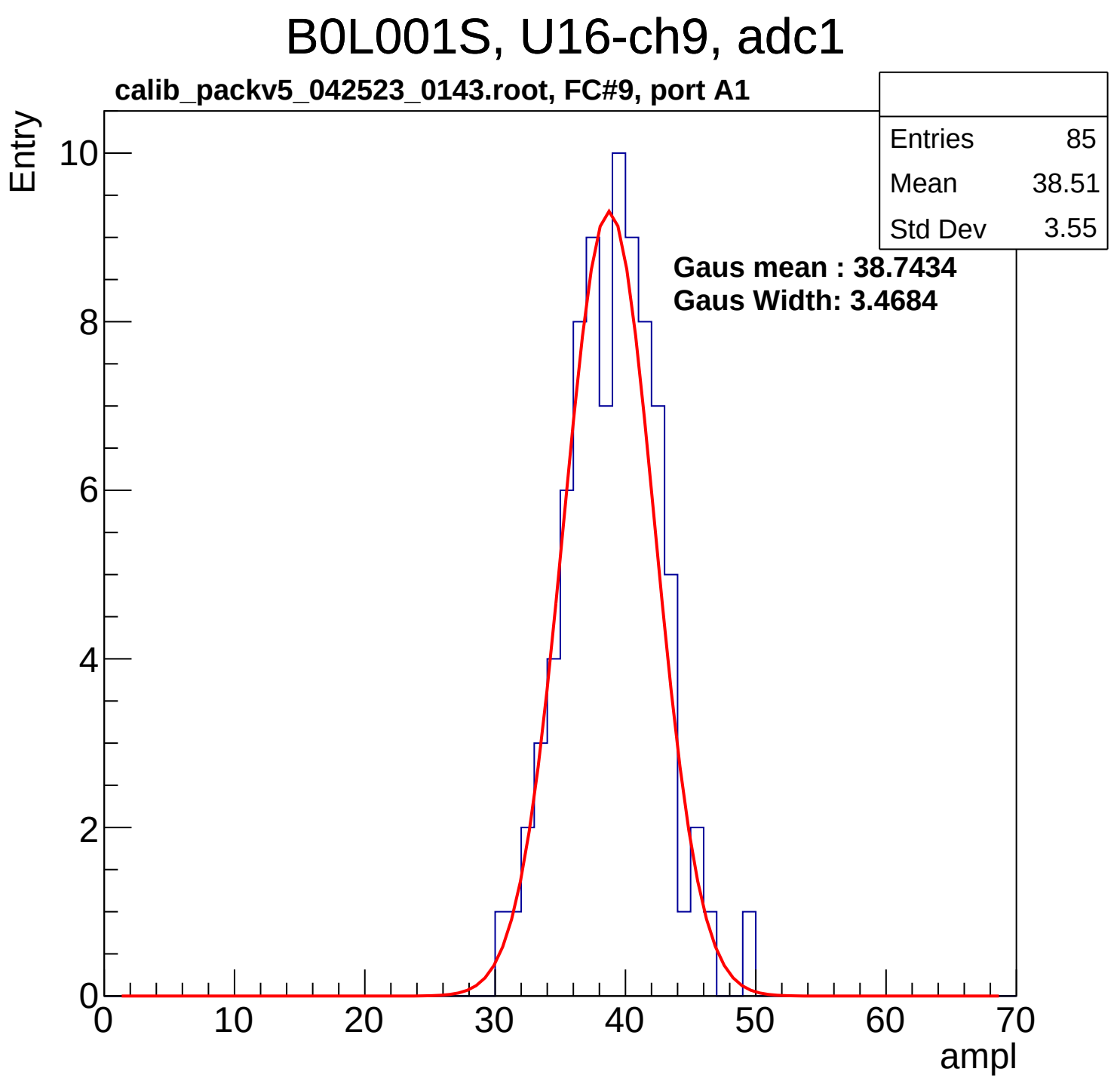
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.41
Std Dev 4.866
Gaus mean : 31.5076
Gaus Width: 2.9129
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 38.51
Std Dev 3.55
Gaus mean : 38.7434
Gaus Width: 3.4684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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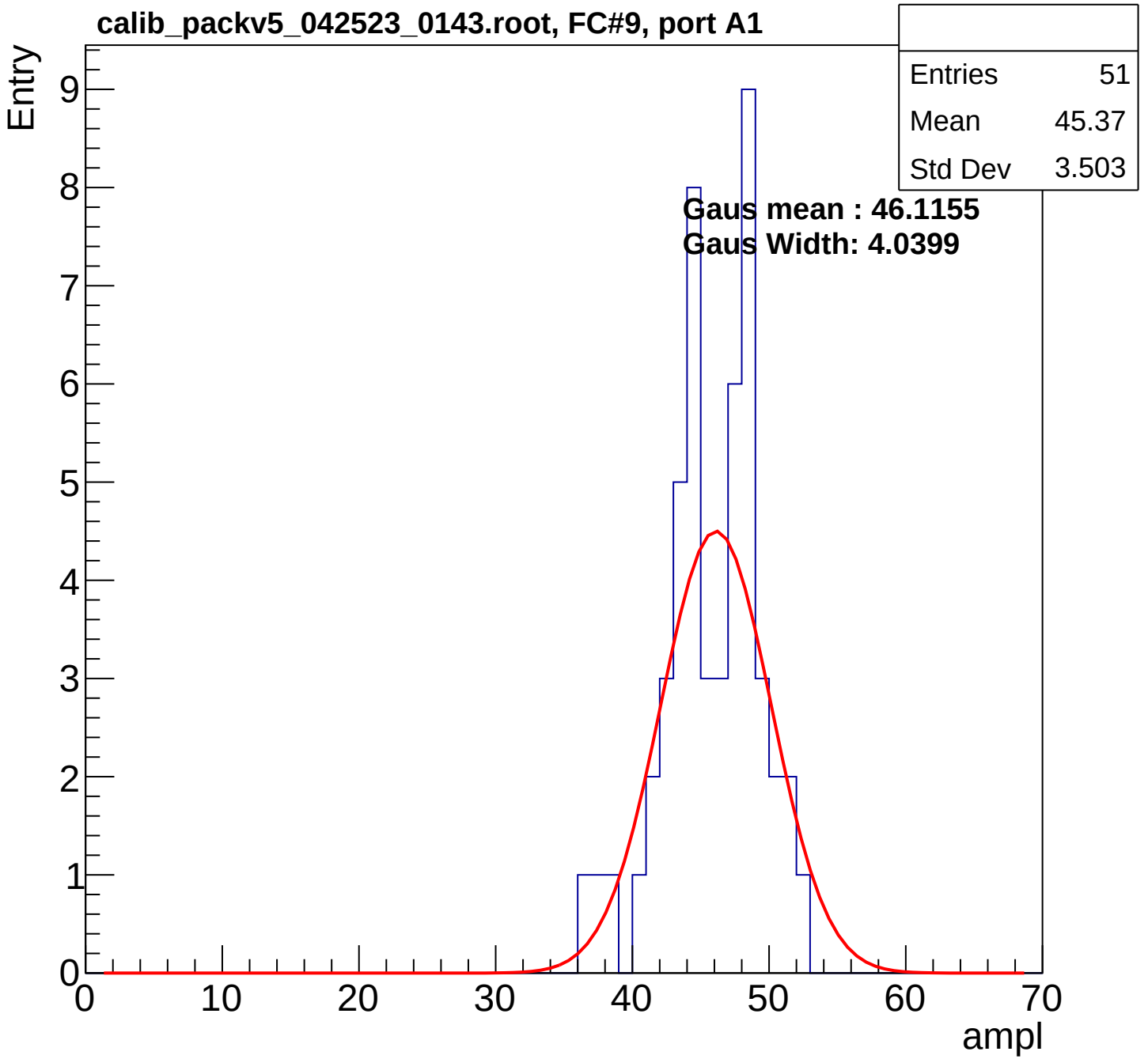
B0L001S, U16-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 45.37
Std Dev 3.503
Gaus mean : 46.1155
Gaus Width: 4.0399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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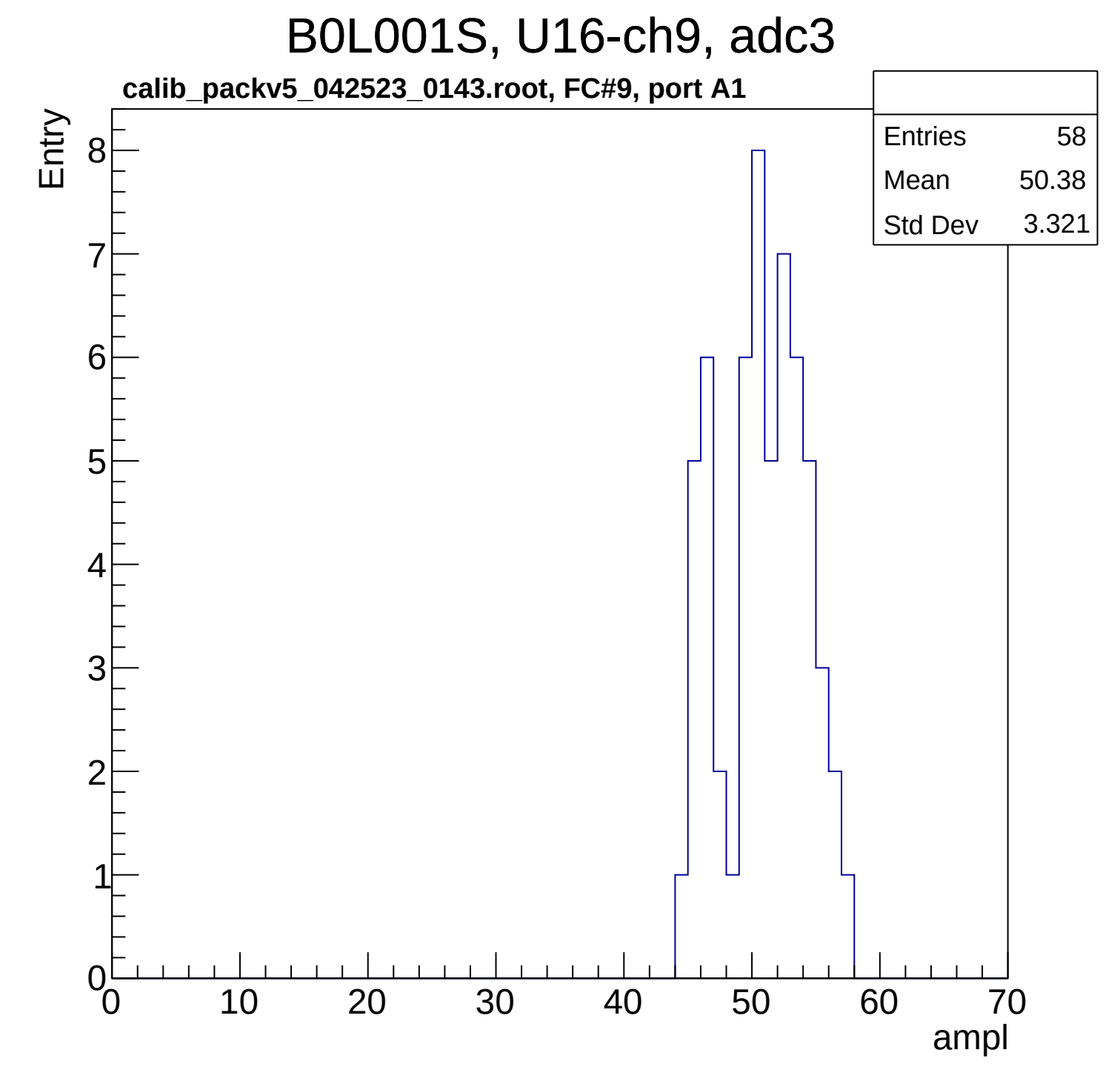
B0L001S, U16-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.38
Std Dev 3.321
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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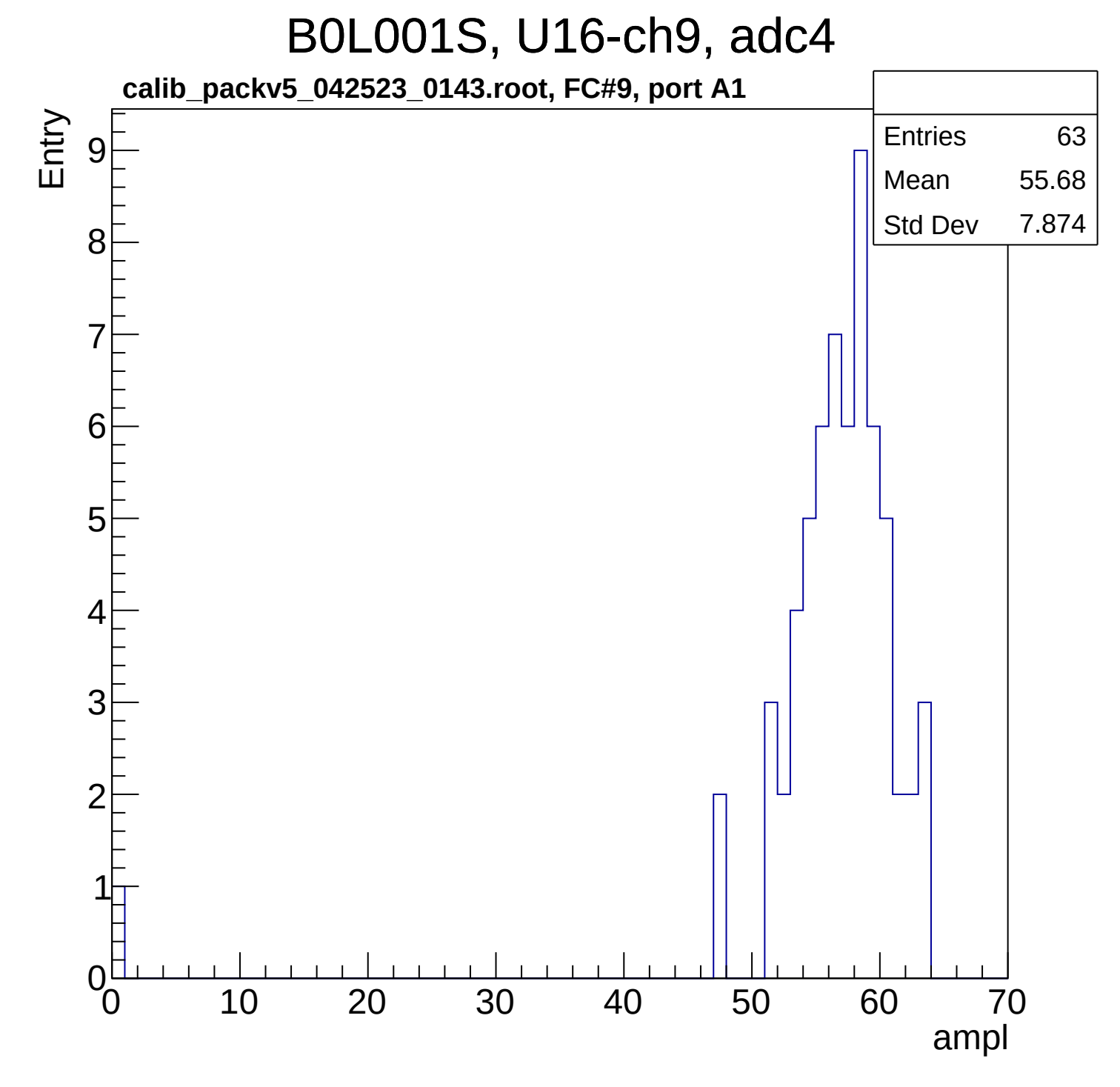
B0L001S, U16-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.68
Std Dev 7.874
Entry
ampl
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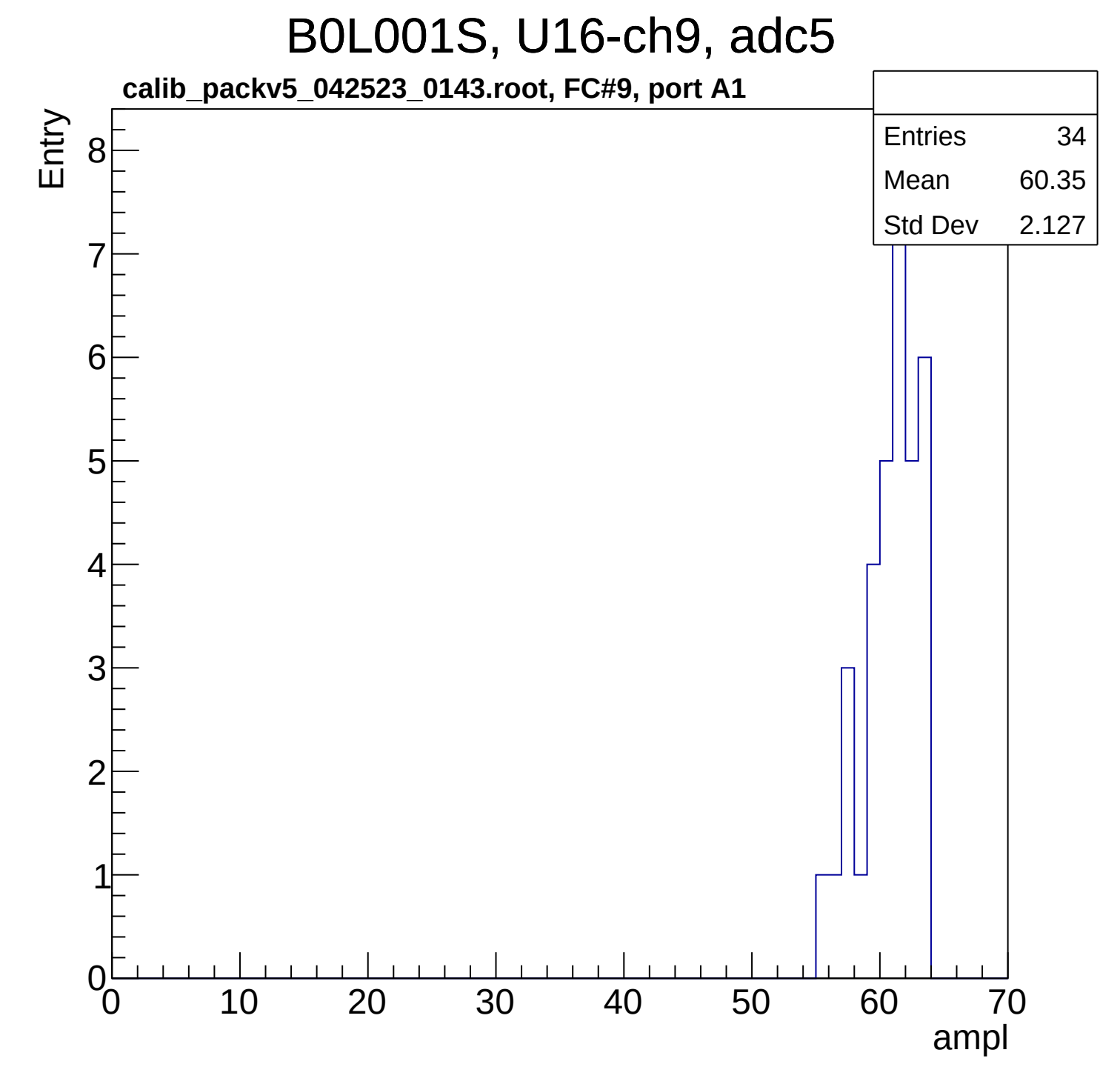
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.35
Std Dev 2.127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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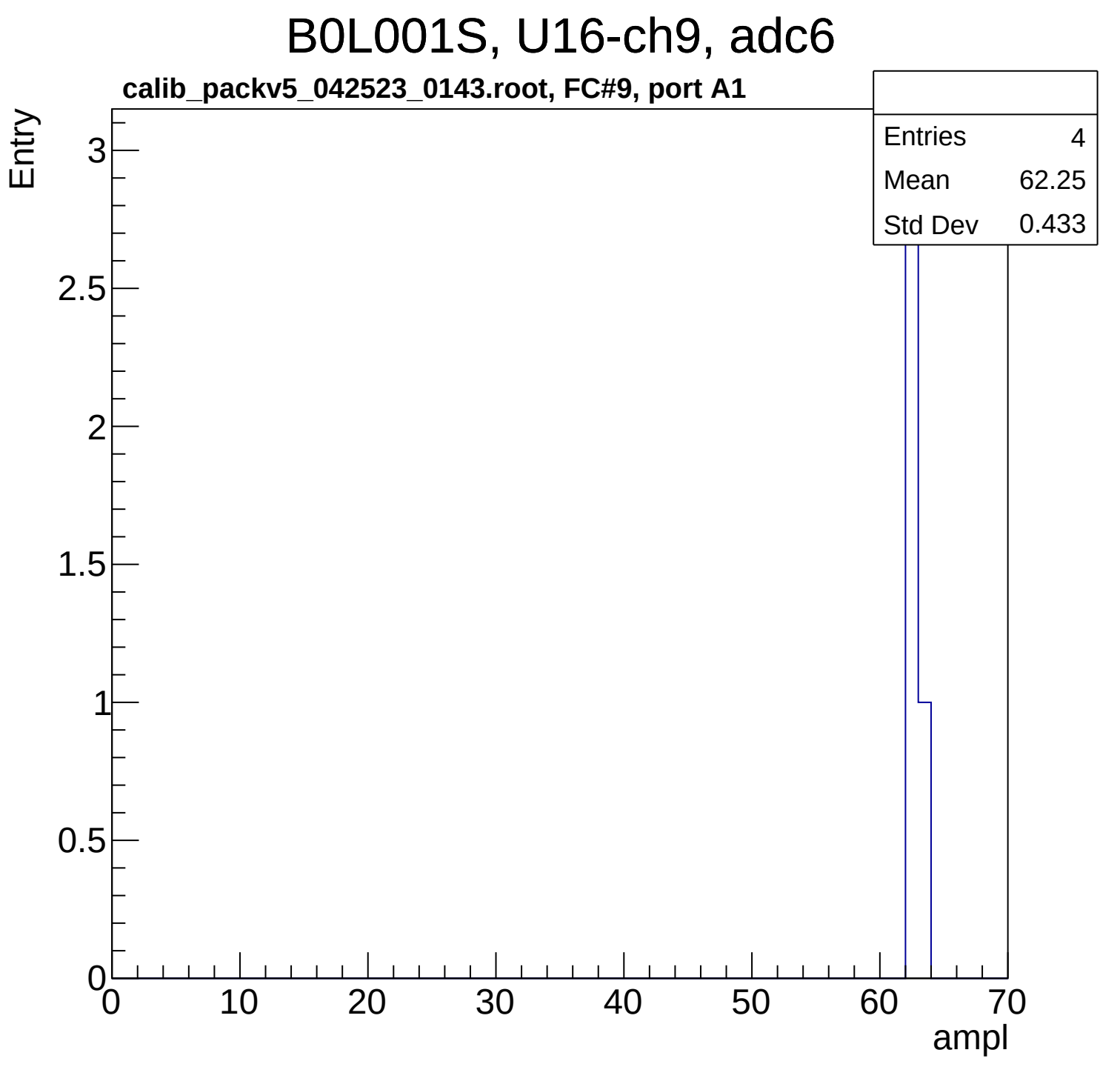
B0L001S, U16-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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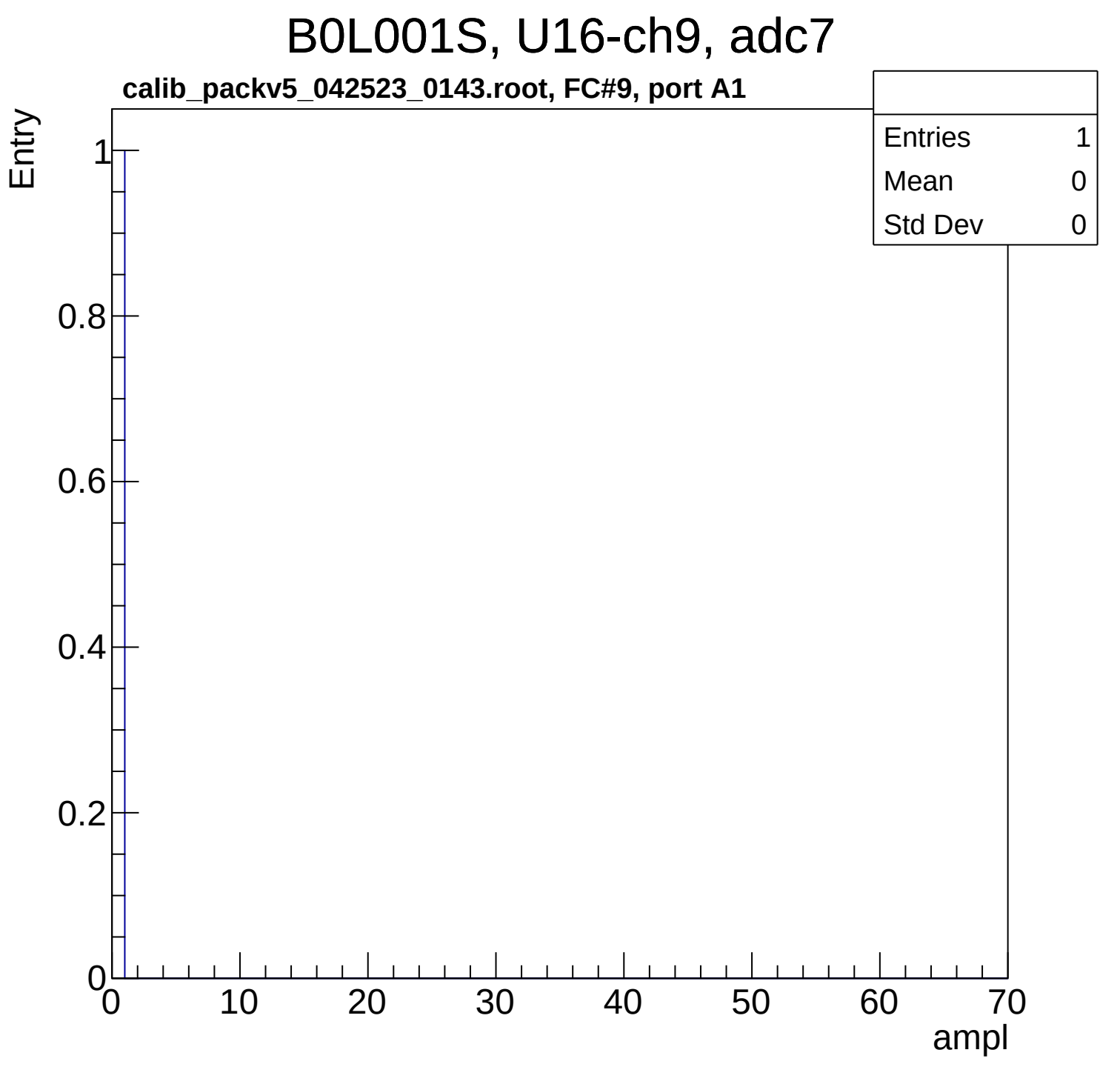
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
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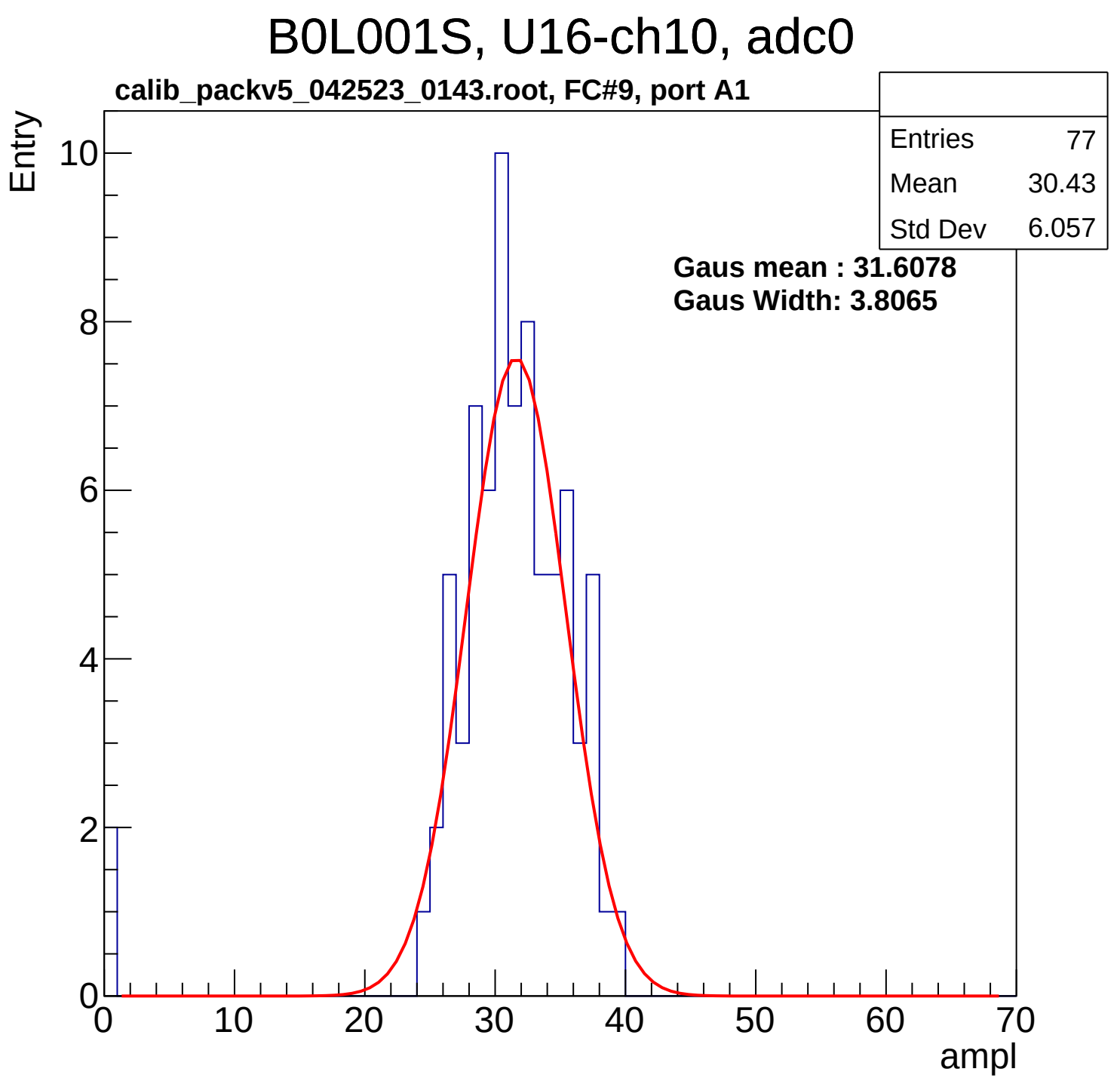
50
60
70
ampl


B0L001S, U16-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.43
Std Dev 6.057
Gaus mean : 31.6078
Gaus Width: 3.8065
Entry
ampl
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2
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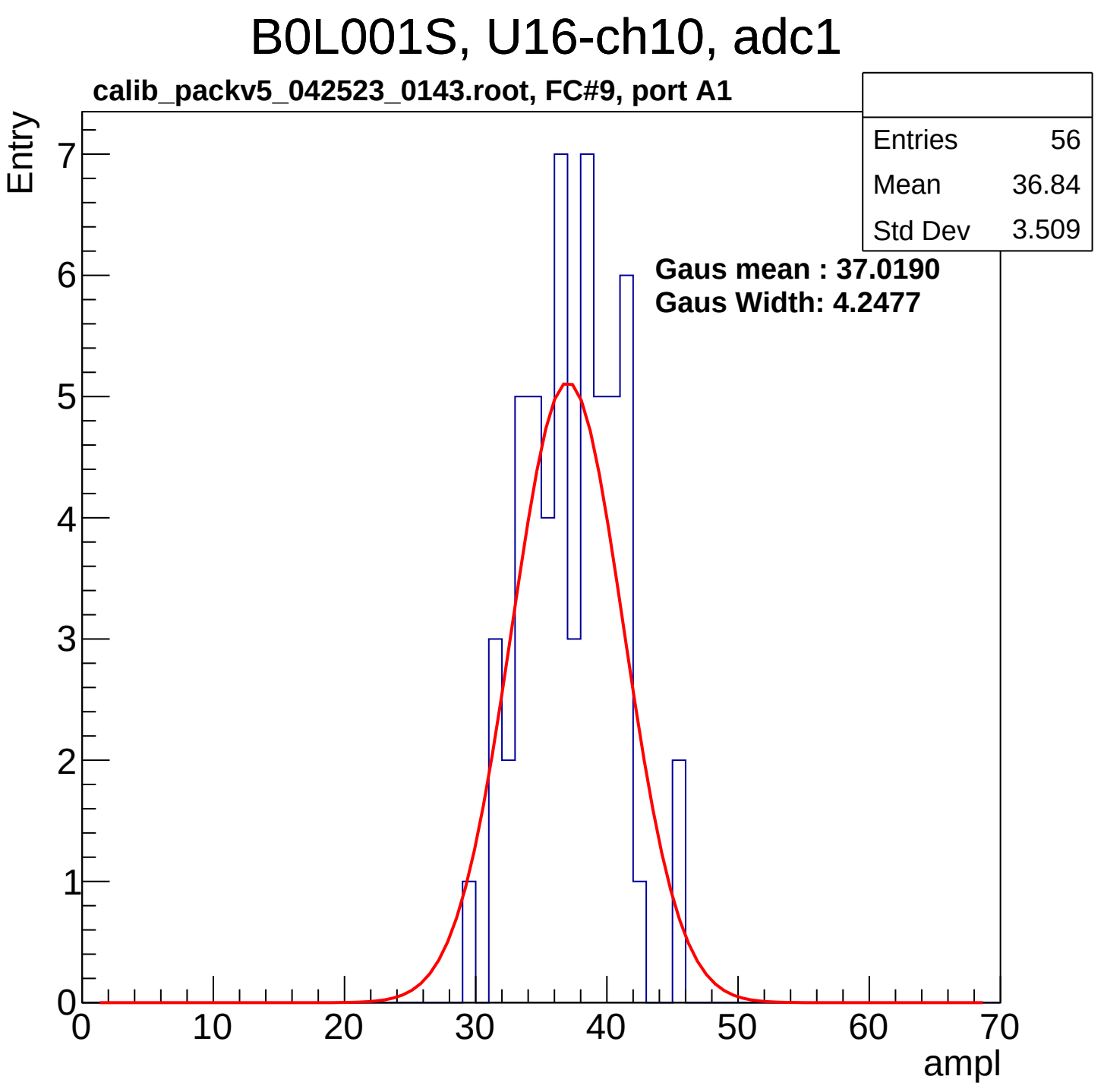
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 36.84
Std Dev 3.509
Gaus mean : 37.0190
Gaus Width: 4.2477
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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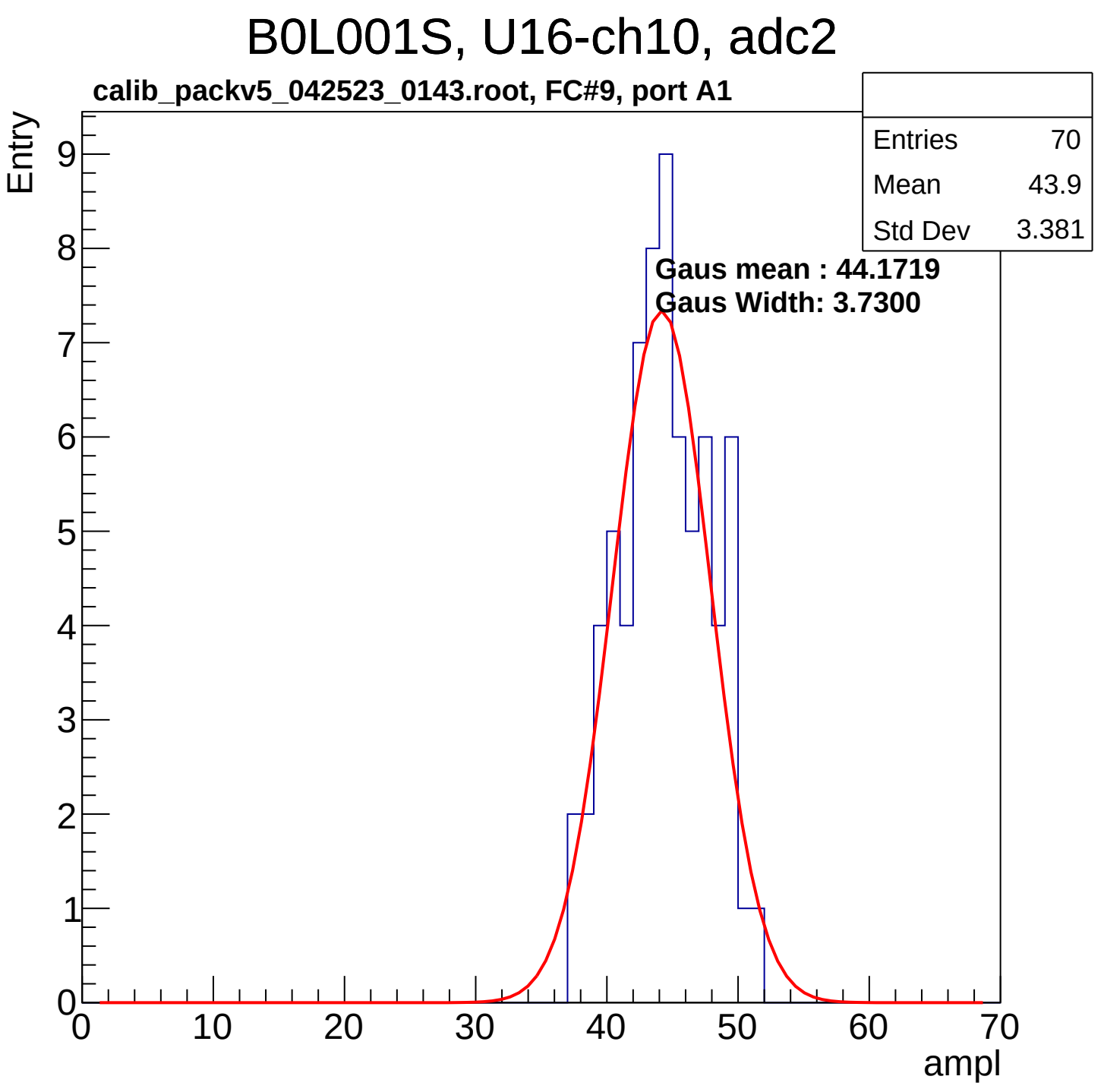
B0L001S, U16-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.9
Std Dev 3.381
Gaus mean : 44.1719
Gaus Width: 3.7300
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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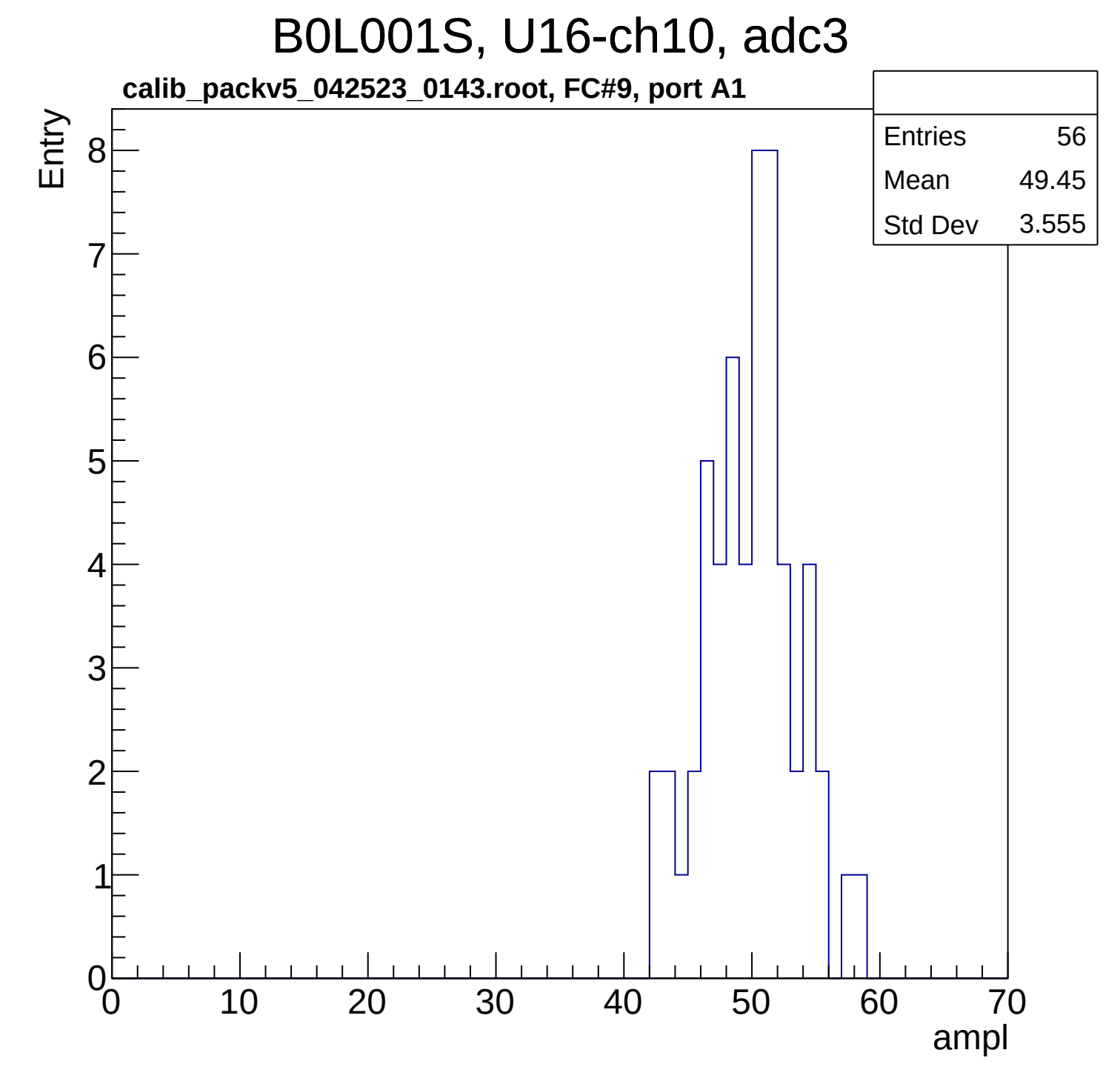
60
70


B0L001S, U16-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.45
Std Dev 3.555
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
50
60
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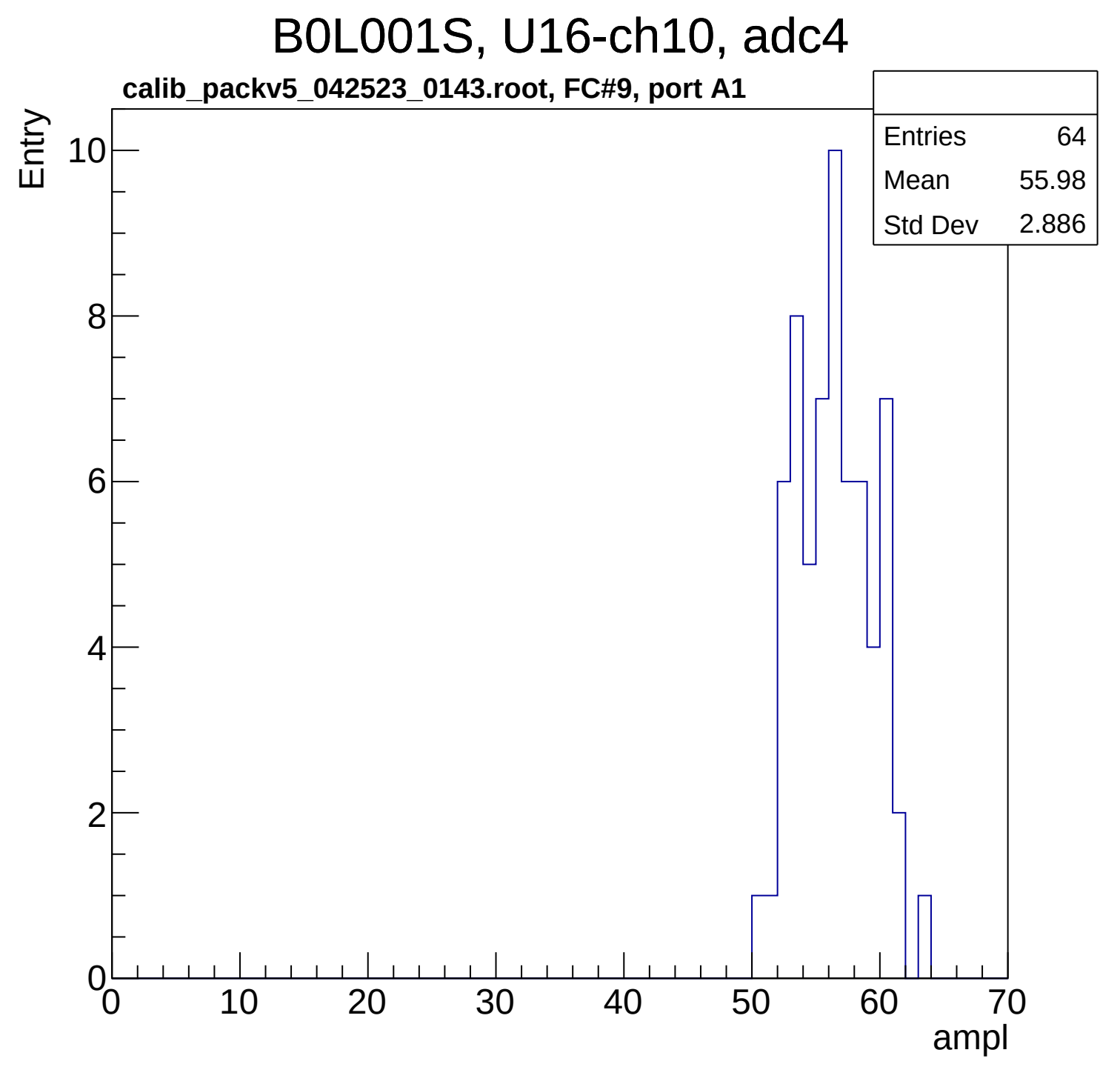
B0L001S, U16-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.98
Std Dev 2.886
Entry
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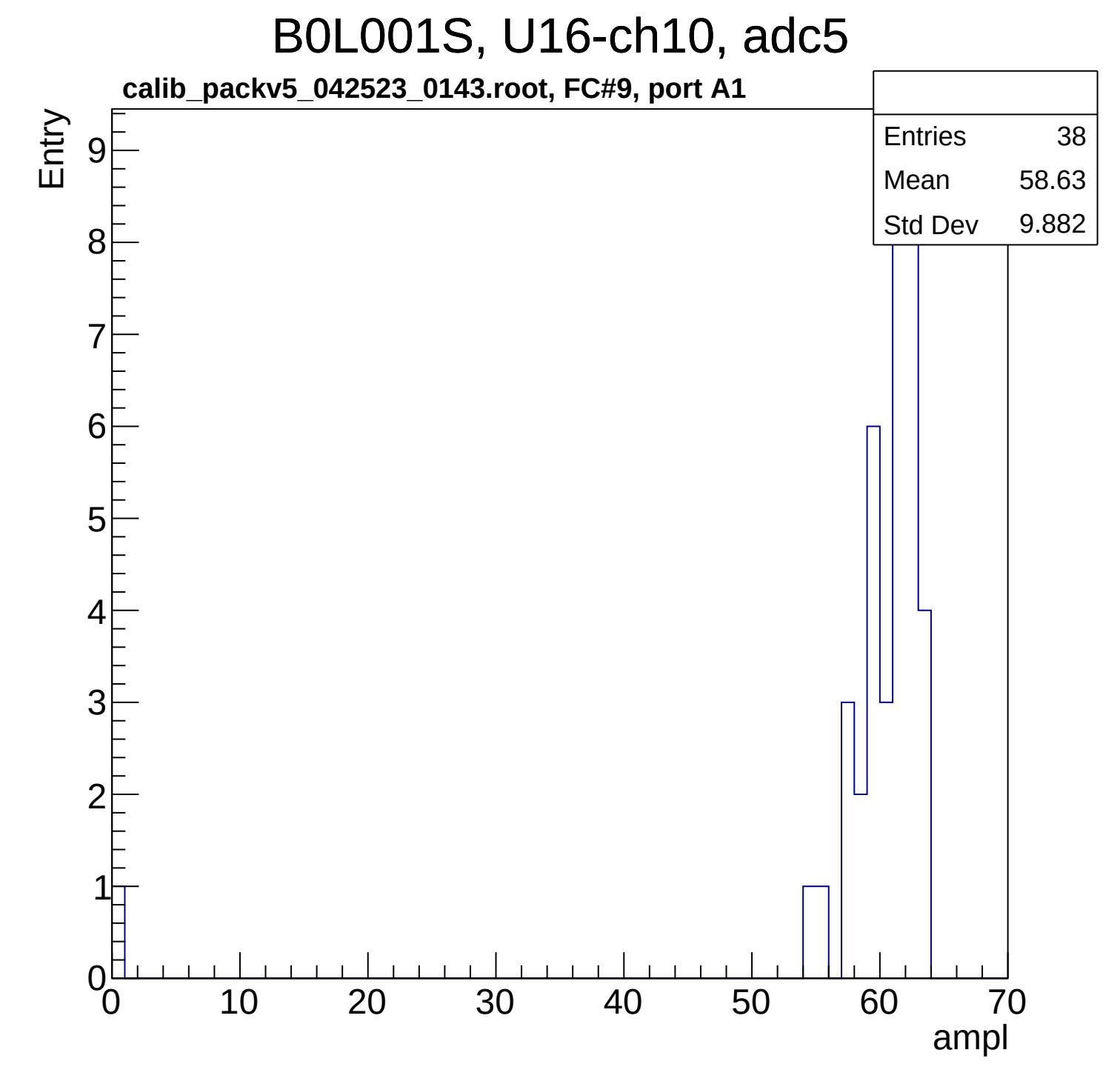
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.63
Std Dev 9.882
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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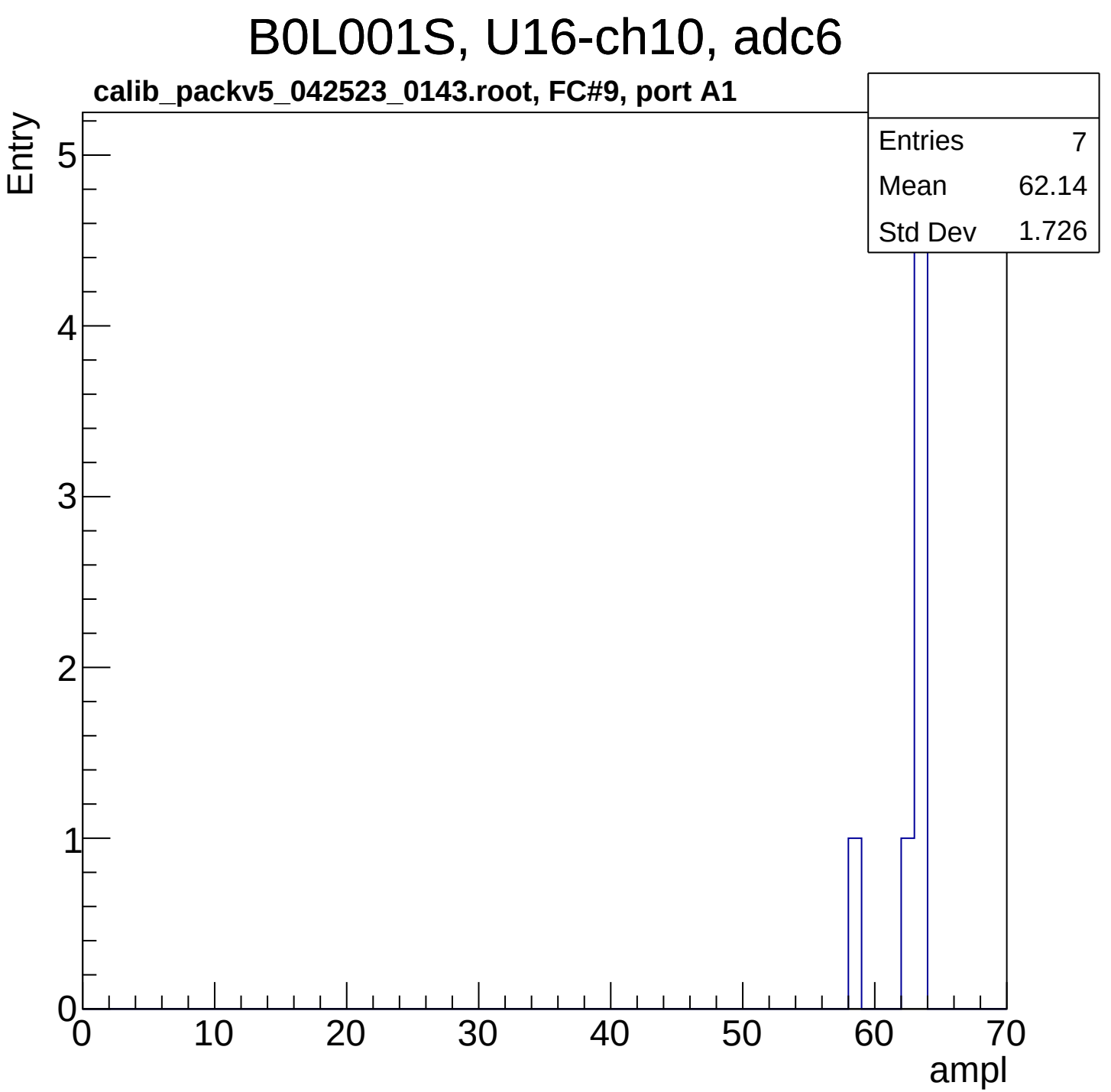
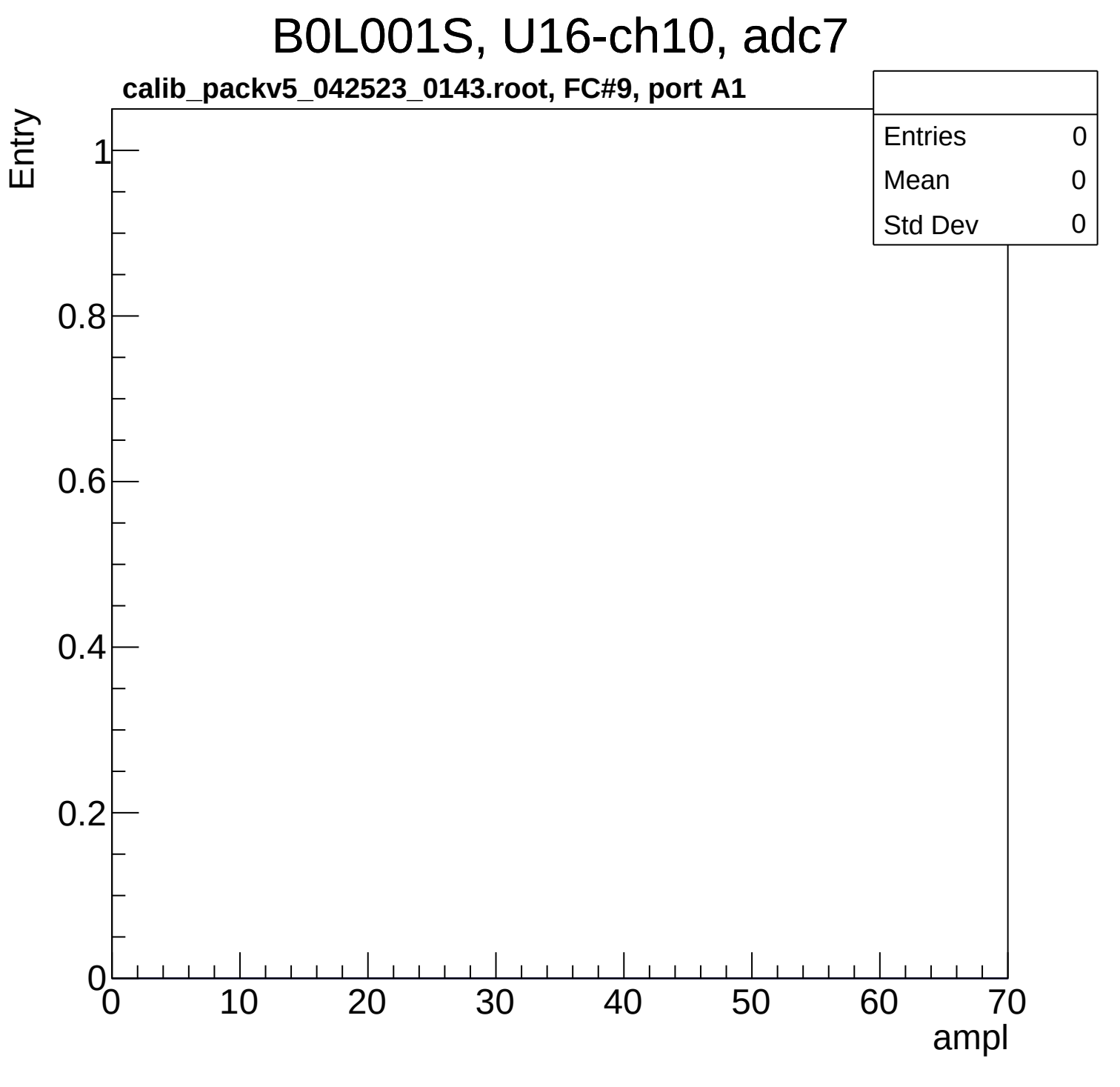

B0L001S, U16-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.14
Std Dev 1.726
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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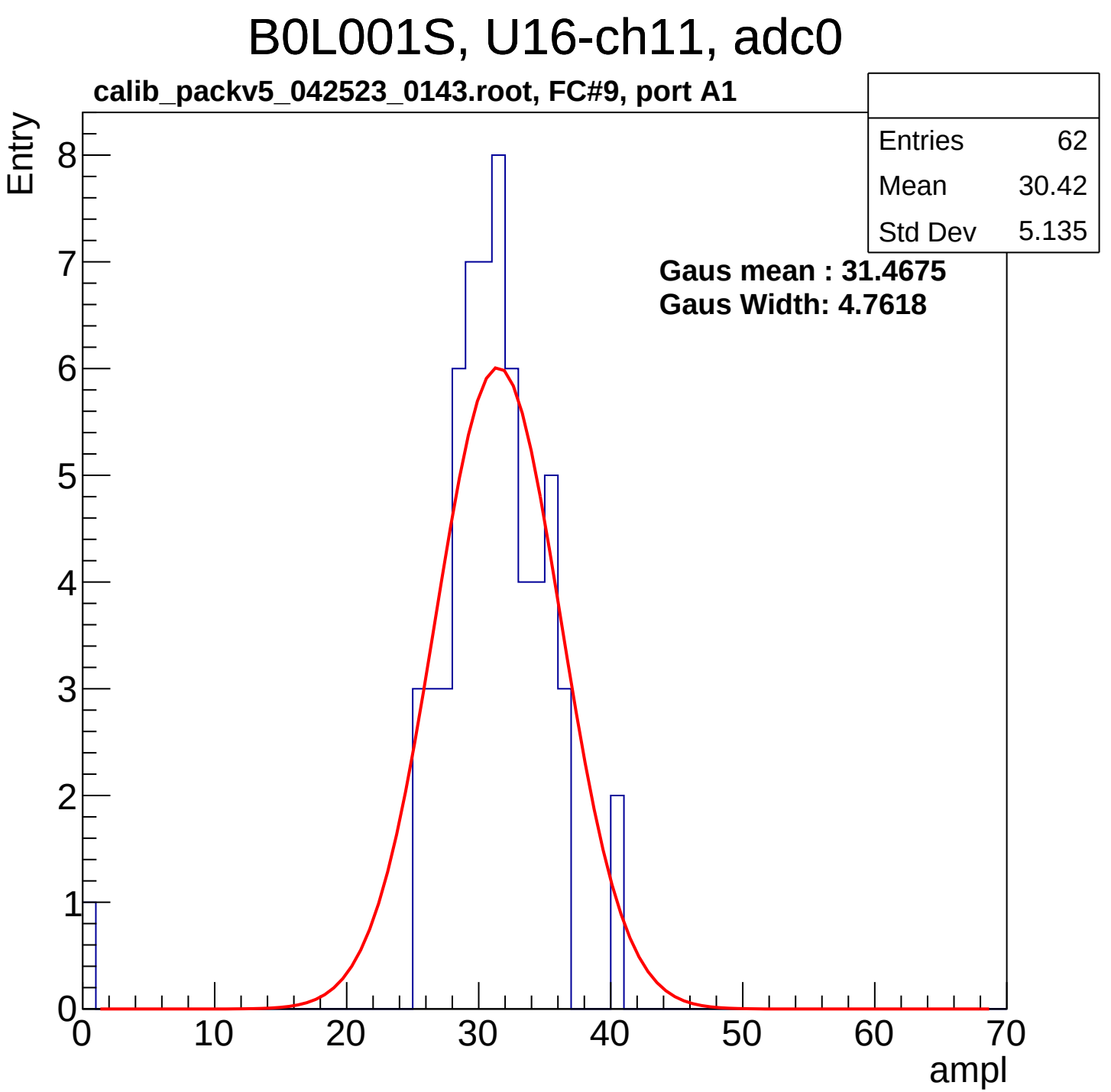
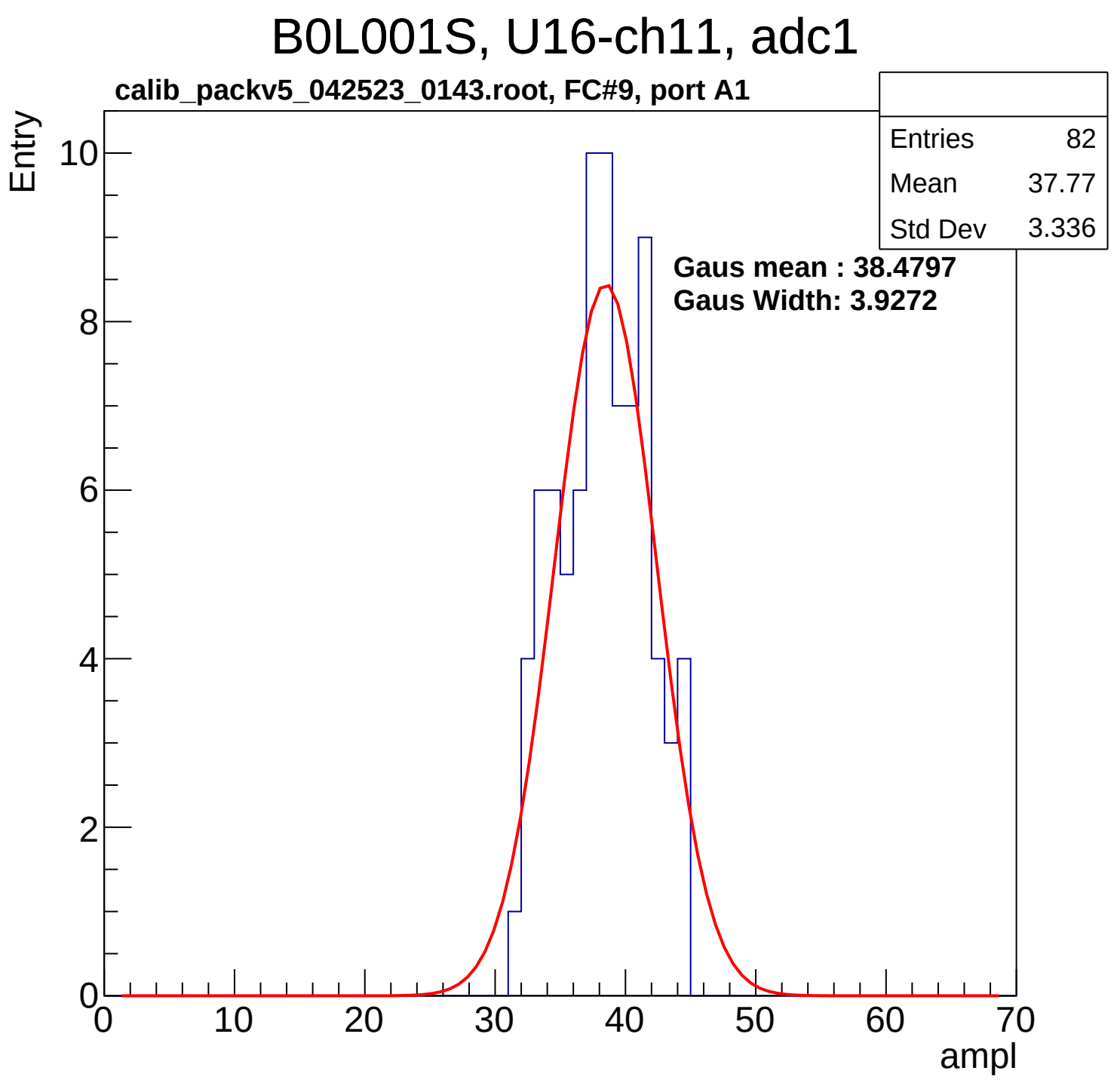
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.42
Std Dev 5.135
Gaus mean : 31.4675
Gaus Width: 4.7618
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 37.77
Std Dev 3.336
Gaus mean : 38.4797
Gaus Width: 3.9272
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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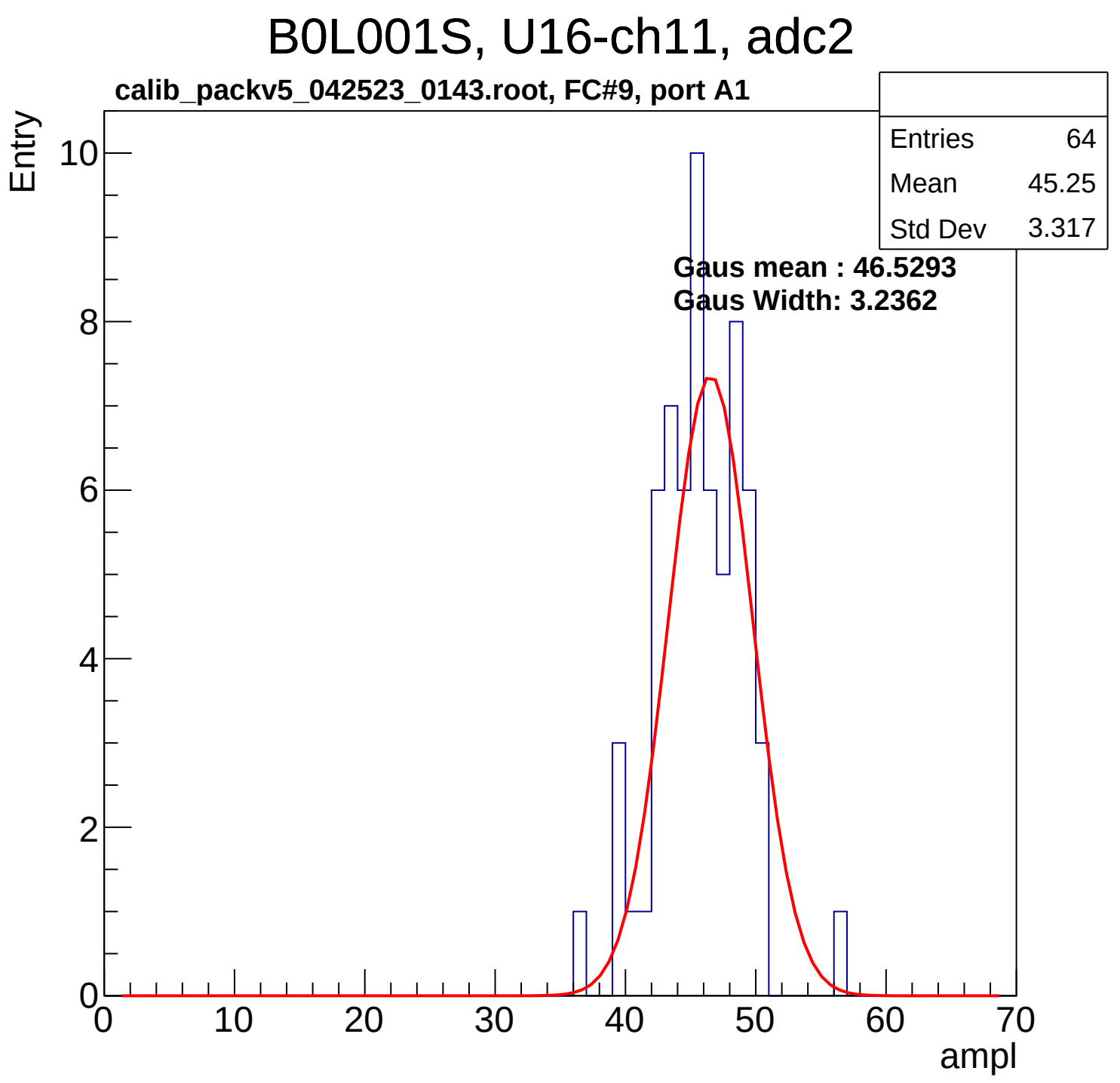
B0L001S, U16-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 45.25
Std Dev 3.317
Gaus mean : 46.5293
Gaus Width: 3.2362
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
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60
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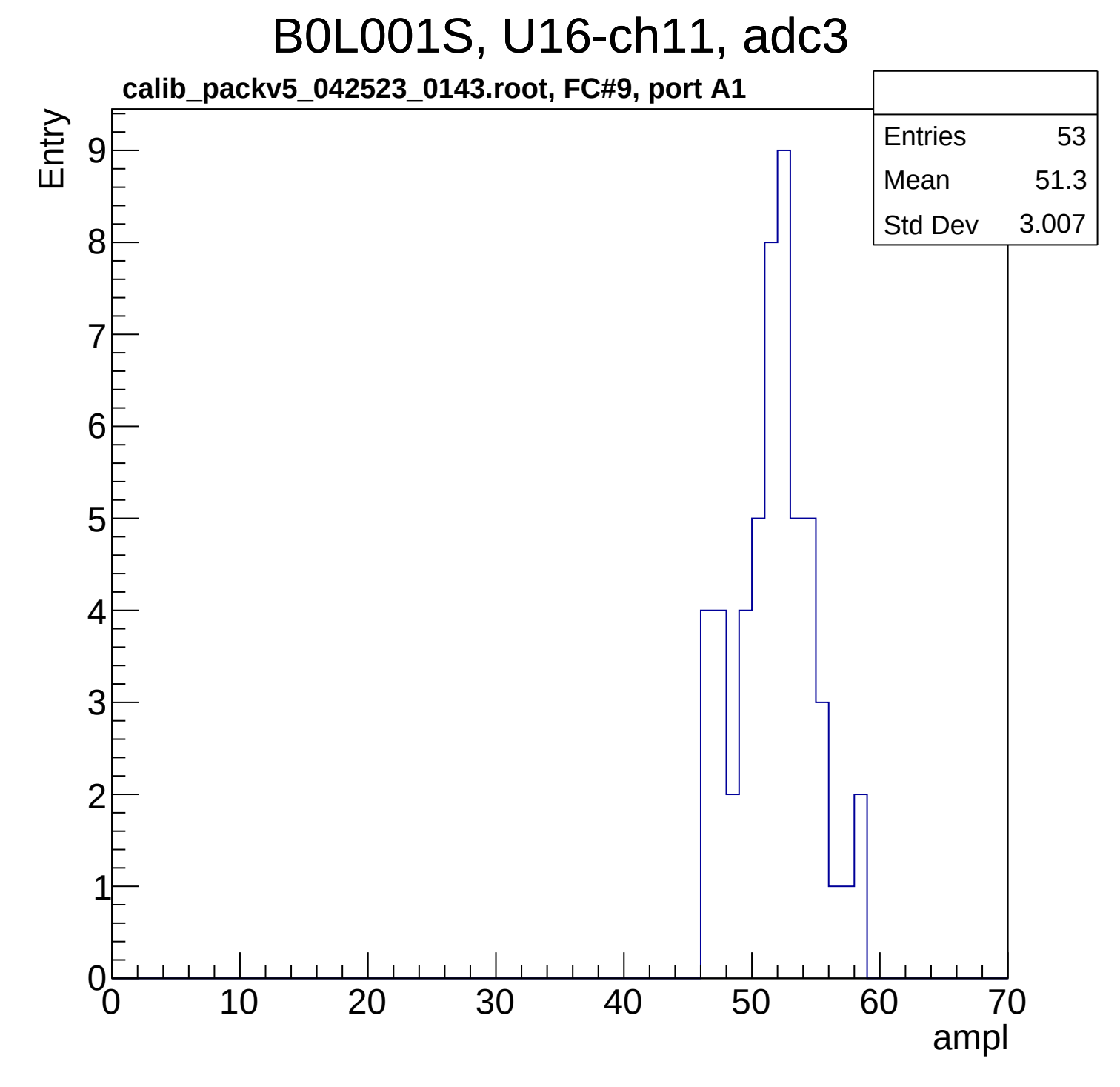
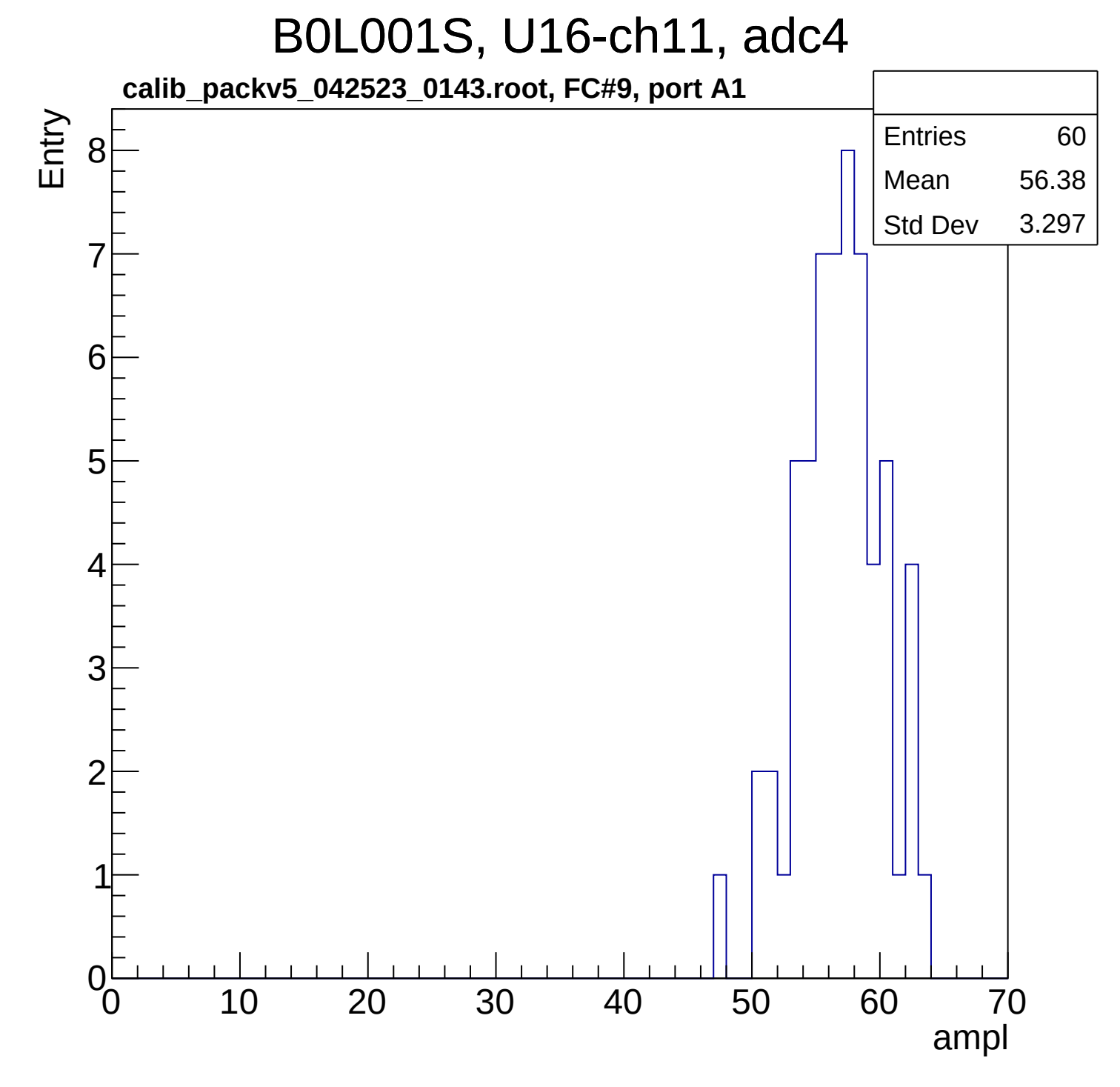

B0L001S, U16-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 51.3
Std Dev 3.007
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.38
Std Dev 3.297
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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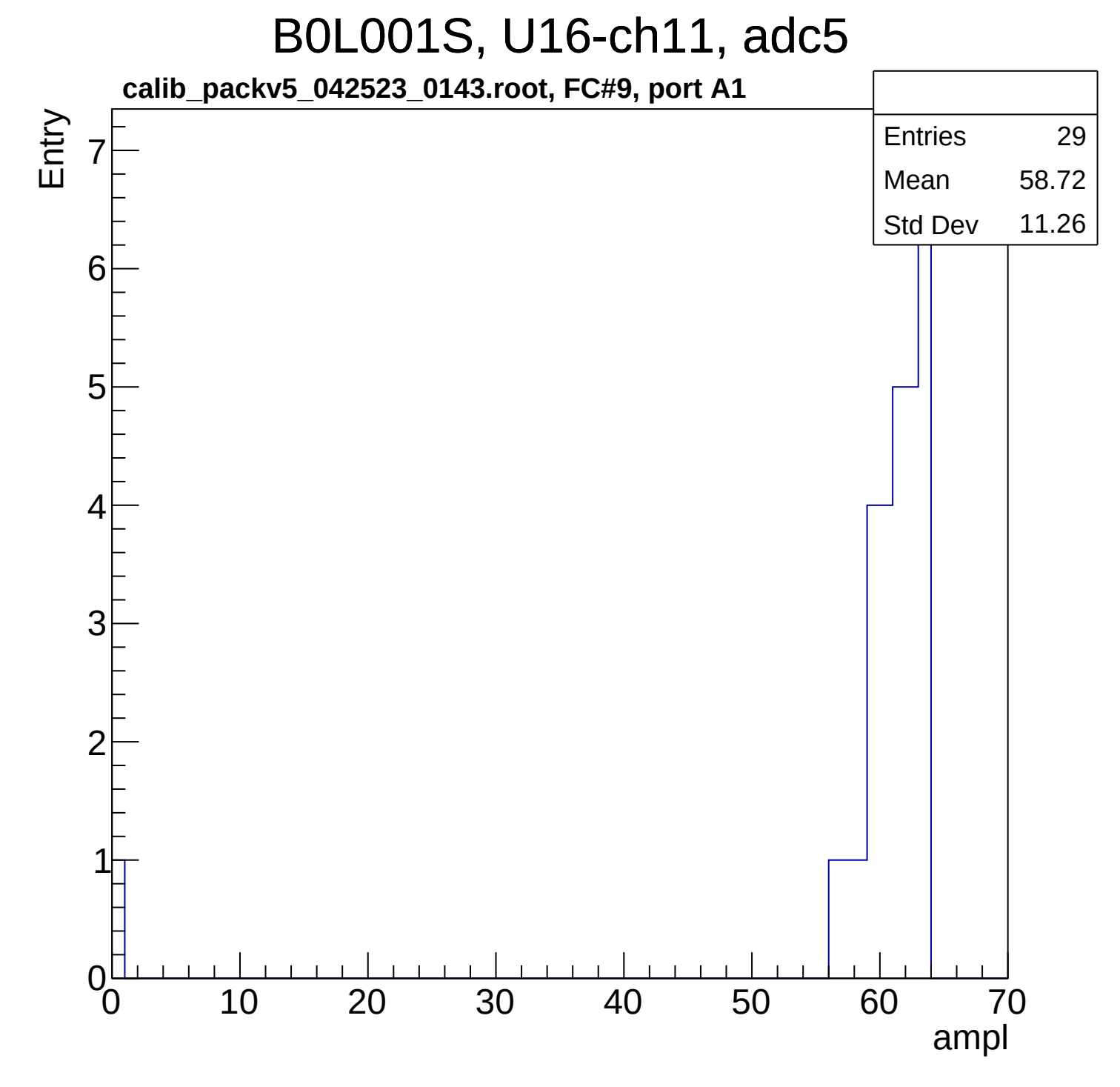
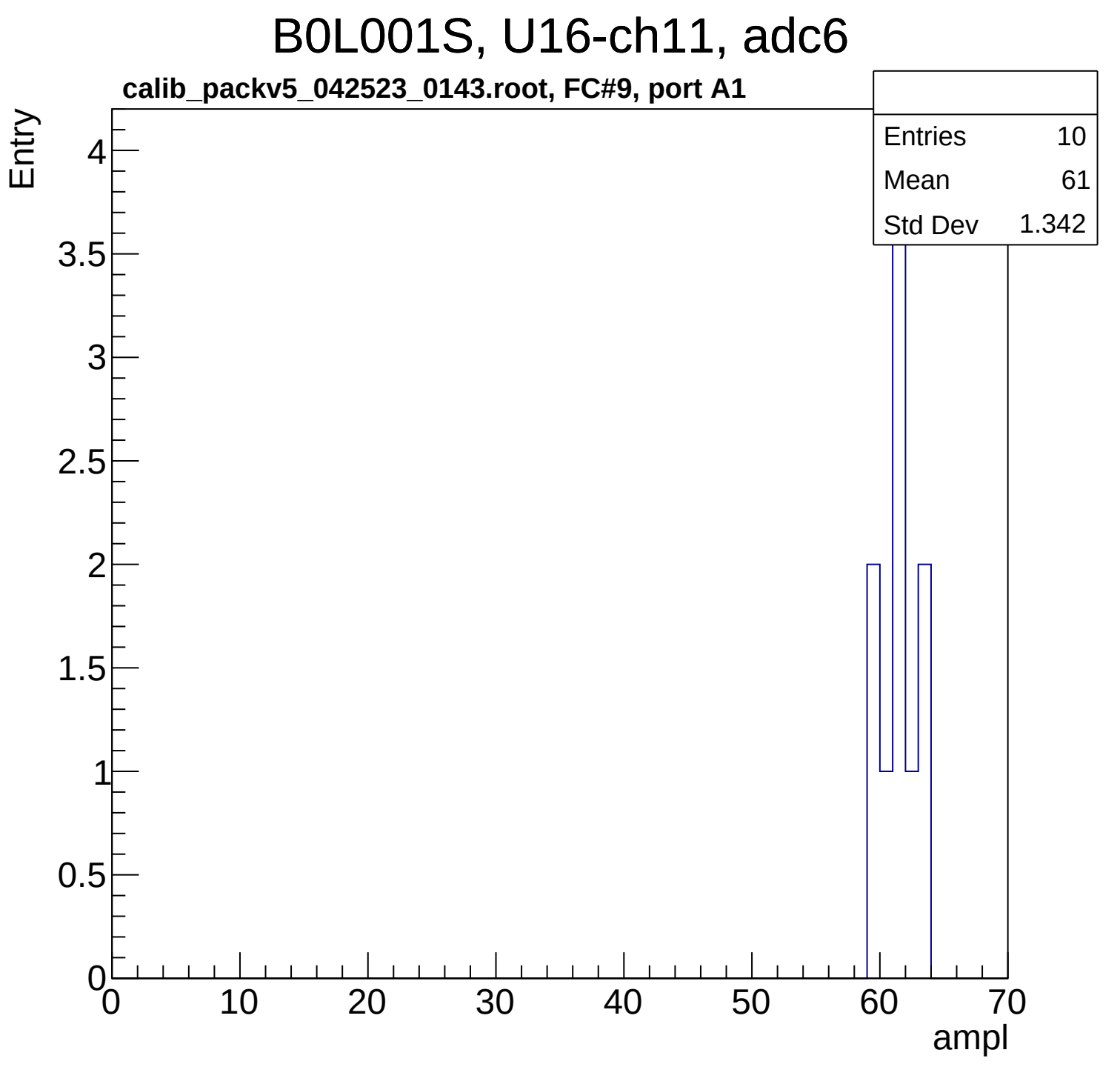
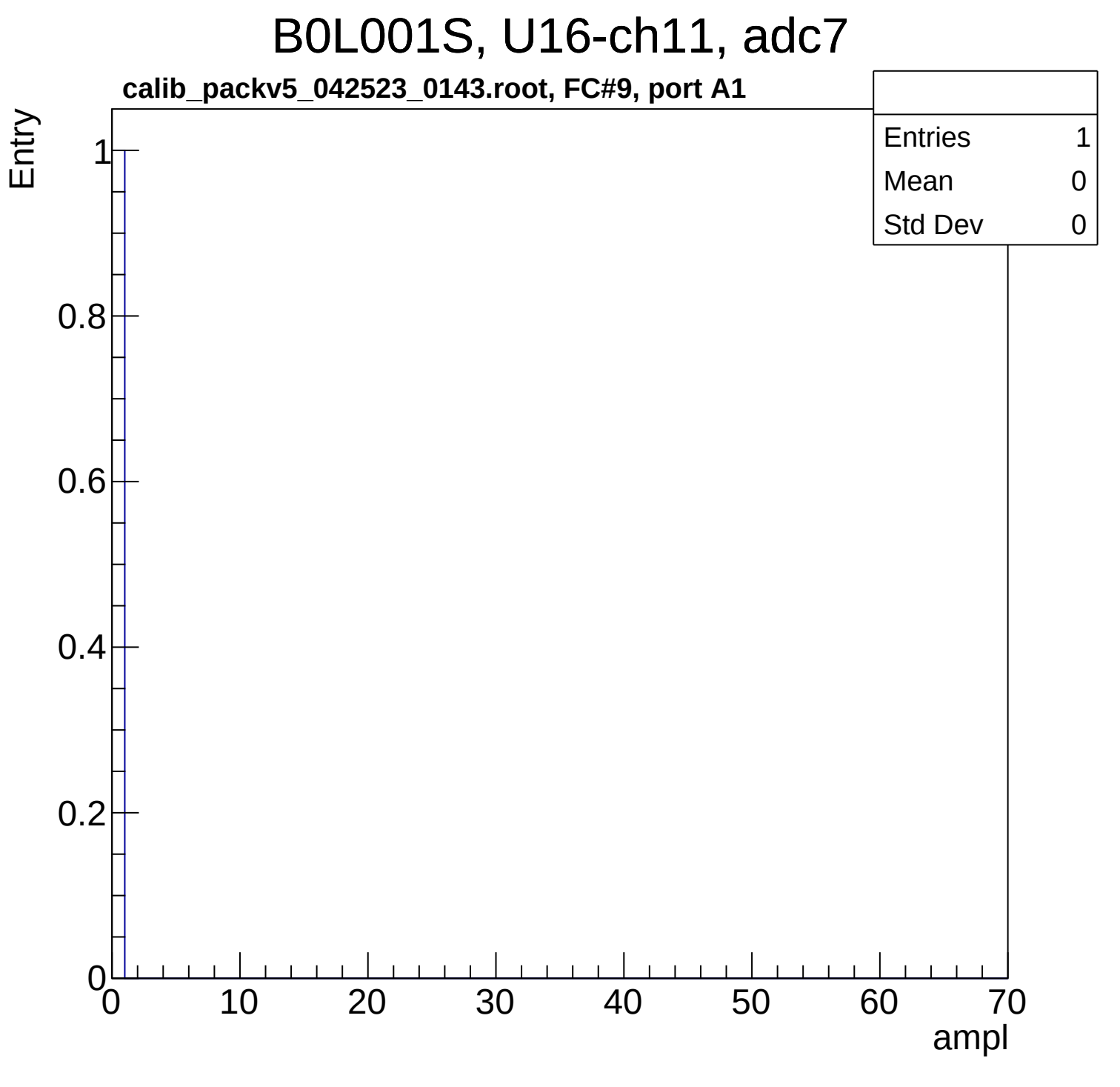
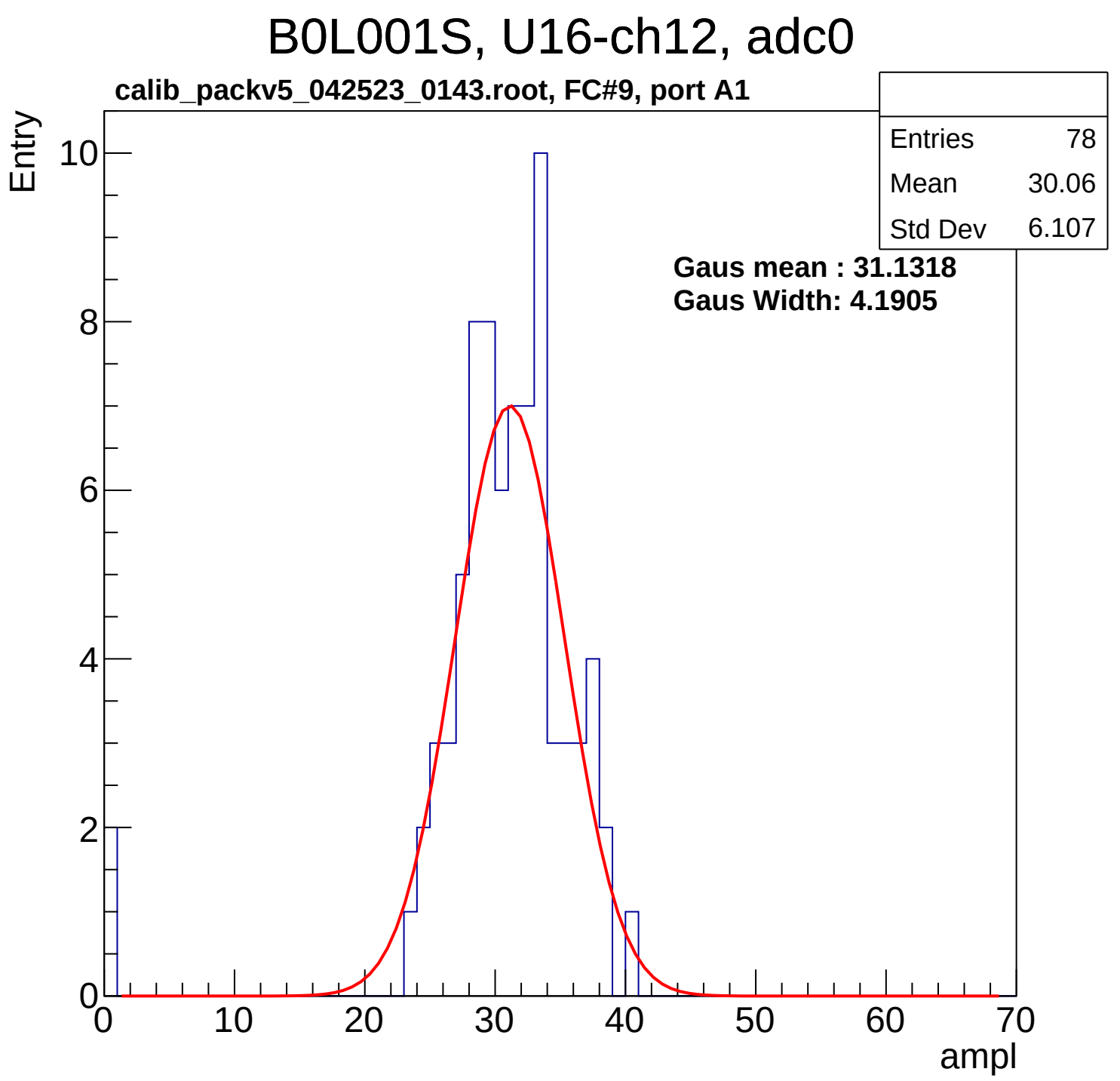
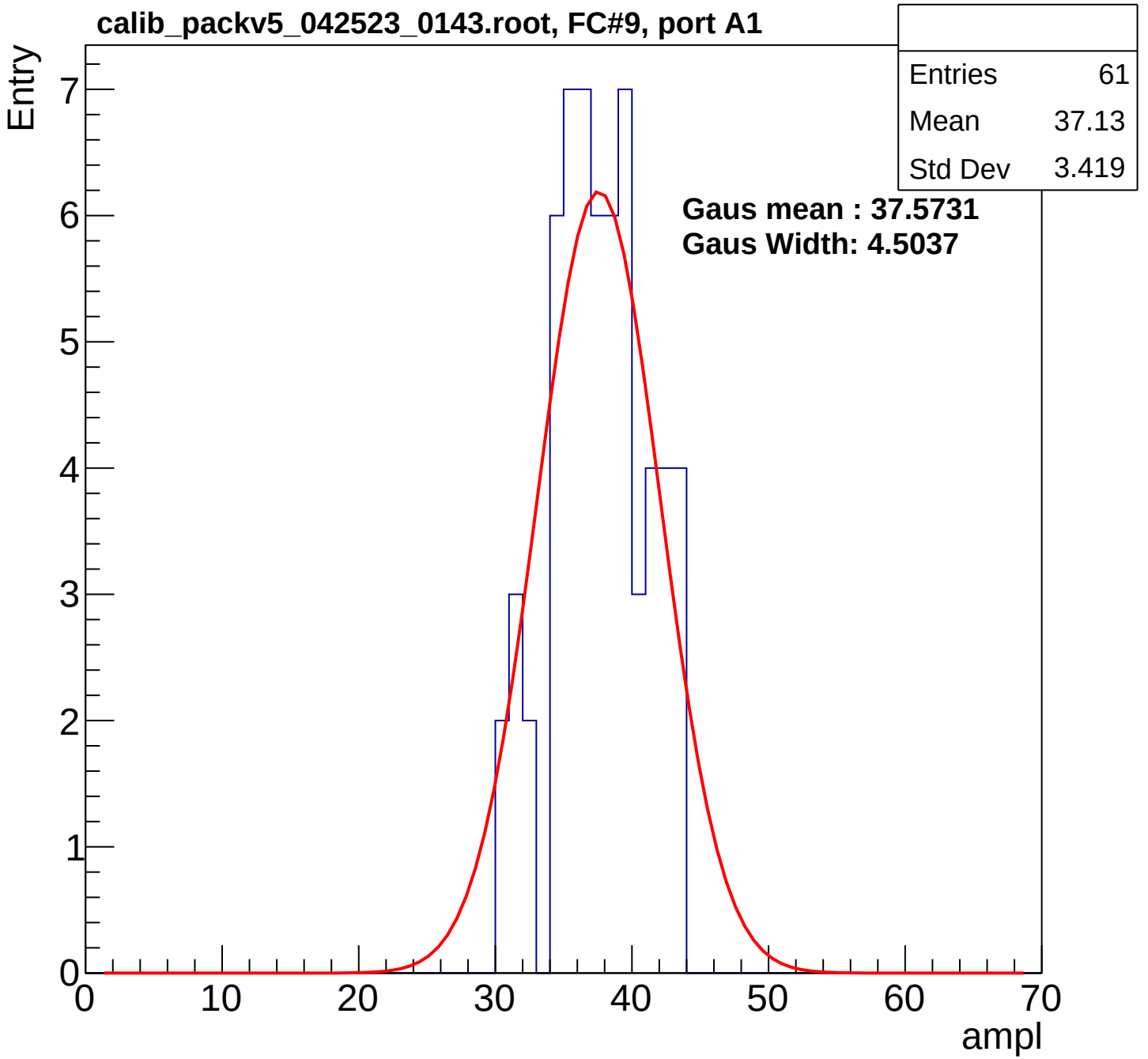

B0L001S, U16-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.72
Std Dev 11.26
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61
Std Dev 1.342
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.06
Std Dev 6.107
Gaus mean : 31.1318
Gaus Width: 4.1905
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.13
Std Dev 3.419
Gaus mean : 37.5731
Gaus Width: 4.5037
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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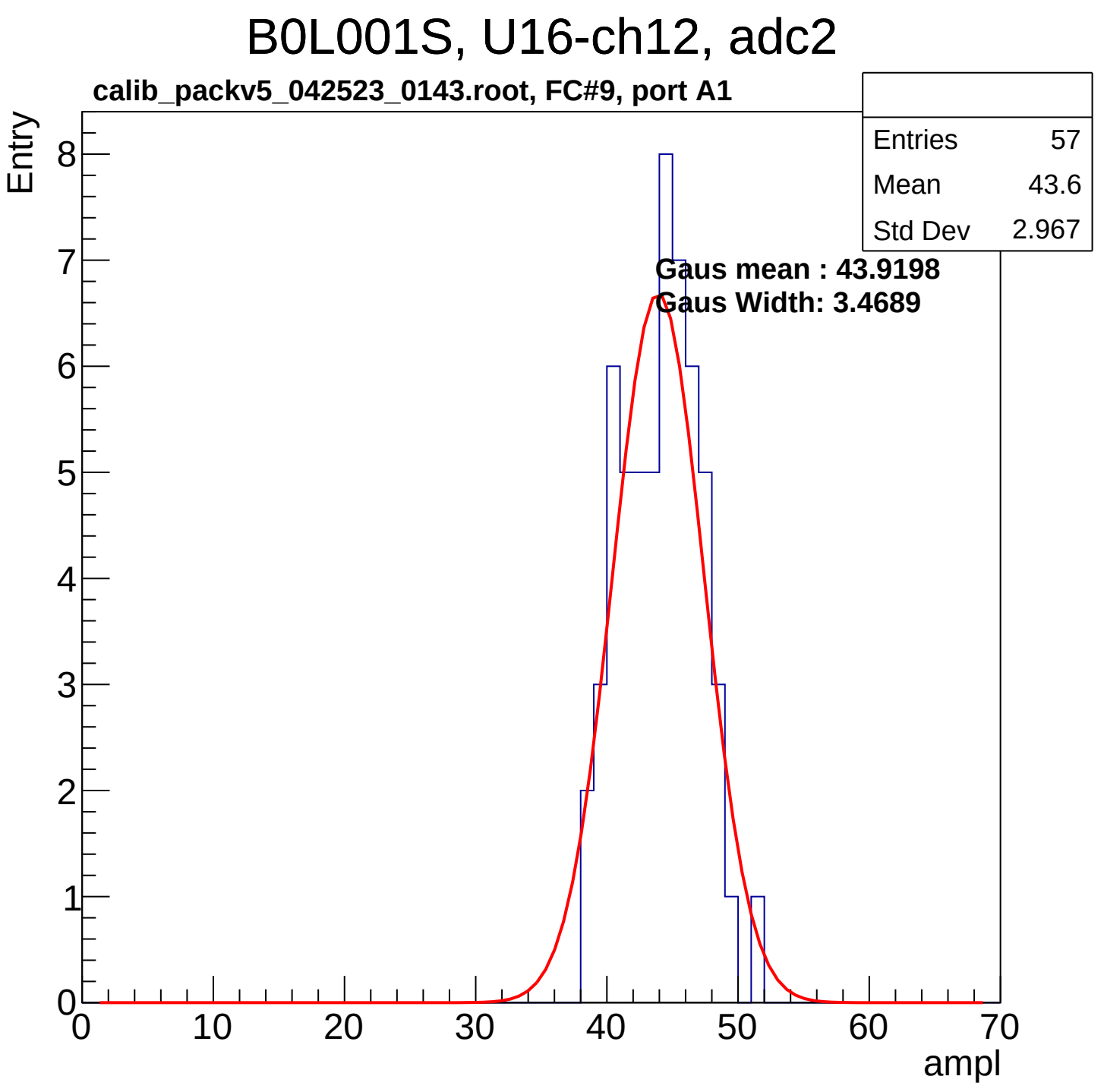
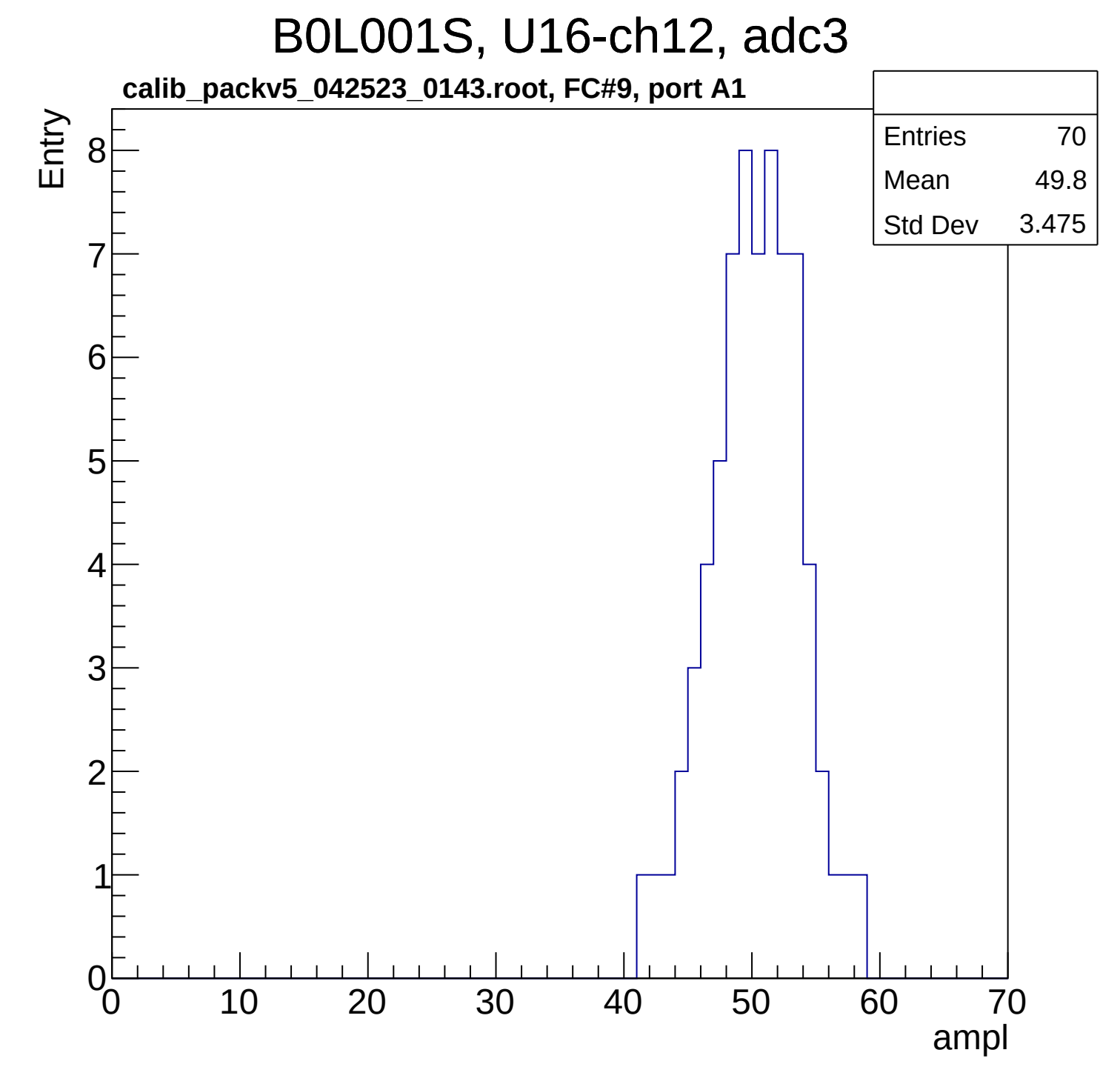
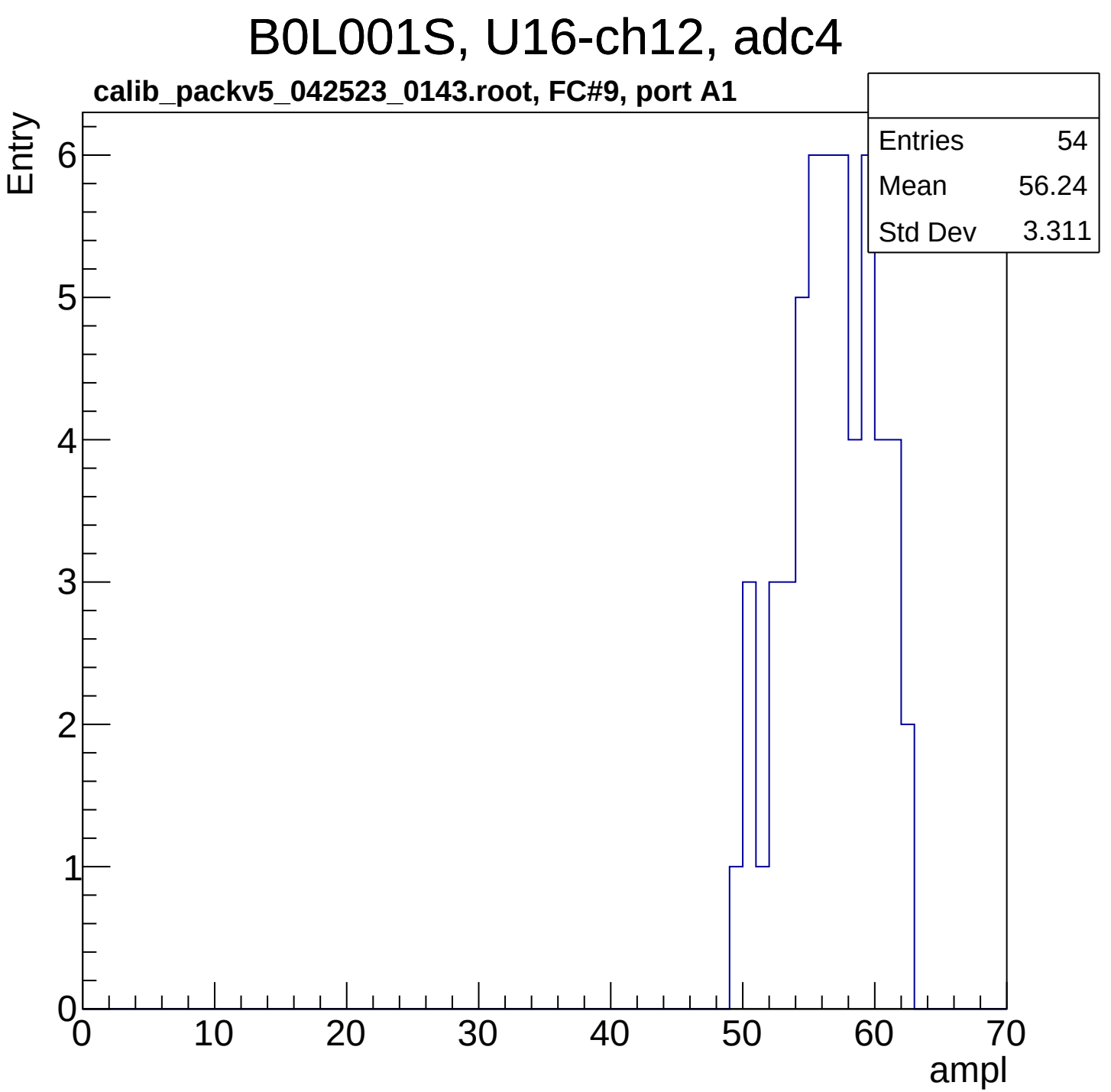
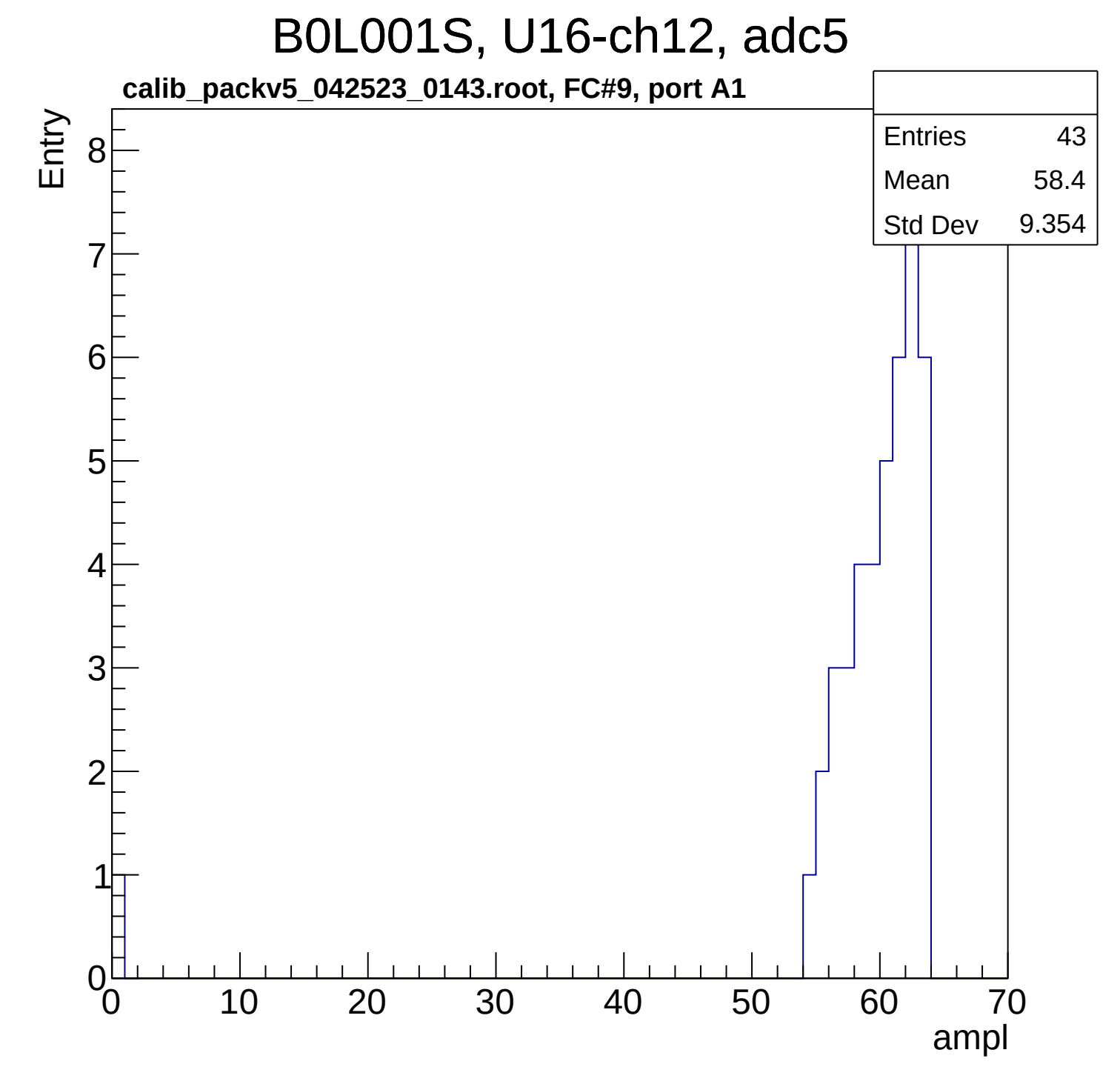

B0L001S, U16-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 43.6
Std Dev 2.967
Gaus mean : 43.9198
Gaus Width: 3.4689
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.8
Std Dev 3.475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.24
Std Dev 3.311
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.4
Std Dev 9.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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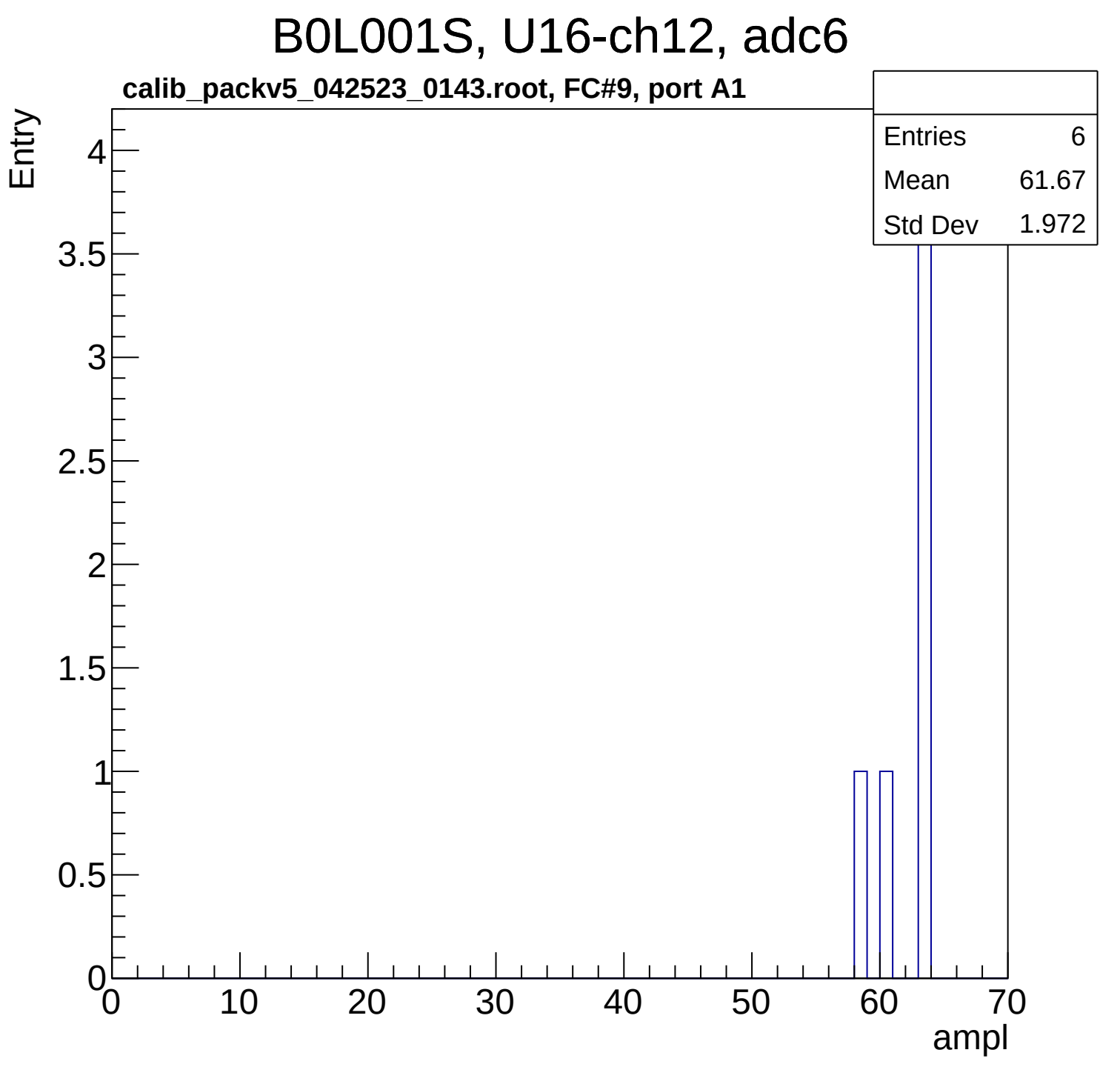
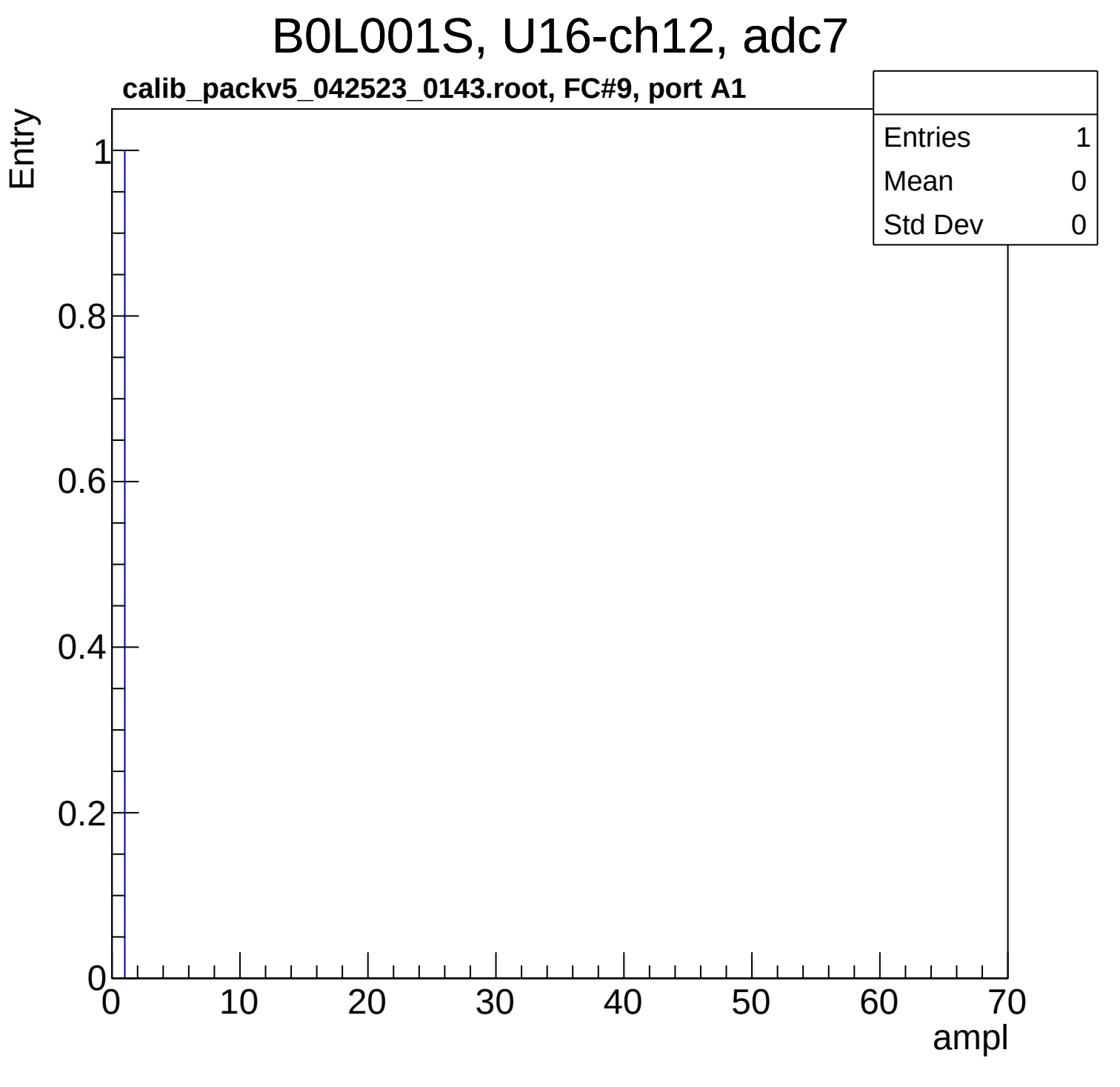
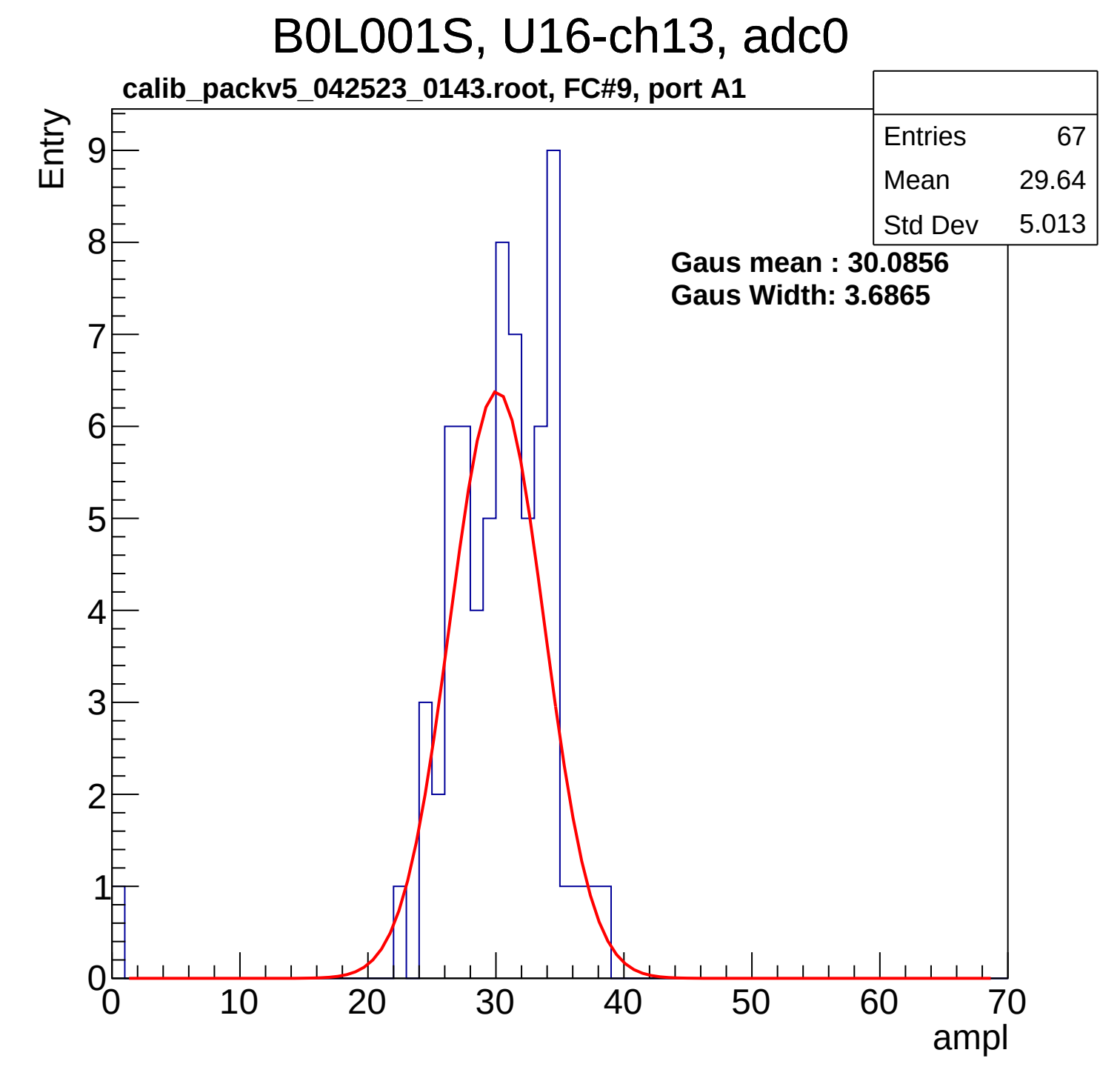
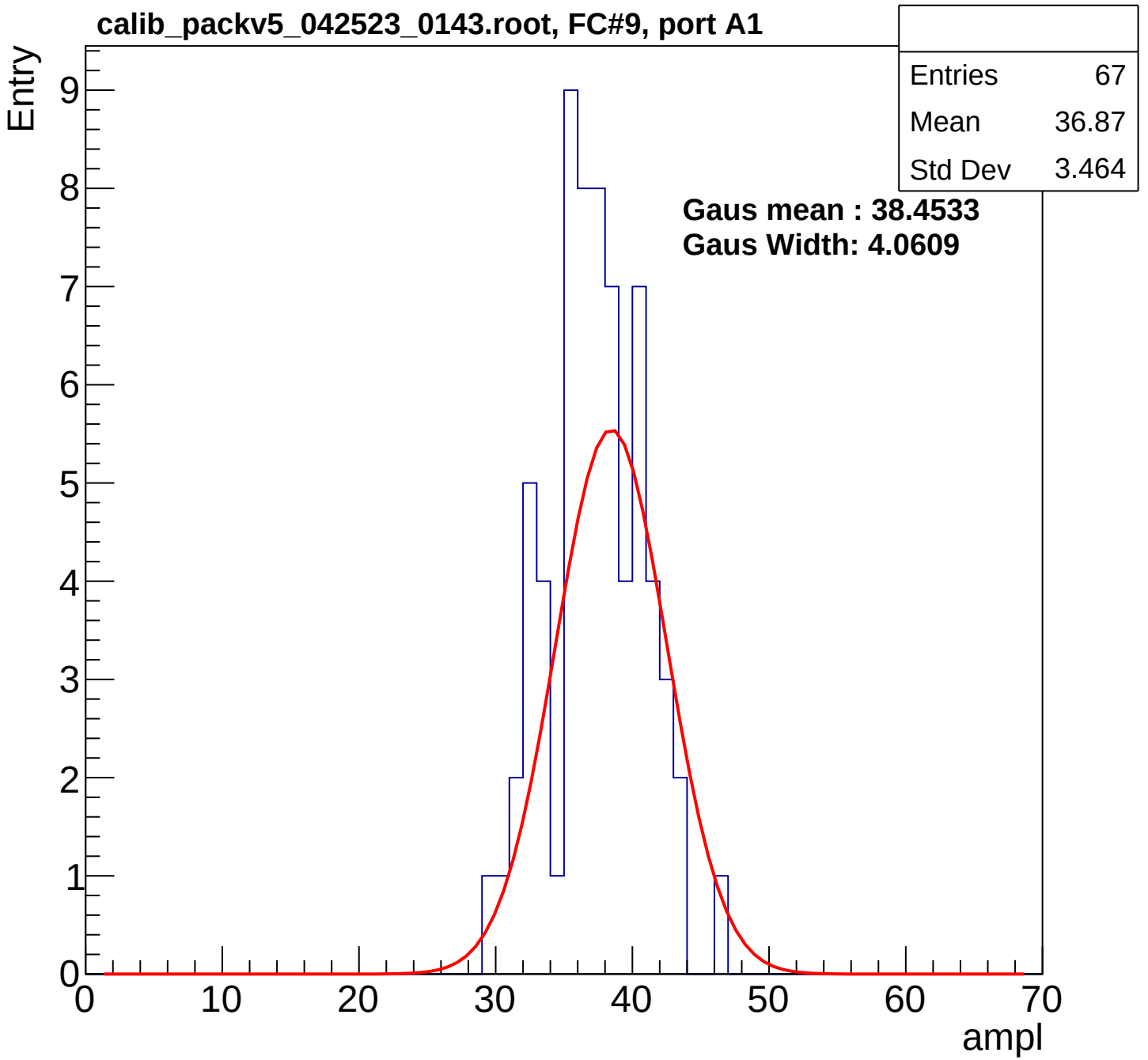
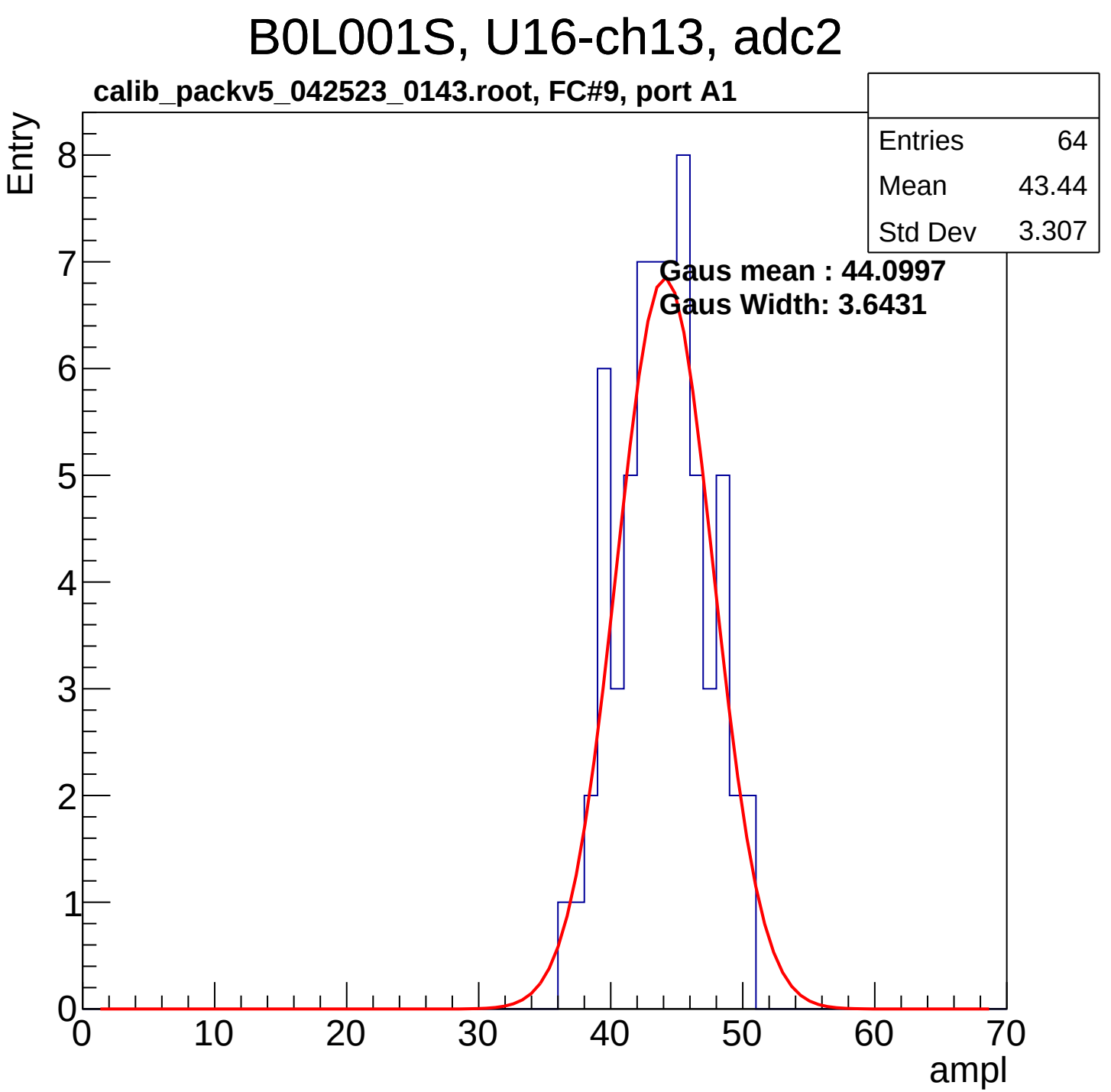
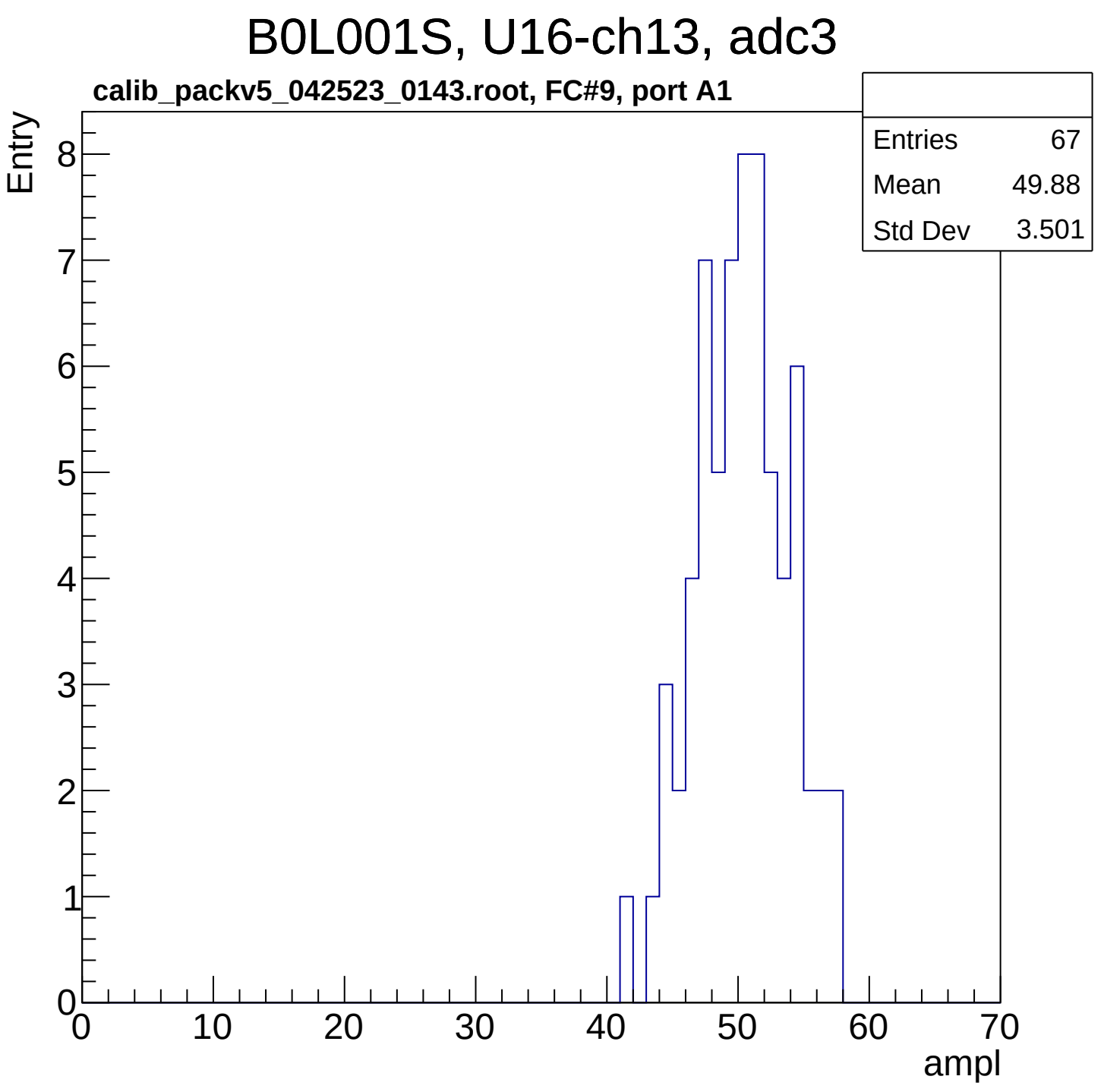

B0L001S, U16-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.67
Std Dev 1.972
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

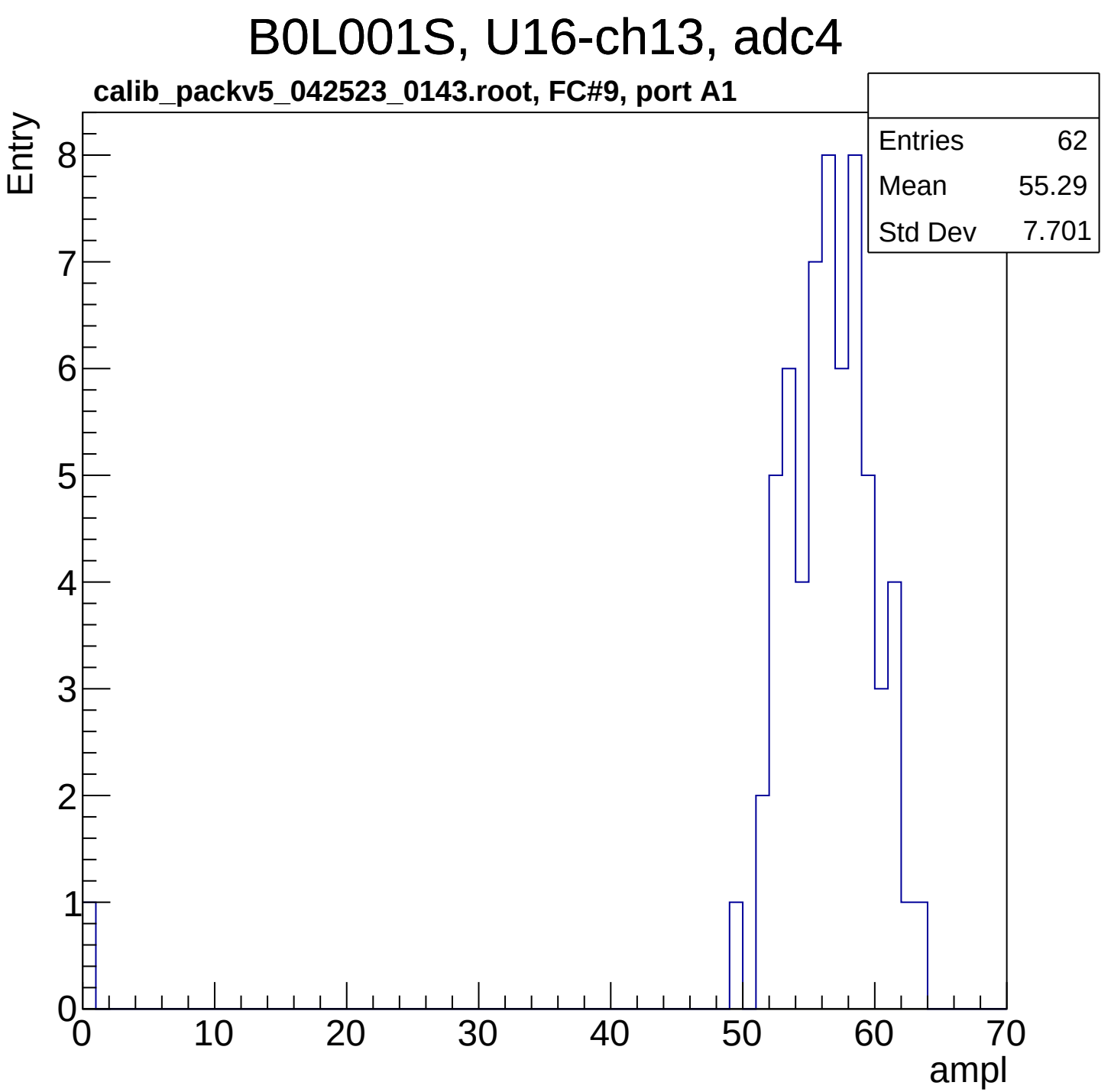

B0L001S, U16-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

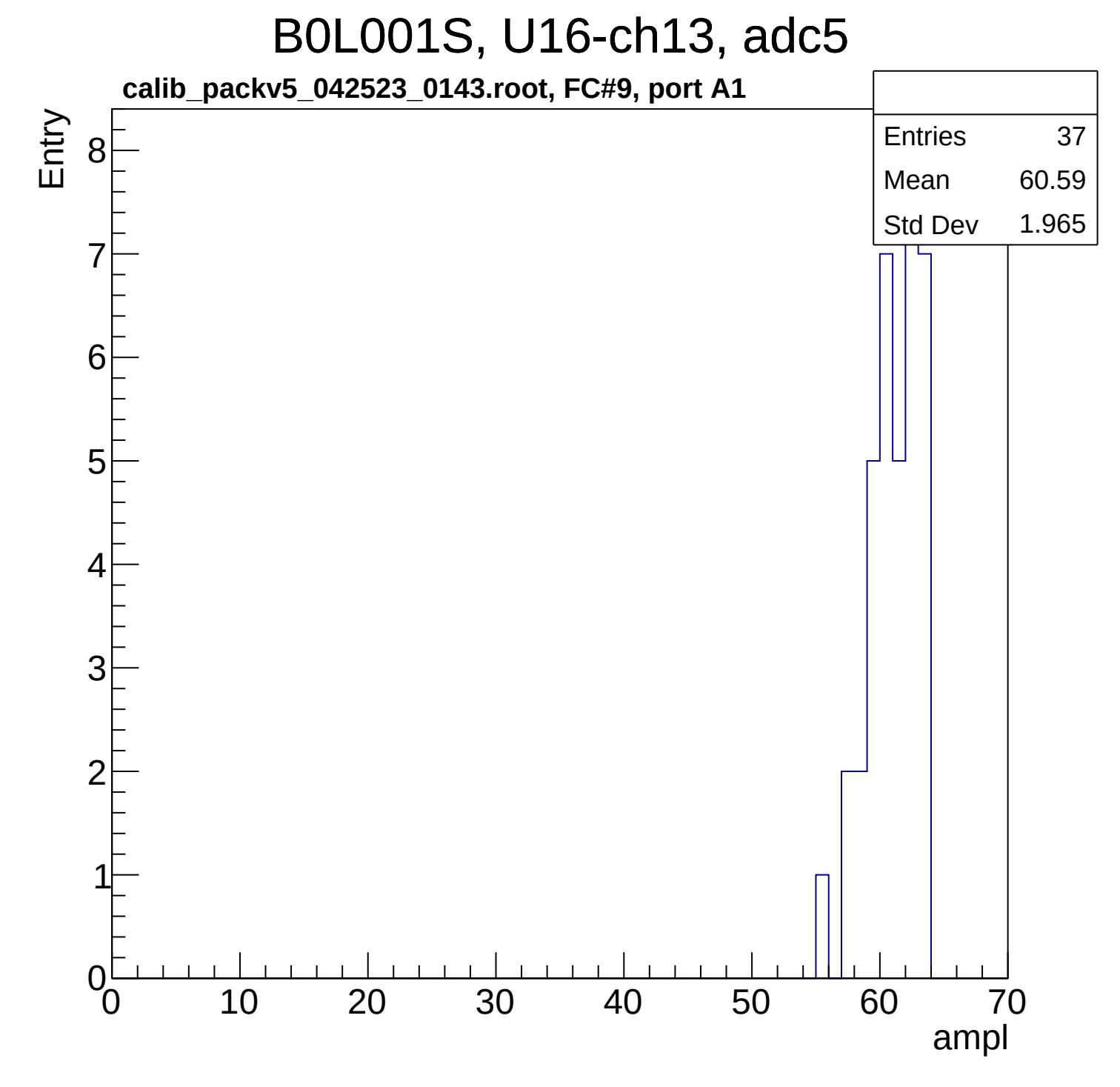

B0L001S, U16-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.64
Std Dev 5.013
Gaus mean : 30.0856
Gaus Width: 3.6865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

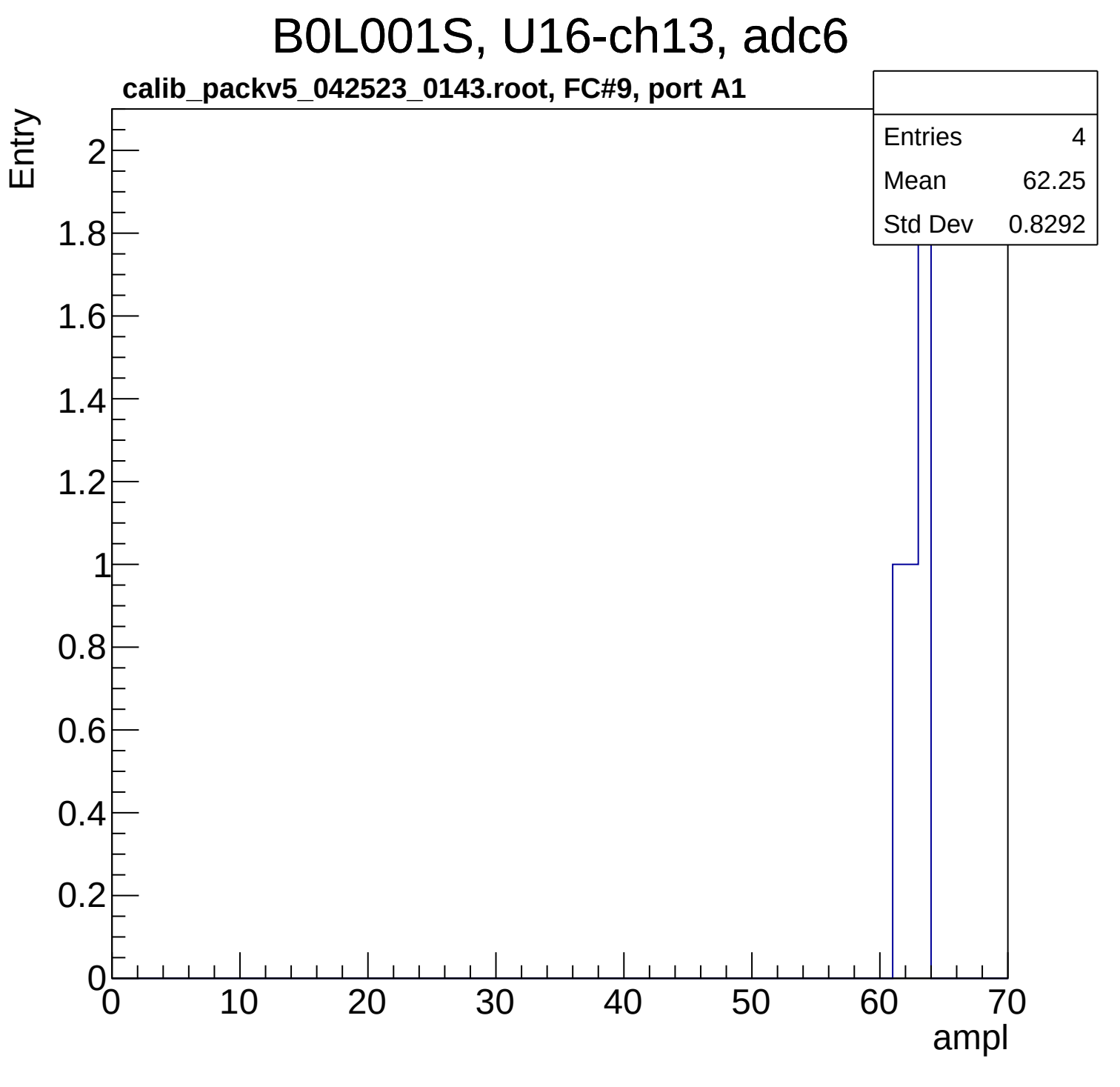

B0L001S, U16-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.87
Std Dev 3.464
Gaus mean : 38.4533
Gaus Width: 4.0609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

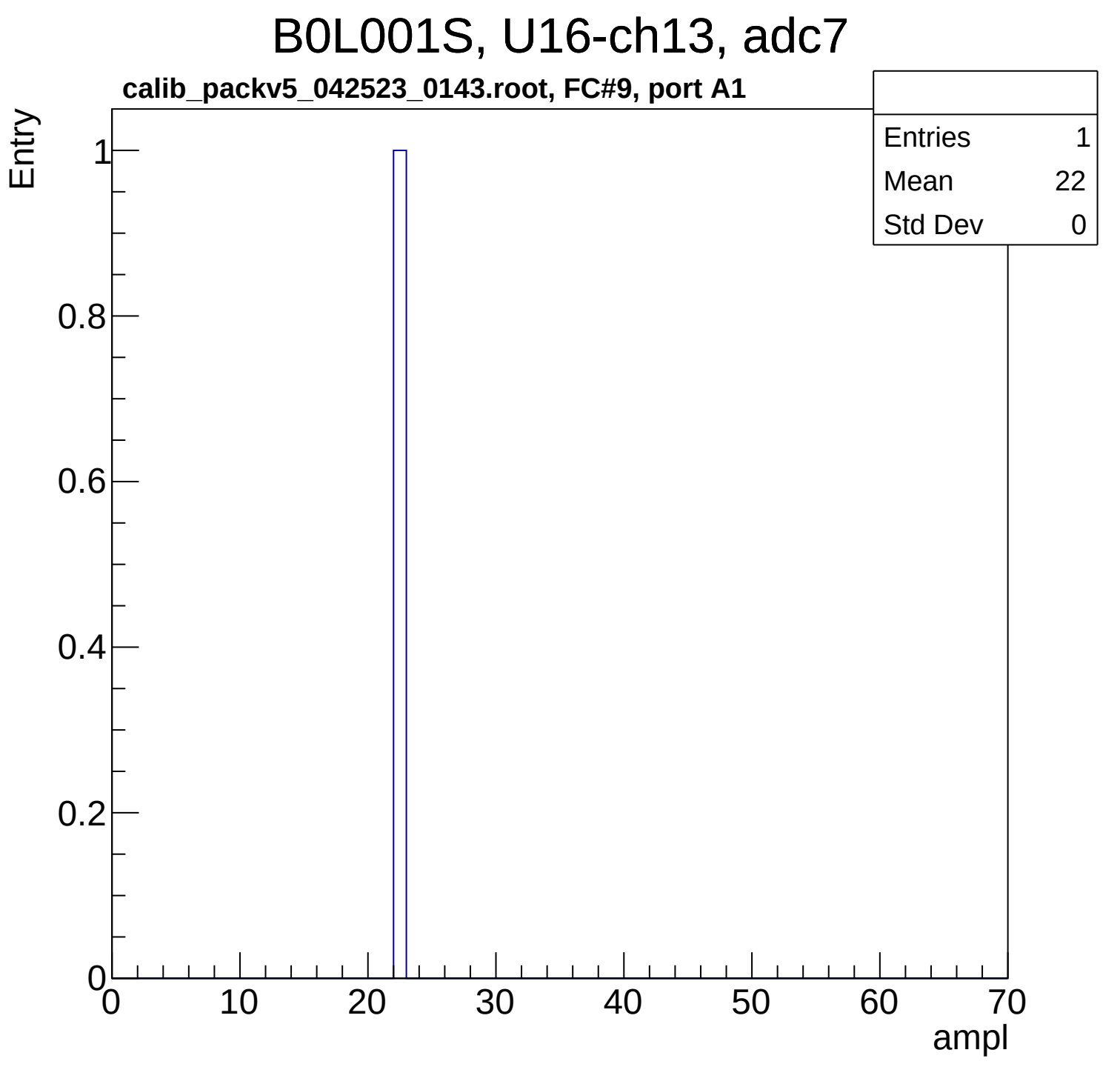

B0L001S, U16-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.44
Std Dev 3.307
Gaus mean : 44.0997
Gaus Width: 3.6431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

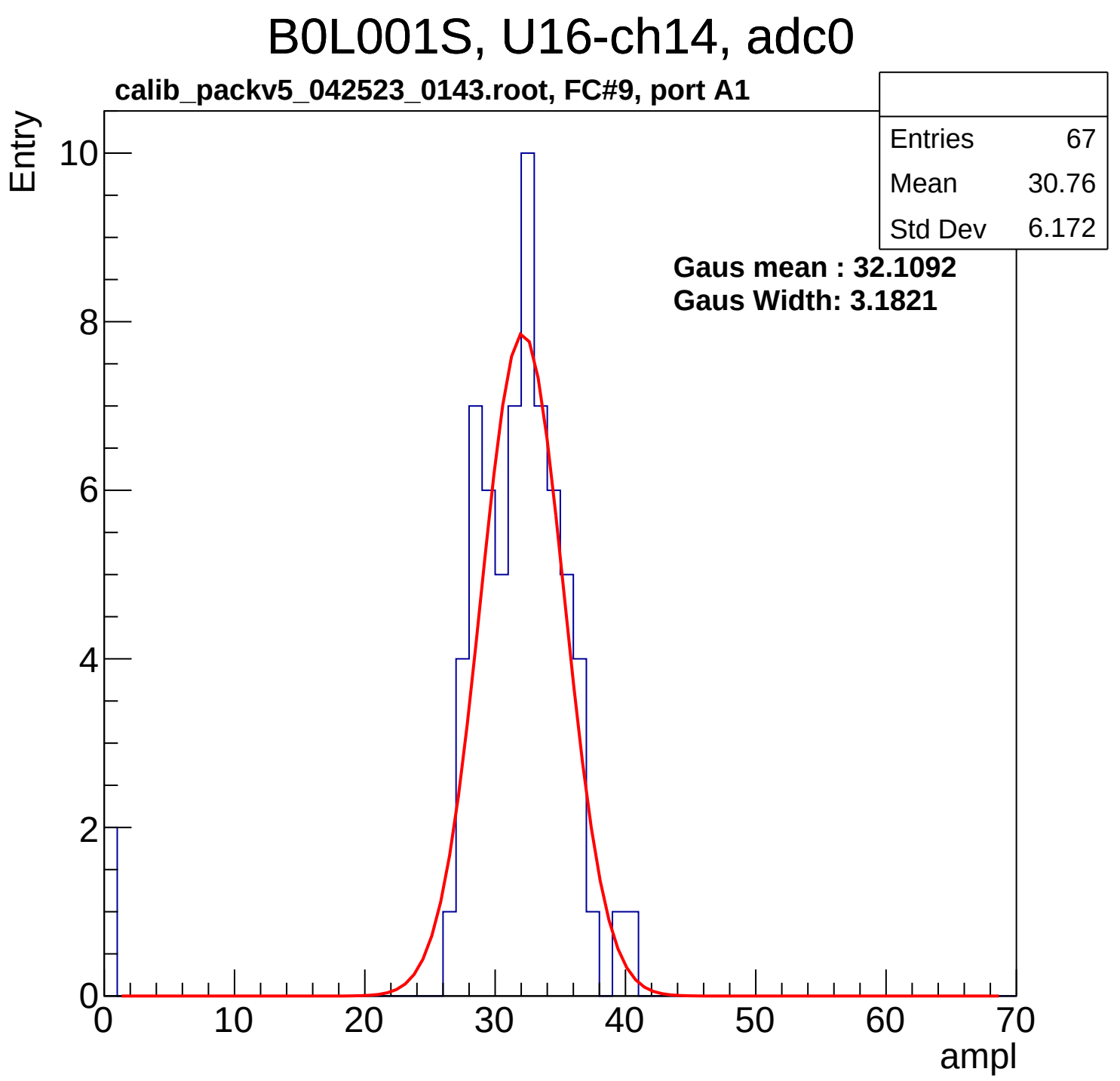

B0L001S, U16-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.88
Std Dev 3.501
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

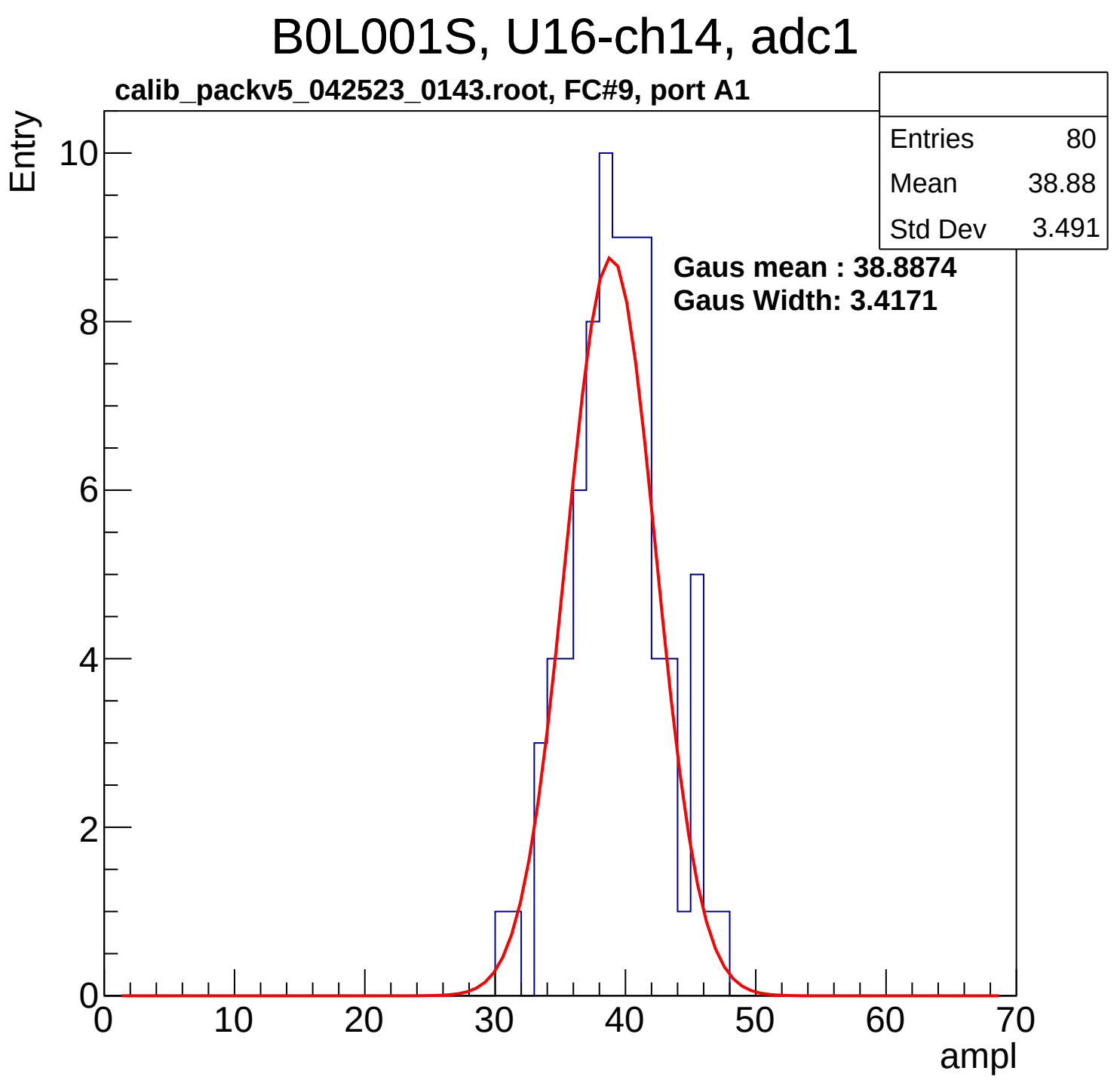

B0L001S, U16-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.29
Std Dev 7.701
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

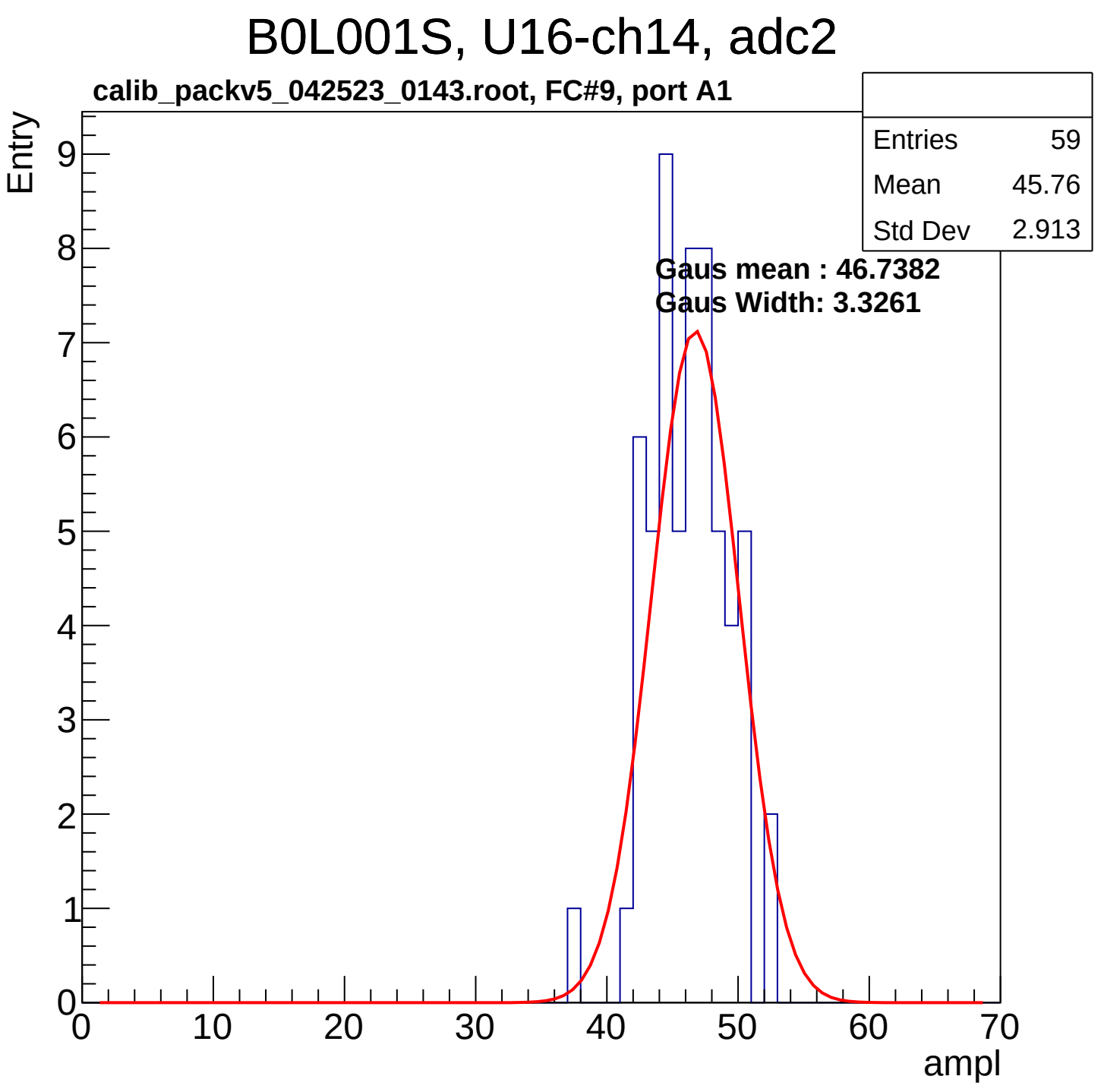

B0L001S, U16-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.59
Std Dev 1.965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

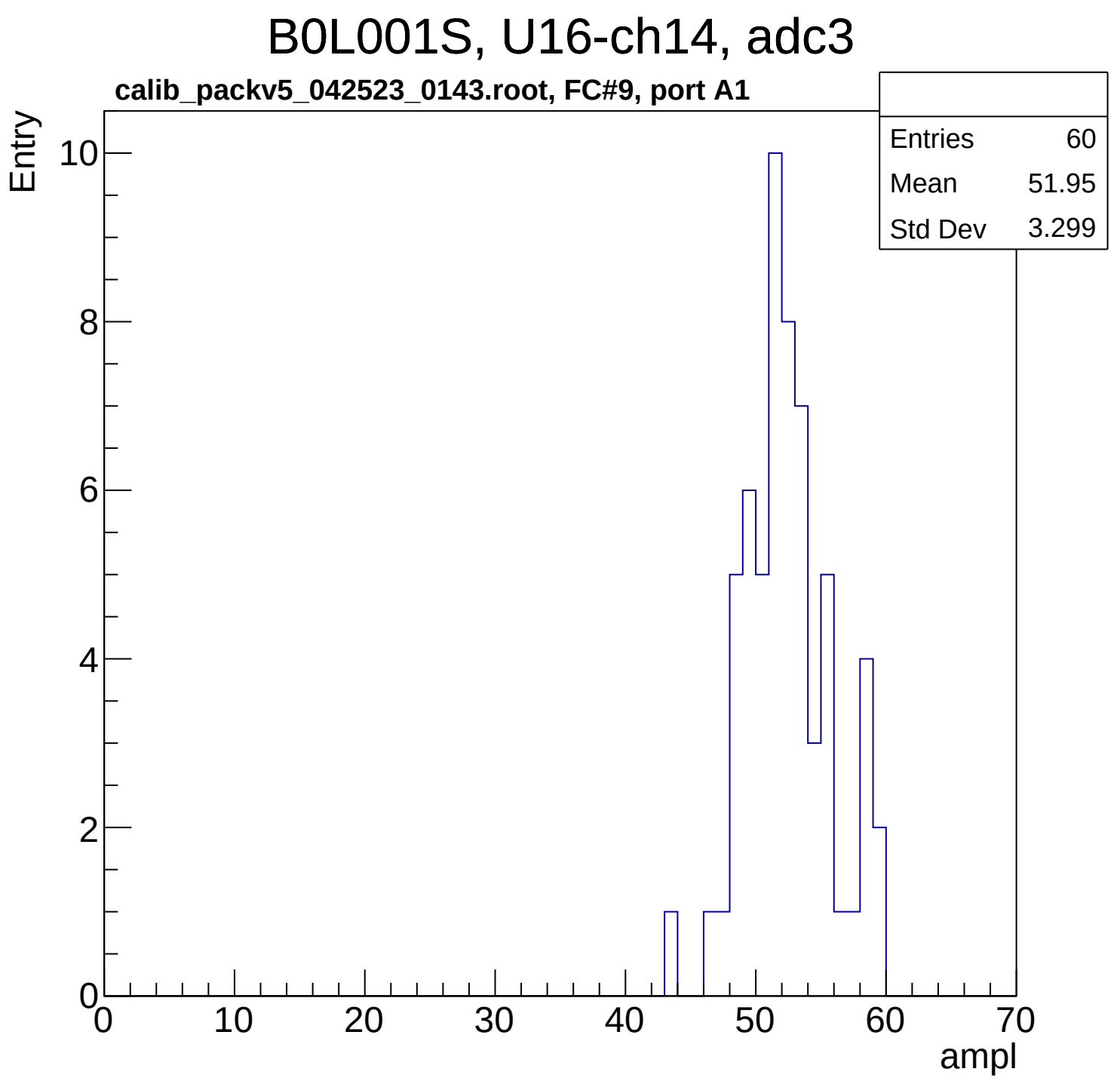

B0L001S, U16-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

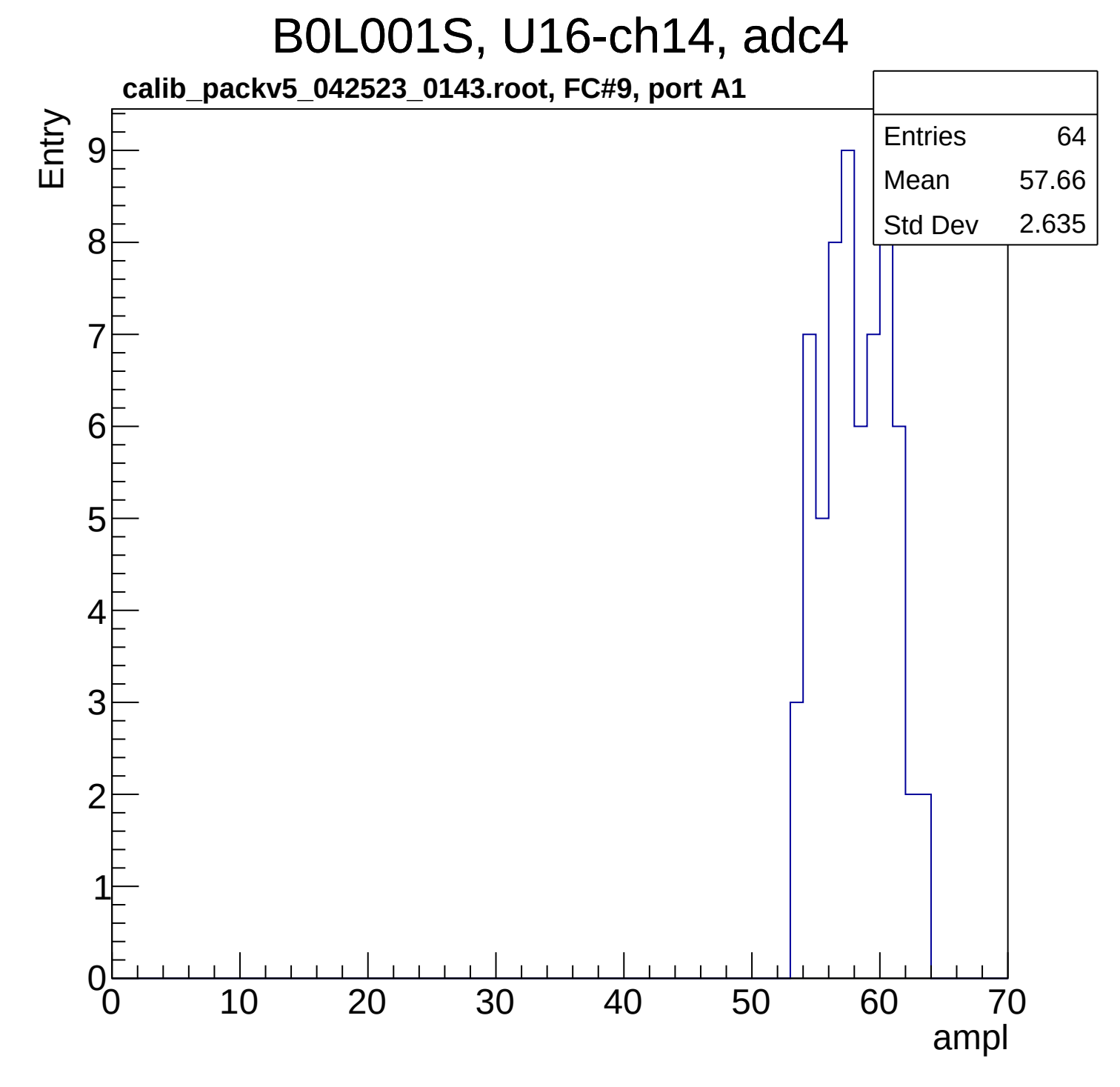

B0L001S, U16-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.76
Std Dev 6.172
Gaus mean : 32.1092
Gaus Width: 3.1821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 38.88
Std Dev 3.491
Gaus mean : 38.8874
Gaus Width: 3.4171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.76
Std Dev 2.913
Gaus mean : 46.7382
Gaus Width: 3.3261
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.95
Std Dev 3.299
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.66
Std Dev 2.635
Entry
ampl
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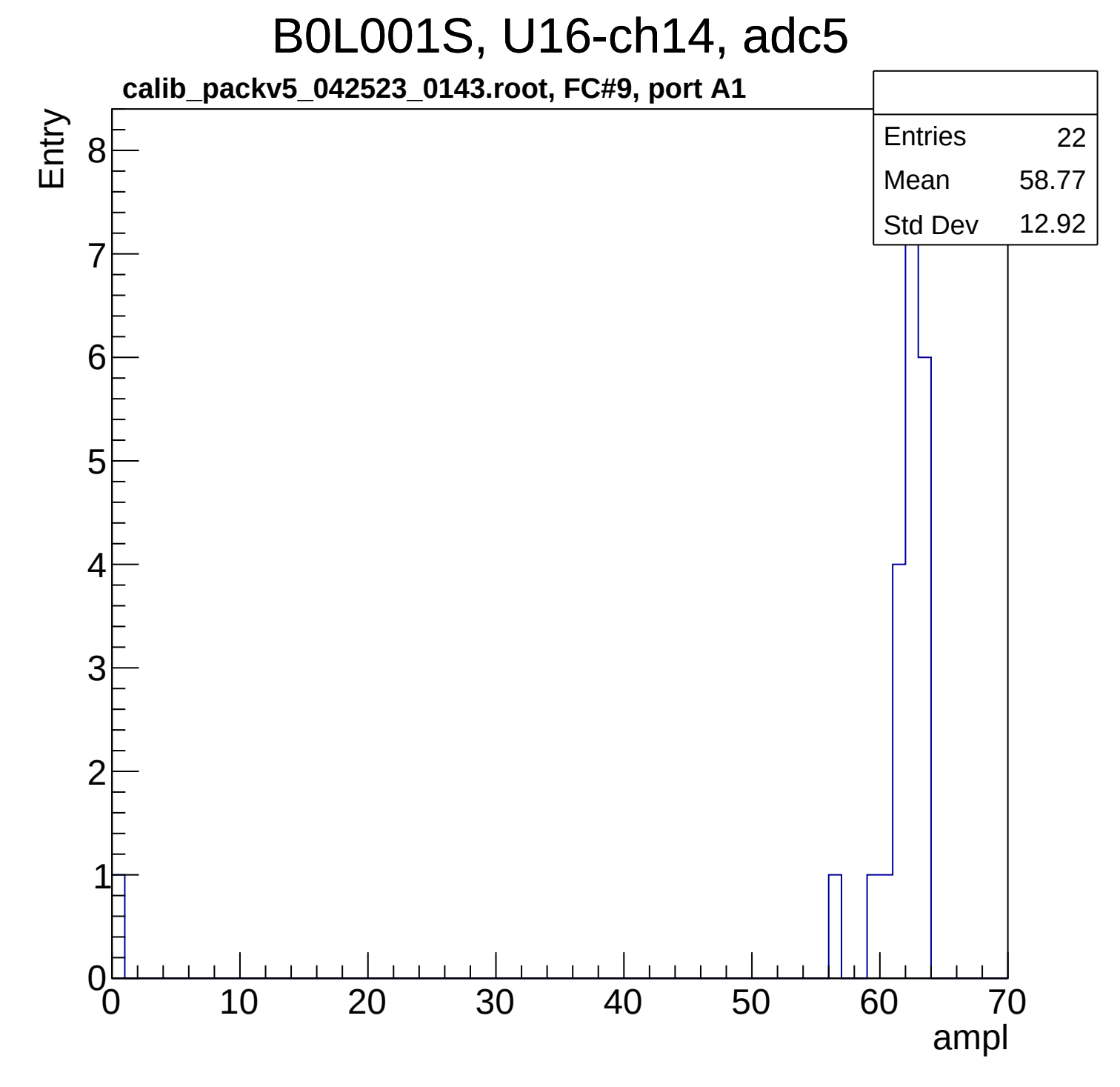
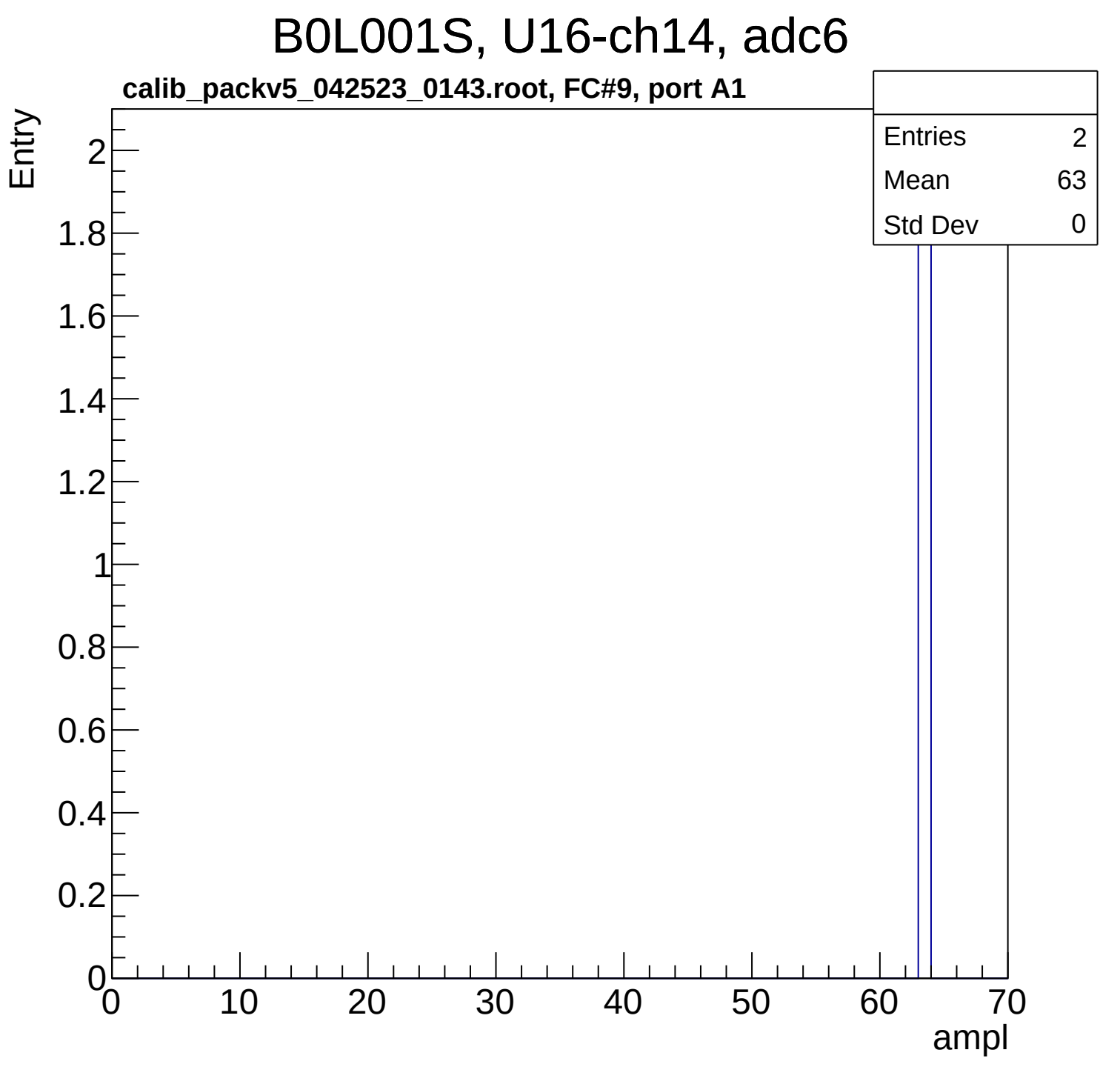
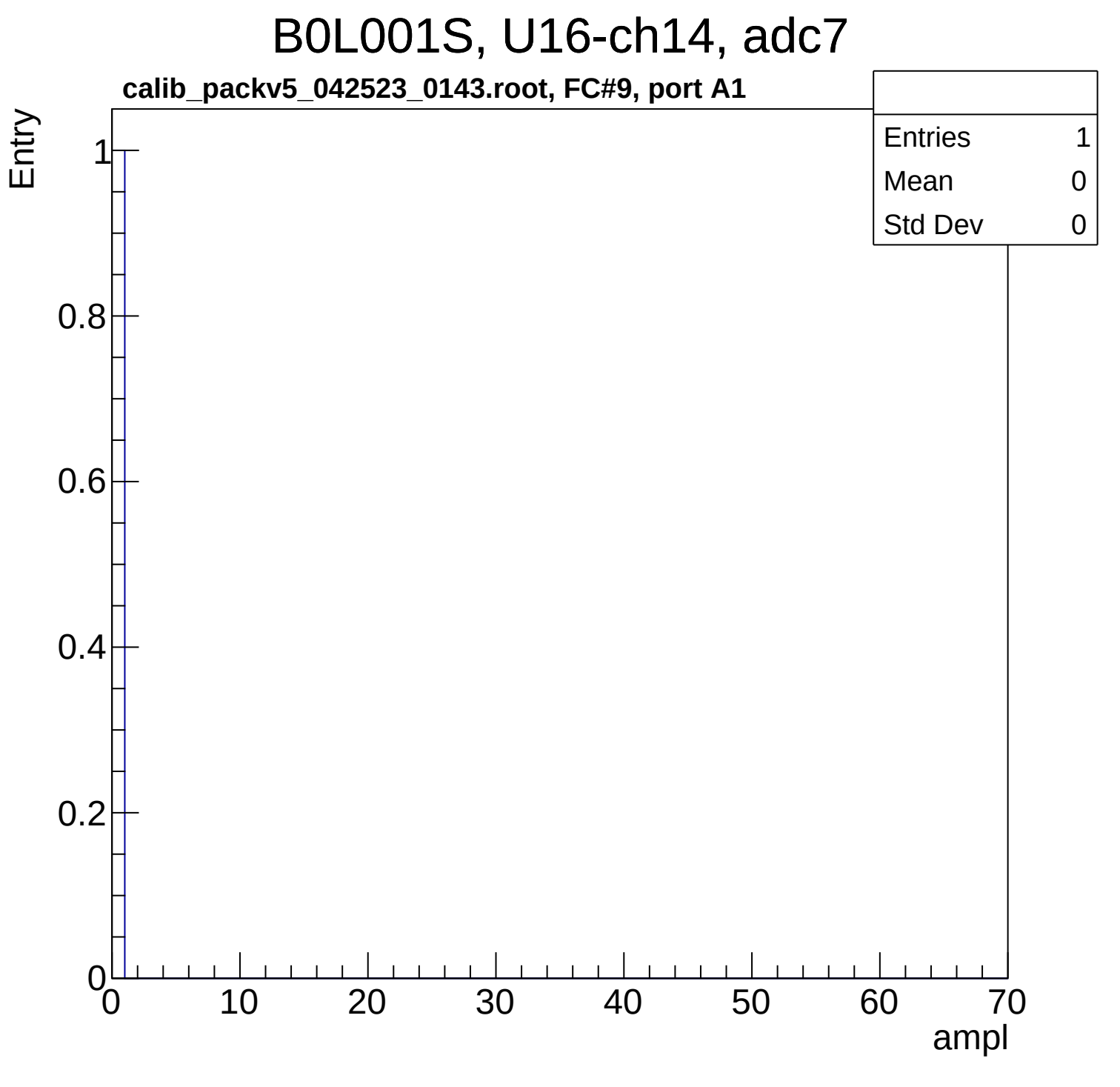
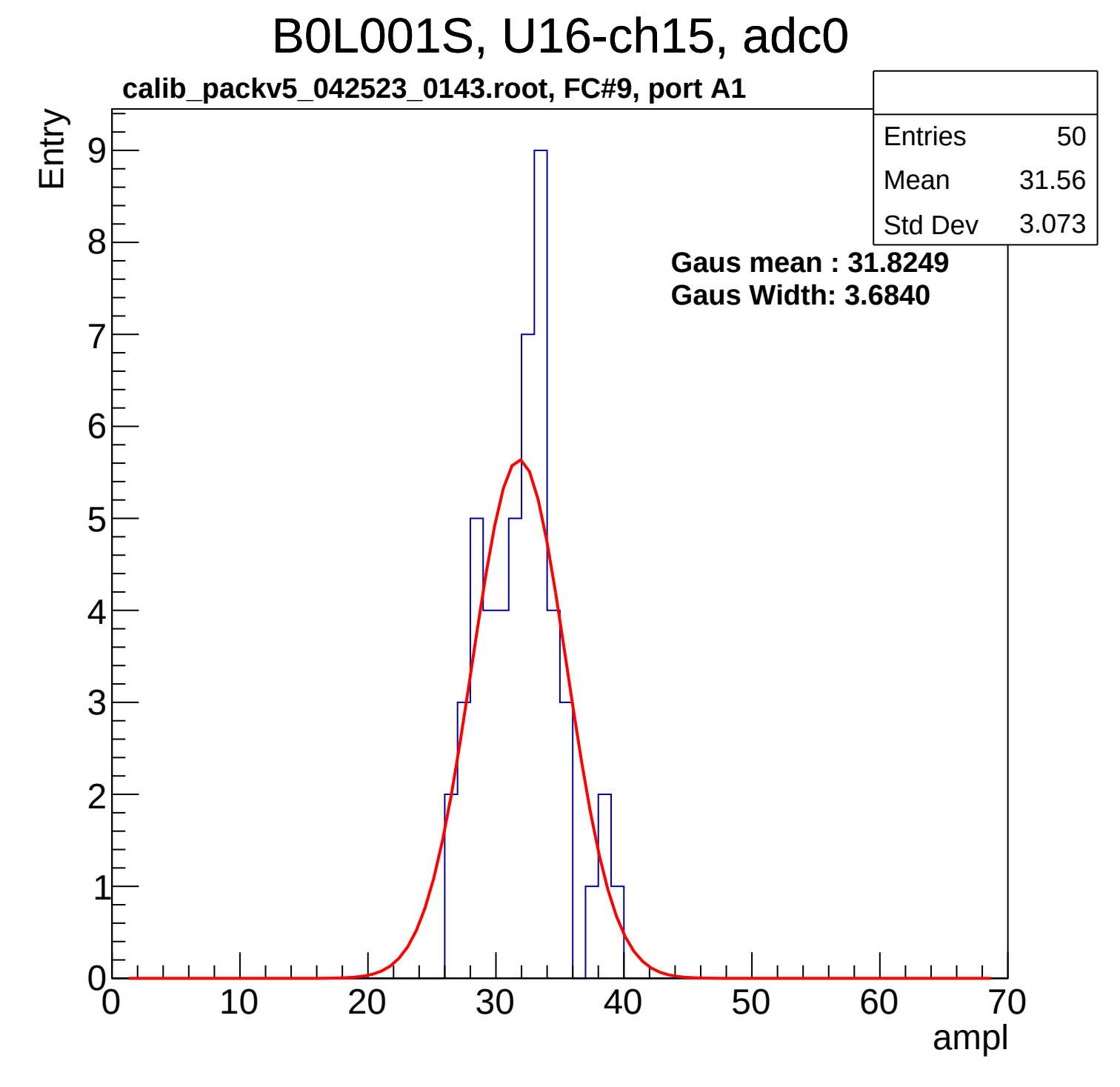
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.77
Std Dev 12.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 31.56
Std Dev 3.073
Gaus mean : 31.8249
Gaus Width: 3.6840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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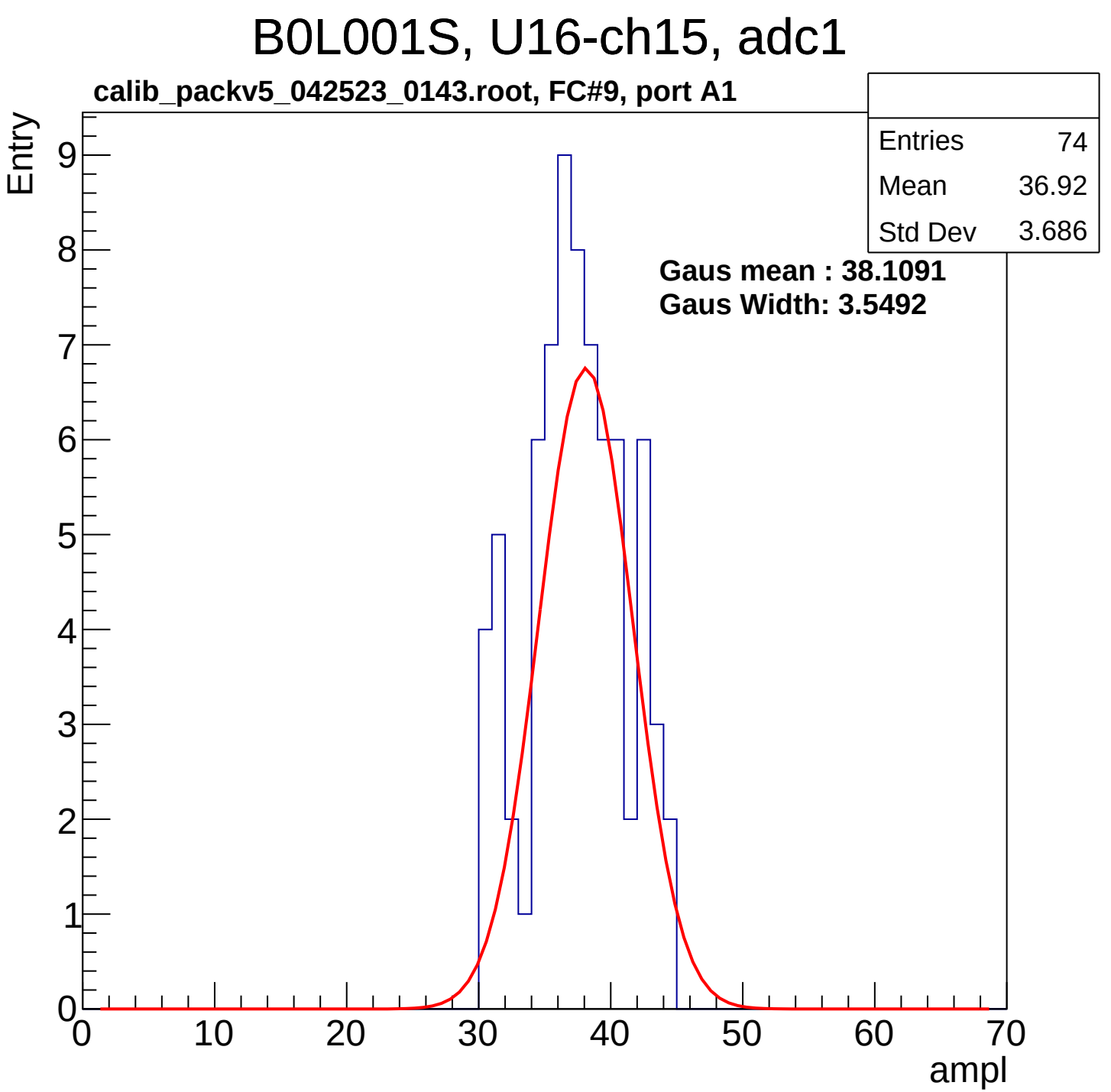
B0L001S, U16-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.92
Std Dev 3.686
Gaus mean : 38.1091
Gaus Width: 3.5492
Entry
ampl
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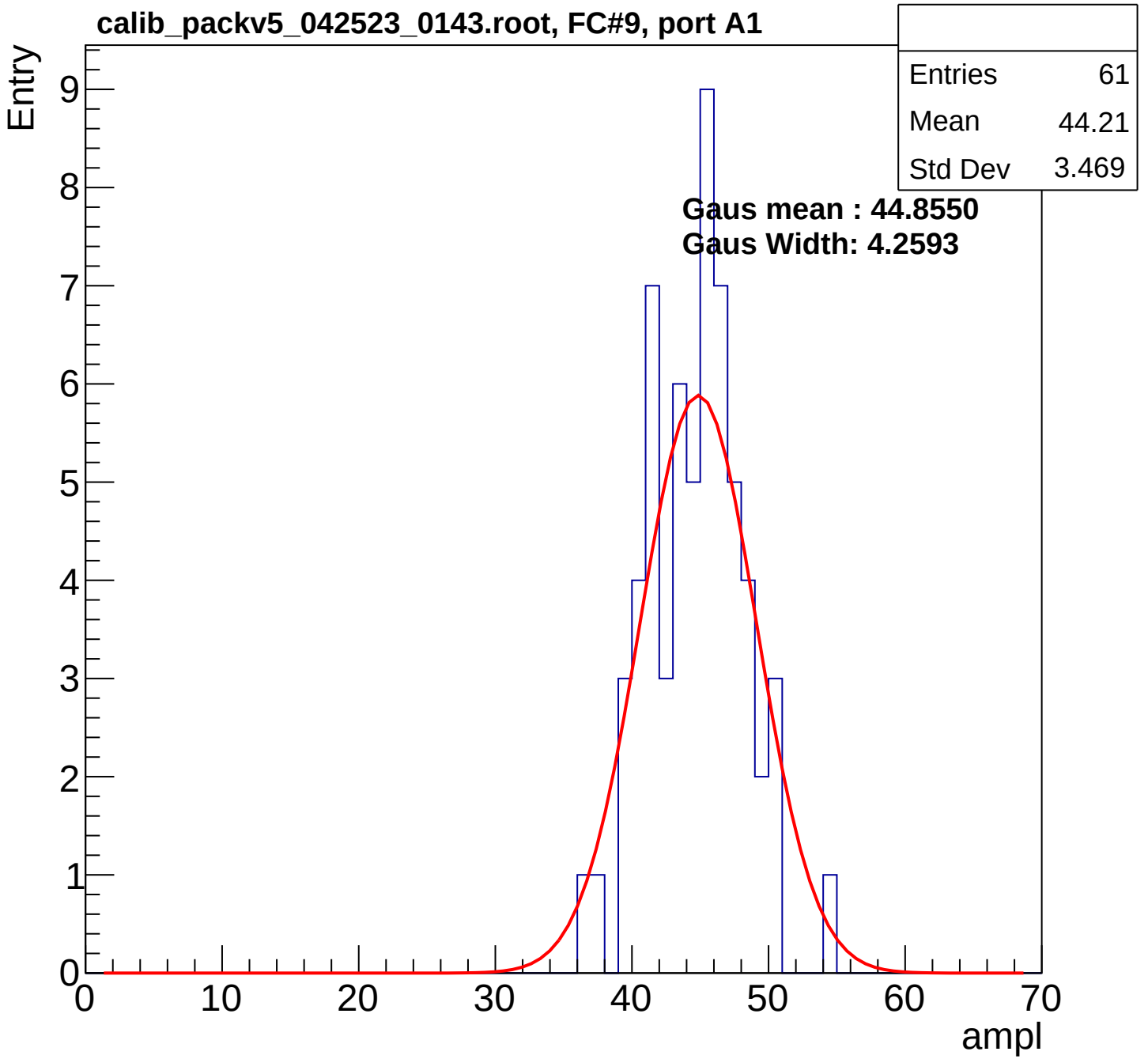
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.21
Std Dev 3.469
Gaus mean : 44.8550
Gaus Width: 4.2593
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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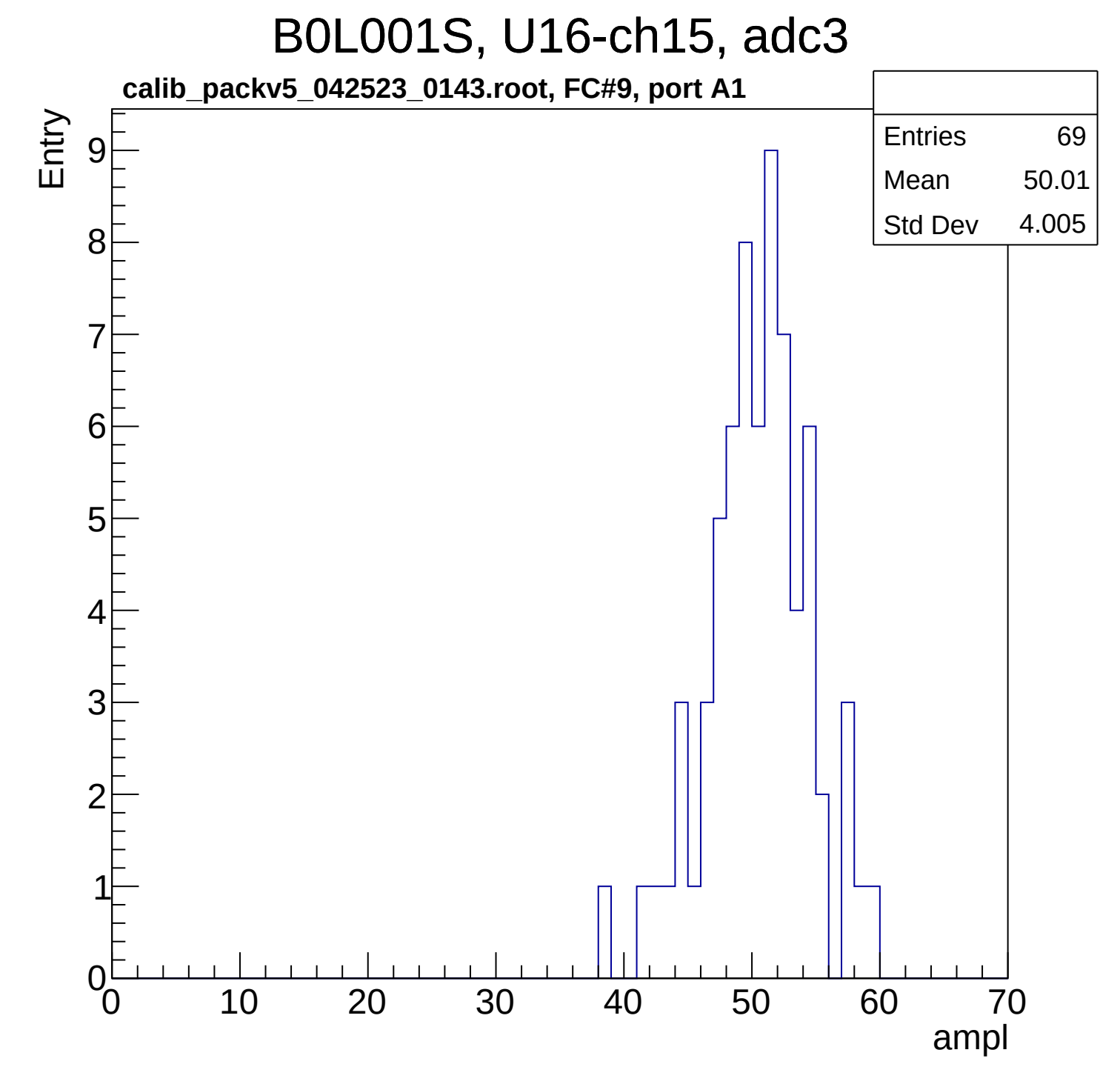
40
50
60
70


B0L001S, U16-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.01
Std Dev 4.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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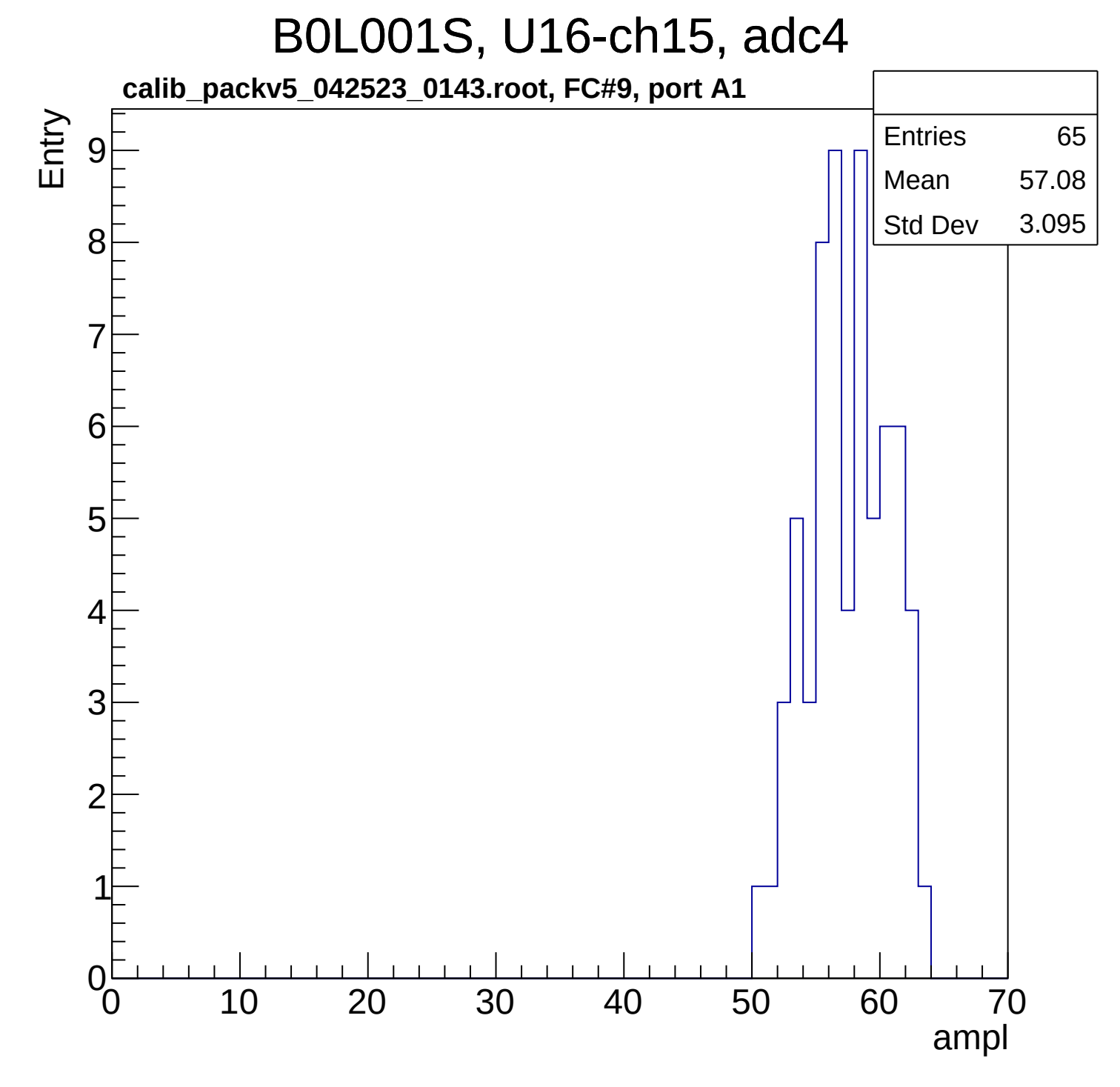
B0L001S, U16-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.08
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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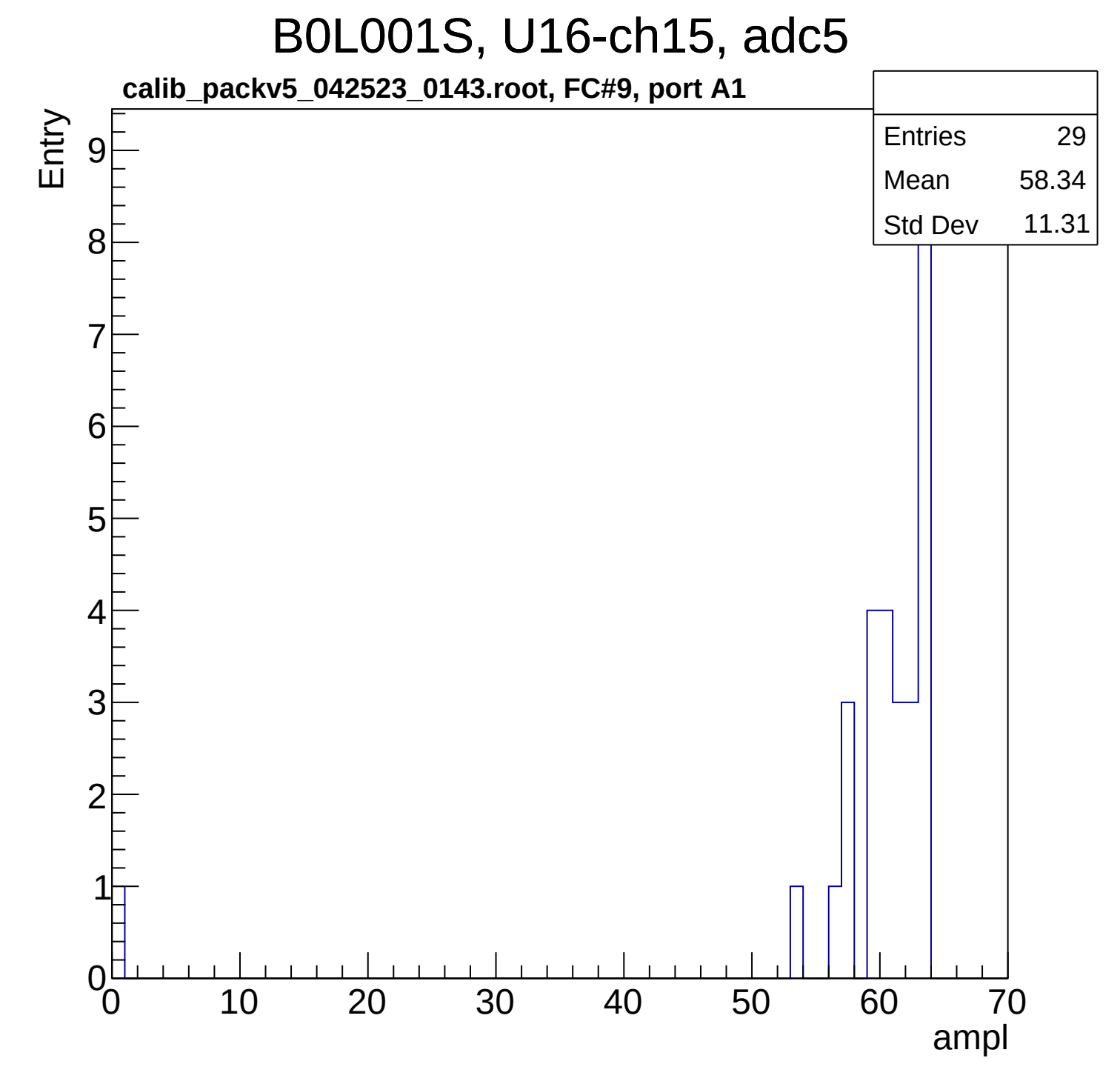
B0L001S, U16-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.34
Std Dev 11.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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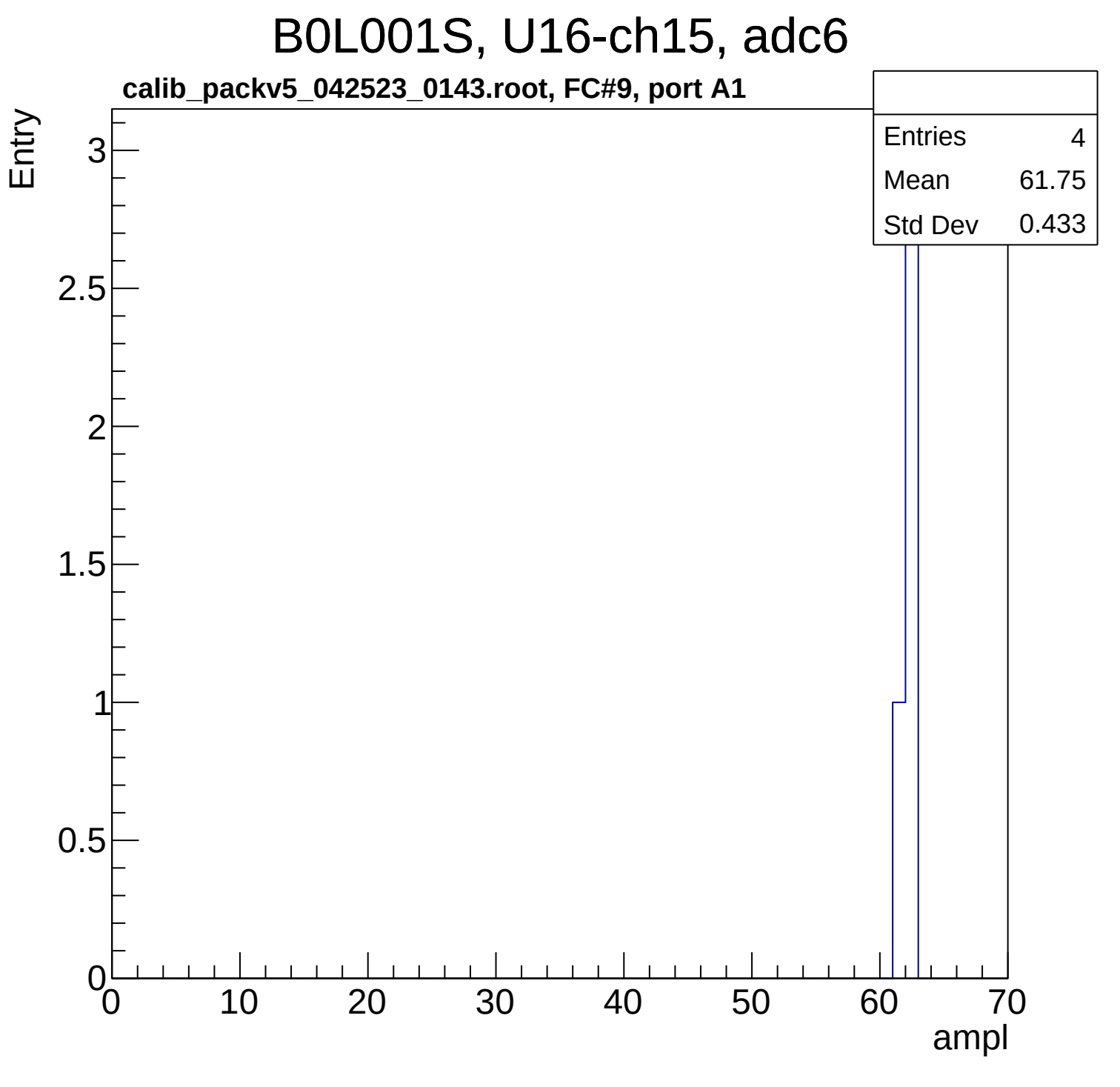
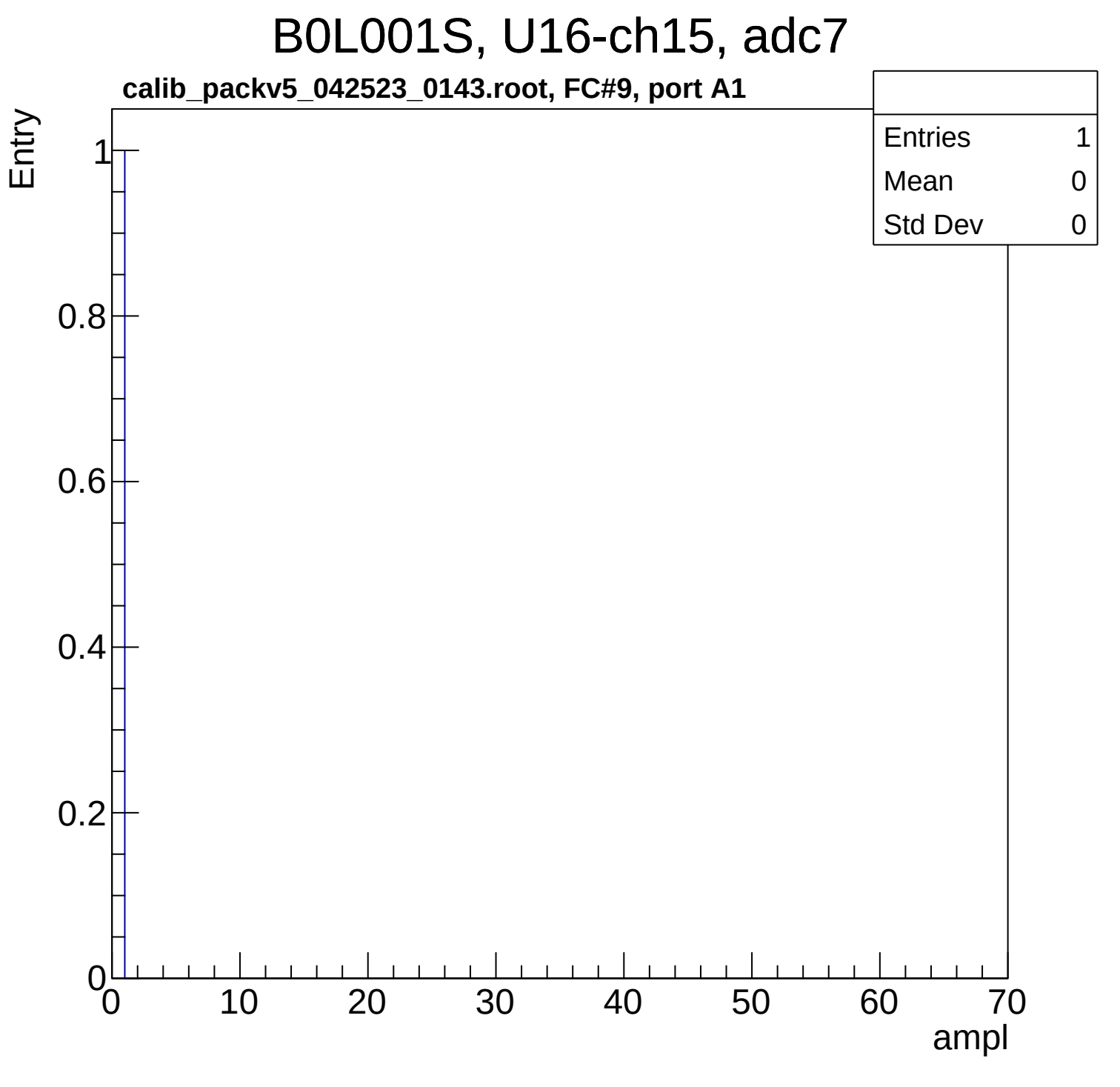
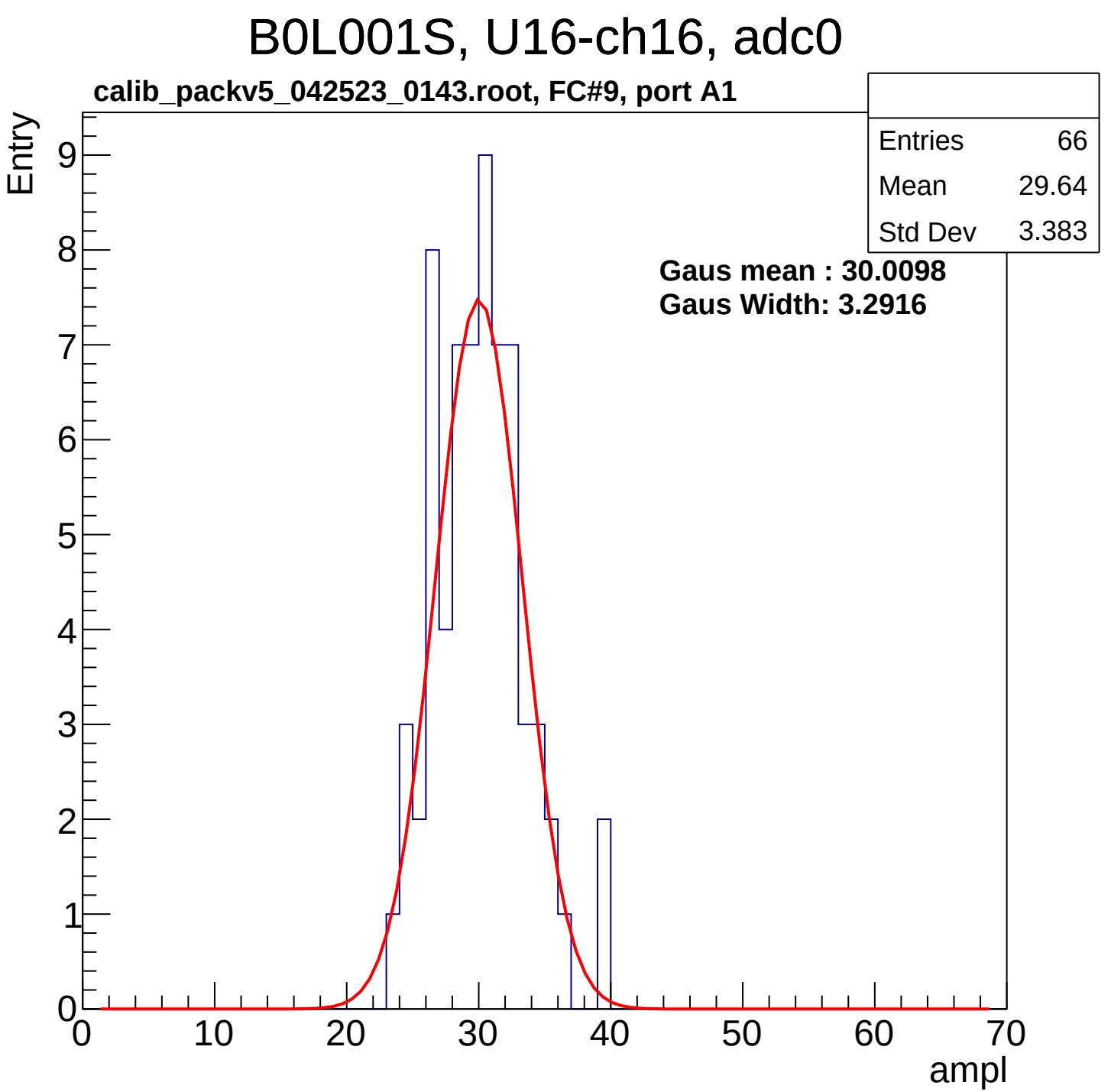

B0L001S, U16-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.64
Std Dev 3.383
Gaus mean : 30.0098
Gaus Width: 3.2916
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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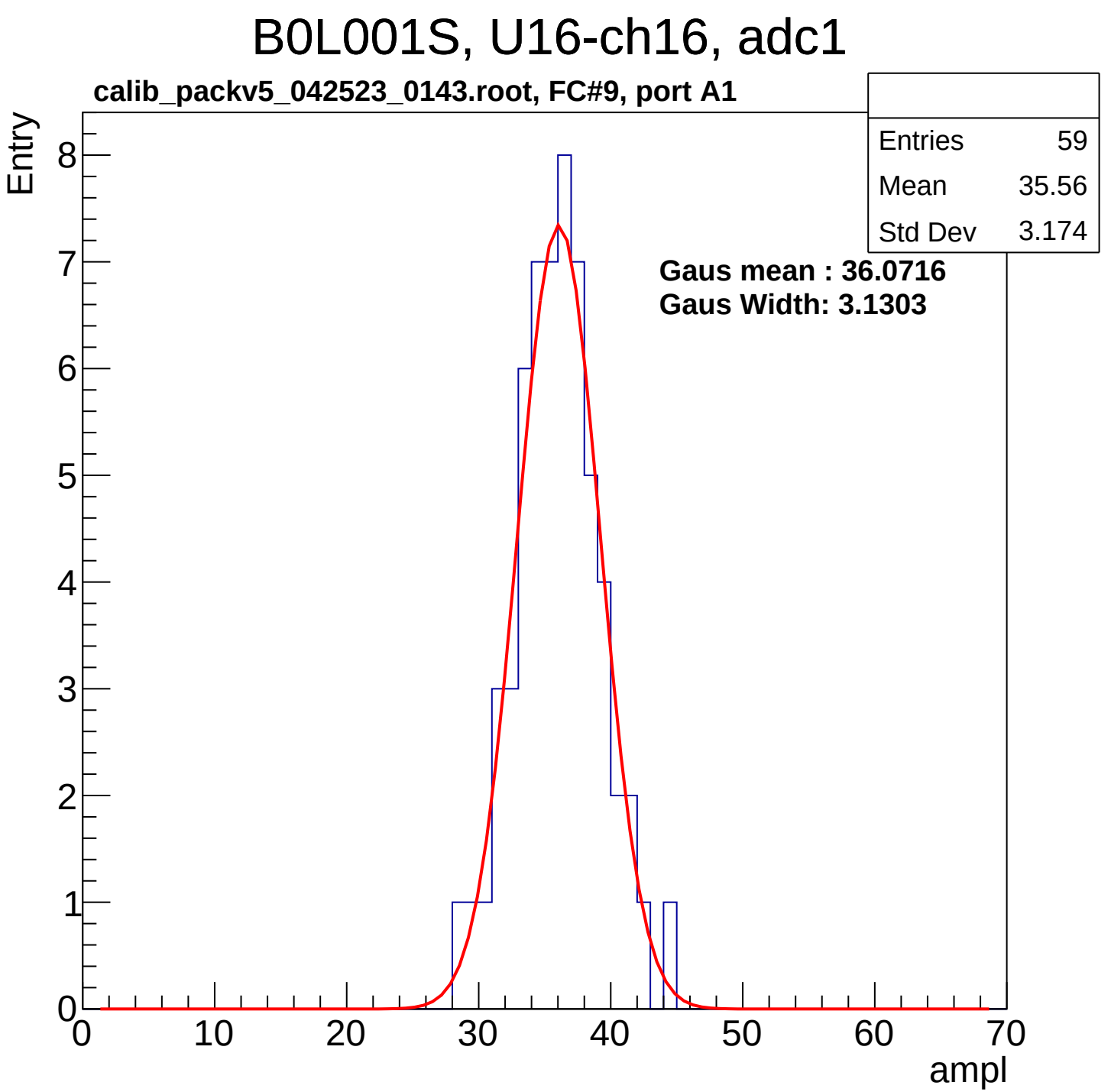
B0L001S, U16-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 35.56
Std Dev 3.174
Gaus mean : 36.0716
Gaus Width: 3.1303
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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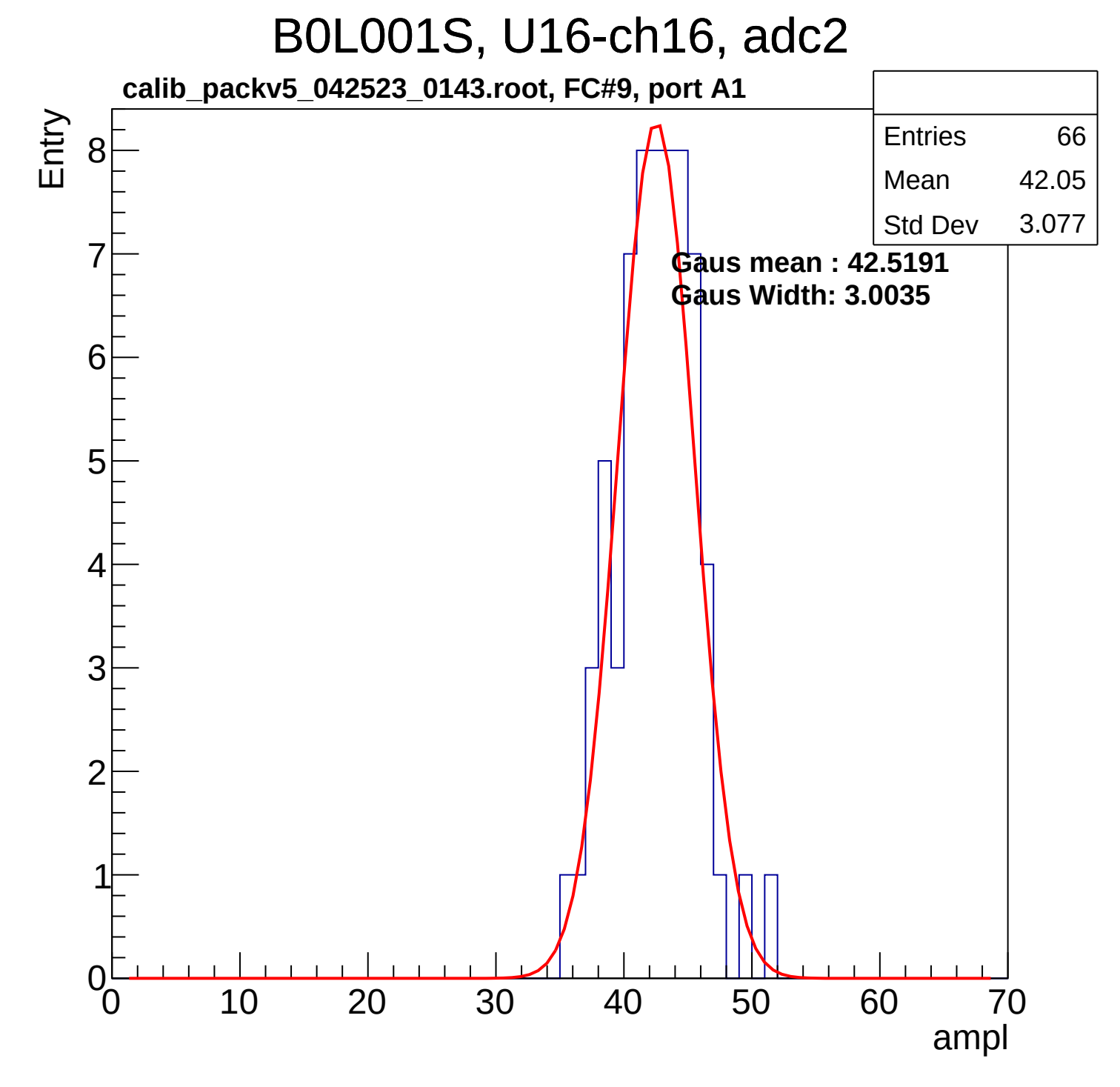
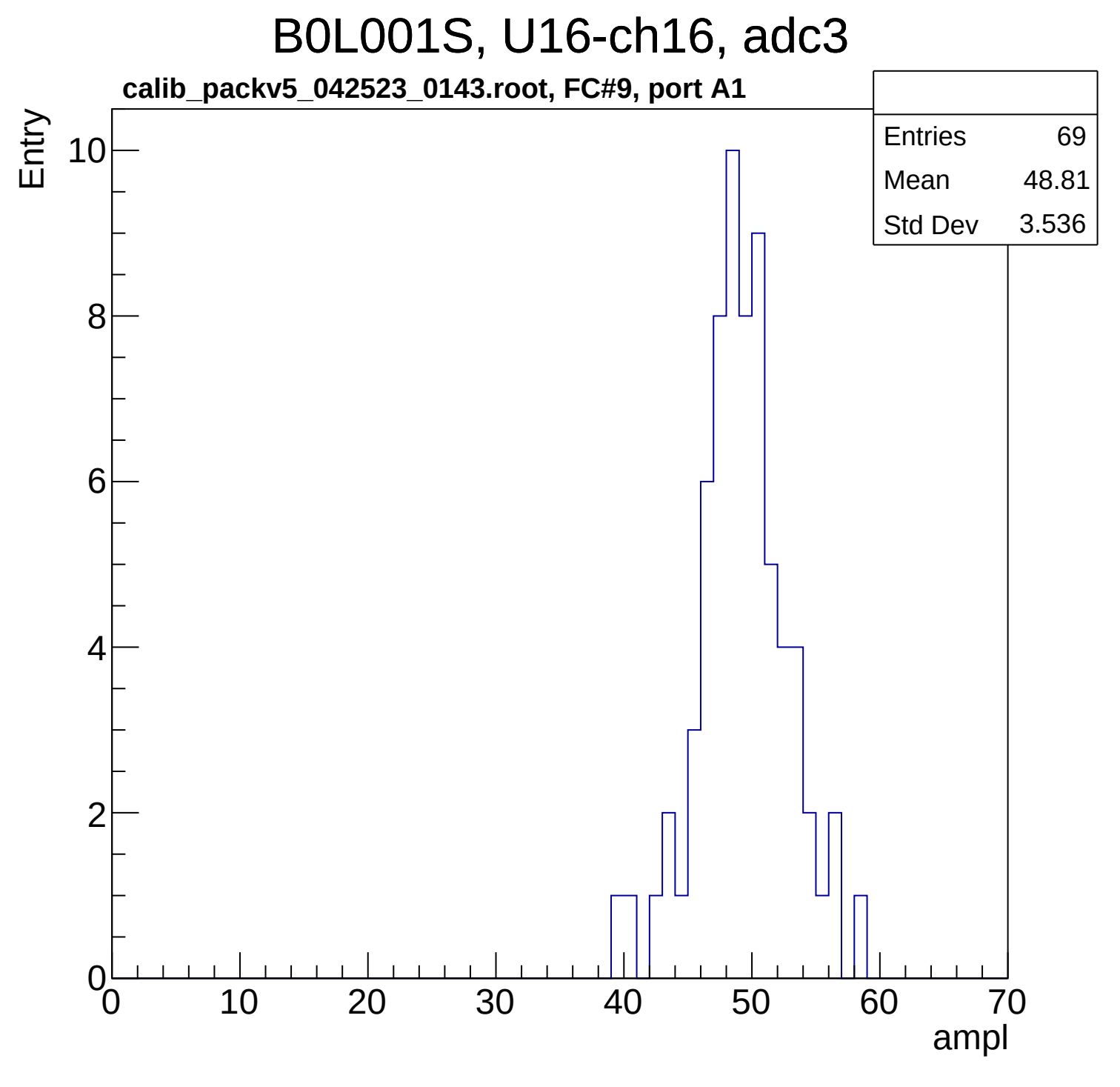
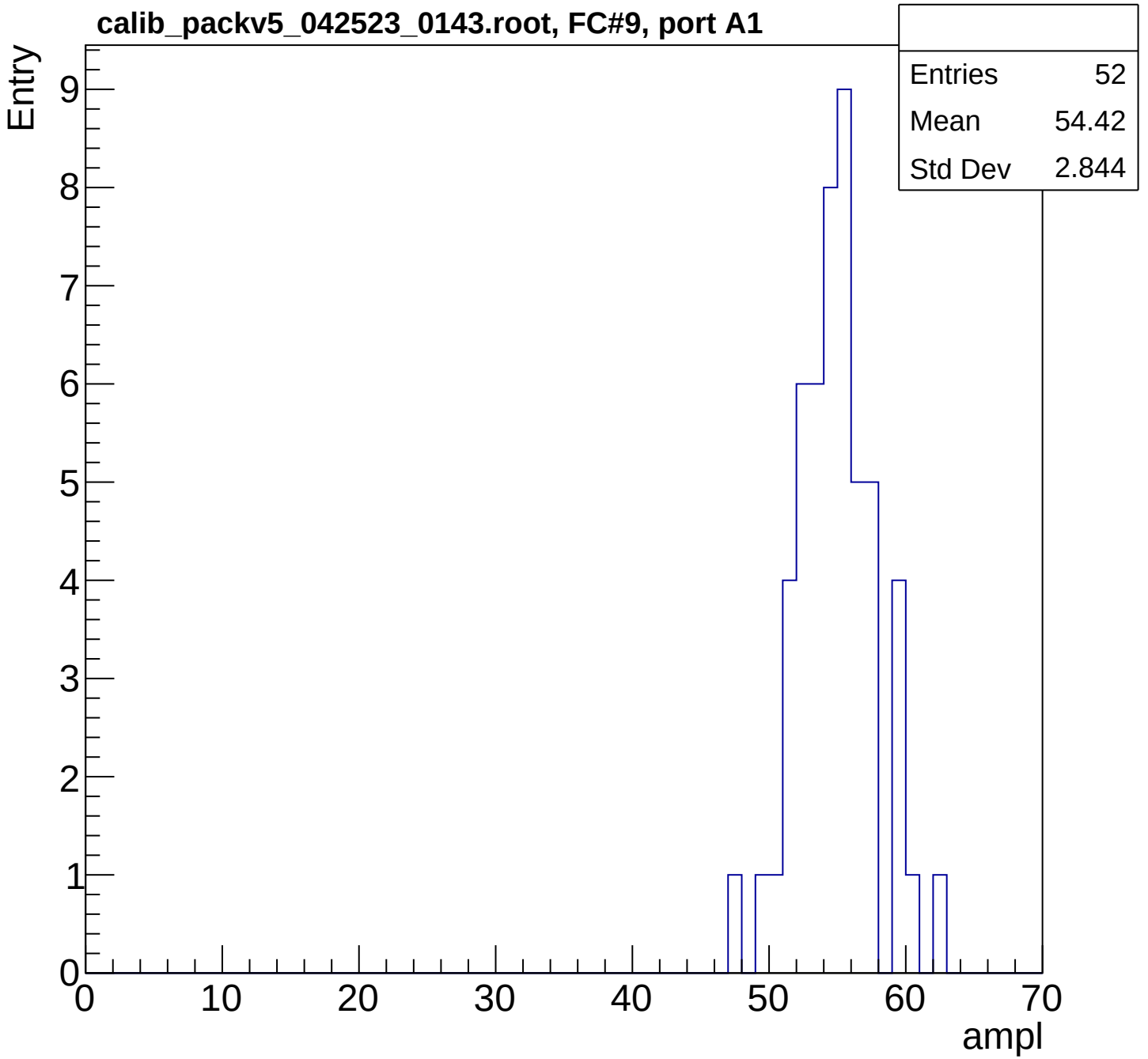
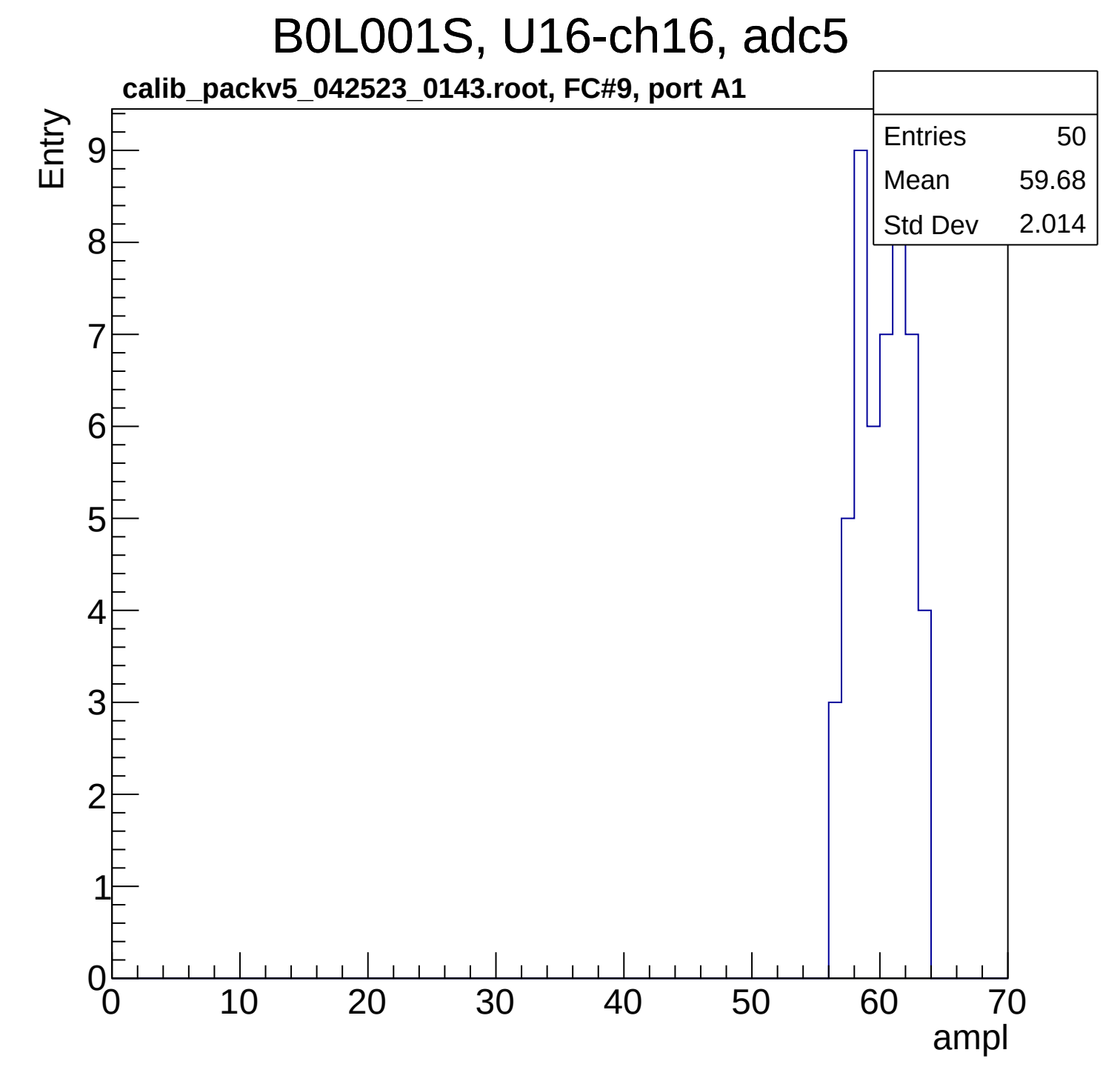
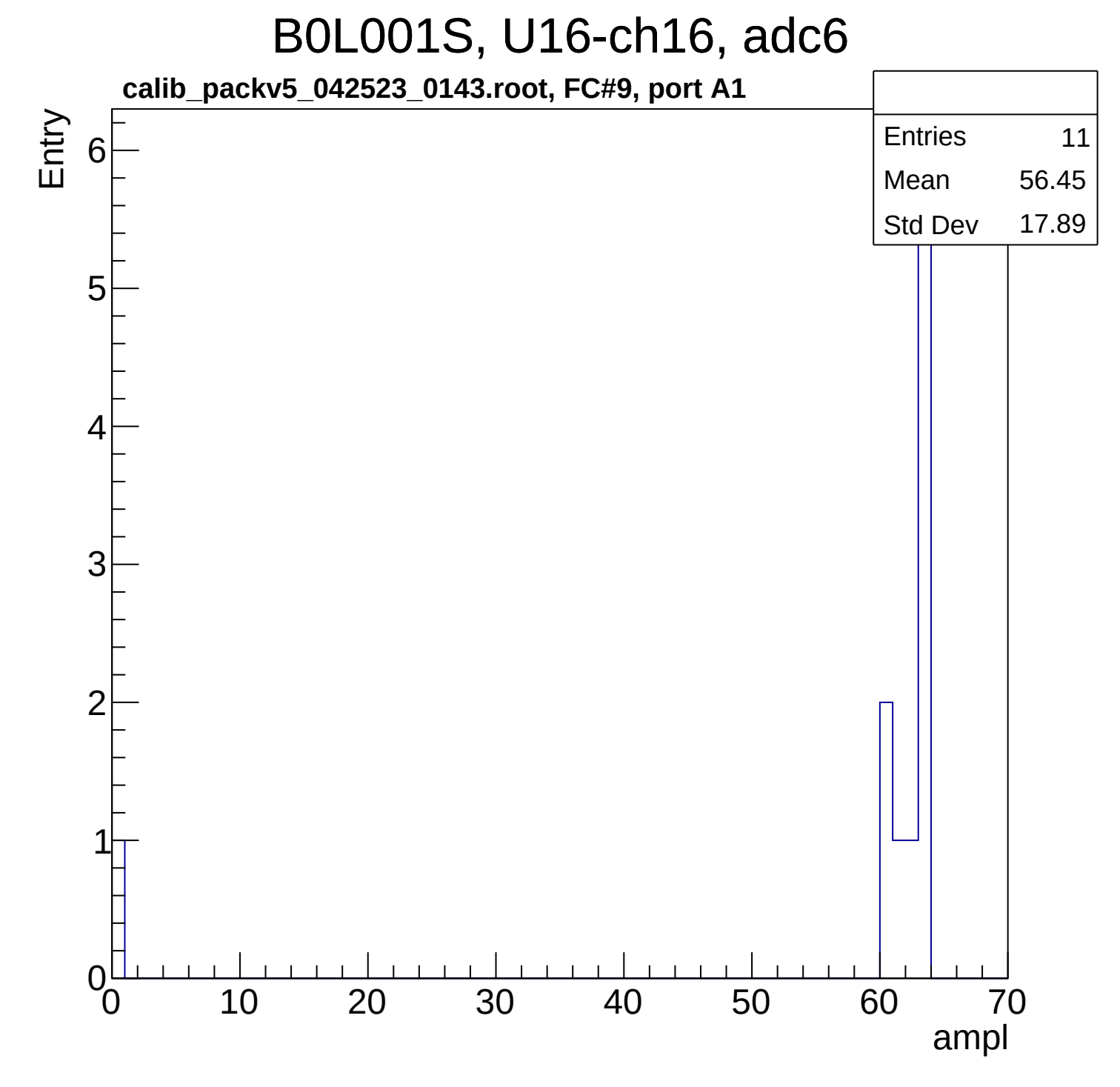
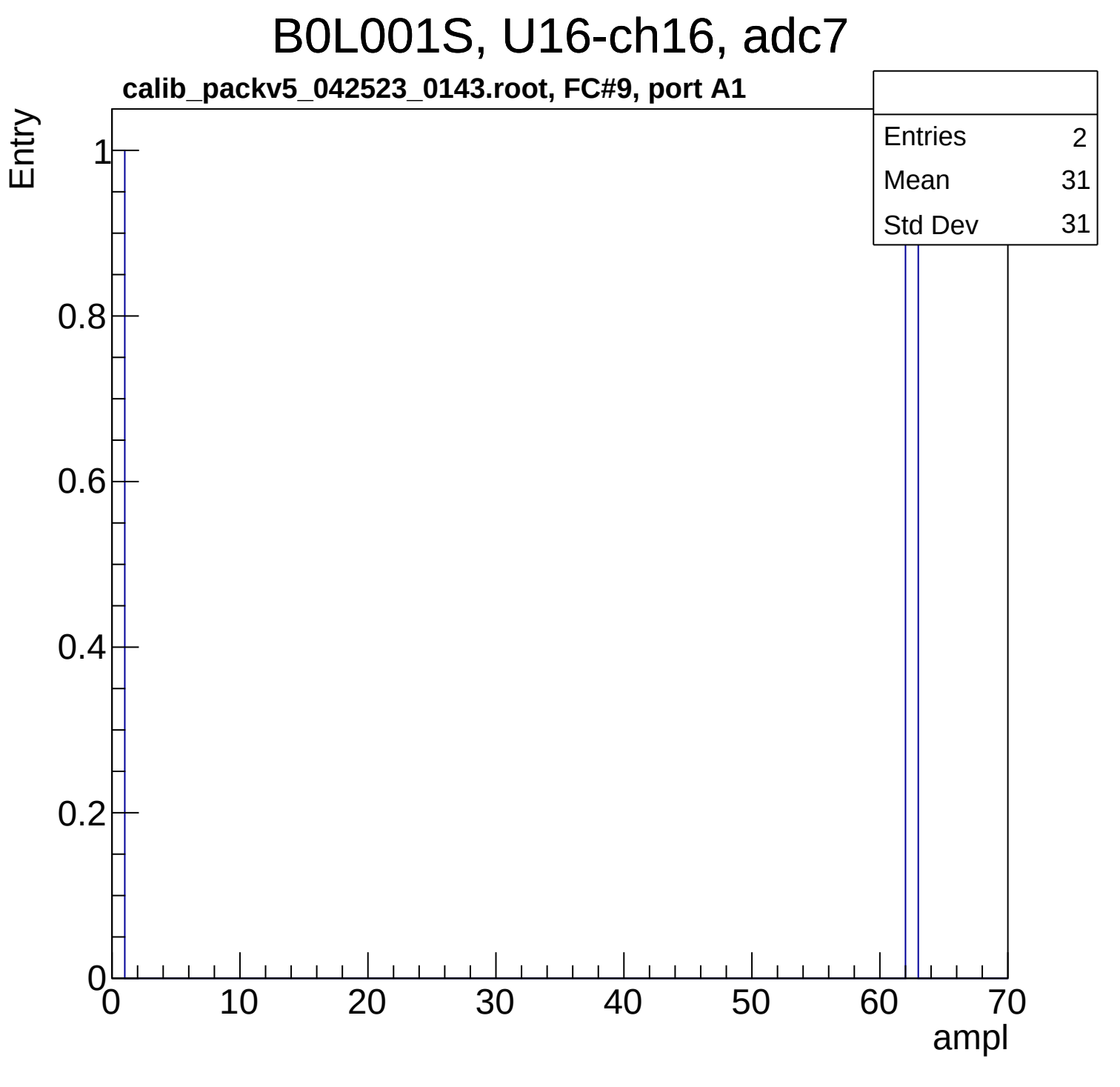

B0L001S, U16-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.05
Std Dev 3.077
Gaus mean : 42.5191
Gaus Width: 3.0035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

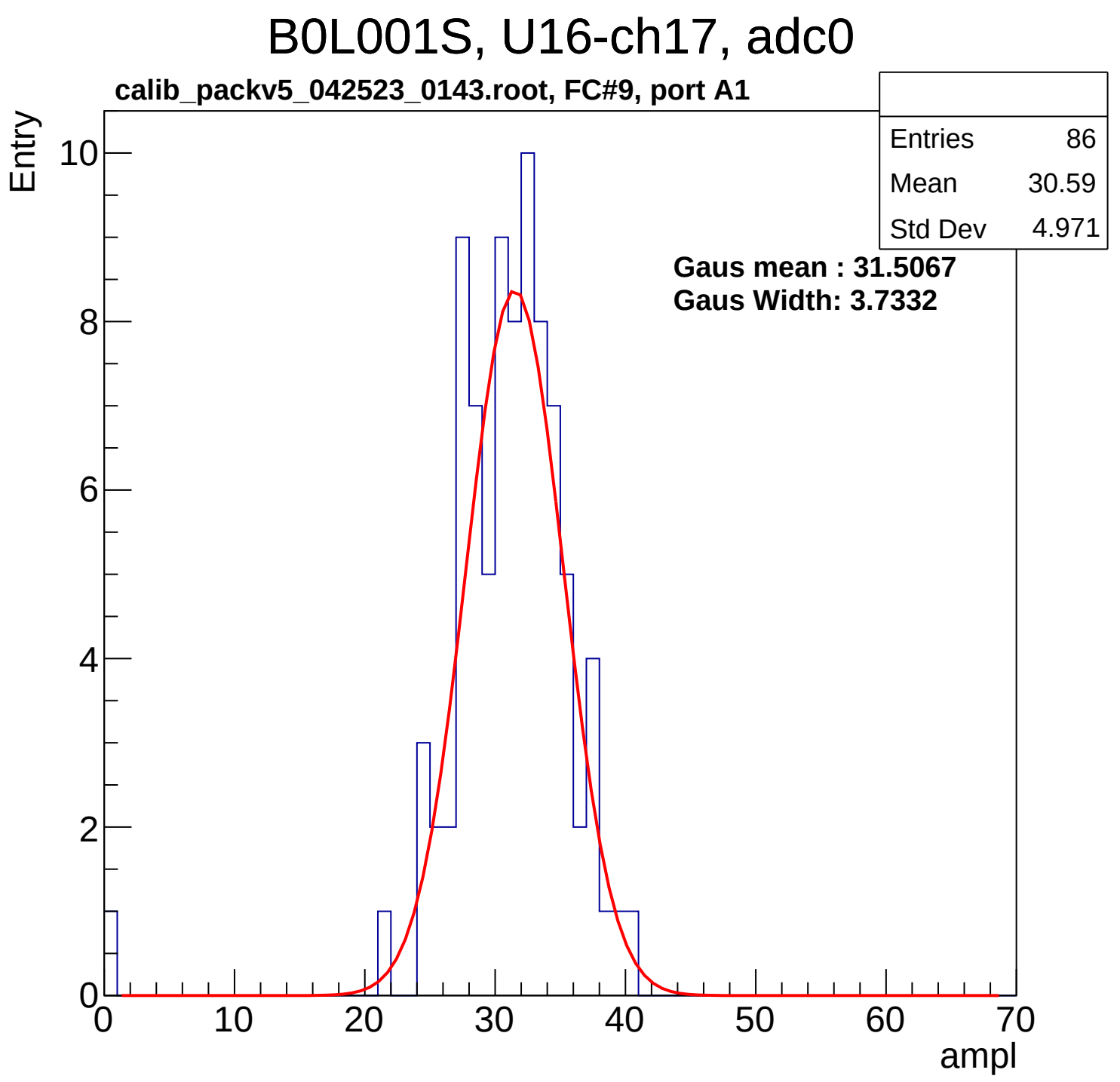

B0L001S, U16-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 48.81
Std Dev 3.536
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

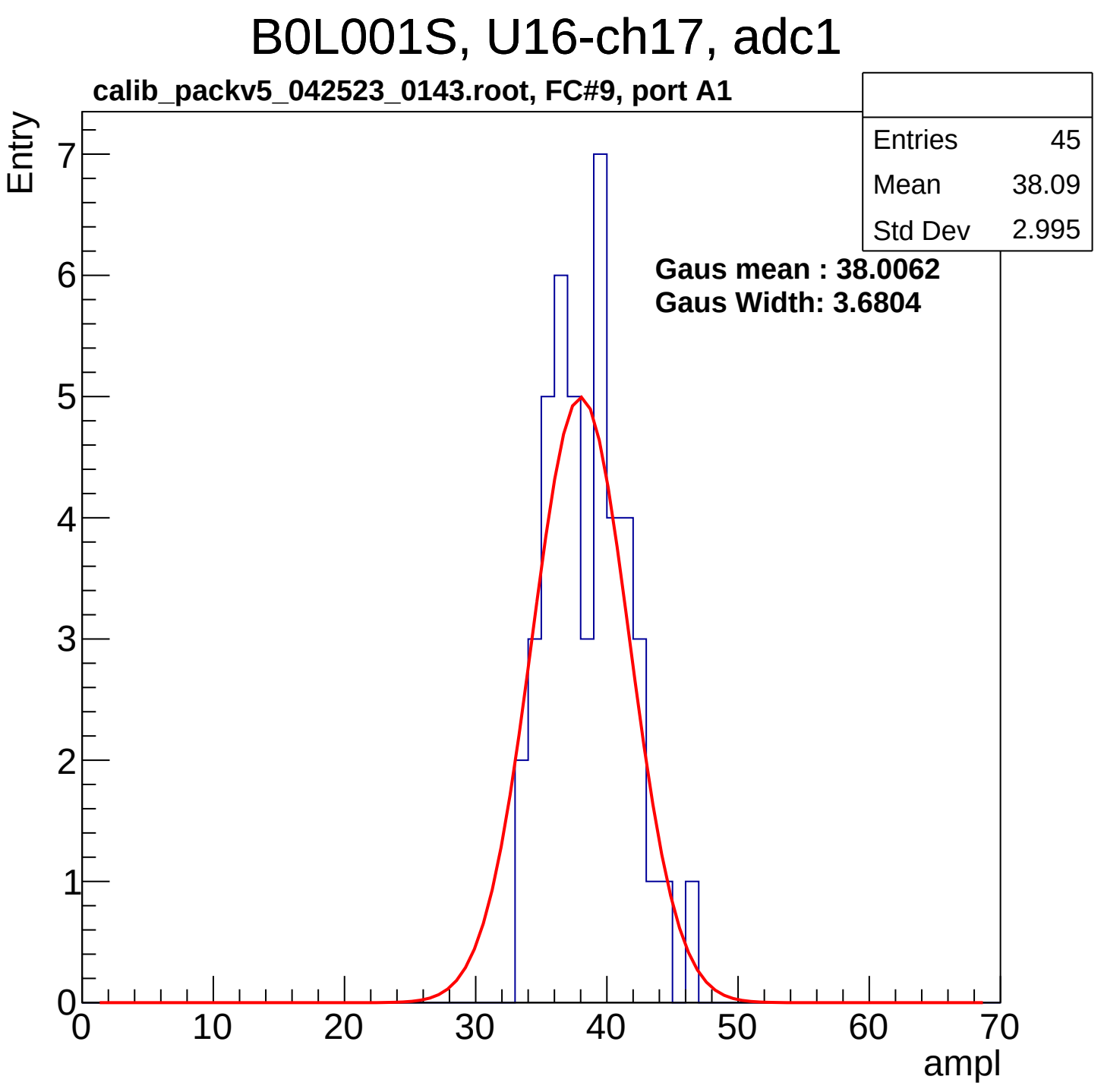

B0L001S, U16-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 54.42
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

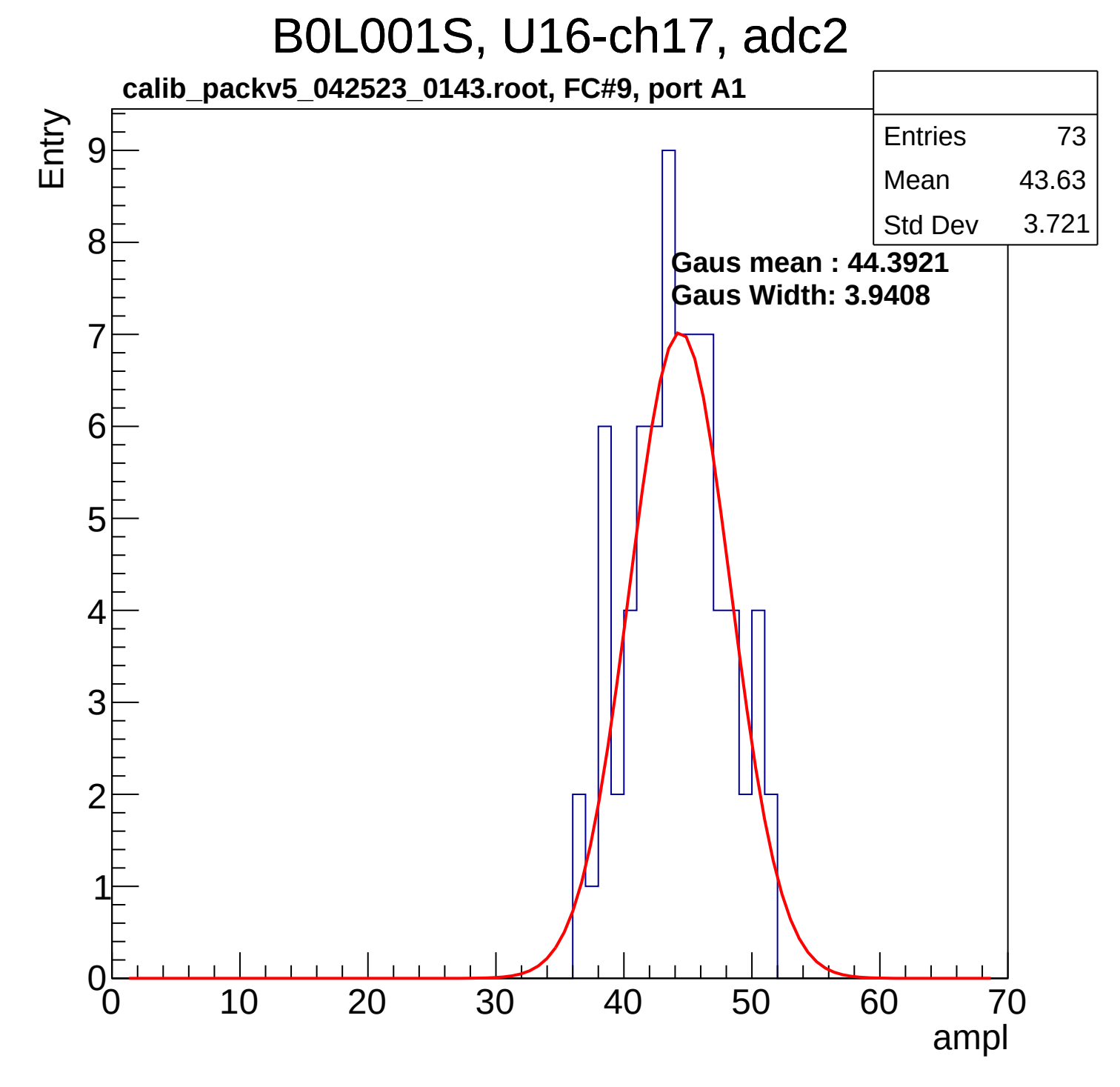

B0L001S, U16-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.68
Std Dev 2.014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 56.45
Std Dev 17.89
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 30.59
Std Dev 4.971
Gaus mean : 31.5067
Gaus Width: 3.7332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 38.09
Std Dev 2.995
Gaus mean : 38.0062
Gaus Width: 3.6804
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.63
Std Dev 3.721
Gaus mean : 44.3921
Gaus Width: 3.9408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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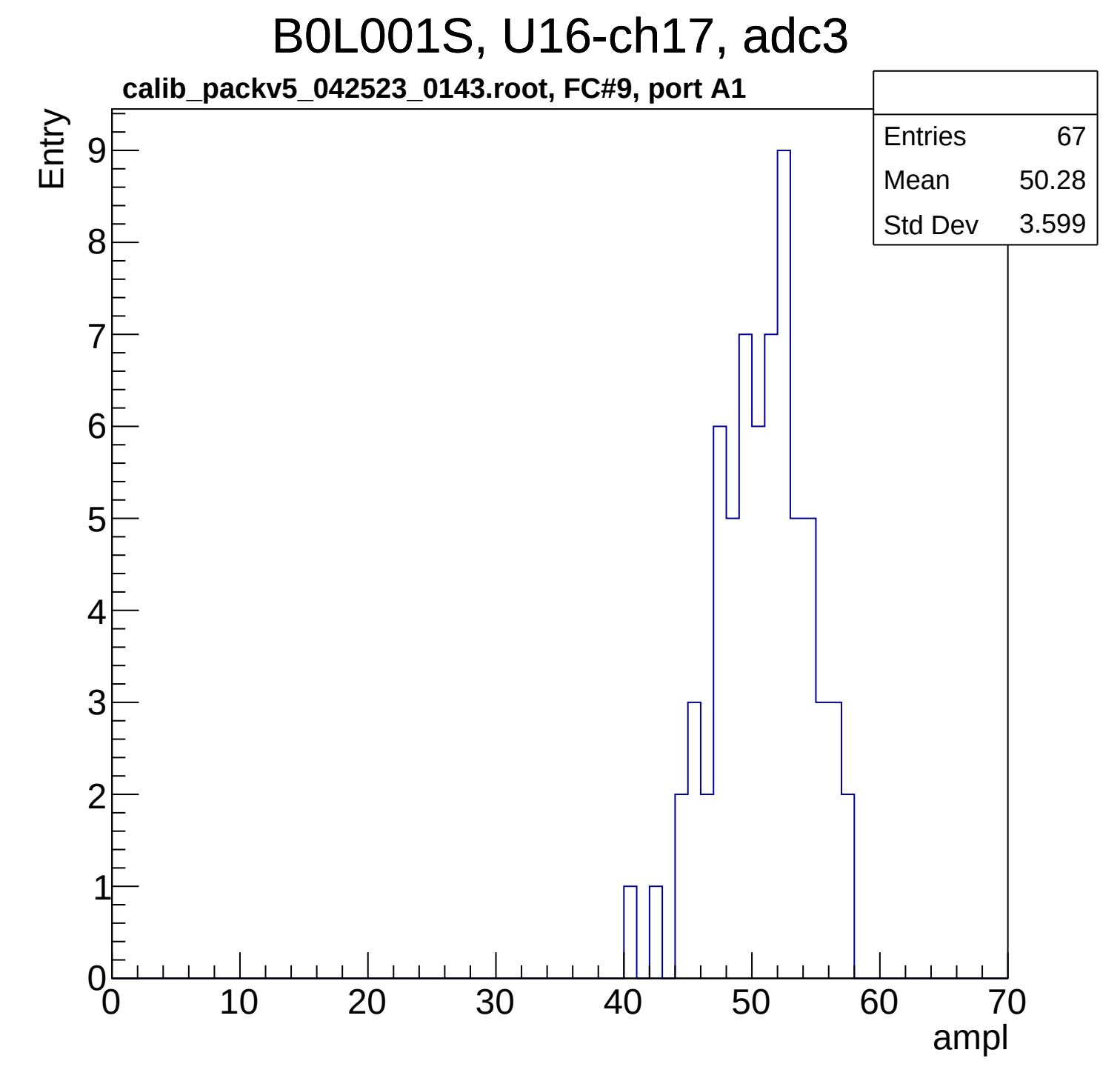
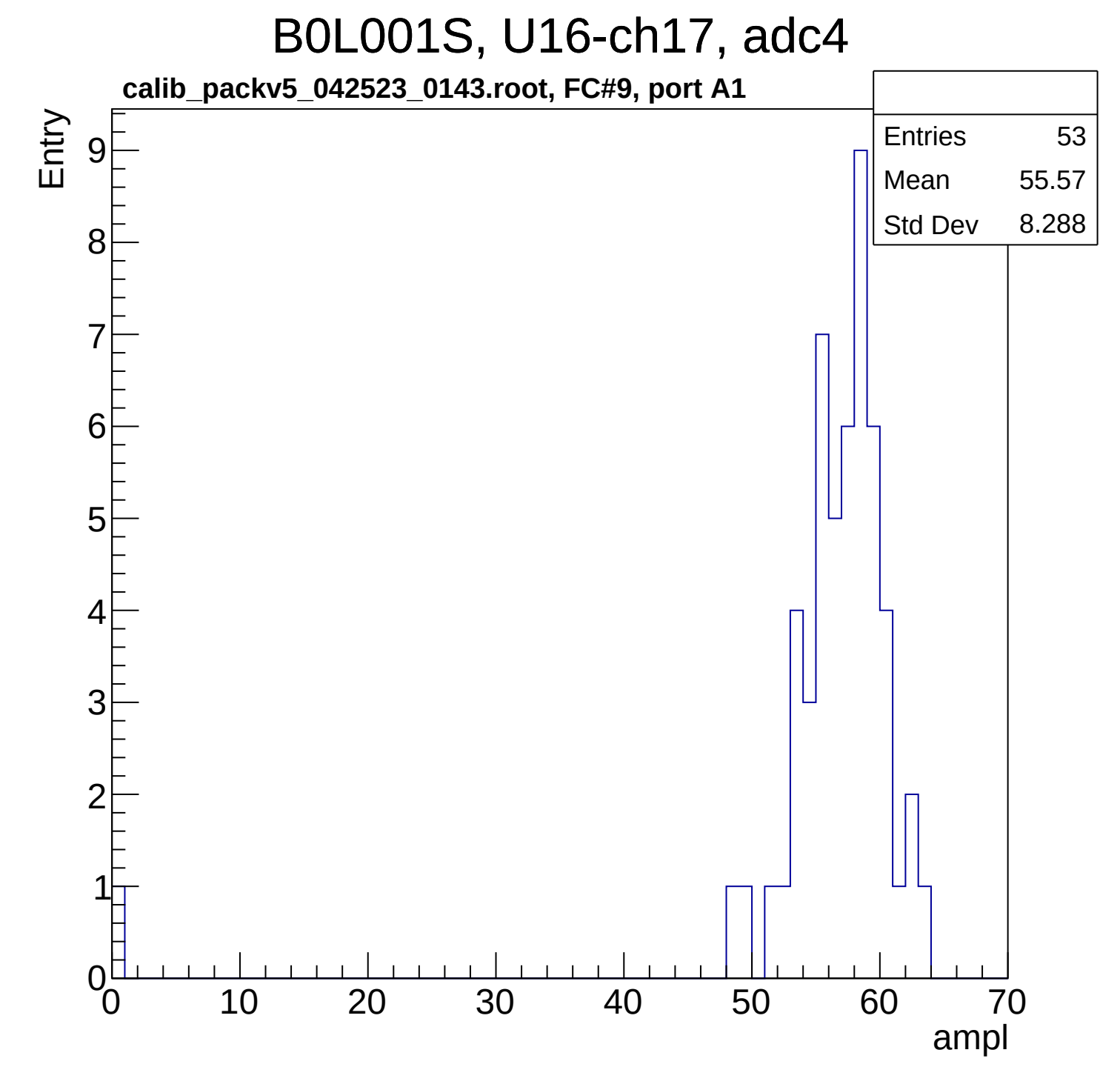

B0L001S, U16-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.28
Std Dev 3.599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.57
Std Dev 8.288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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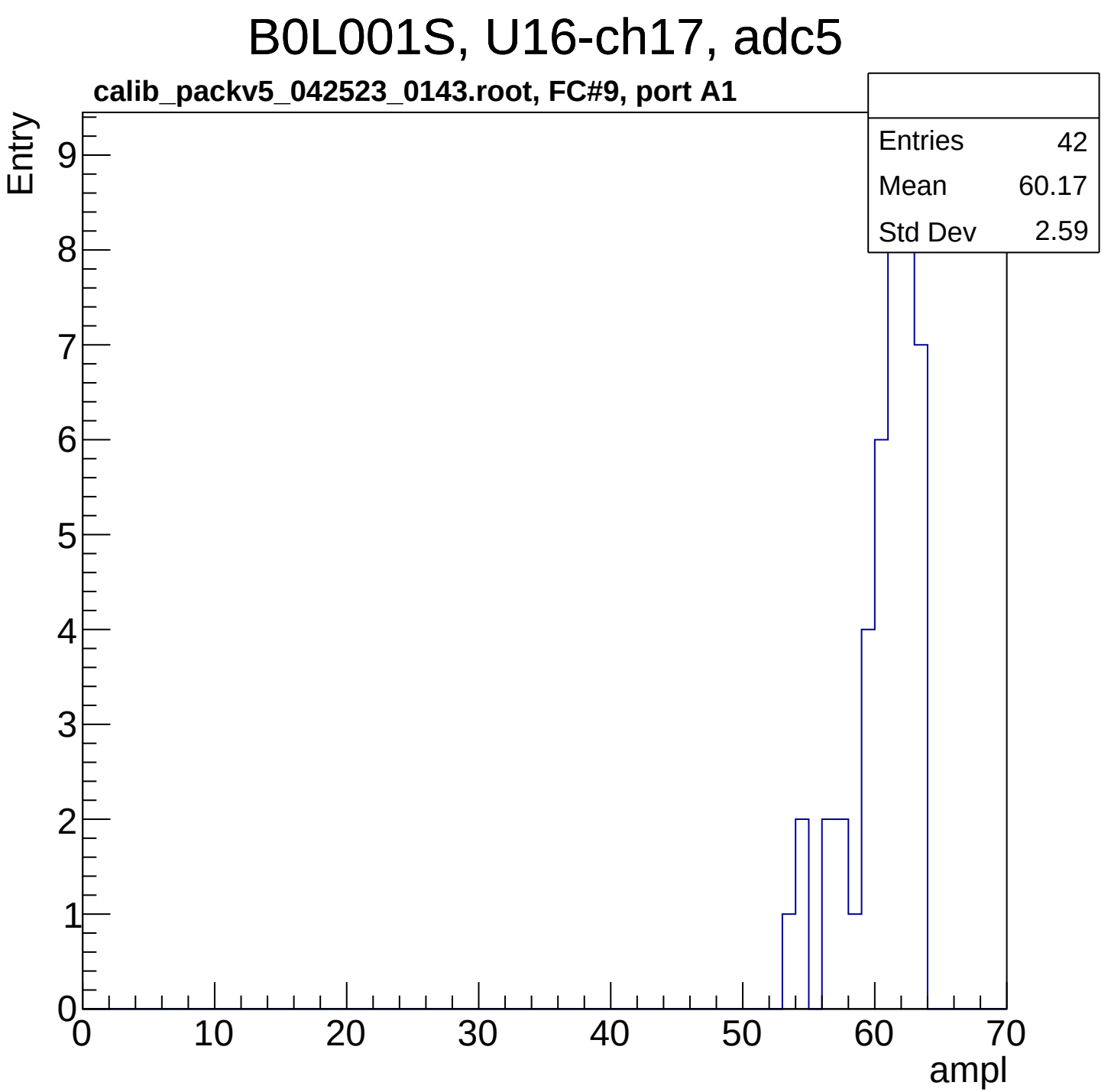
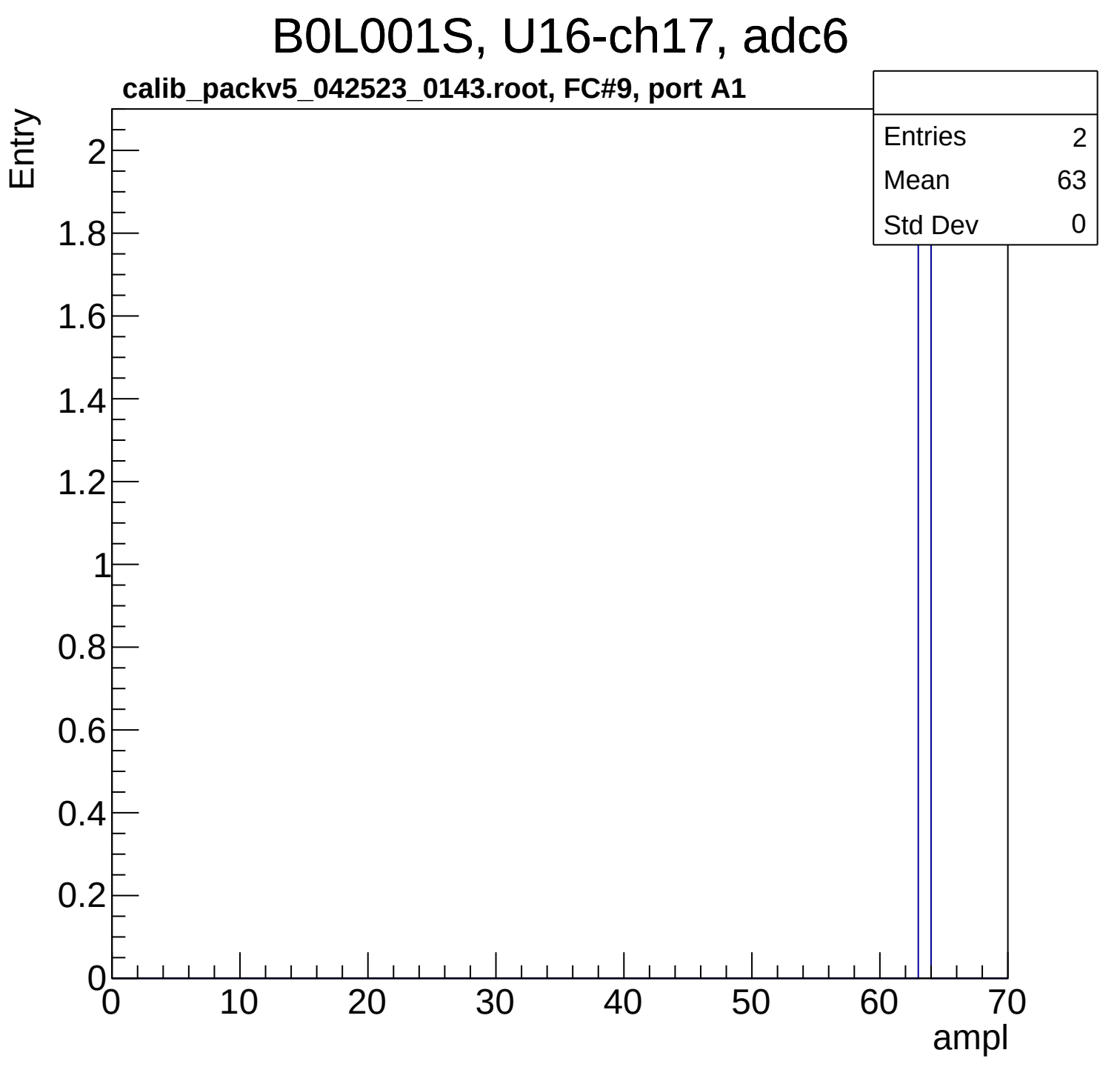
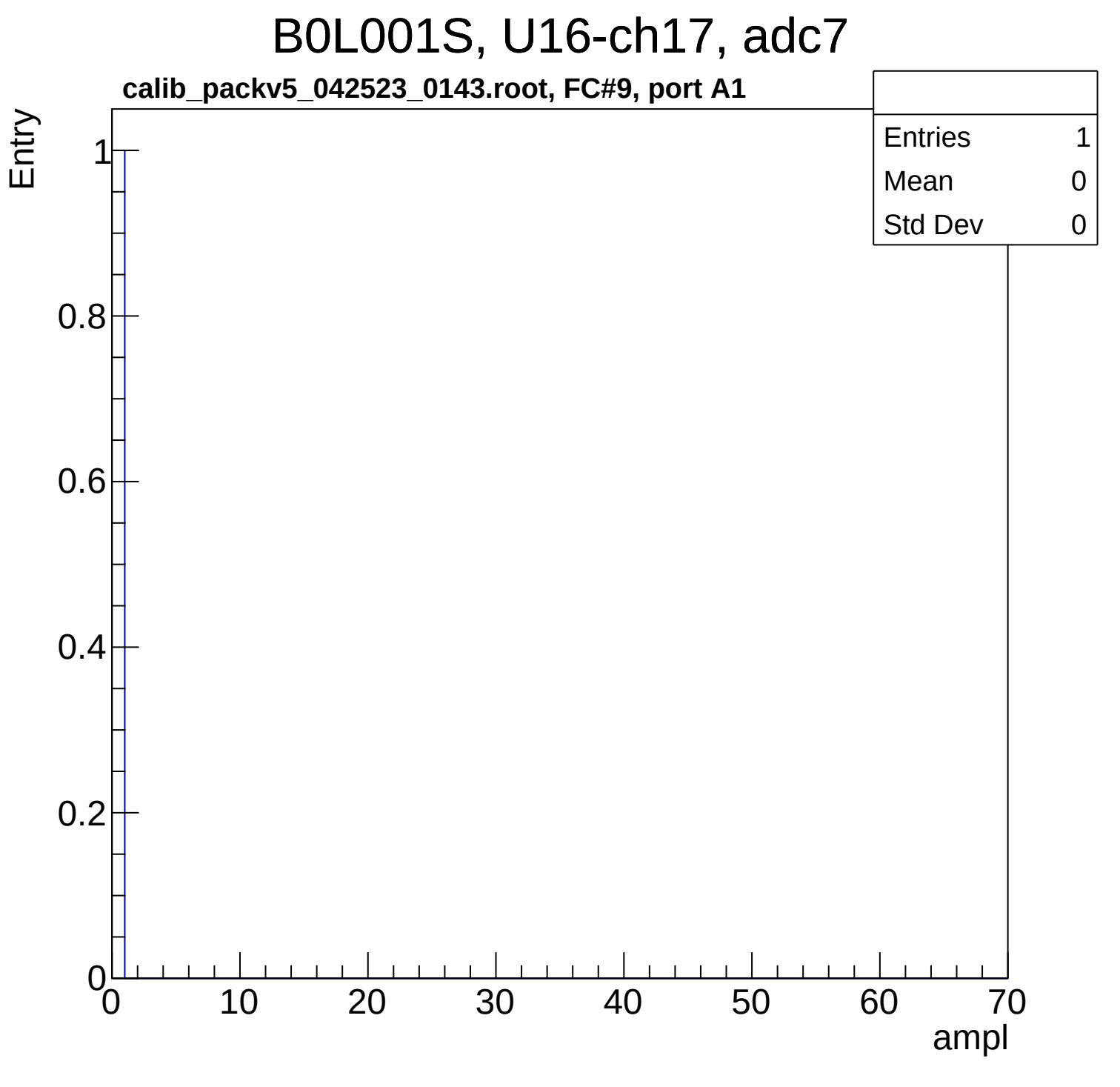
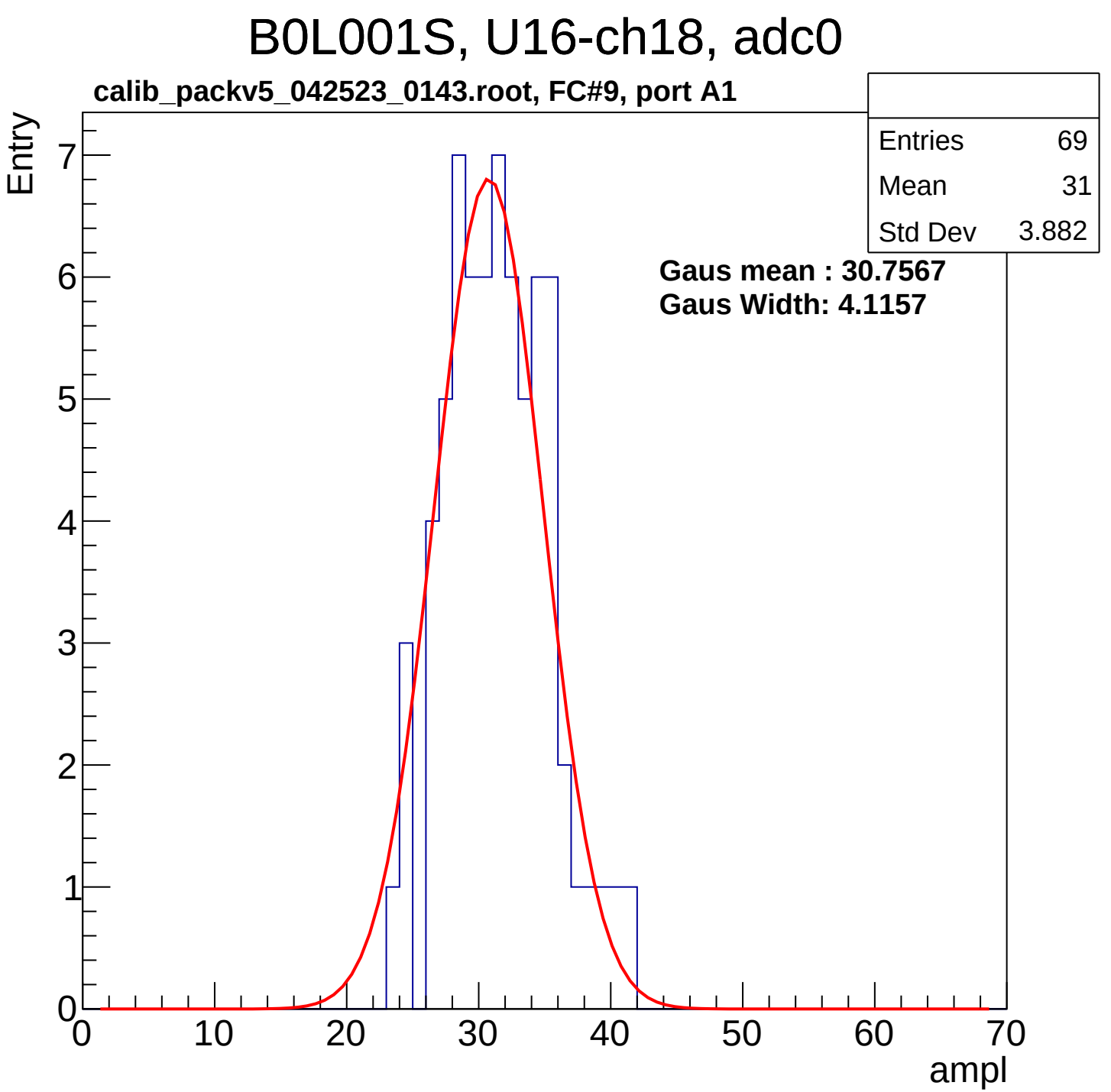
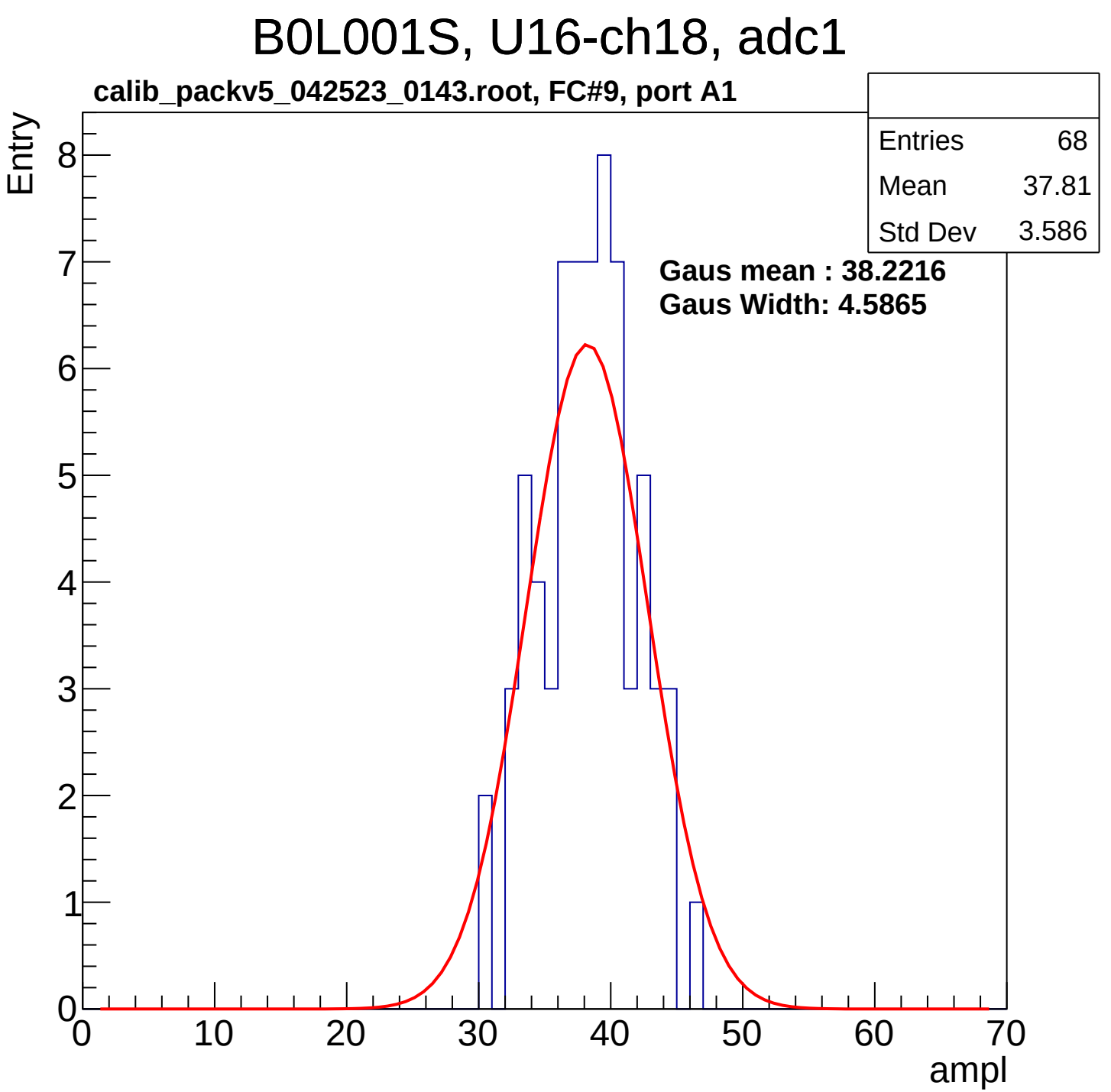
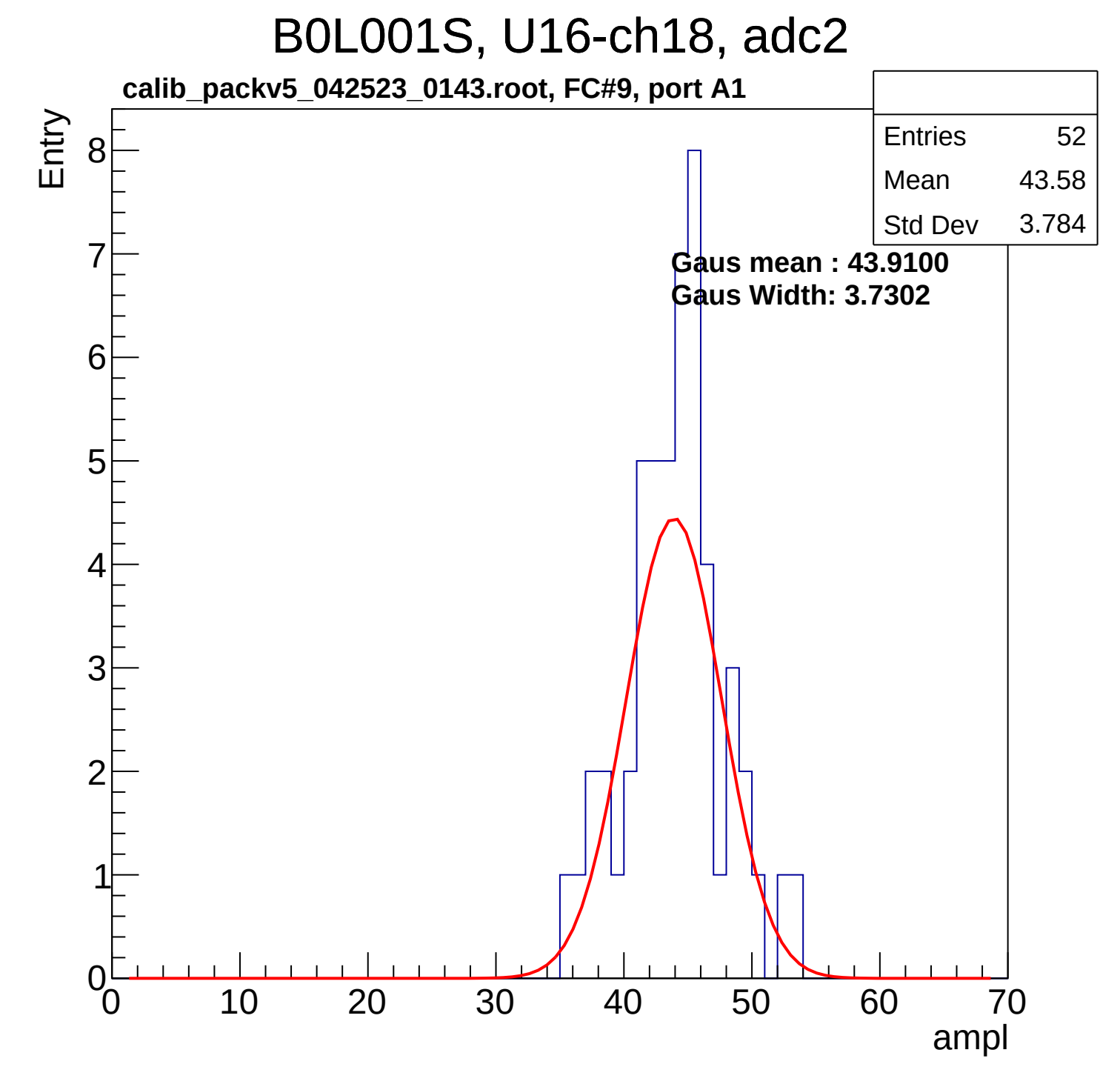

B0L001S, U16-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.17
Std Dev 2.59
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

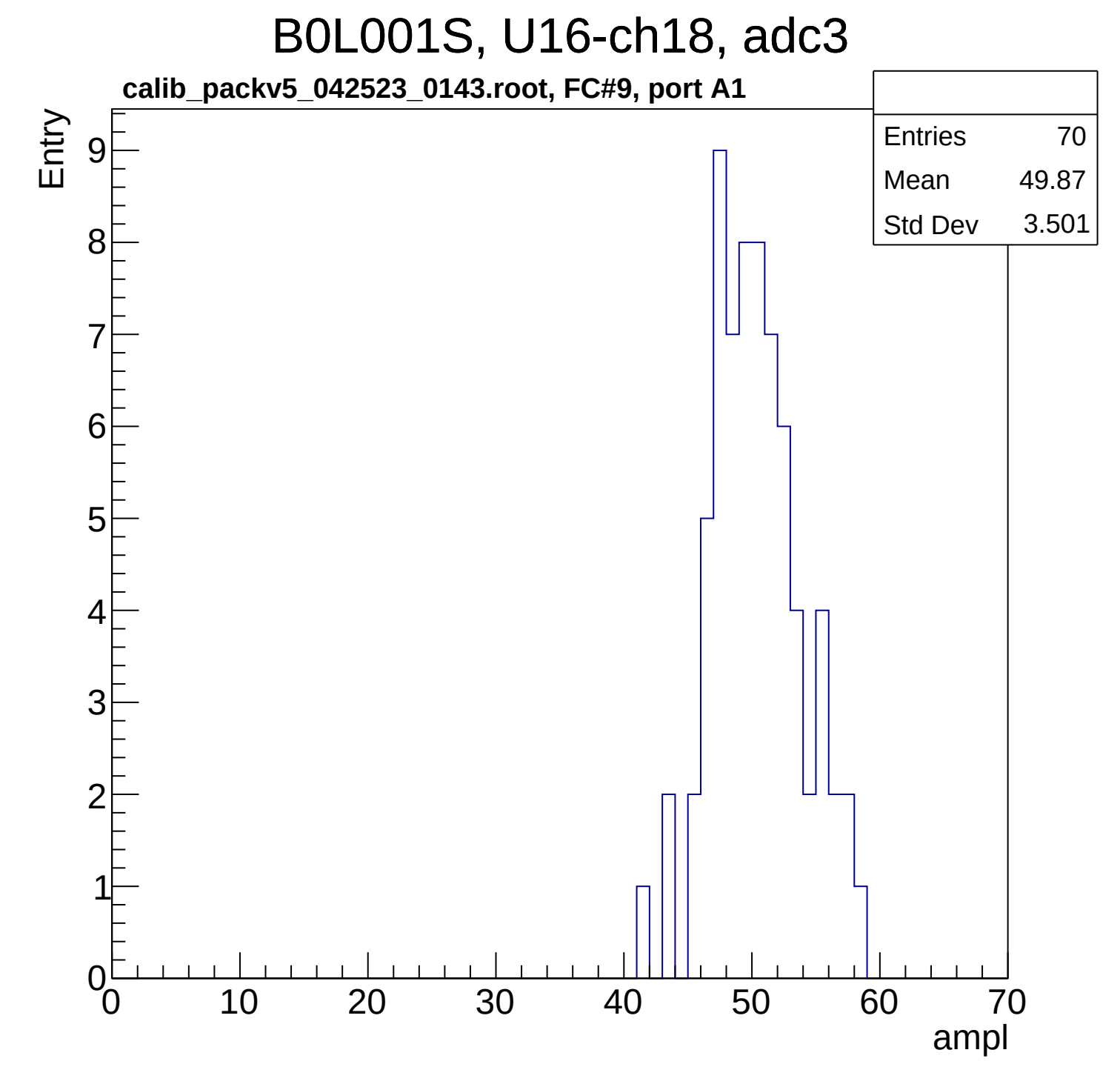

B0L001S, U16-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31
Std Dev 3.882
Gaus mean : 30.7567
Gaus Width: 4.1157
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.81
Std Dev 3.586
Gaus mean : 38.2216
Gaus Width: 4.5865
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.58
Std Dev 3.784
Gaus mean : 43.9100
Gaus Width: 3.7302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.87
Std Dev 3.501
Entry
ampl
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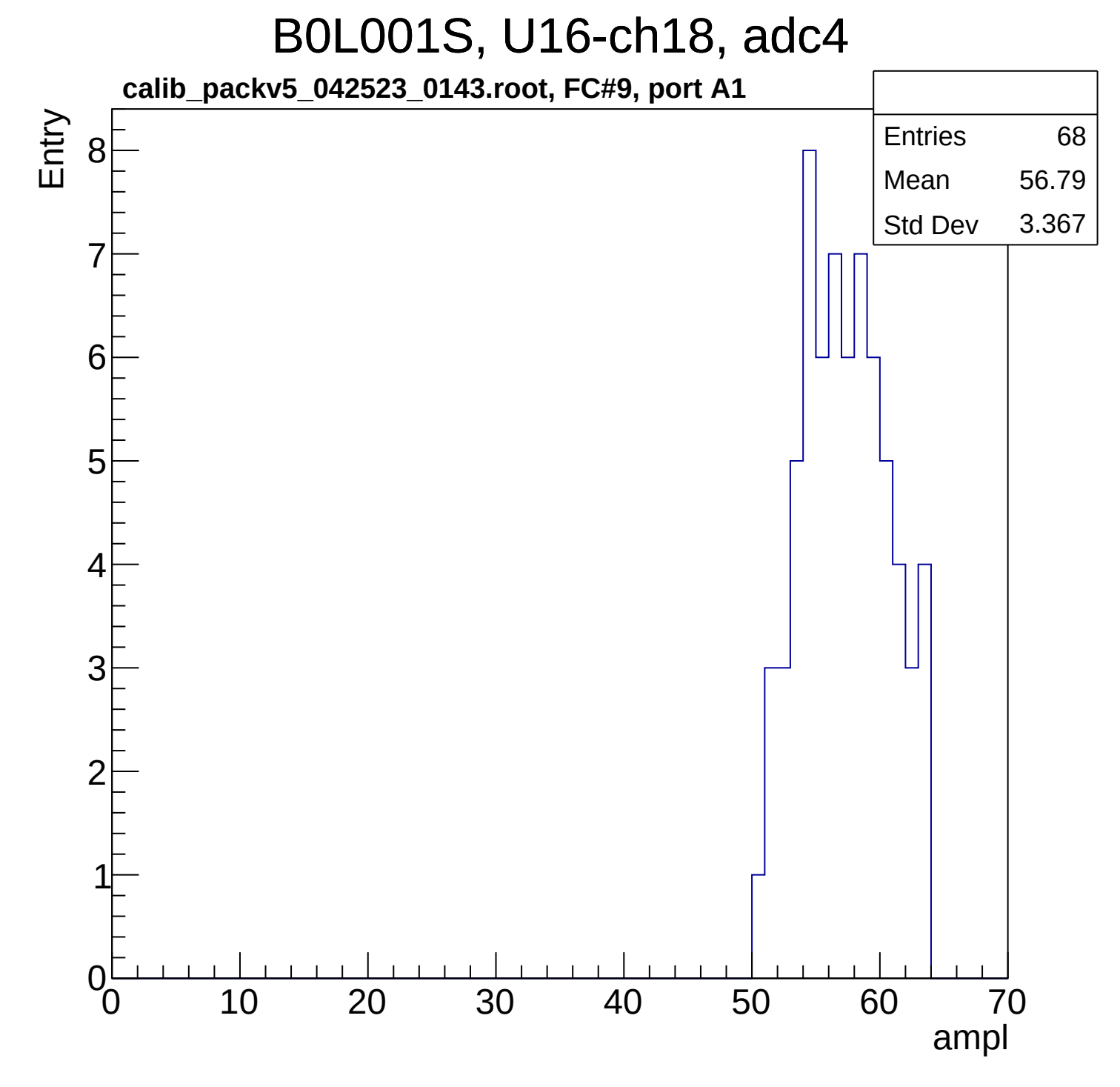
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56.79
Std Dev 3.367
Entry
ampl
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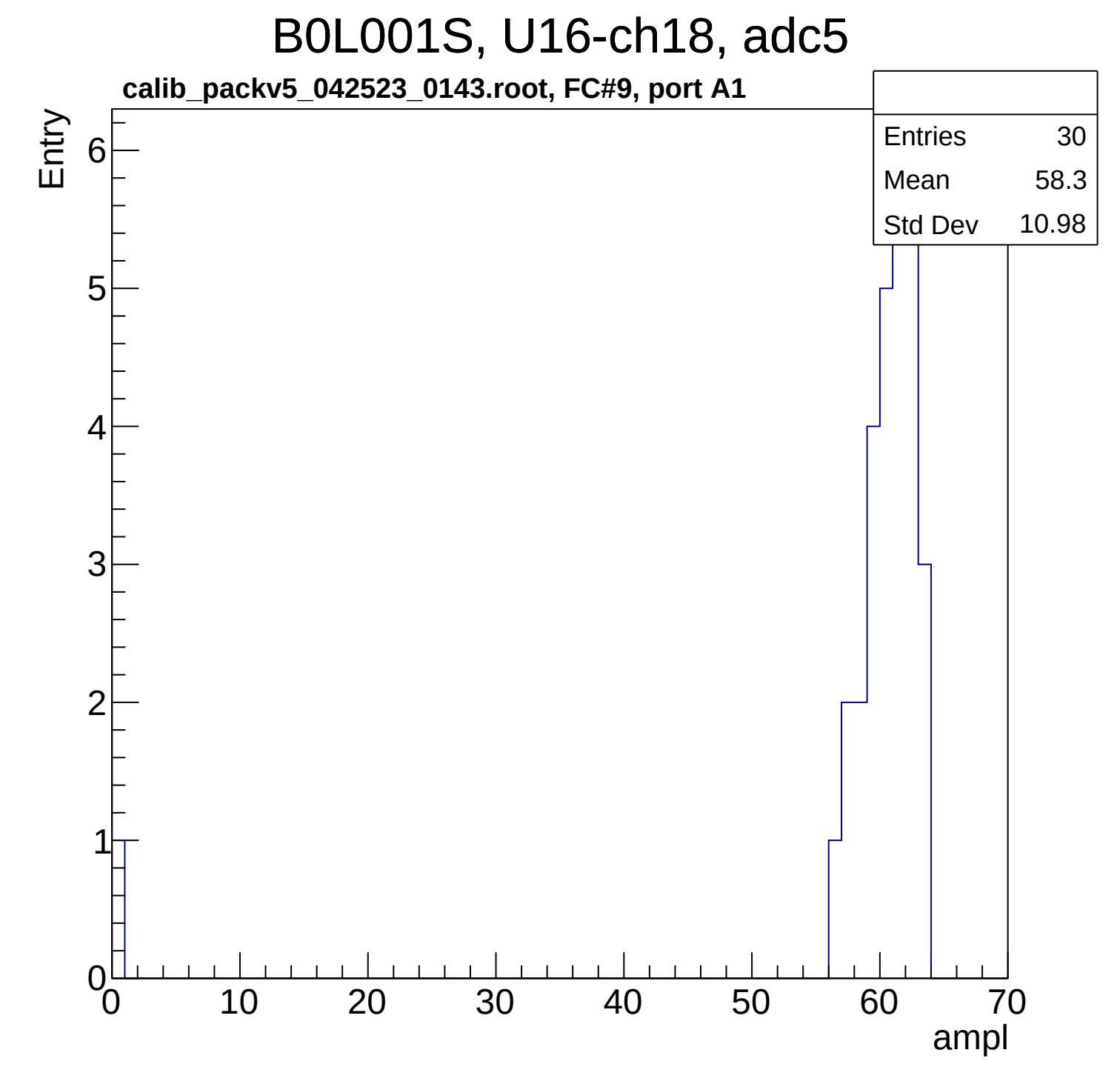
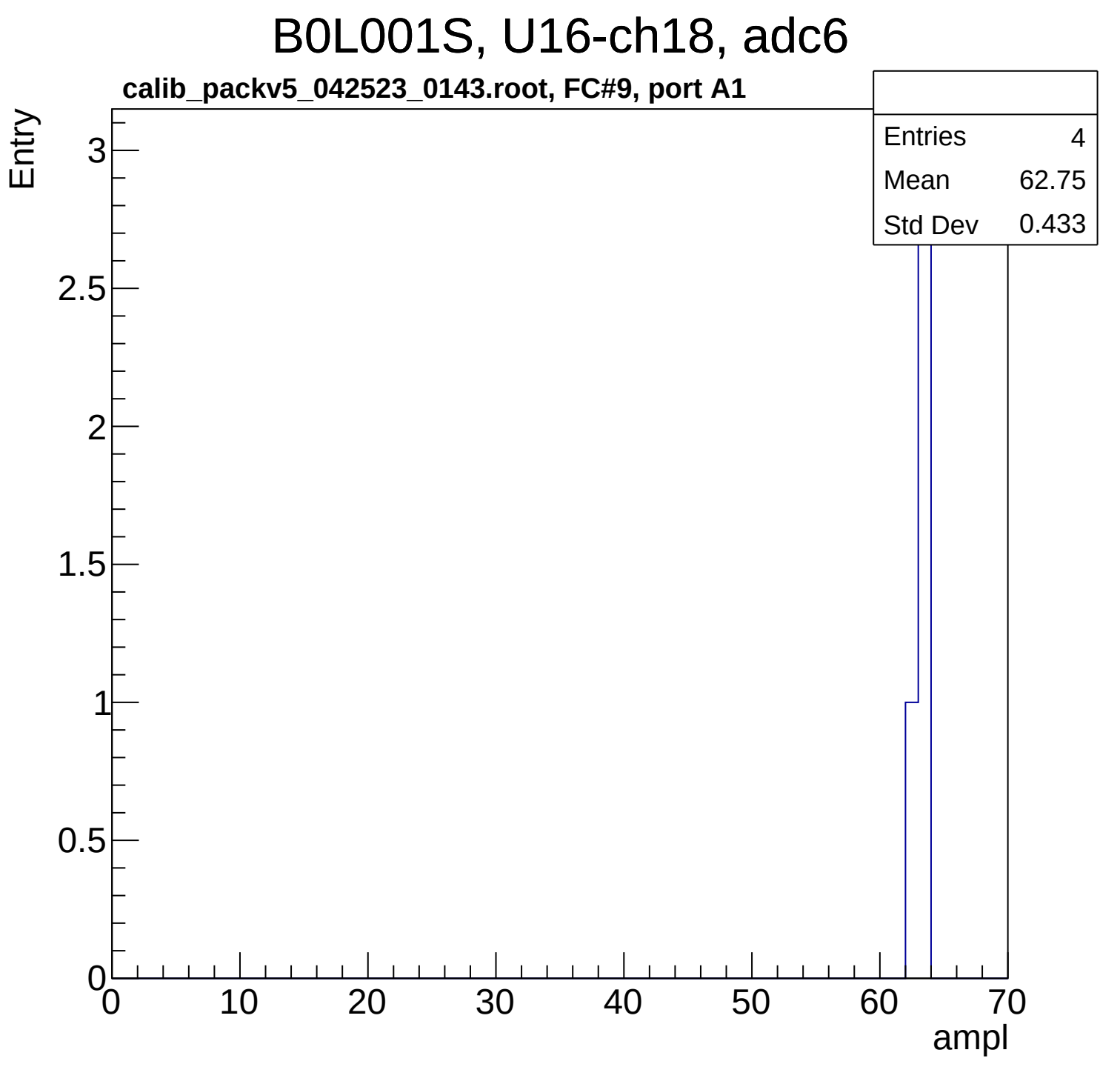
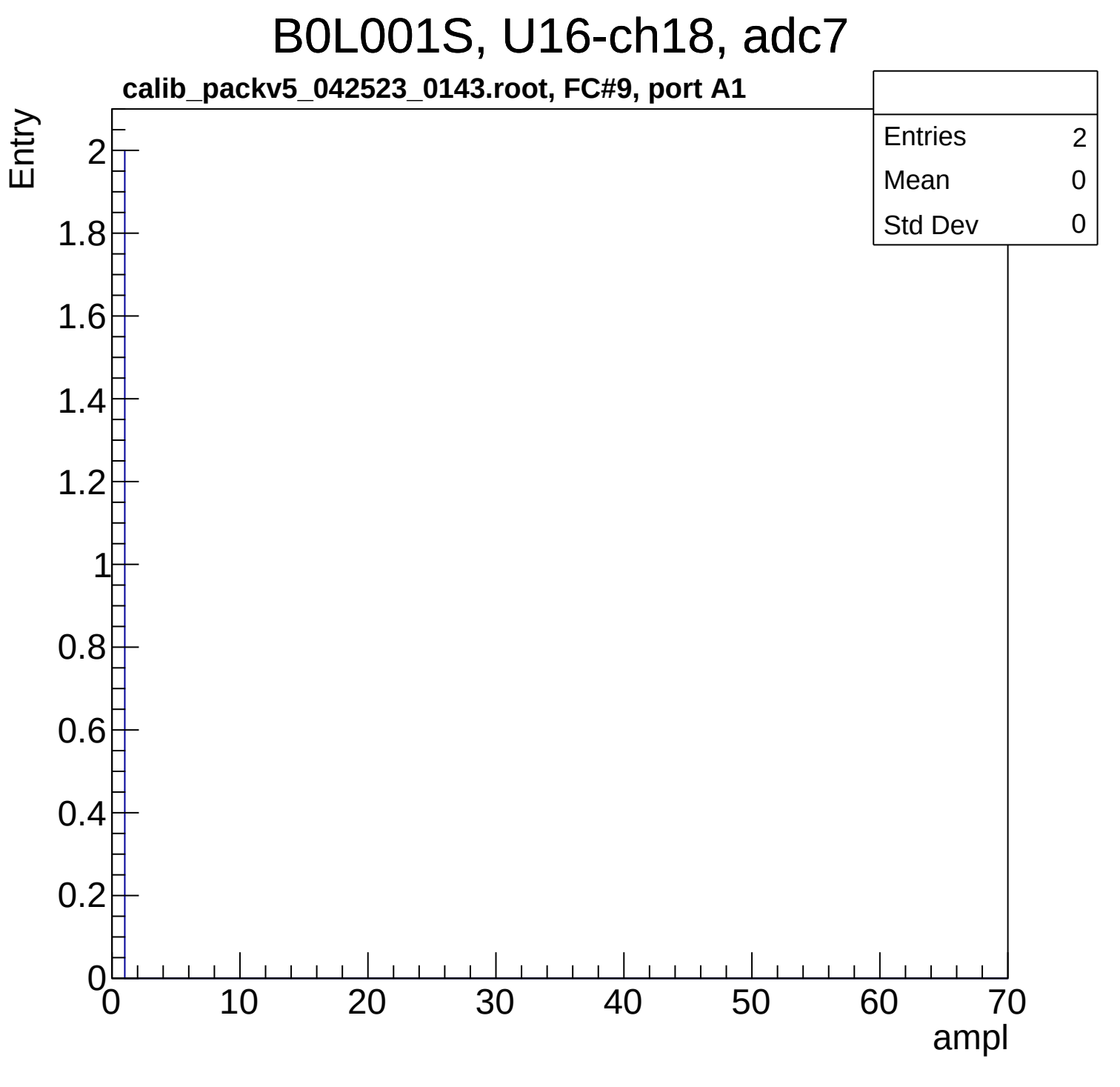
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.3
Std Dev 10.98
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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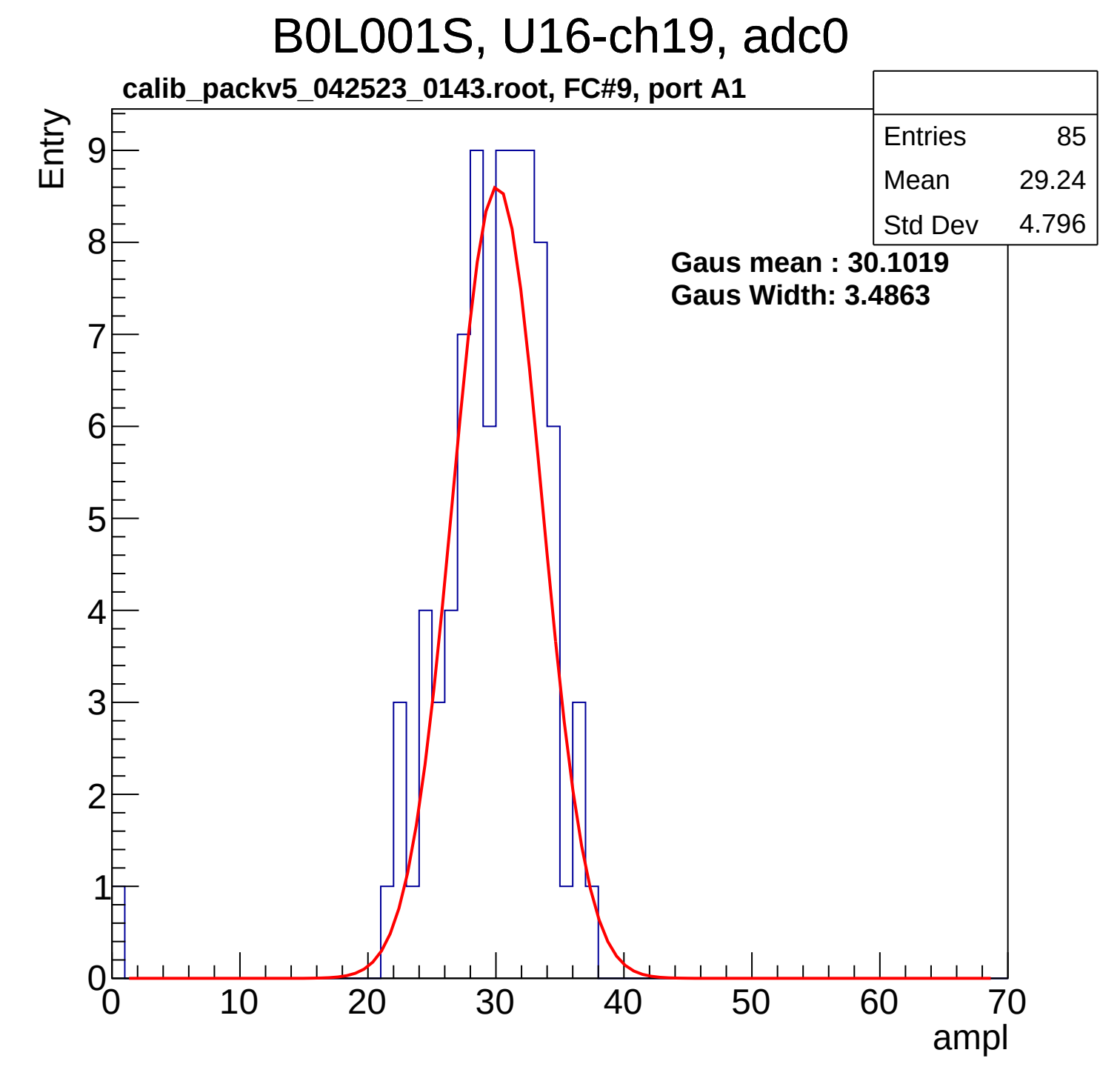
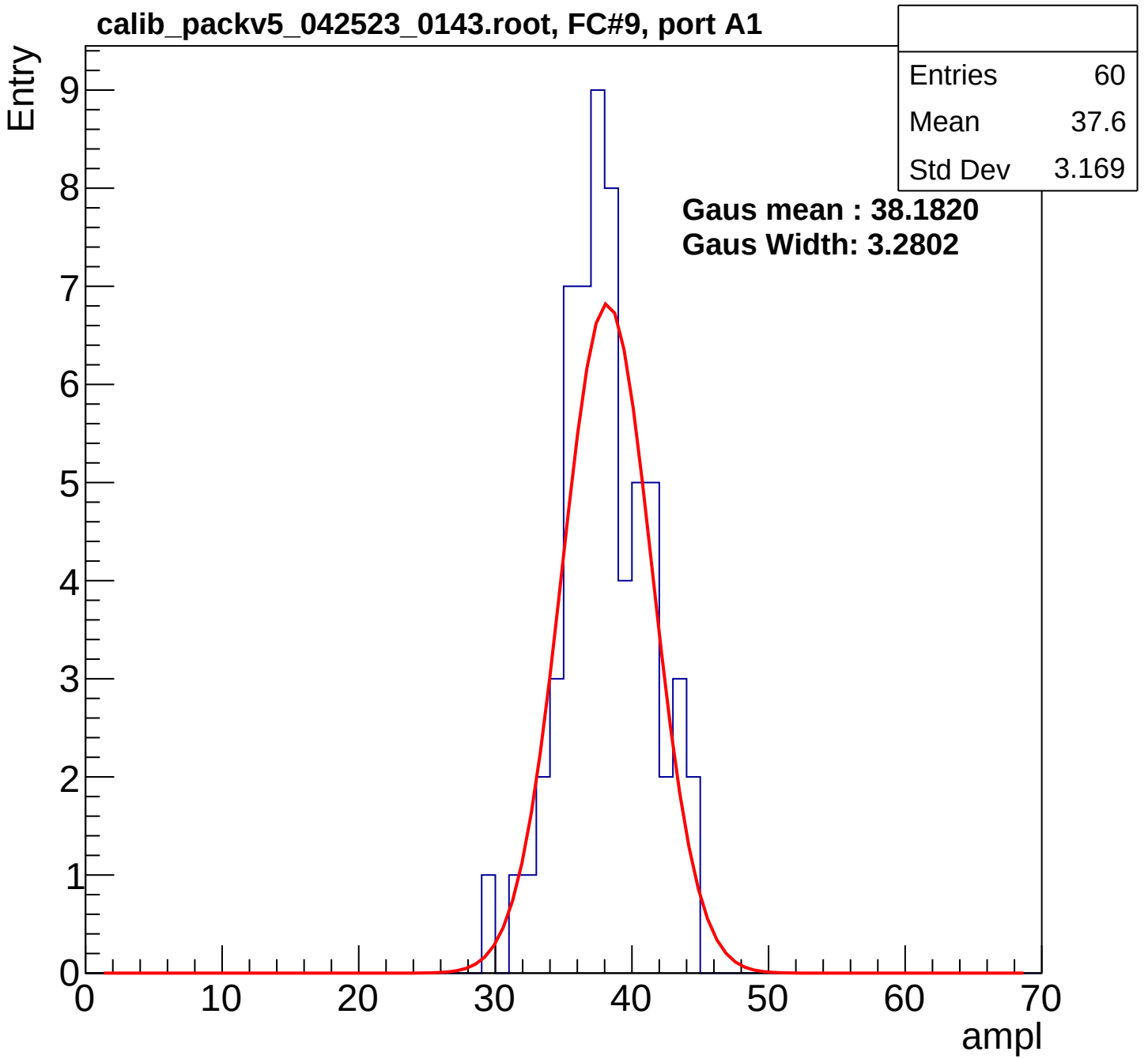
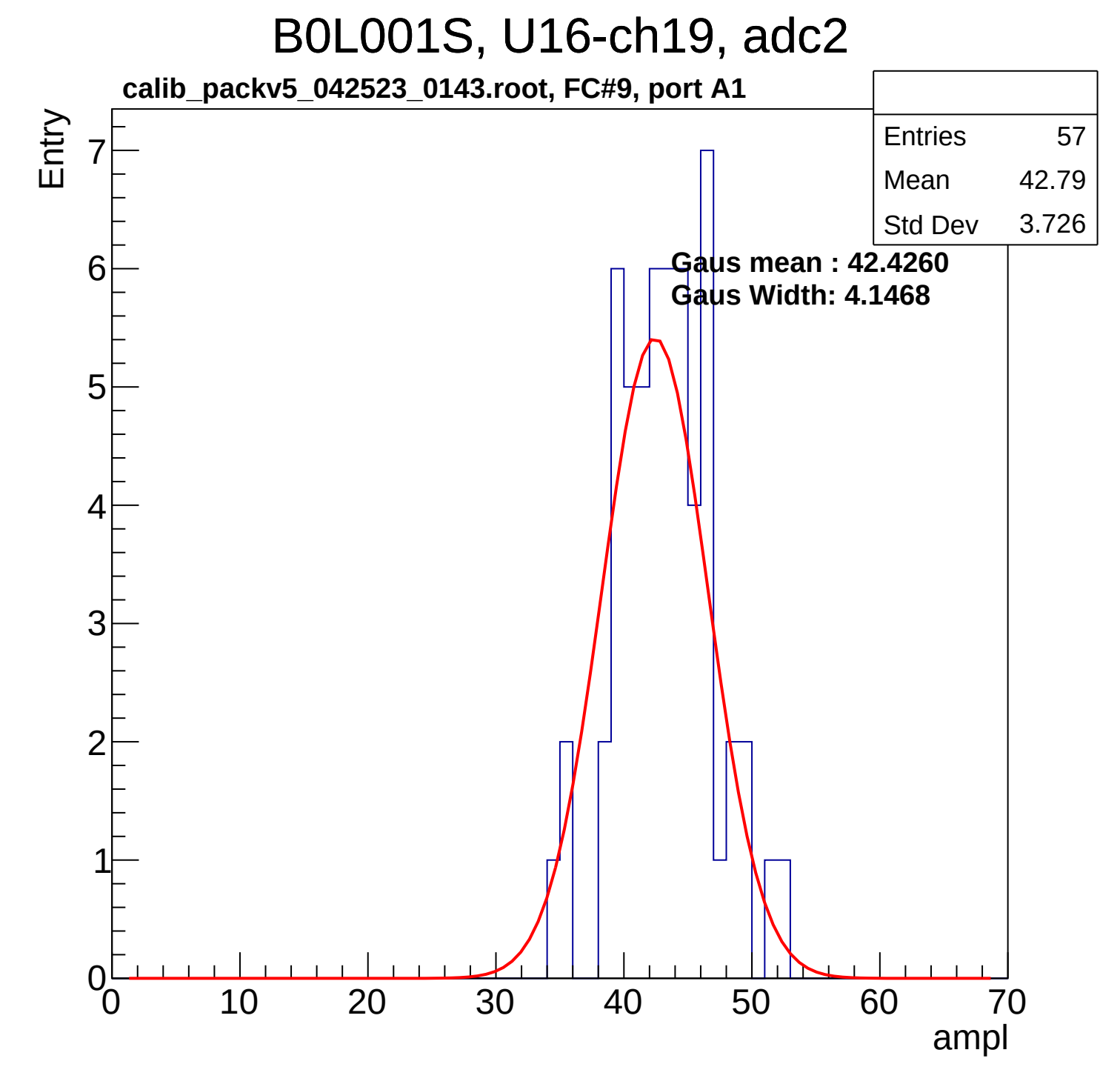
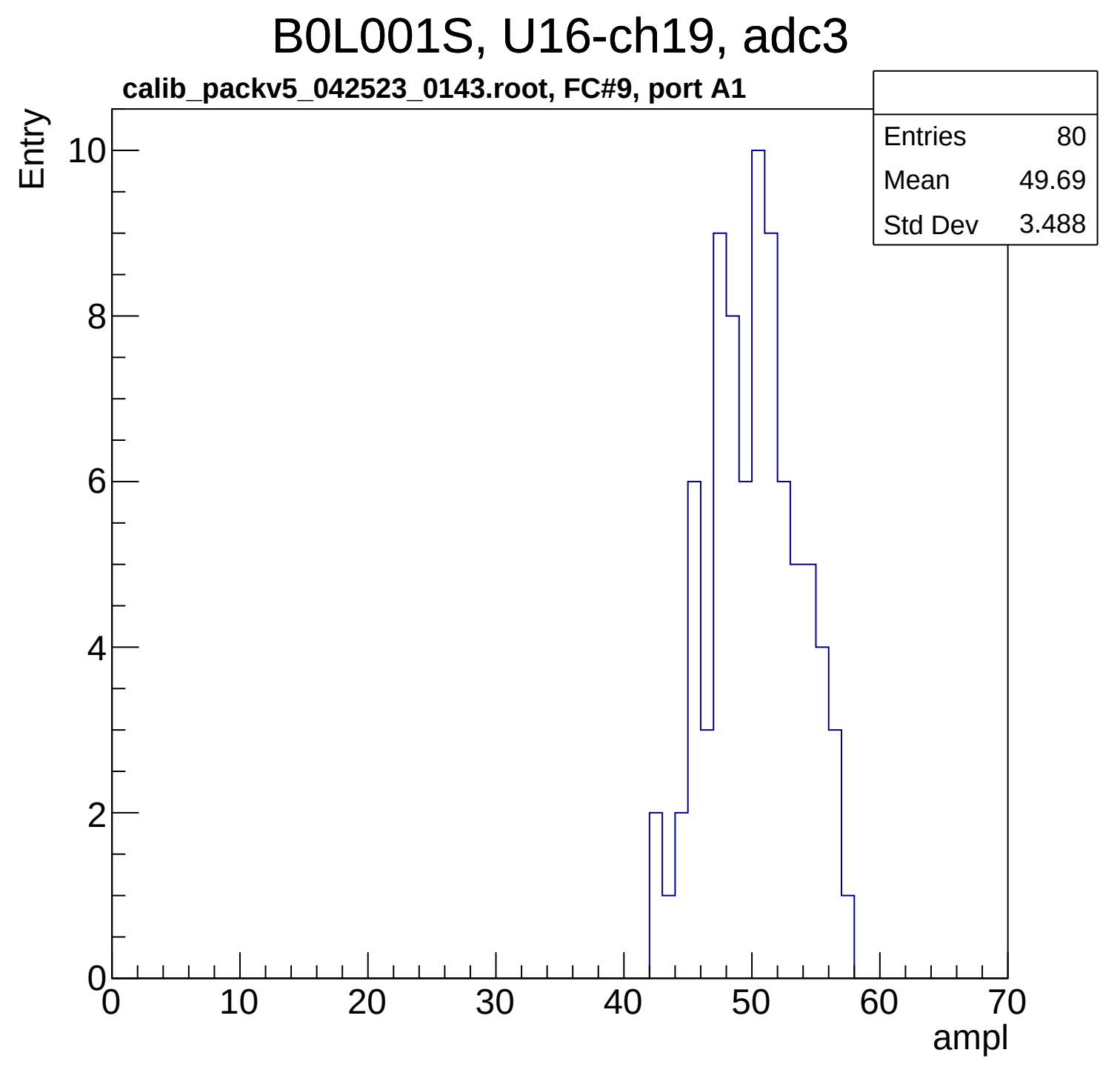
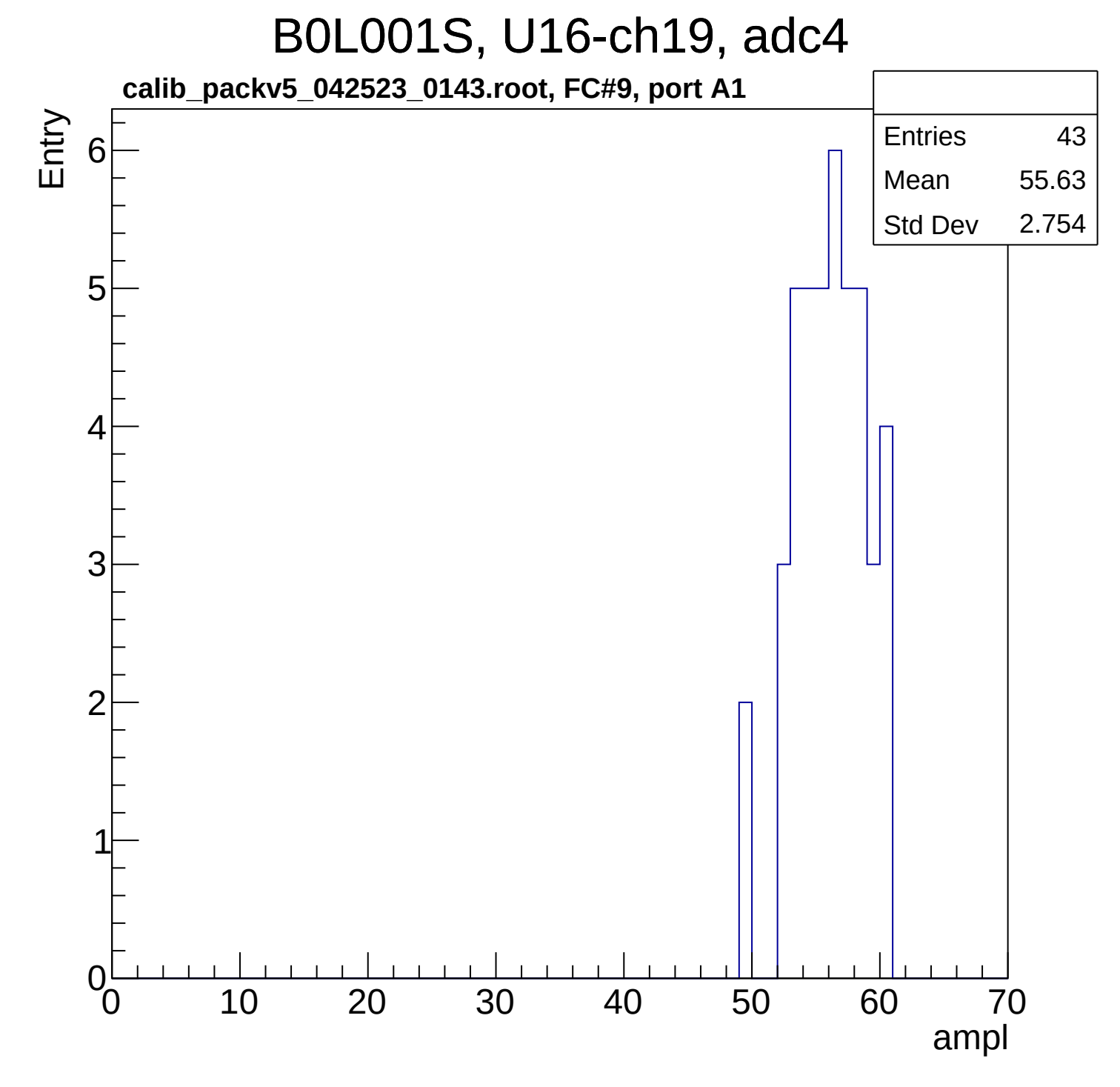
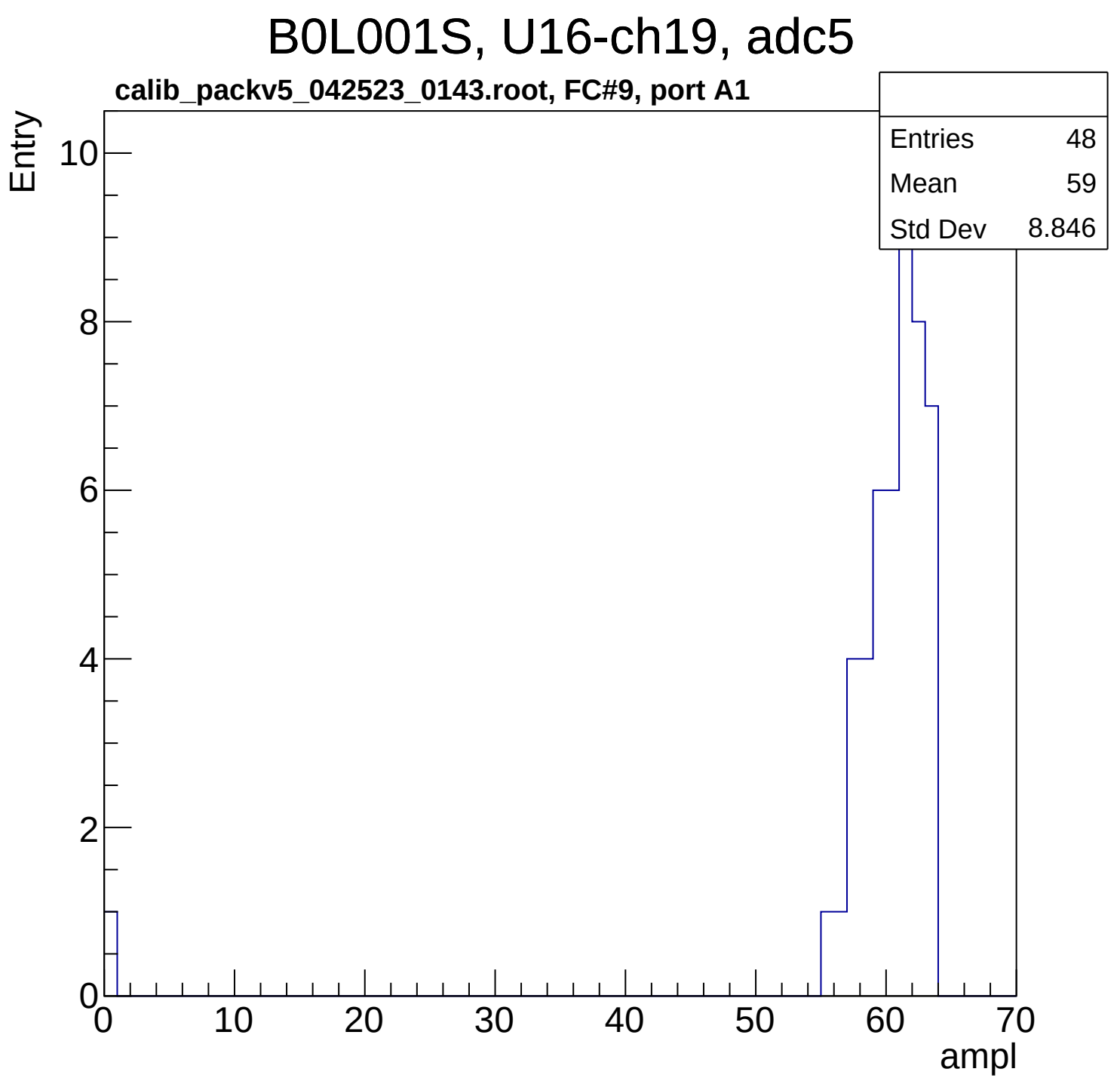

B0L001S, U16-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 29.24
Std Dev 4.796
Gaus mean : 30.1019
Gaus Width: 3.4863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

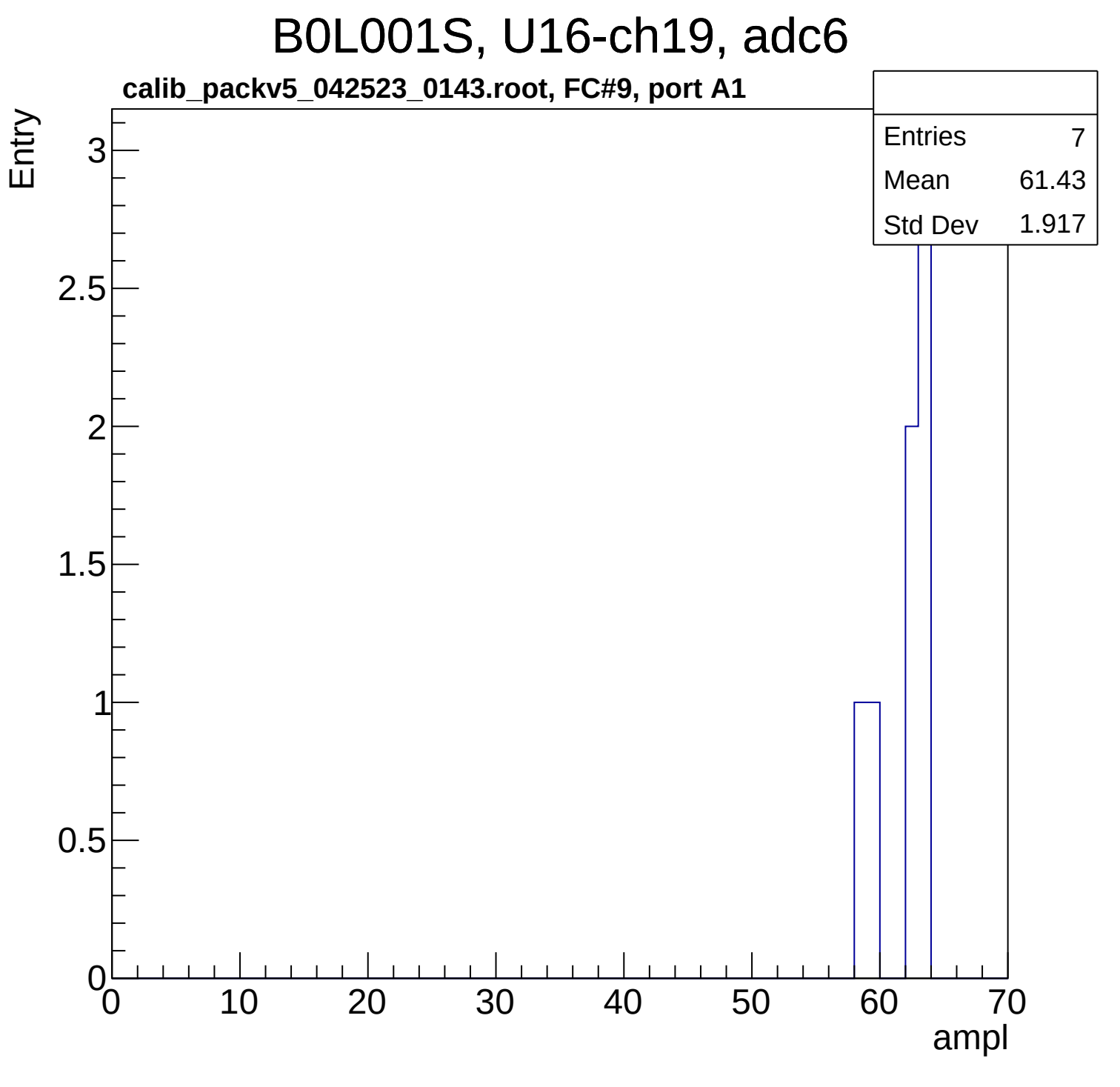

B0L001S, U16-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.6
Std Dev 3.169
Gaus mean : 38.1820
Gaus Width: 3.2802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

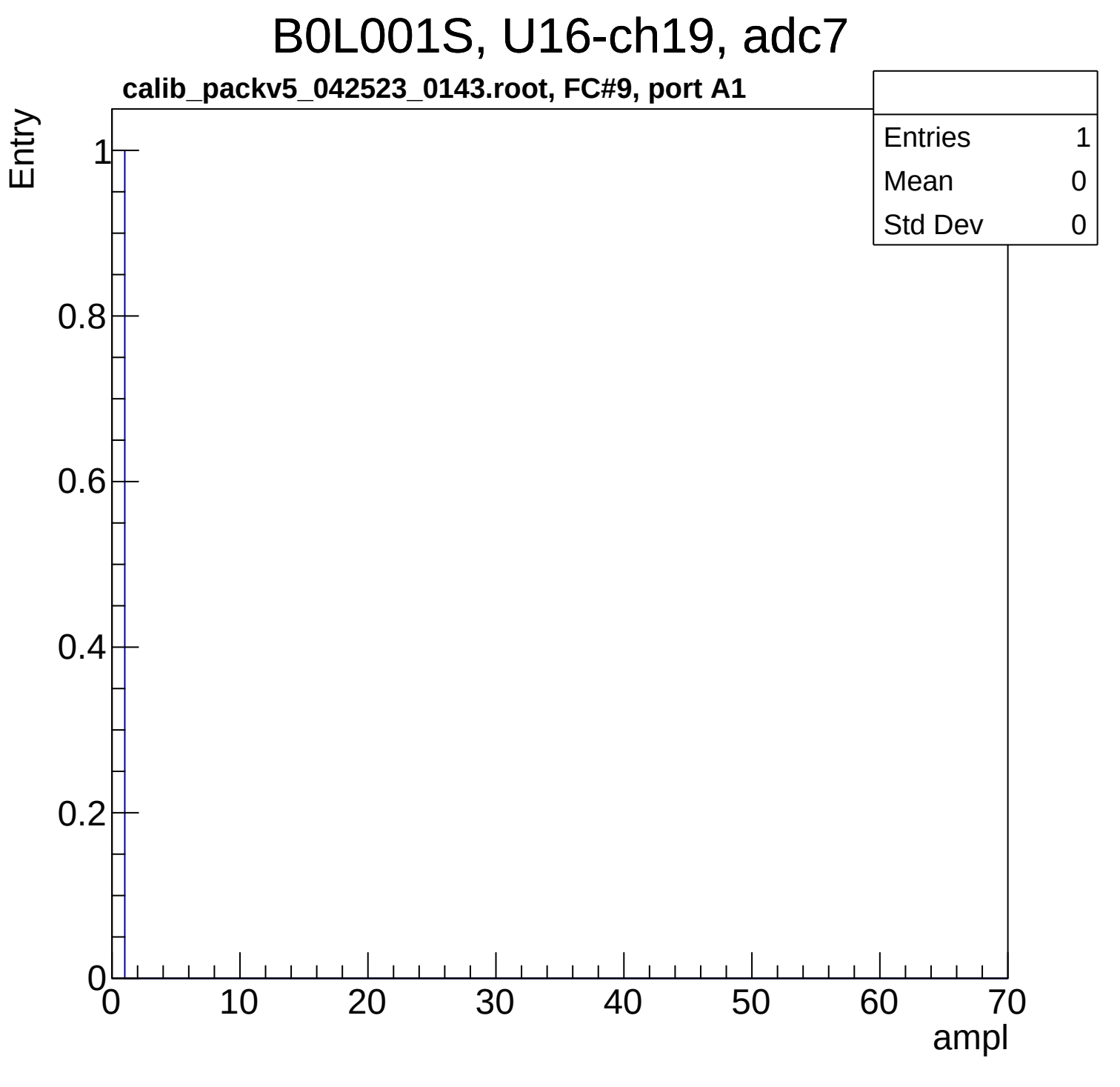

B0L001S, U16-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.79
Std Dev 3.726
Gaus mean : 42.4260
Gaus Width: 4.1468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

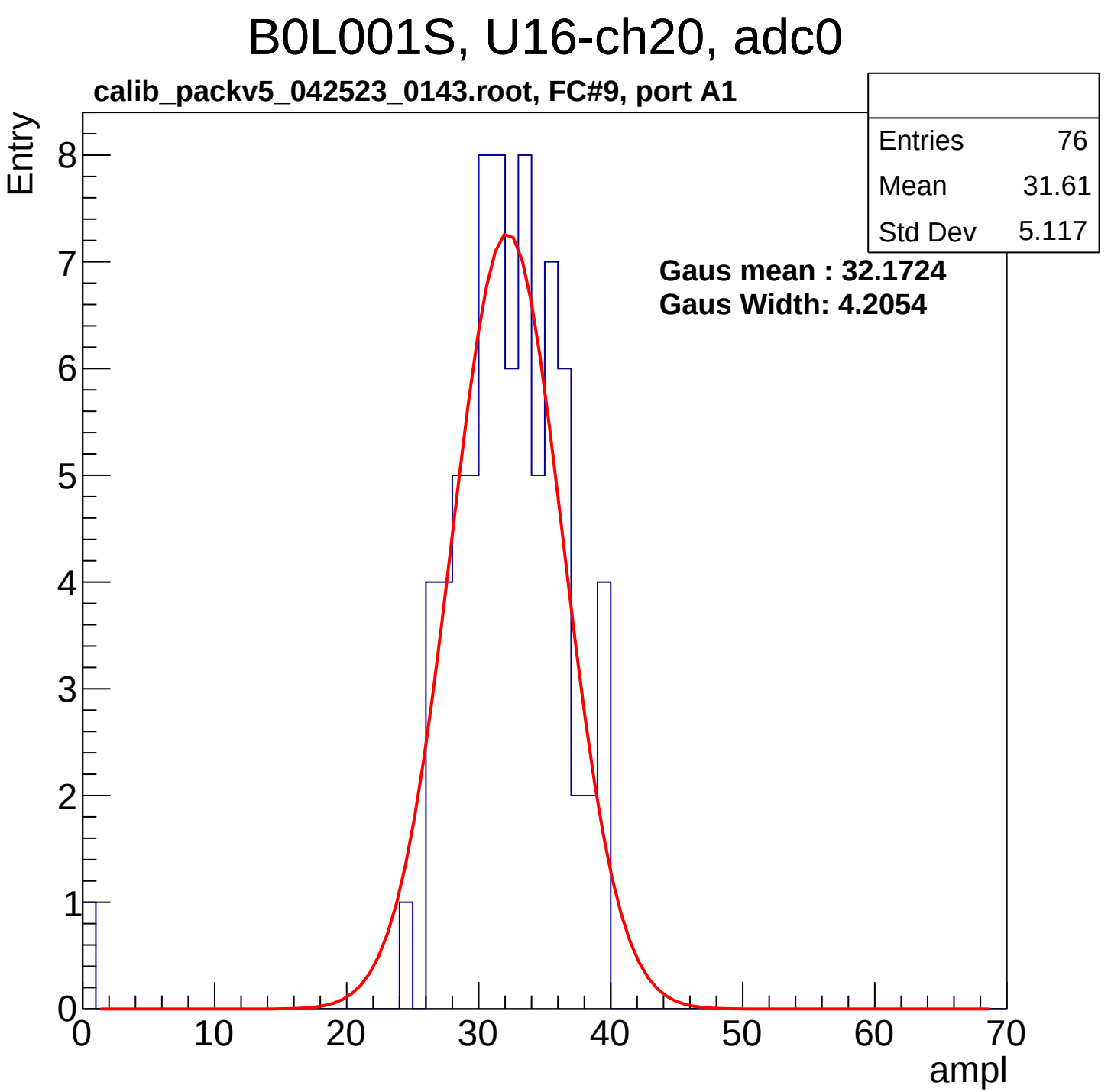

B0L001S, U16-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.69
Std Dev 3.488
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

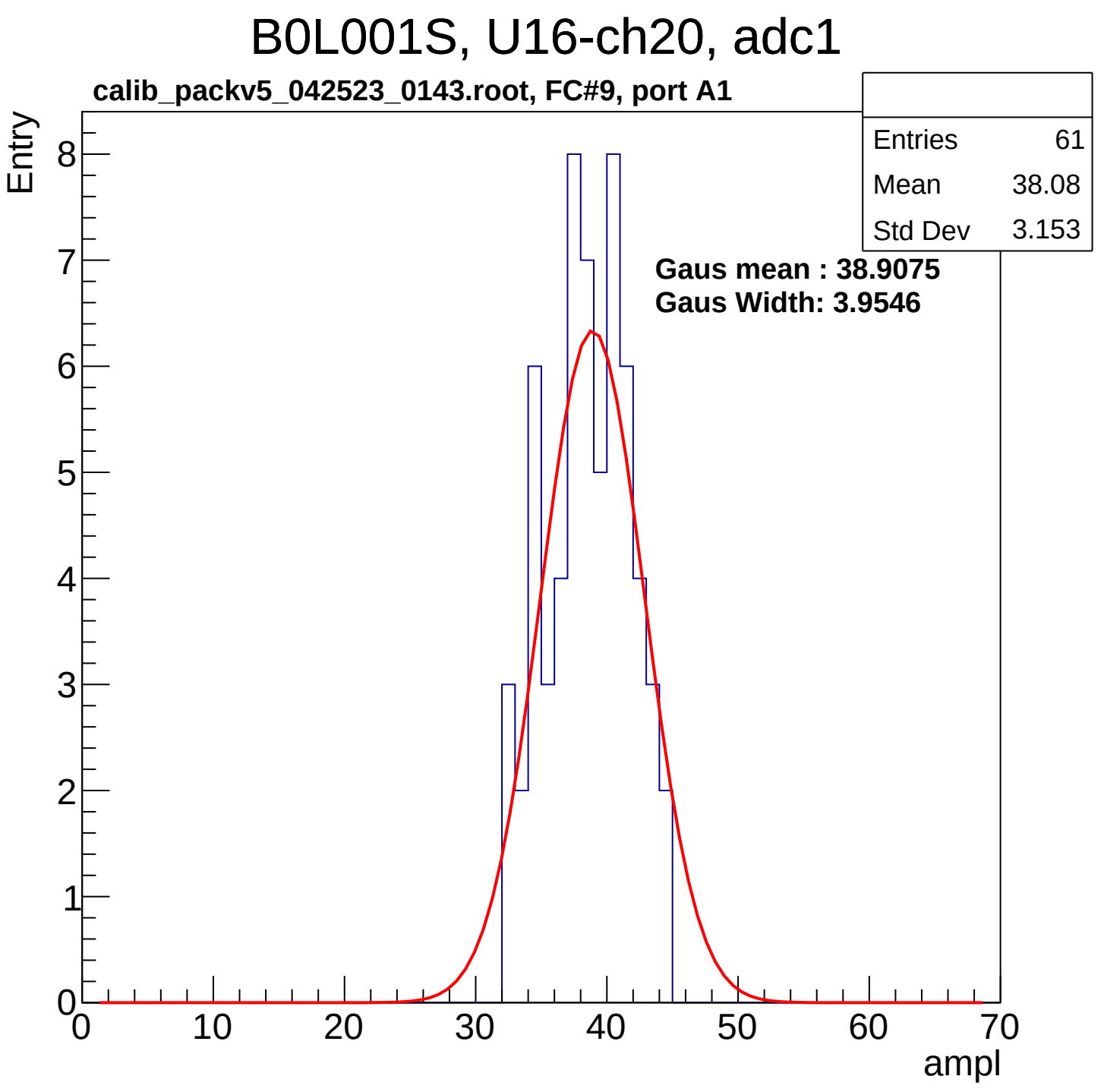

B0L001S, U16-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 55.63
Std Dev 2.754
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

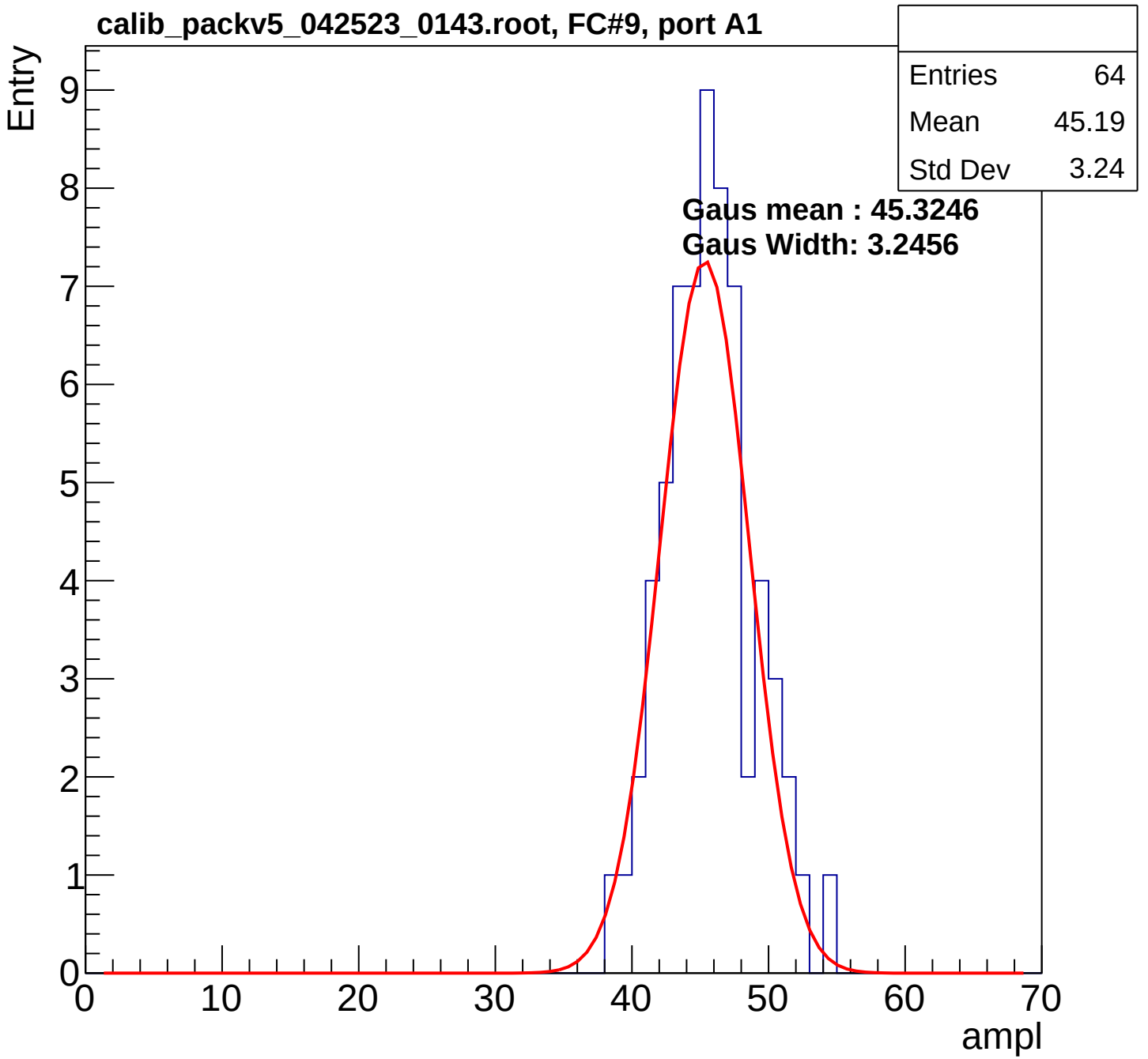

B0L001S, U16-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59
Std Dev 8.846
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

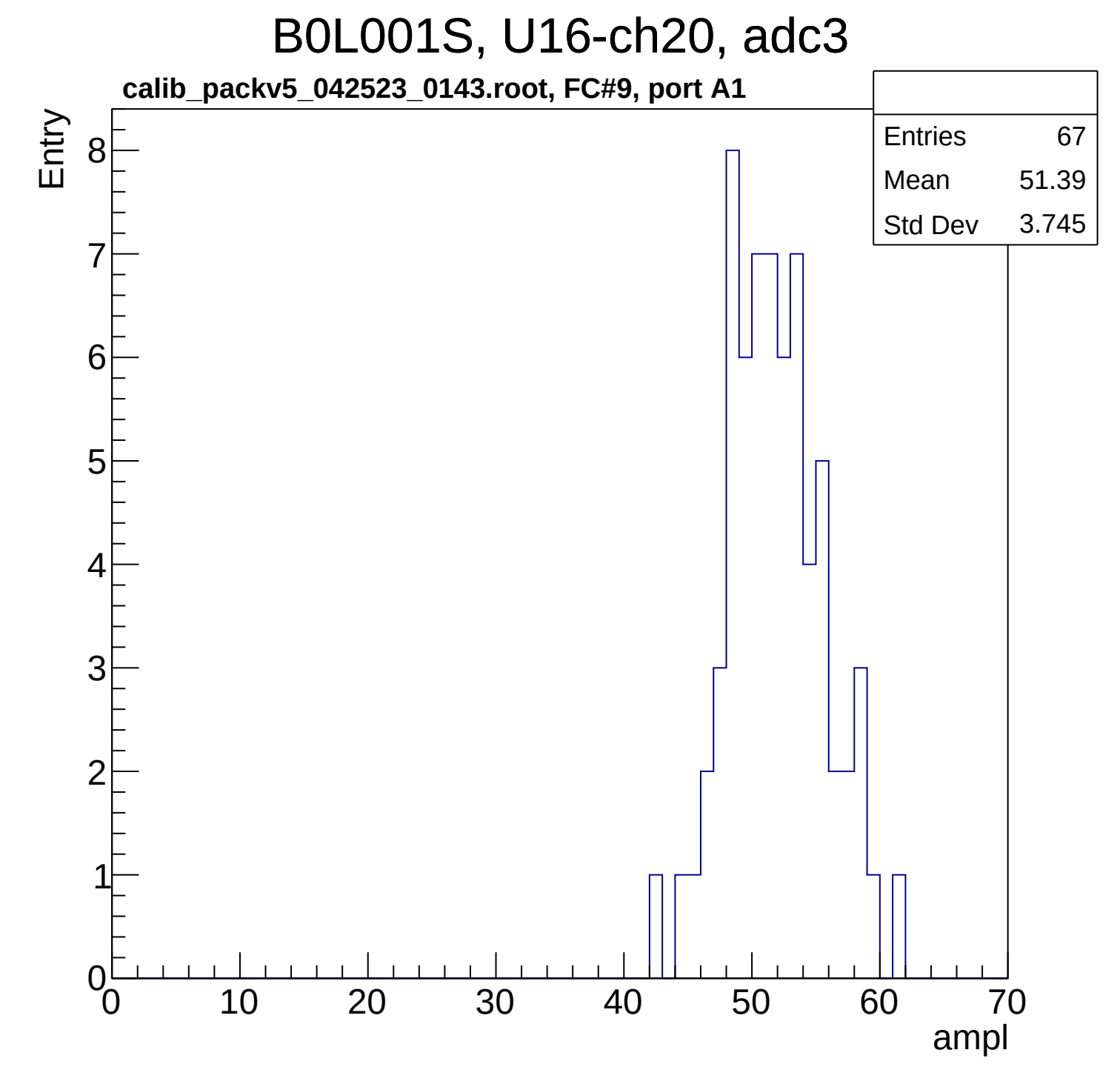

B0L001S, U16-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.43
Std Dev 1.917
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

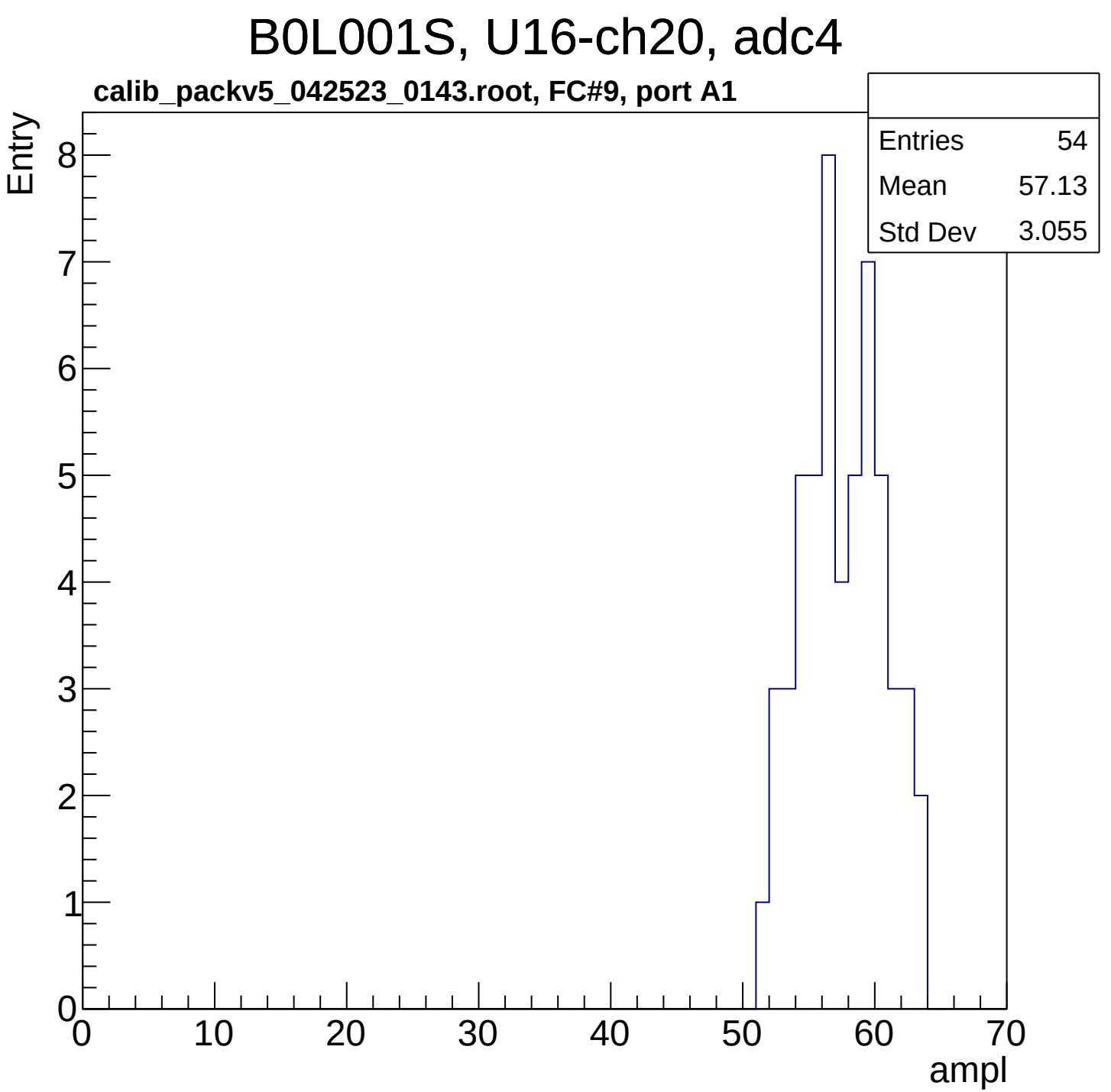

B0L001S, U16-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

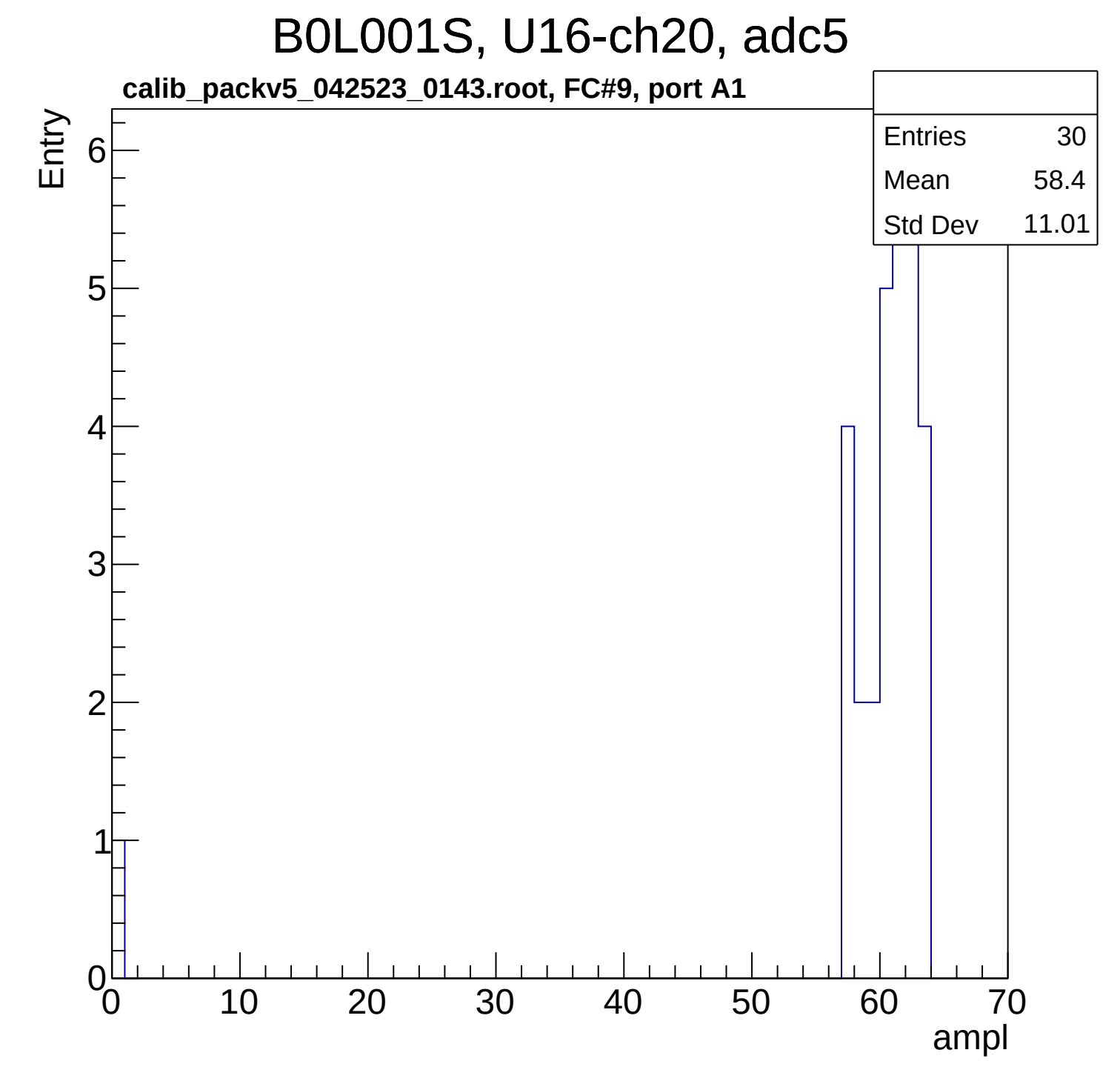

B0L001S, U16-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 31.61
Std Dev 5.117
Gaus mean : 32.1724
Gaus Width: 4.2054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

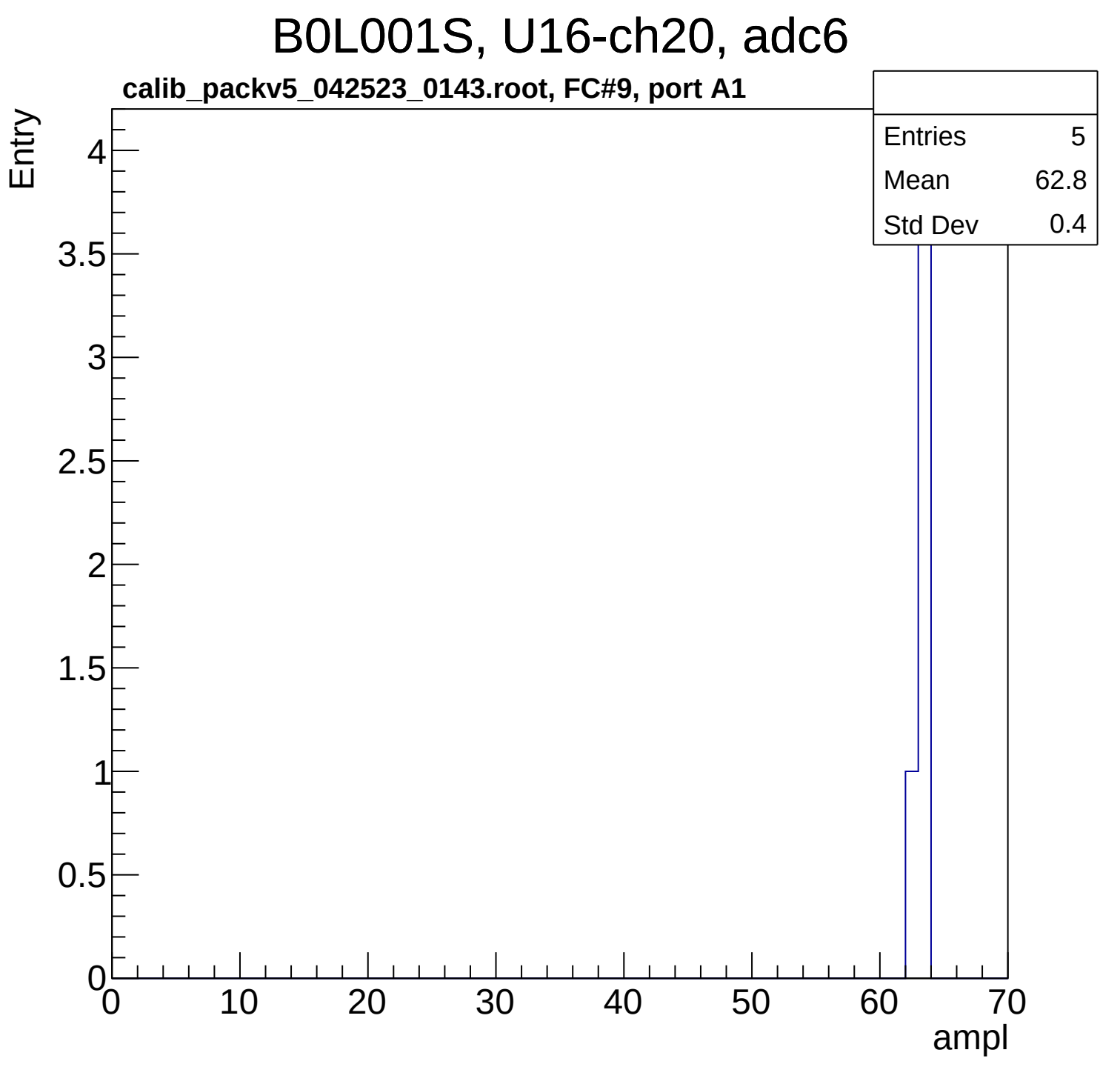

B0L001S, U16-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 38.08
Std Dev 3.153
Gaus mean : 38.9075
Gaus Width: 3.9546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

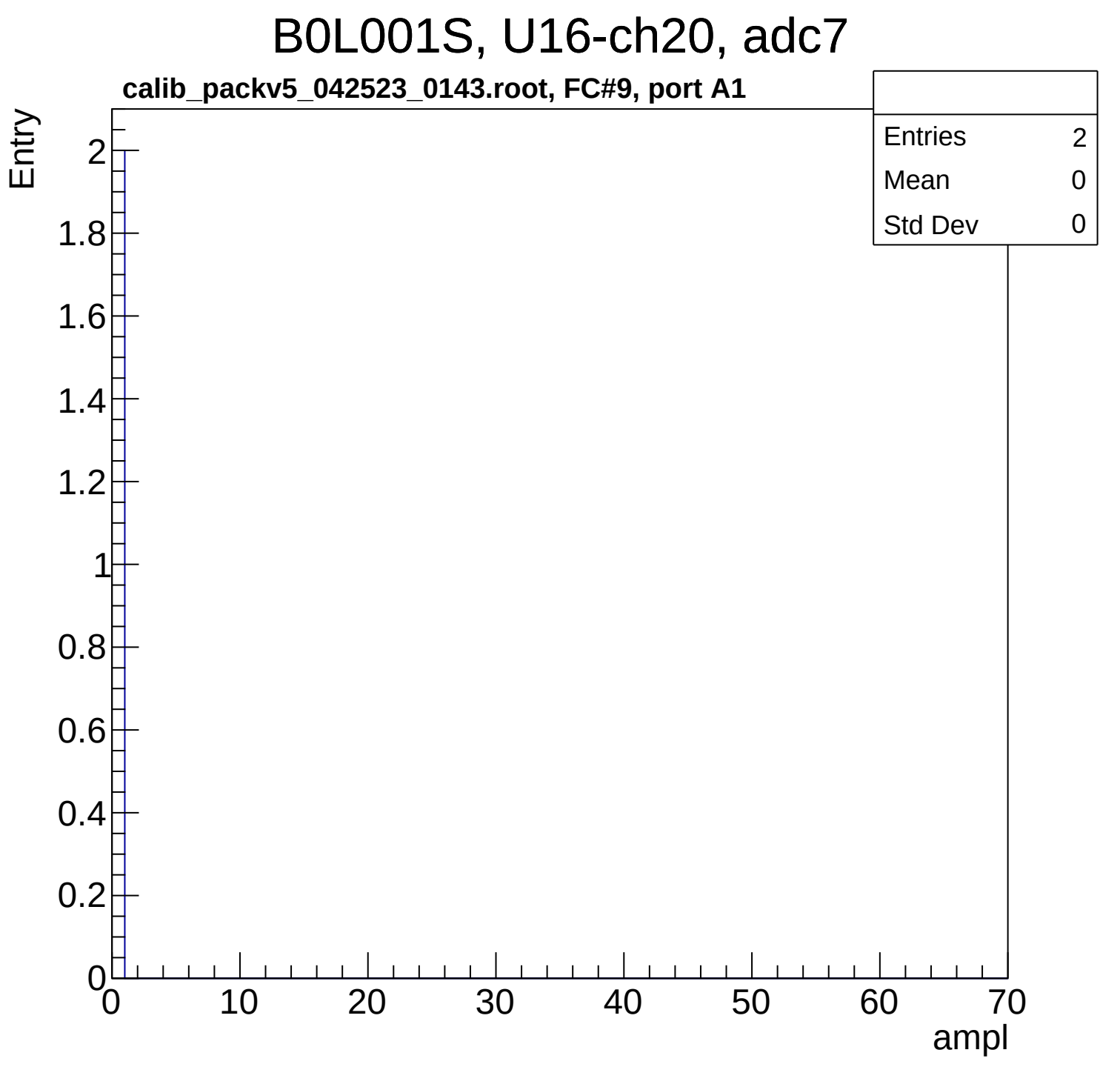

B0L001S, U16-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 45.19
Std Dev 3.24
Gaus mean : 45.3246
Gaus Width: 3.2456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

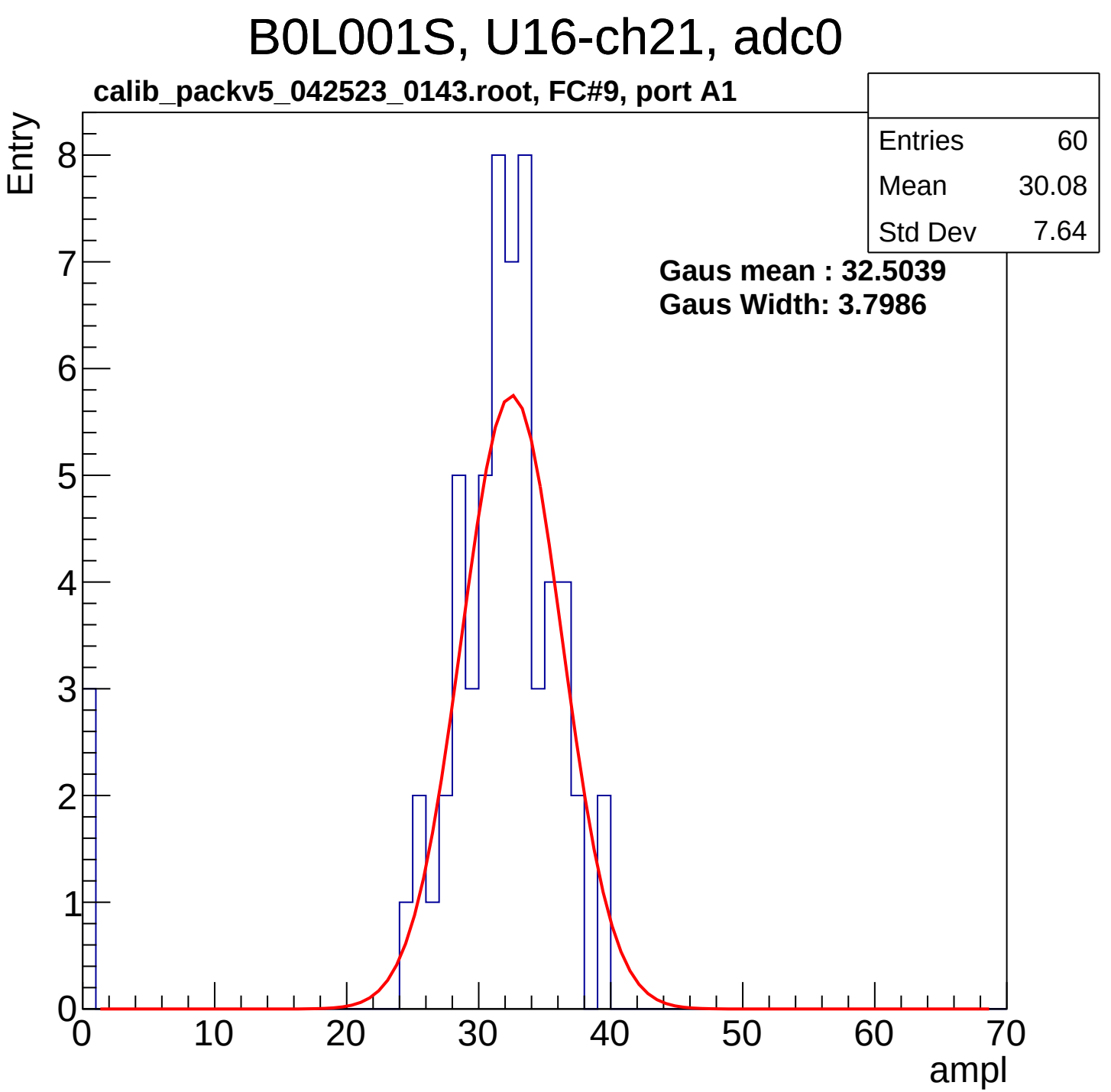

B0L001S, U16-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.39
Std Dev 3.745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

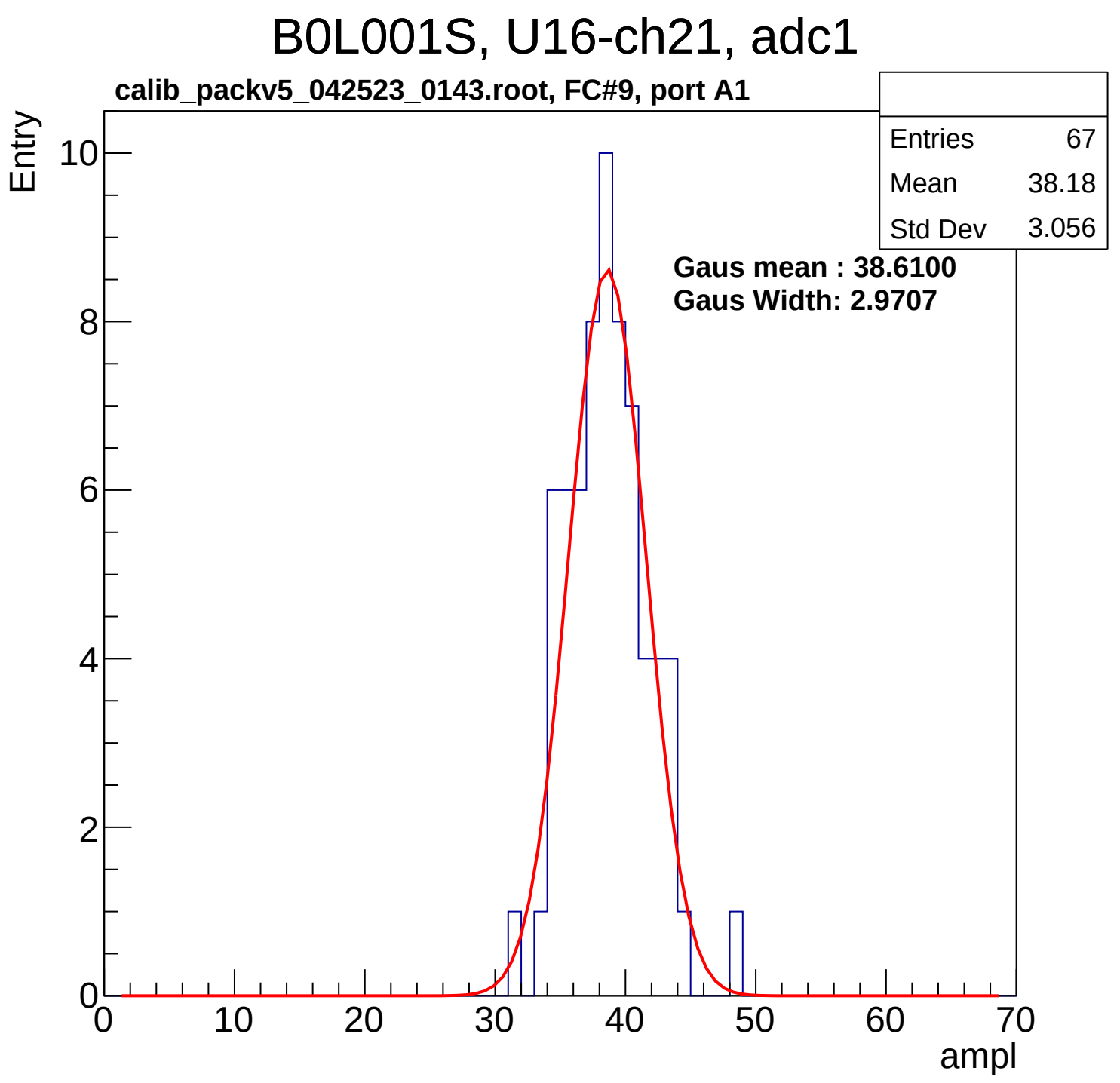

B0L001S, U16-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.13
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

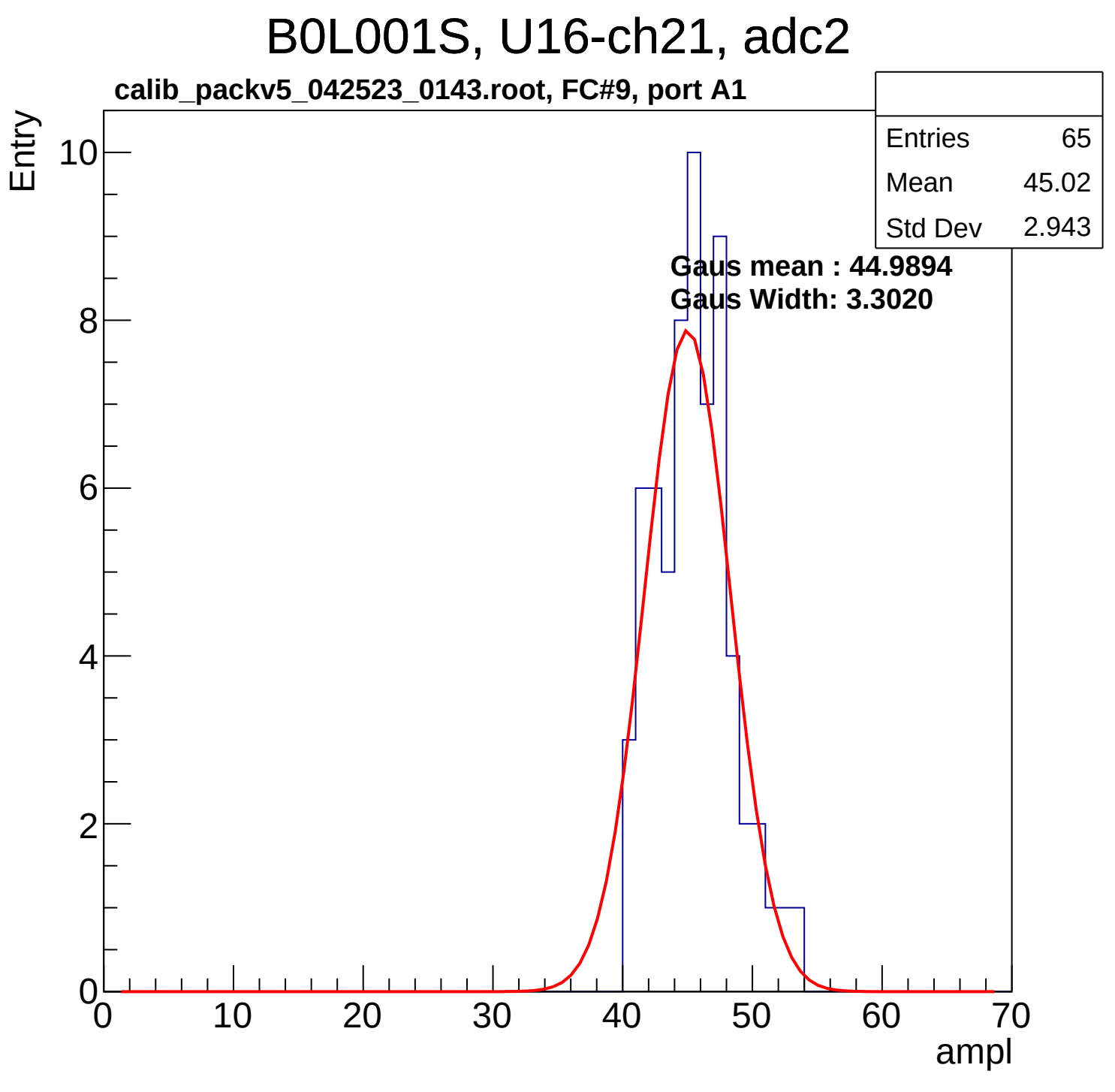

B0L001S, U16-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.4
Std Dev 11.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

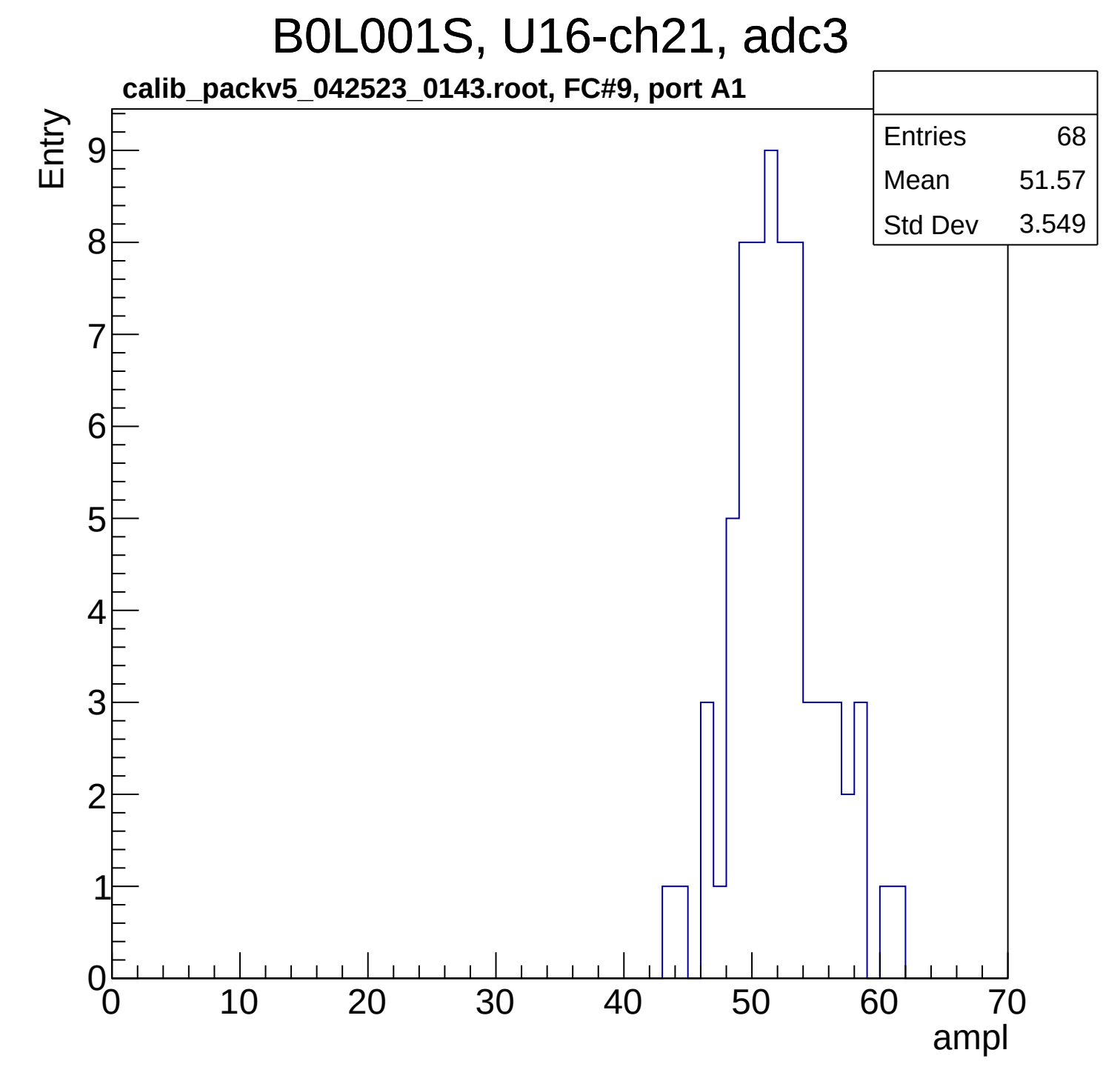

B0L001S, U16-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

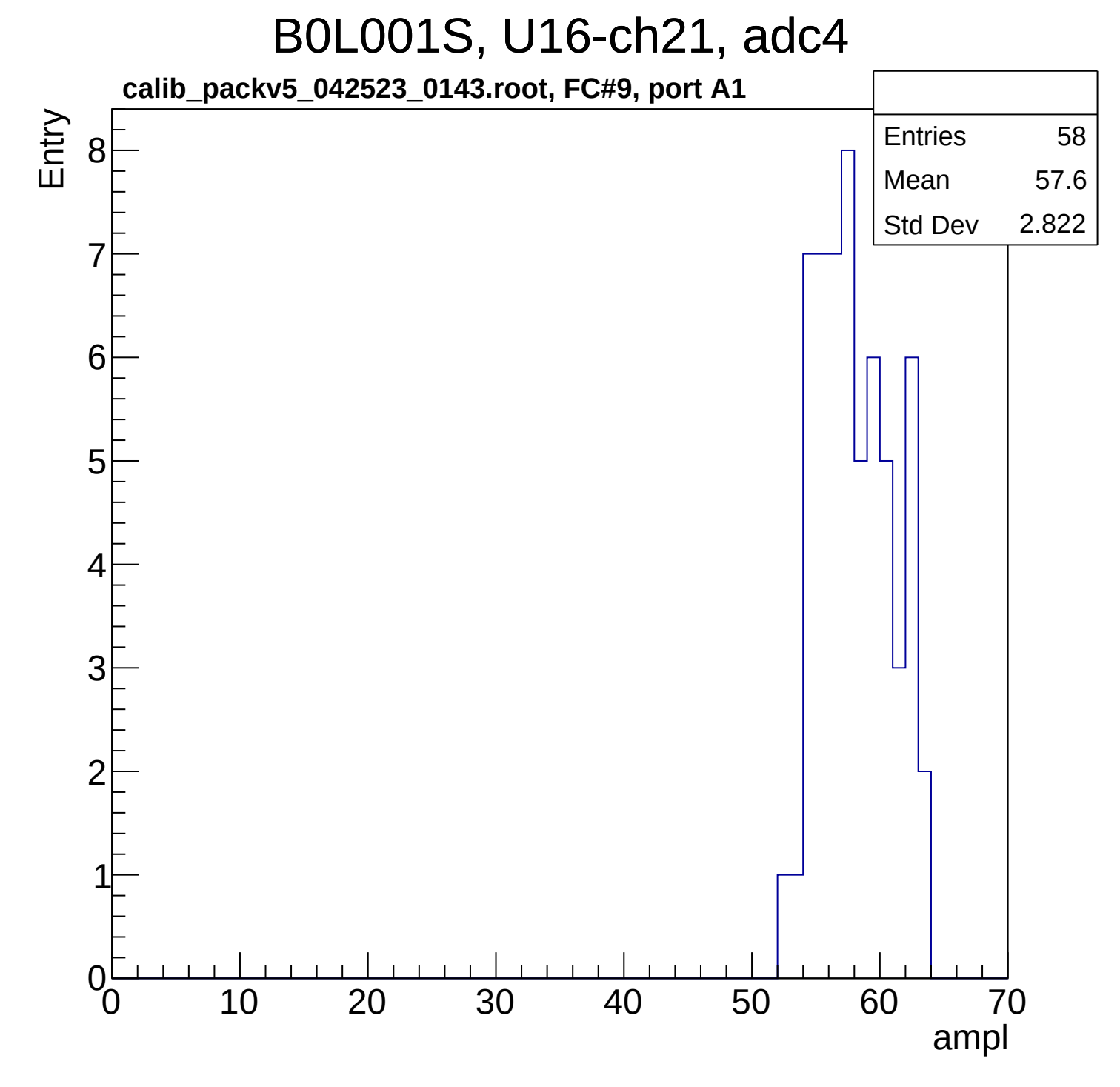

B0L001S, U16-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

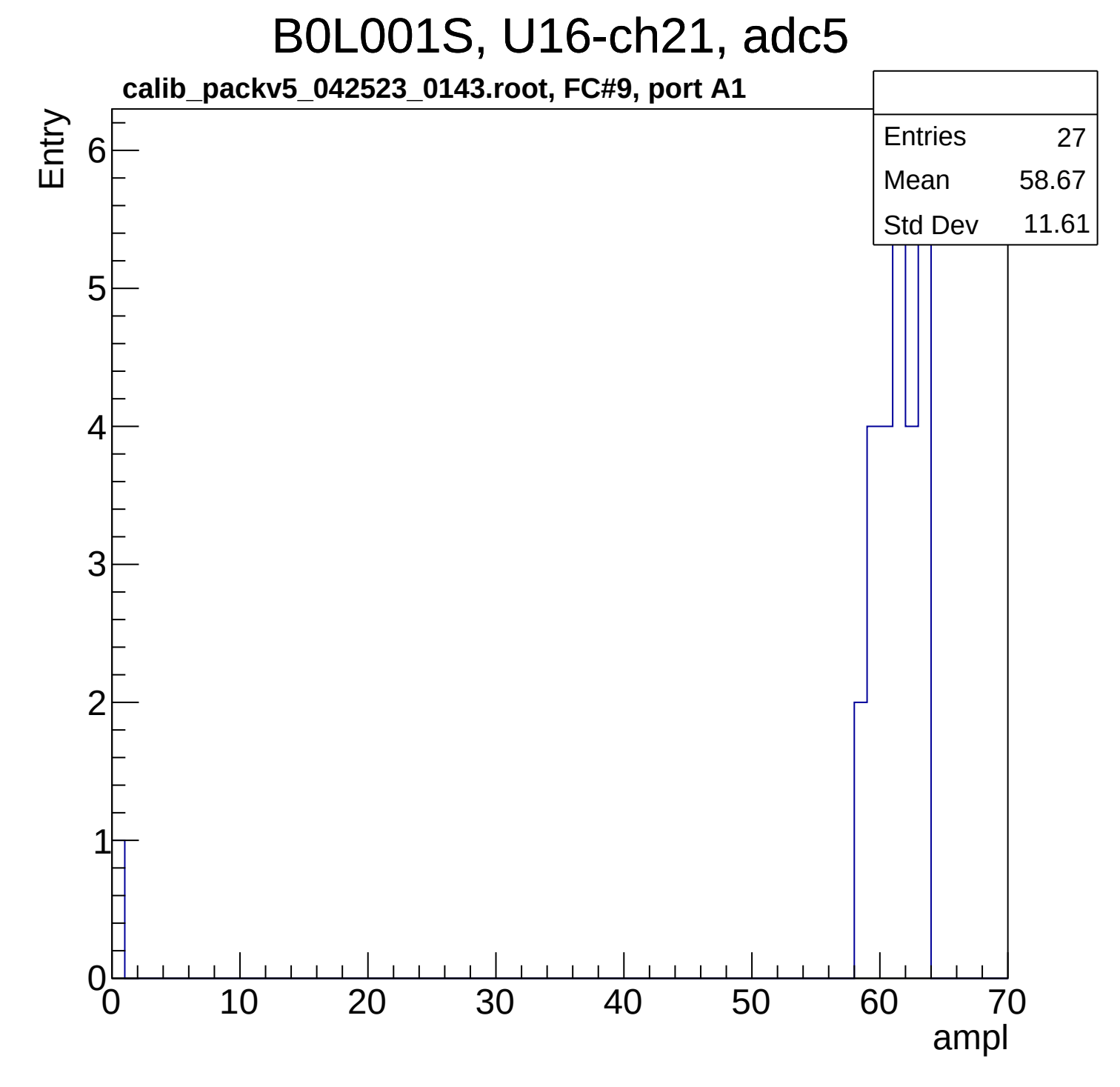

B0L001S, U16-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.08
Std Dev 7.64
Gaus mean : 32.5039
Gaus Width: 3.7986
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.18
Std Dev 3.056
Gaus mean : 38.6100
Gaus Width: 2.9707
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.02
Std Dev 2.943
Gaus mean : 44.9894
Gaus Width: 3.3020
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.57
Std Dev 3.549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.6
Std Dev 2.822
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.67
Std Dev 11.61
Entry
0
1
2
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10
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30
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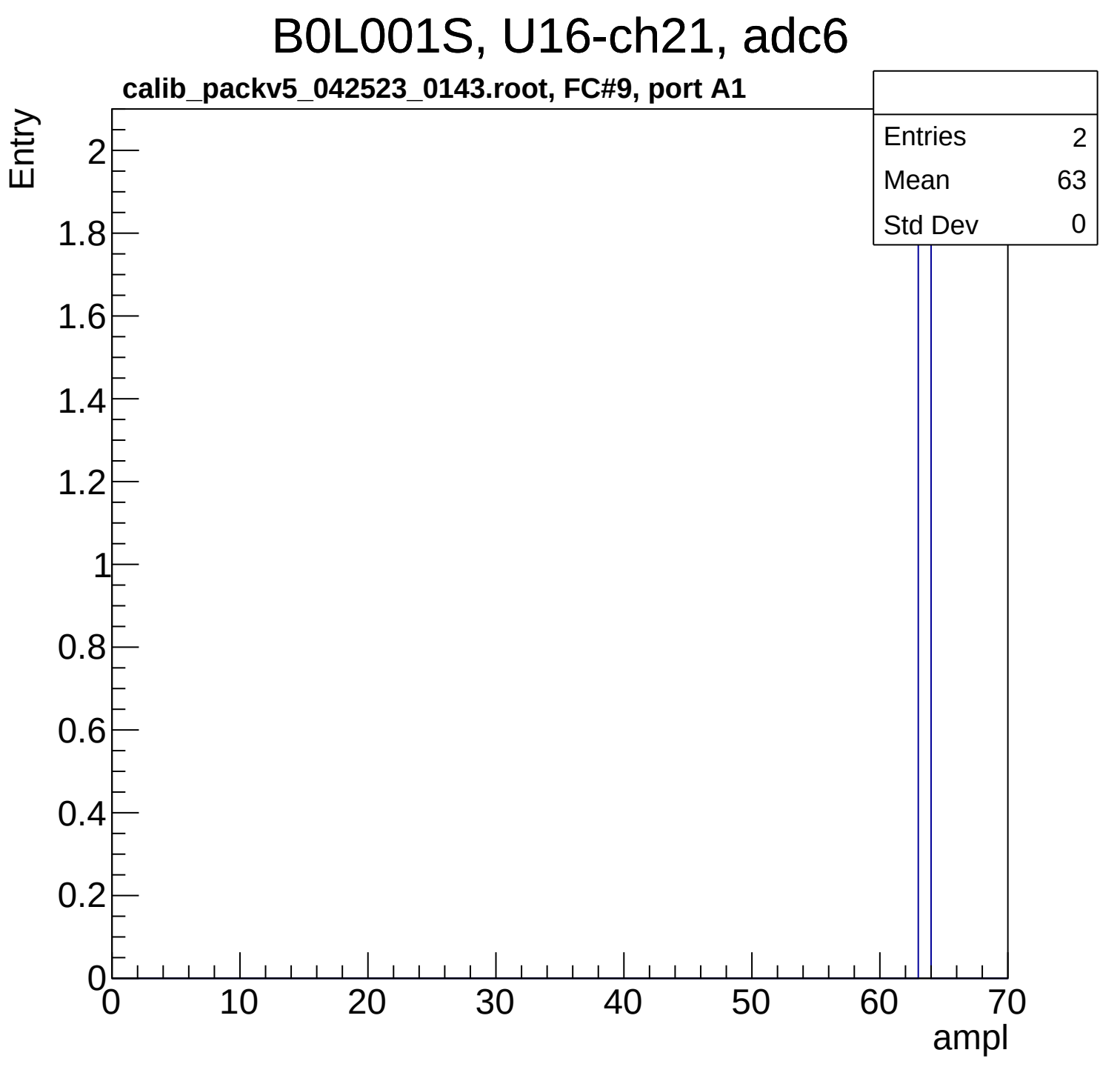
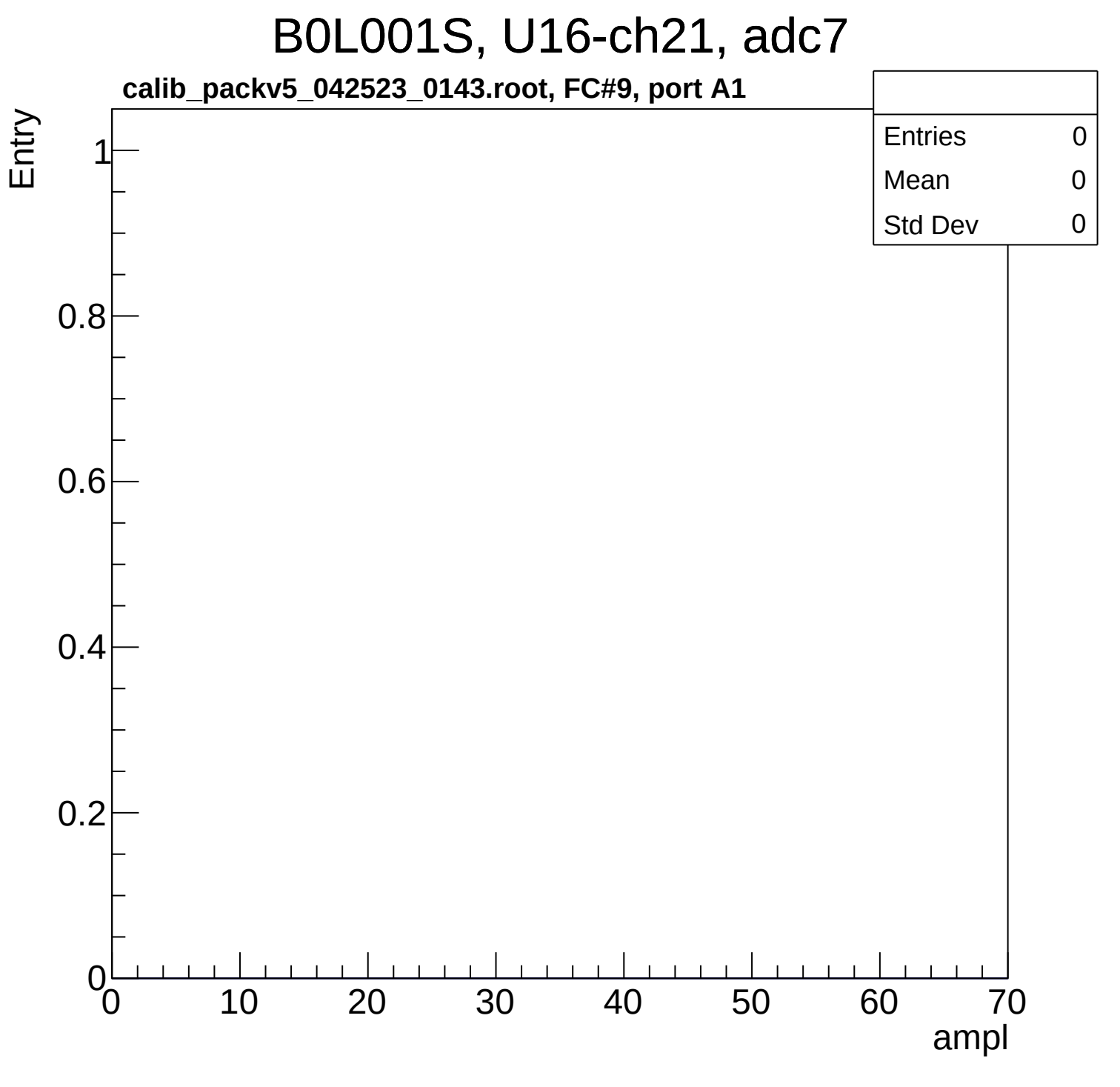
50
60
70
ampl


B0L001S, U16-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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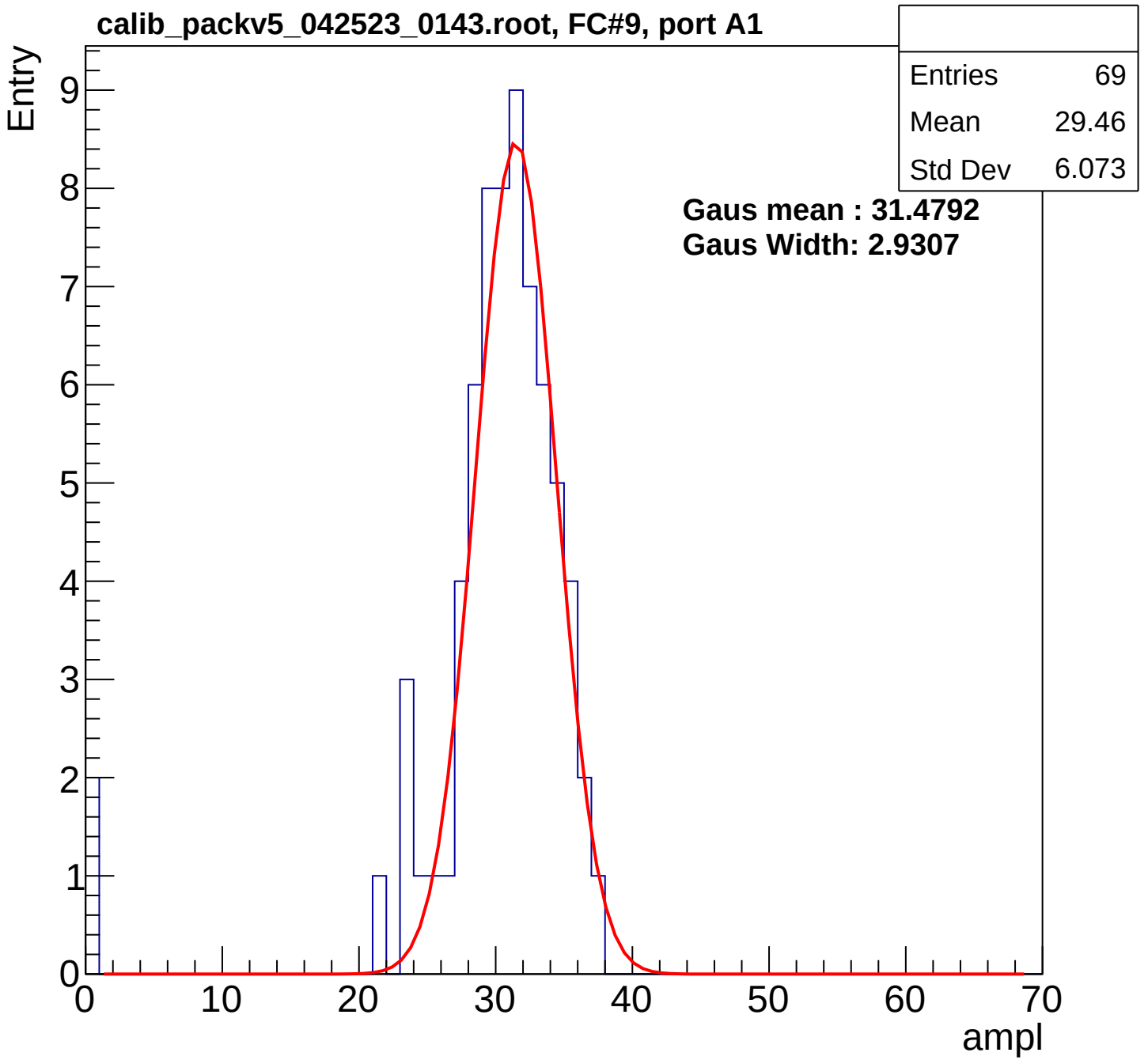
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.46
Std Dev 6.073
Gaus mean : 31.4792
Gaus Width: 2.9307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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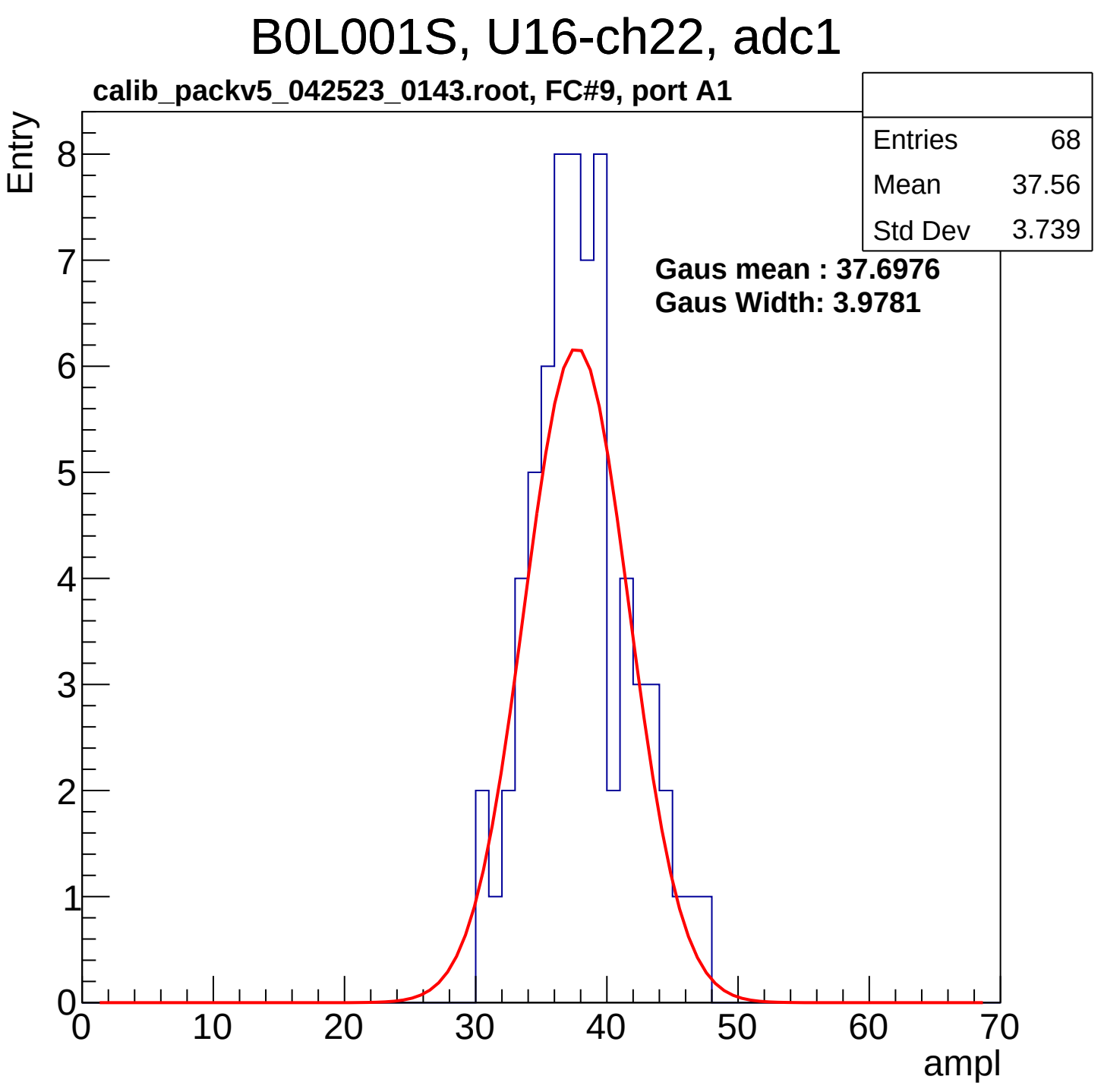
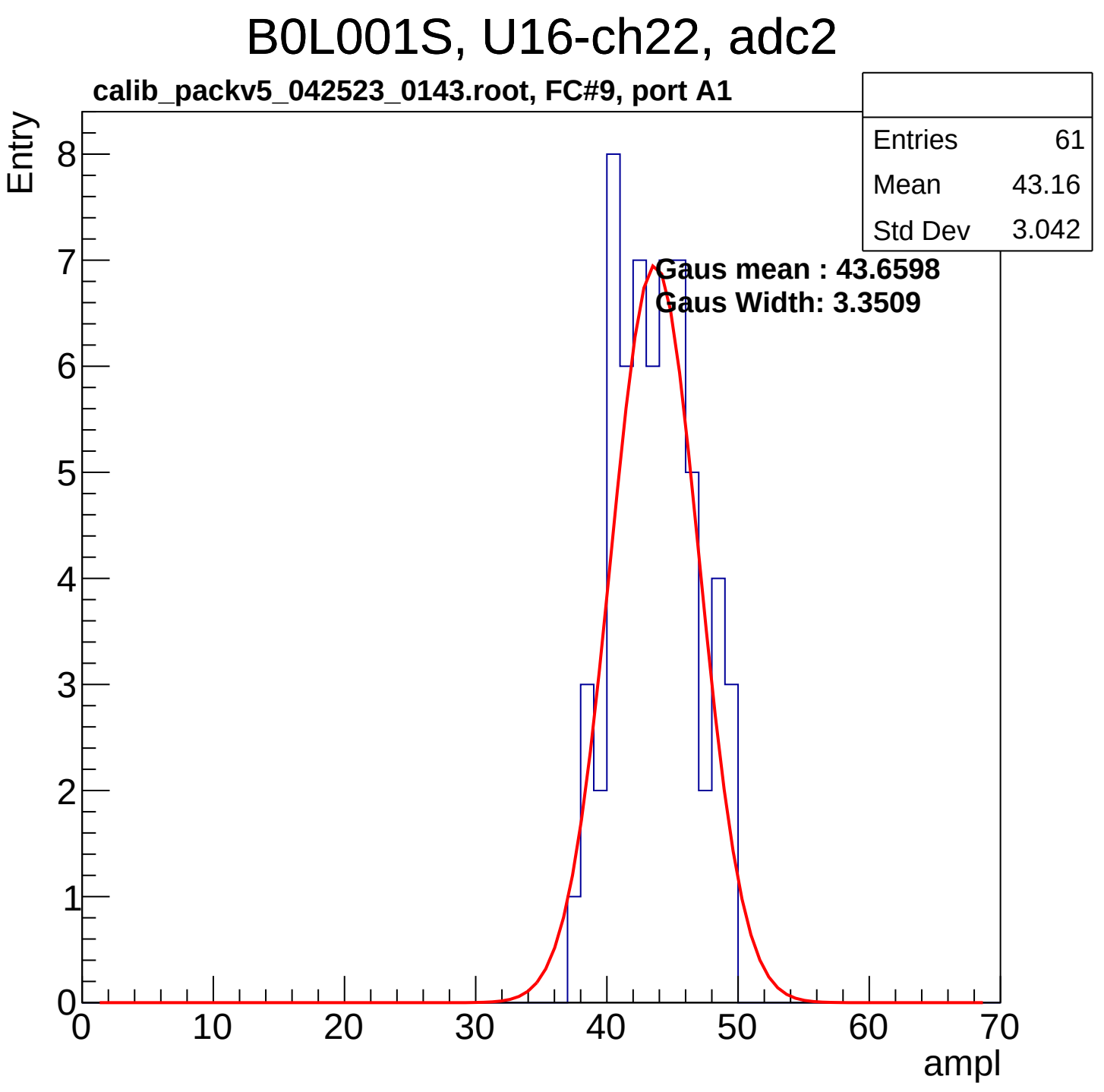
40
50
60
70


B0L001S, U16-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.56
Std Dev 3.739
Gaus mean : 37.6976
Gaus Width: 3.9781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.16
Std Dev 3.042
Gaus mean : 43.6598
Gaus Width: 3.3509
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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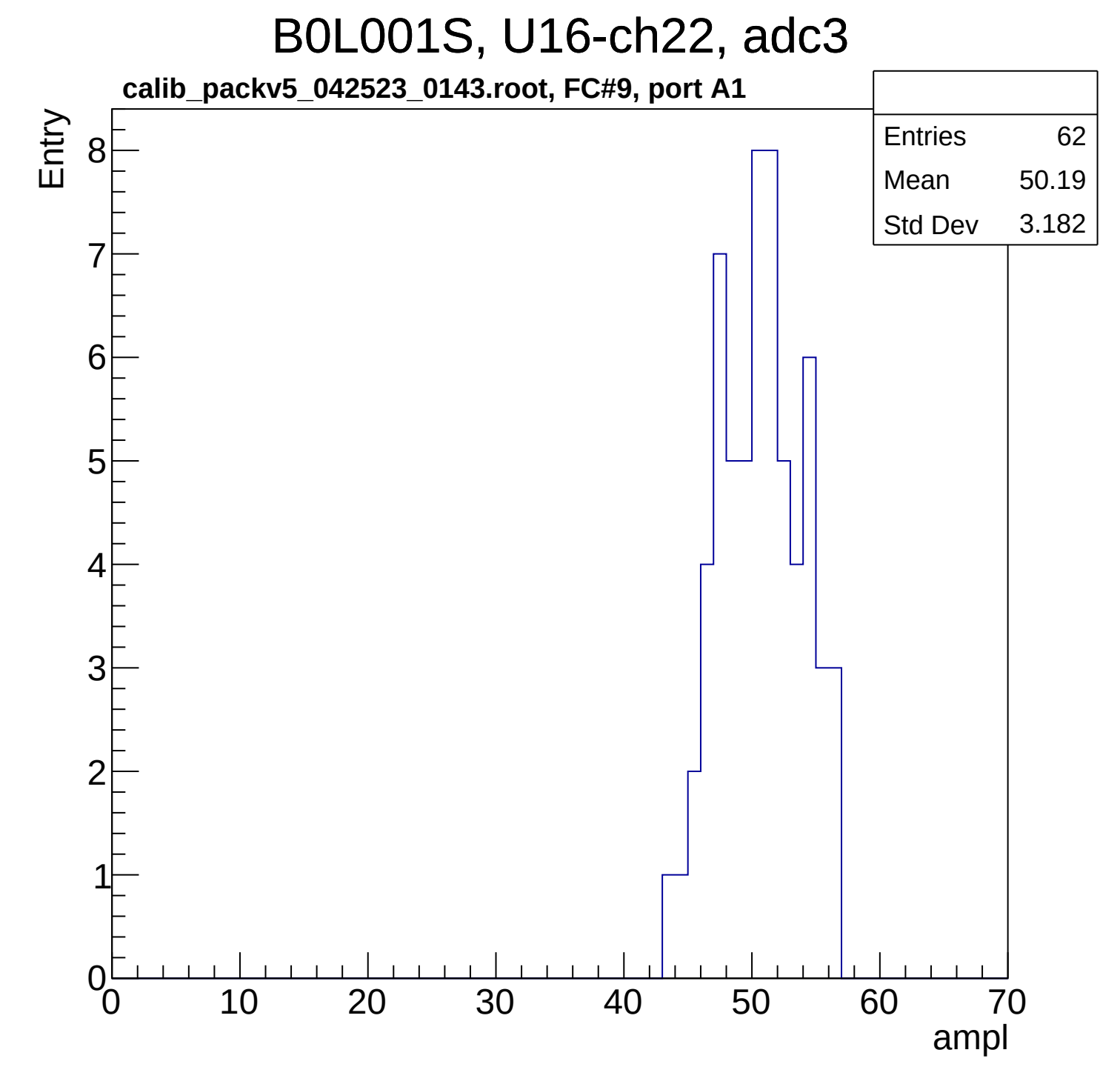
B0L001S, U16-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.19
Std Dev 3.182
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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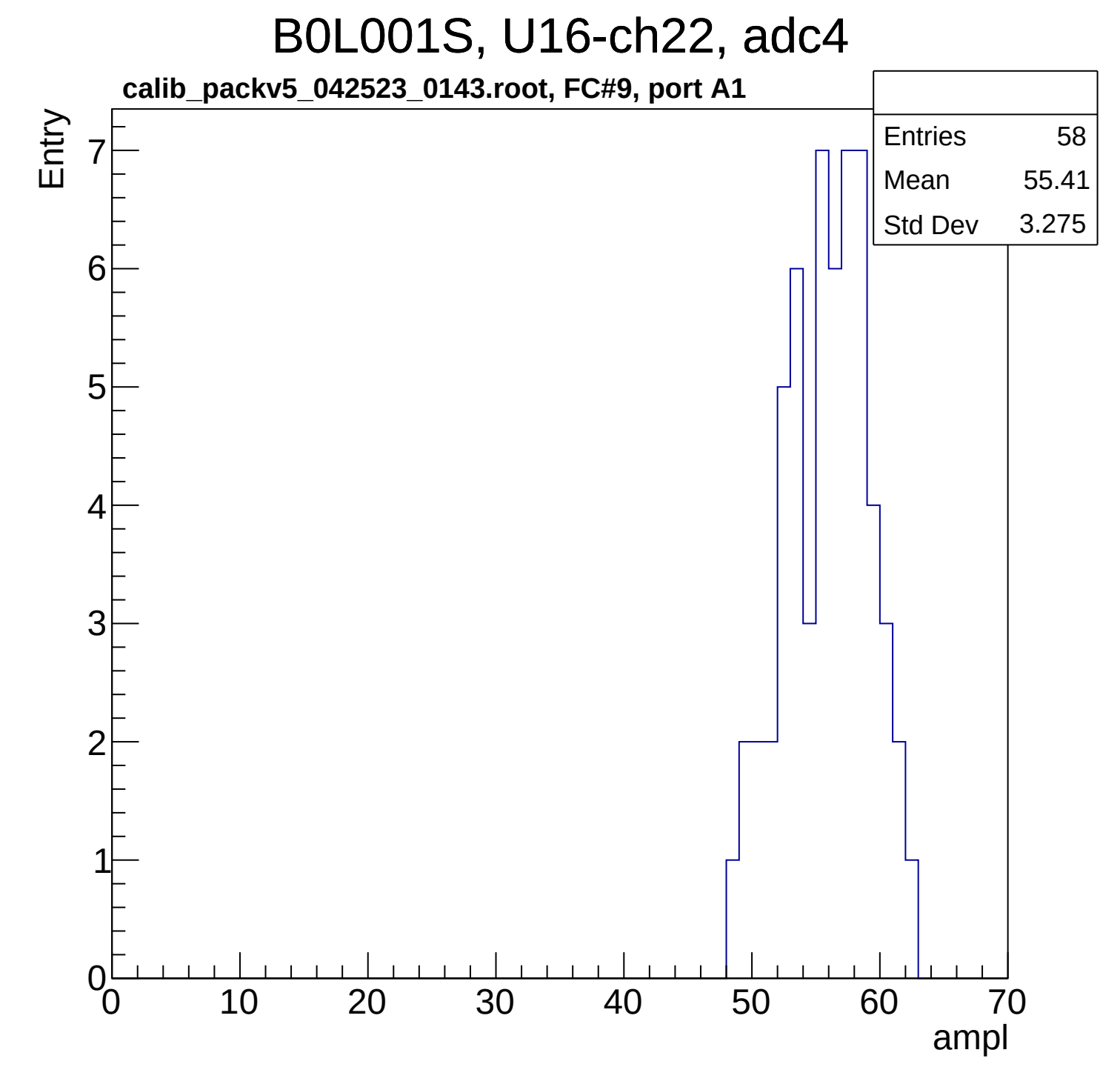
B0L001S, U16-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.41
Std Dev 3.275
Entry
ampl
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1
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4
5
6
7
0
10
20
30
40
50
60
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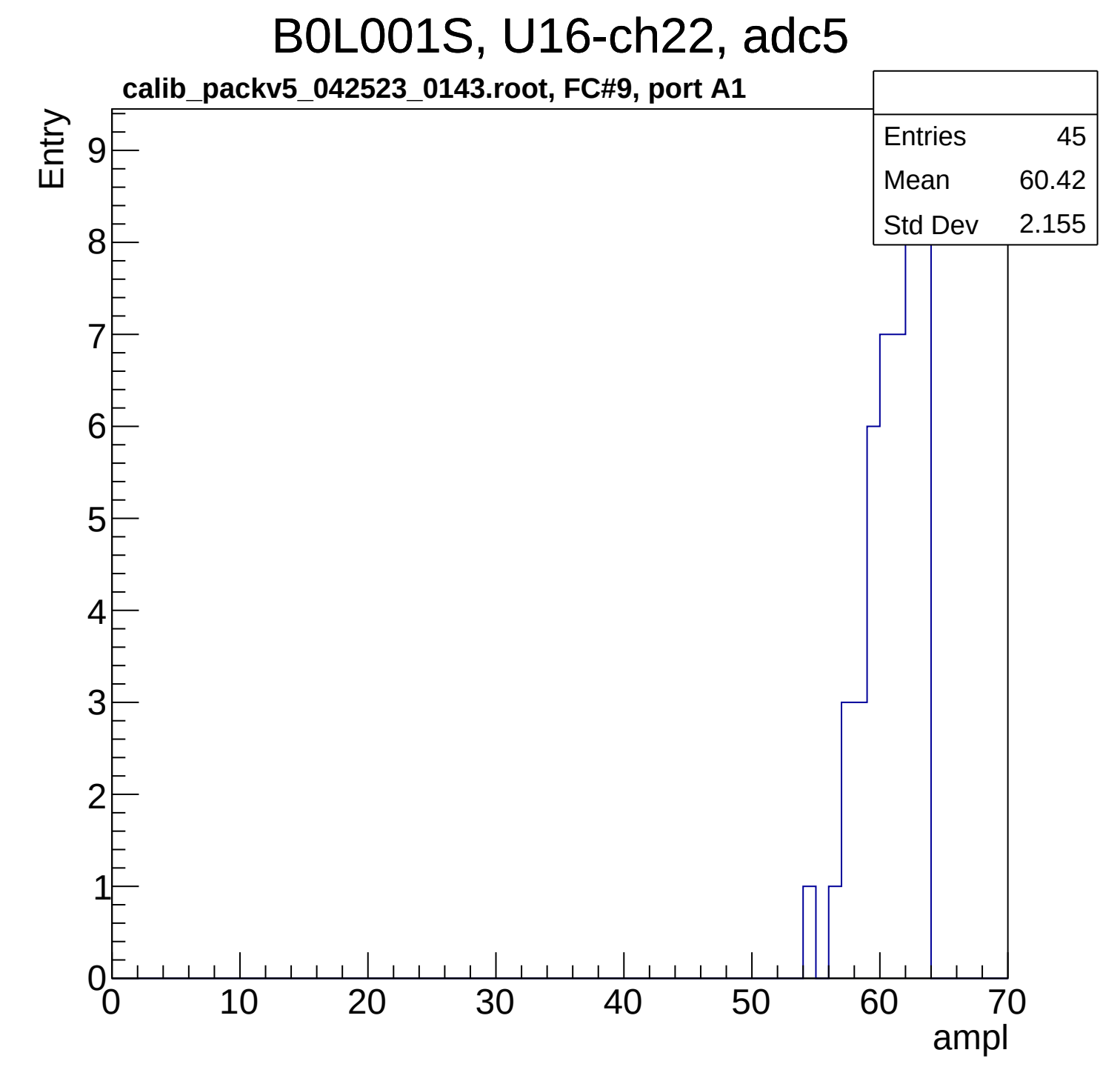
B0L001S, U16-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 60.42
Std Dev 2.155
Entry
ampl
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2
3
4
5
6
7
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0
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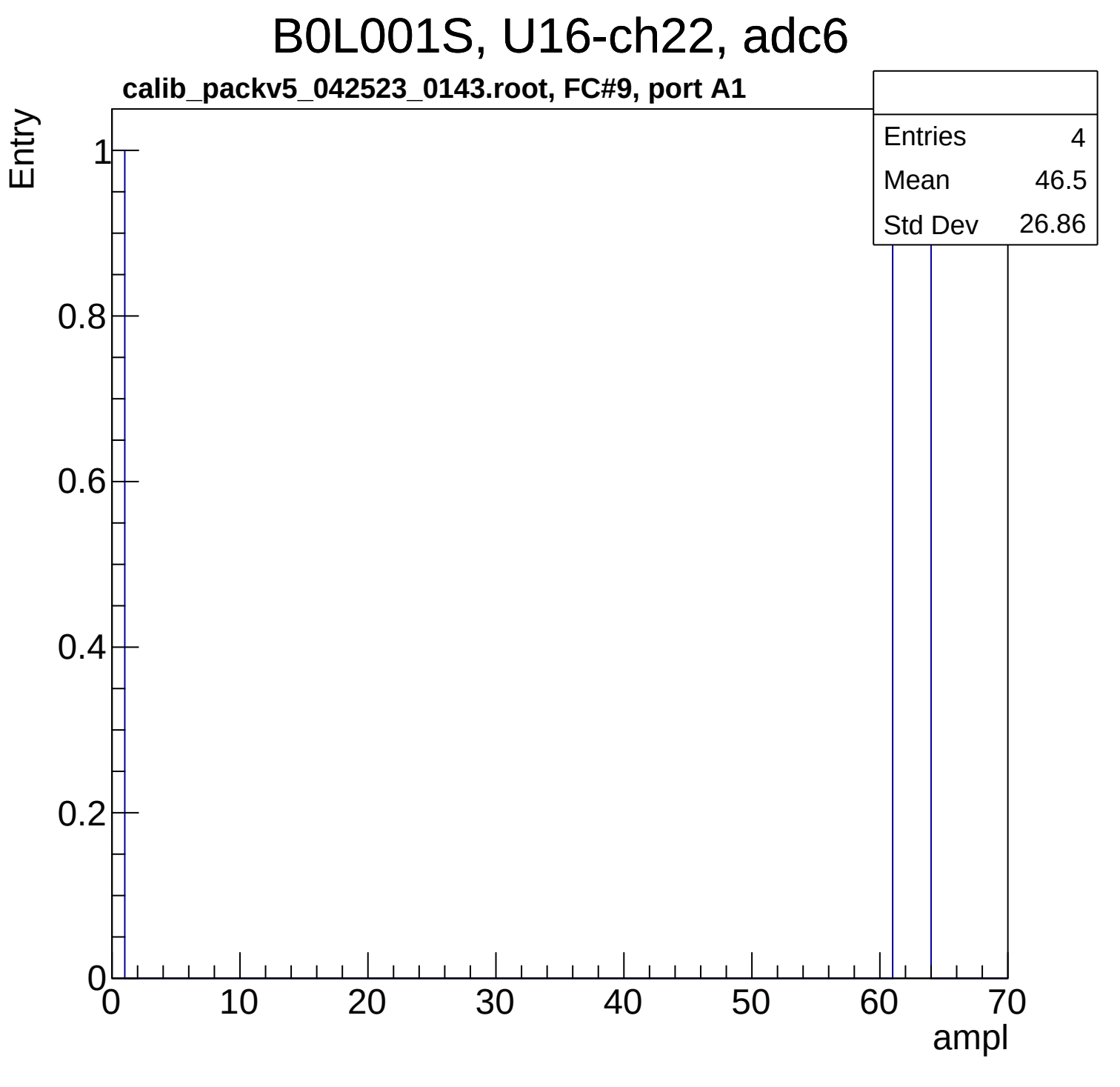
B0L001S, U16-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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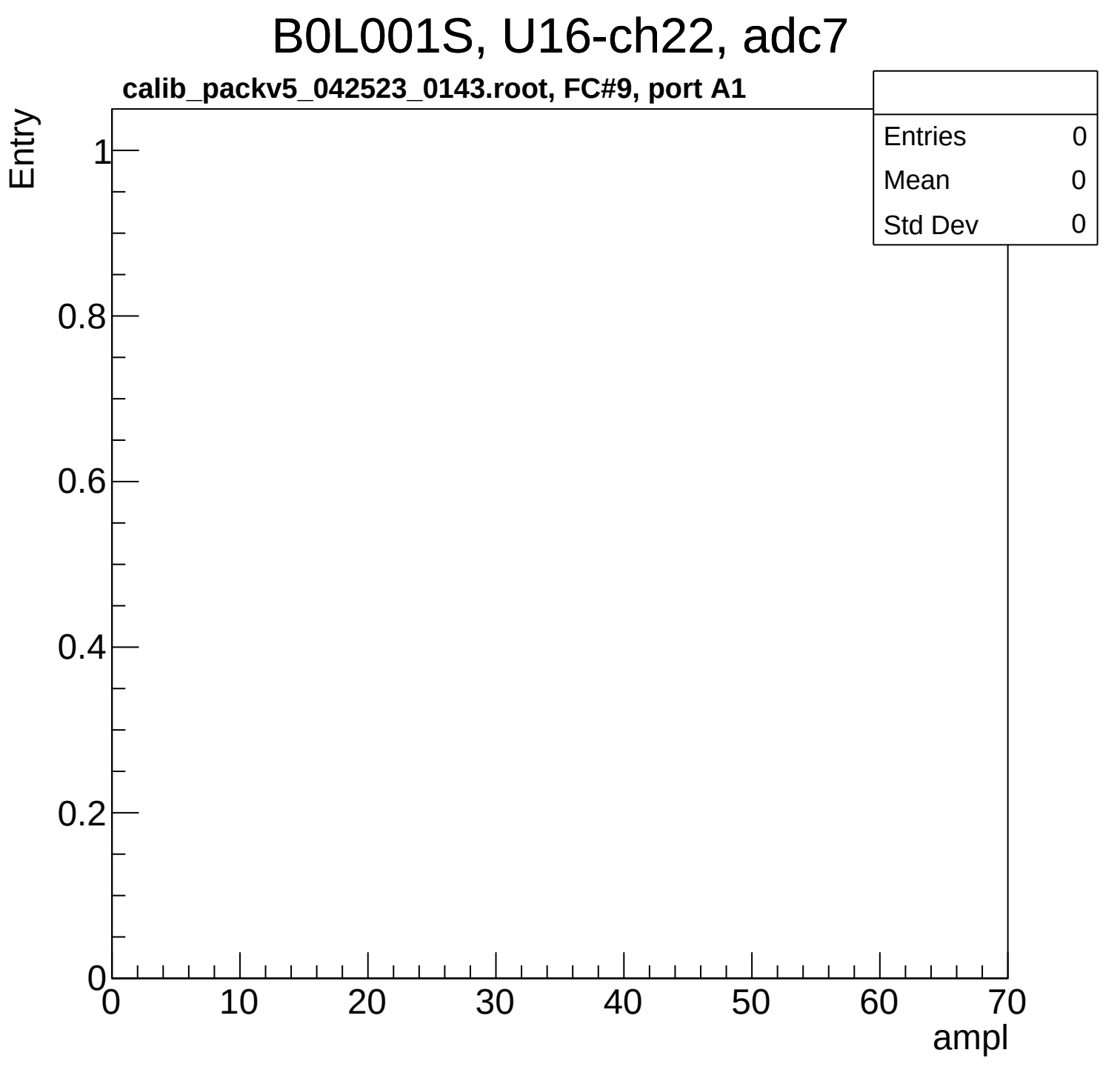
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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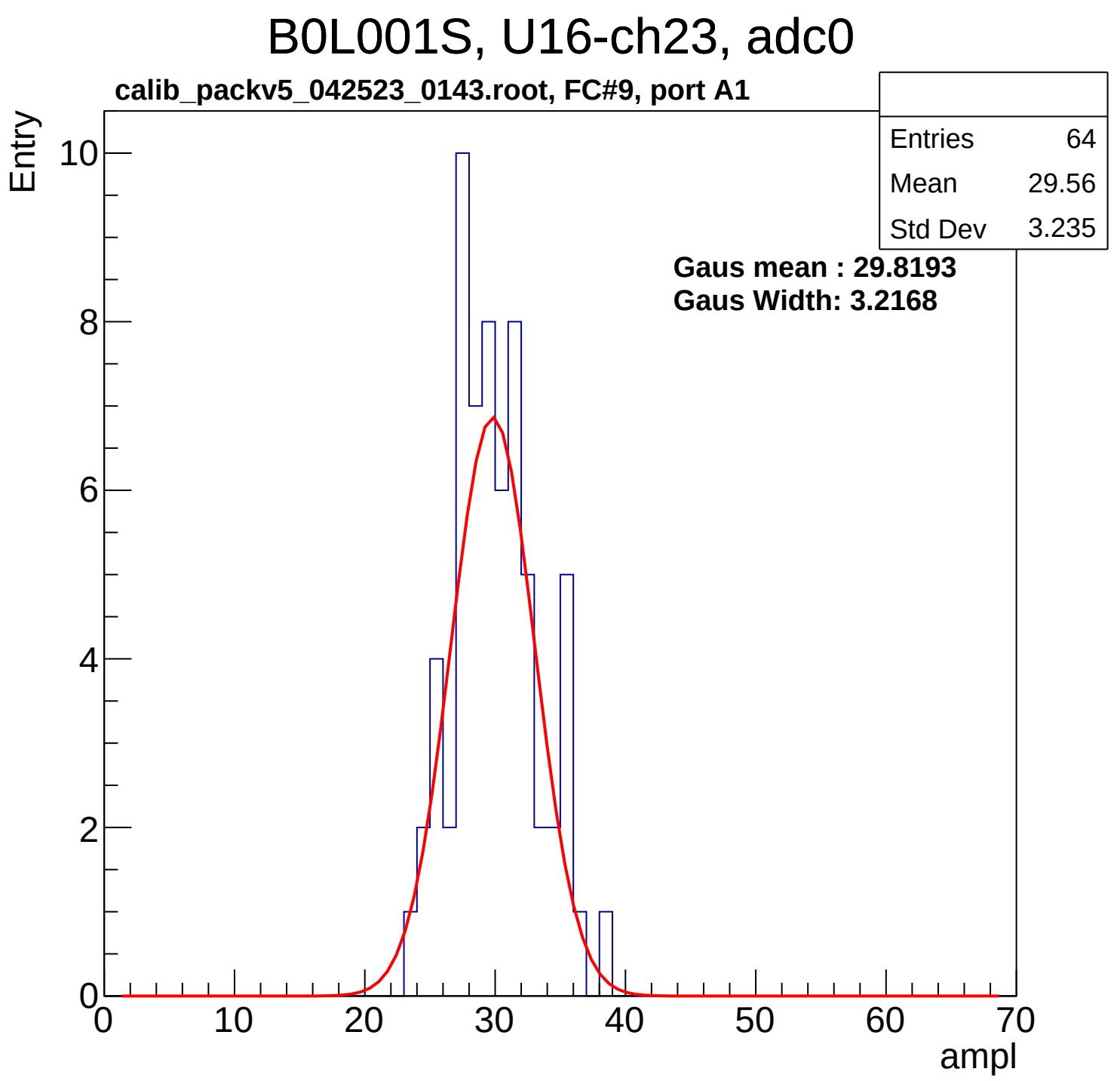
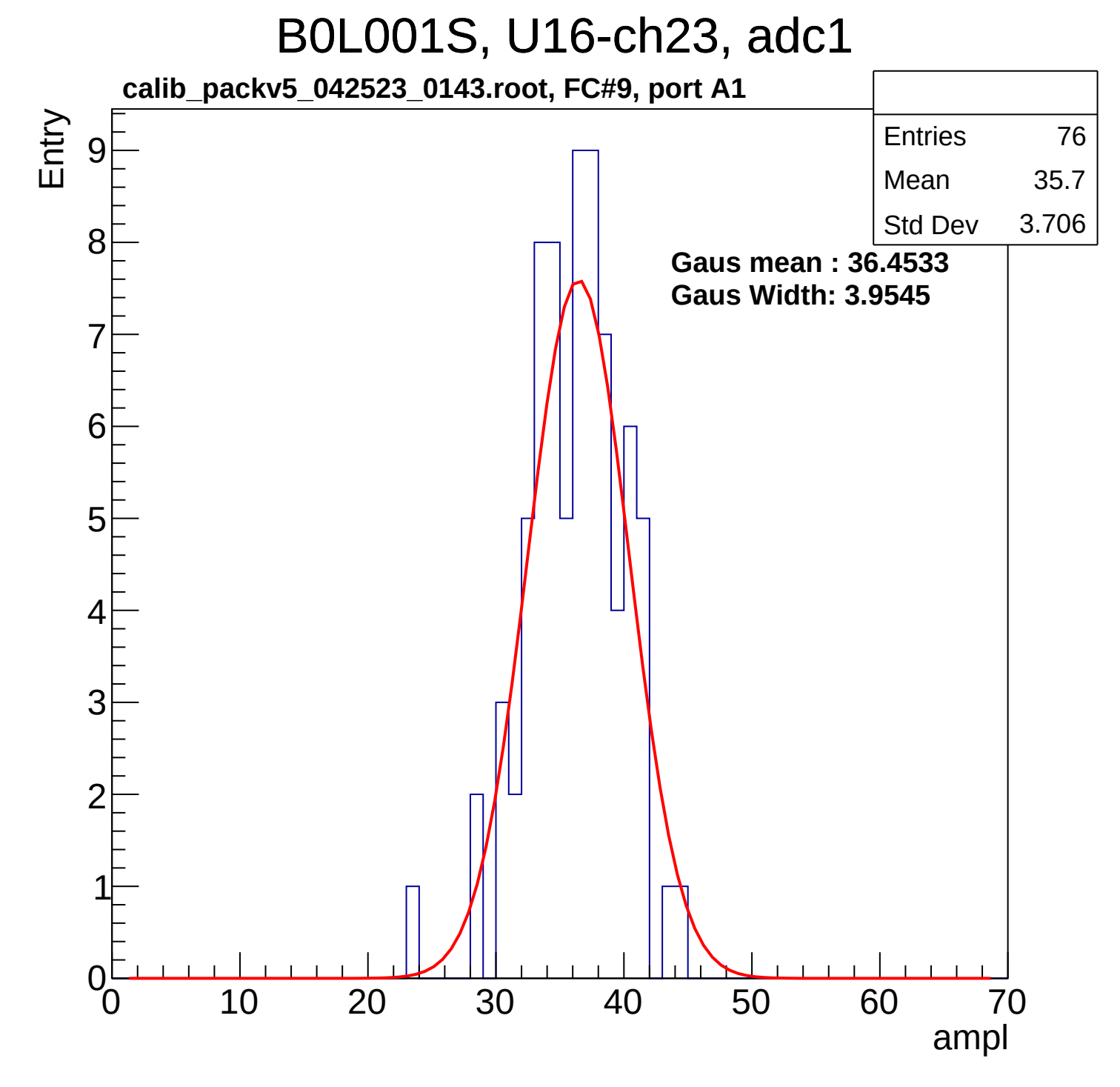
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 29.56
Std Dev 3.235
Gaus mean : 29.8193
Gaus Width: 3.2168
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 35.7
Std Dev 3.706
Gaus mean : 36.4533
Gaus Width: 3.9545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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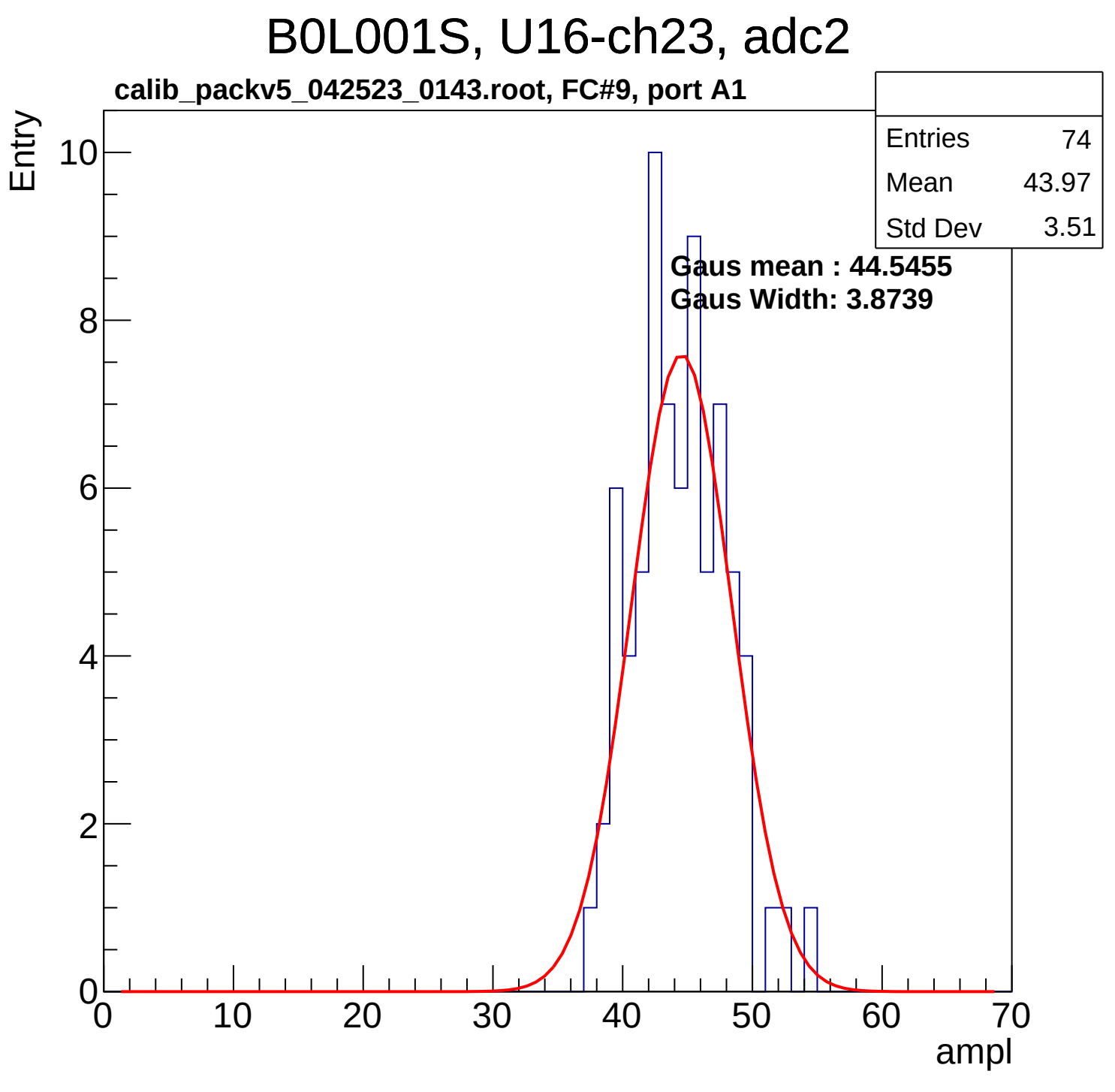
B0L001S, U16-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.97
Std Dev 3.51
Gaus mean : 44.5455
Gaus Width: 3.8739
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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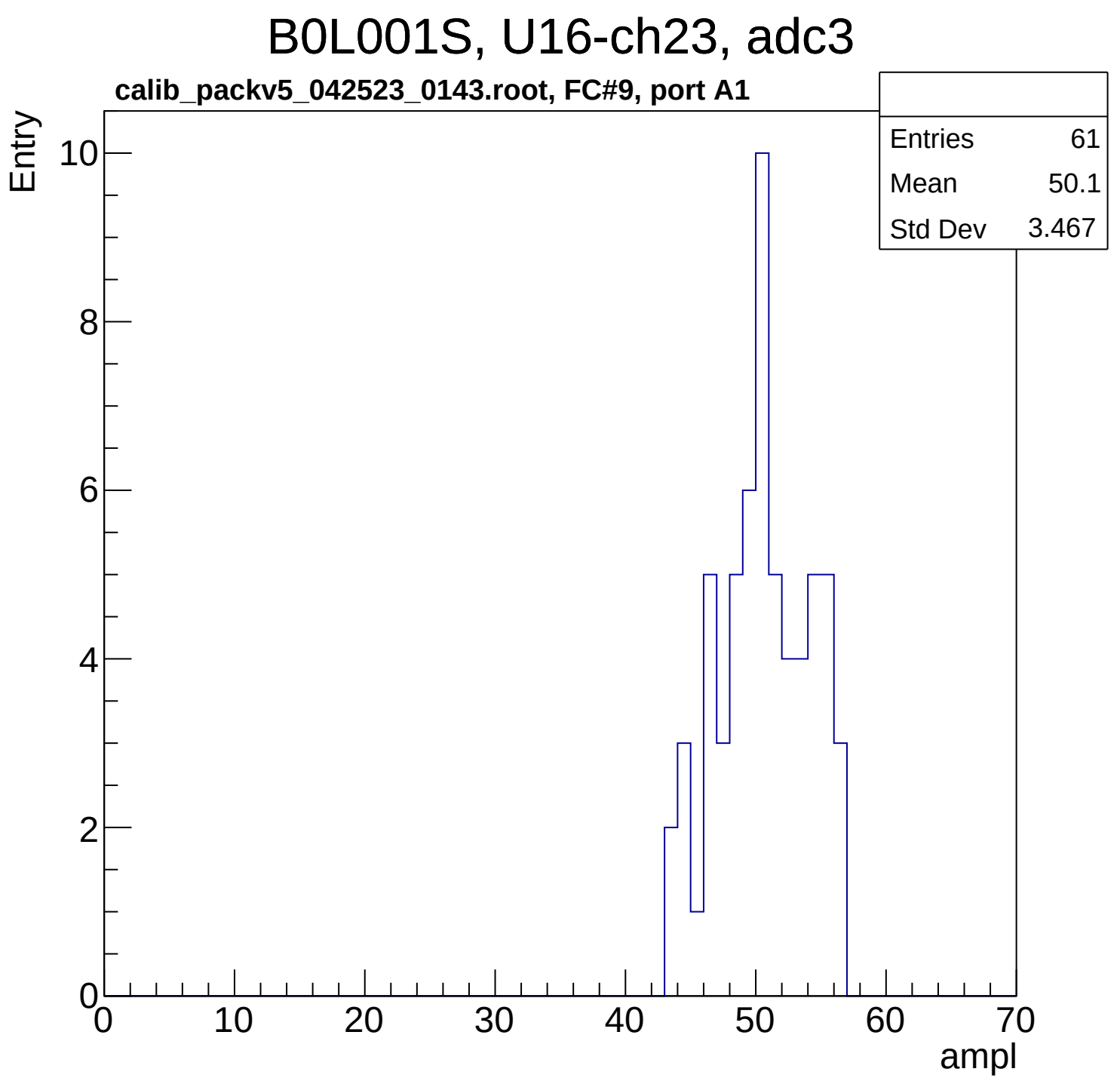
B0L001S, U16-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 50.1
Std Dev 3.467
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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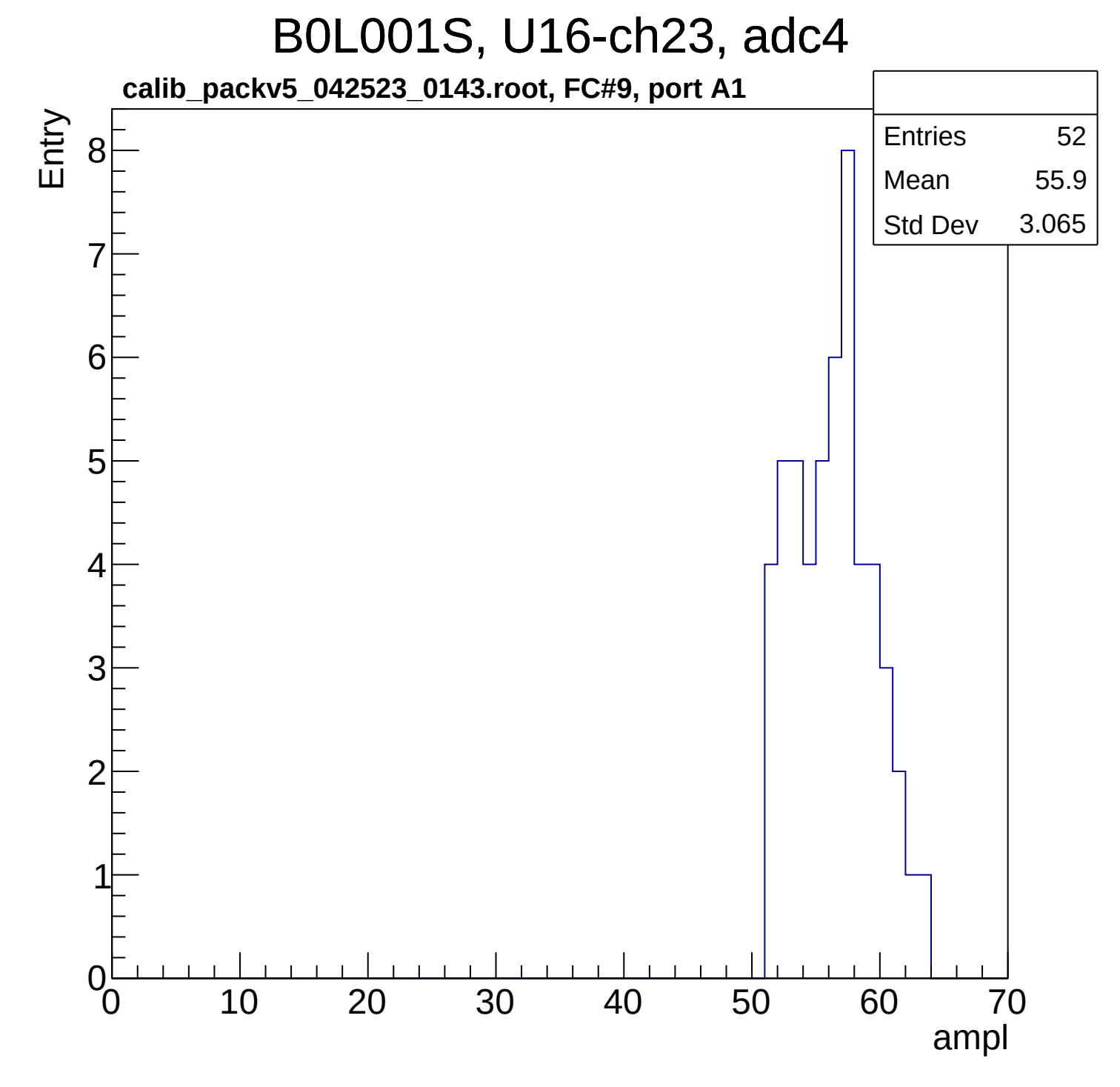
B0L001S, U16-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.9
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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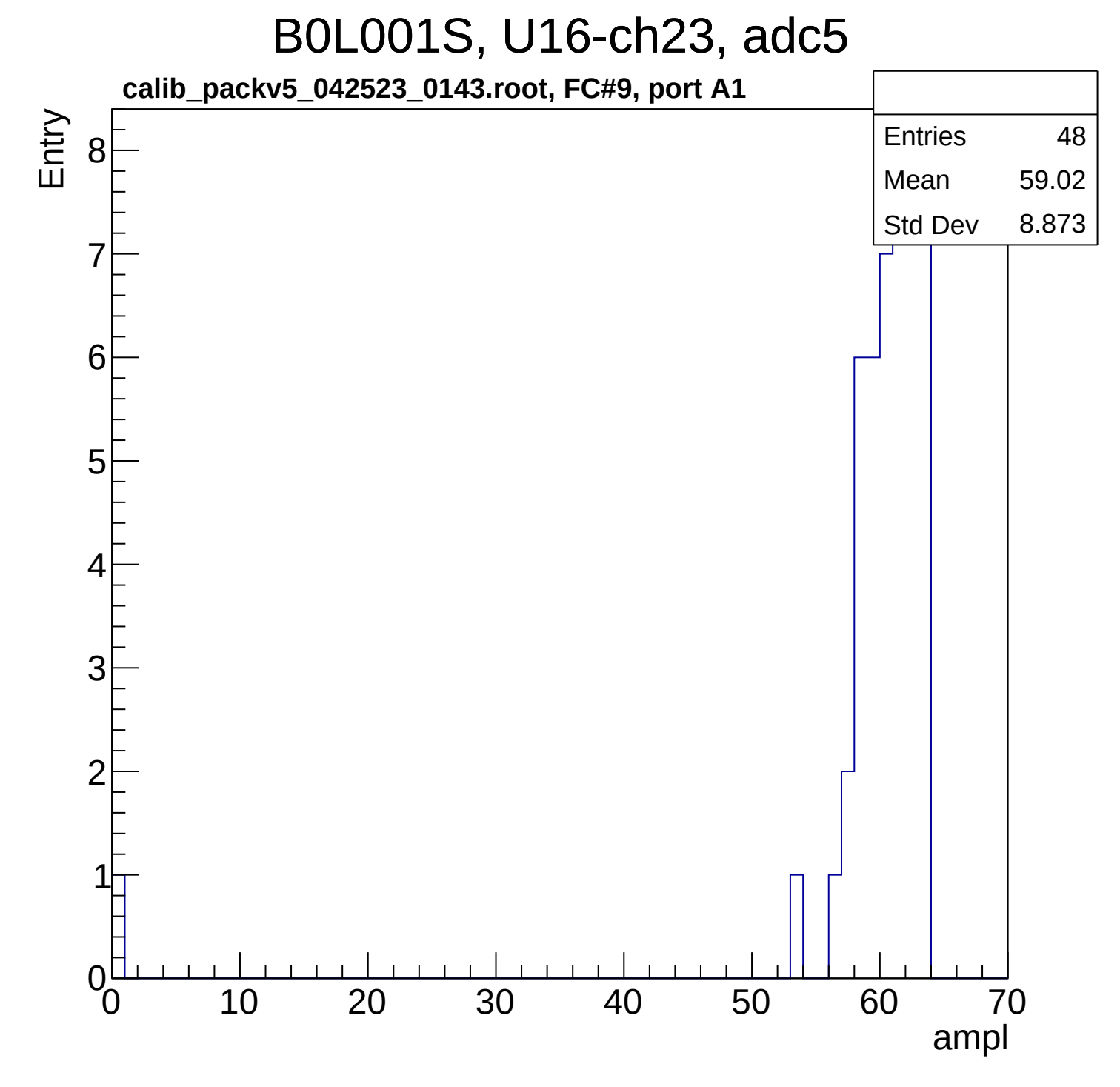
B0L001S, U16-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59.02
Std Dev 8.873
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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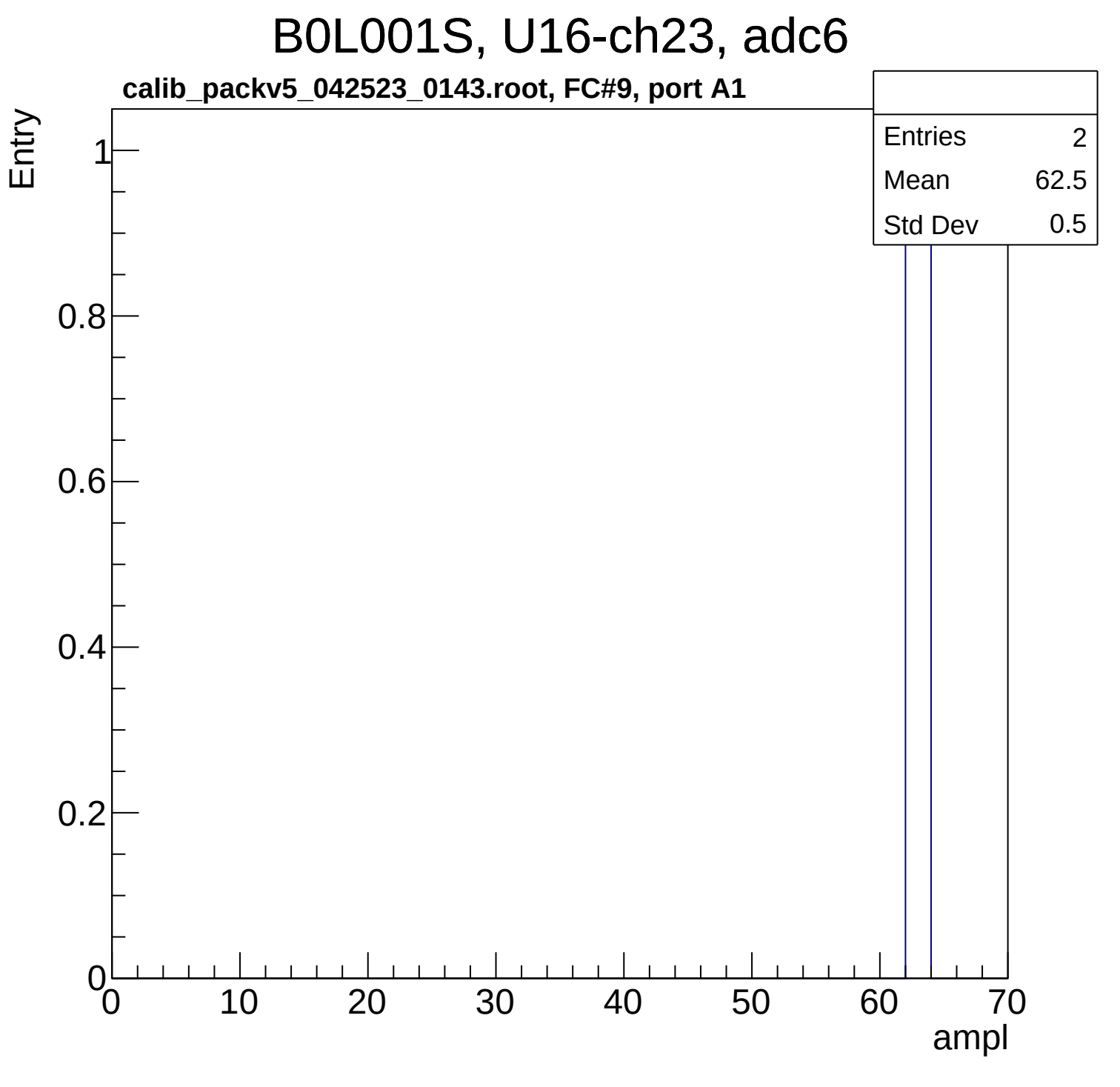
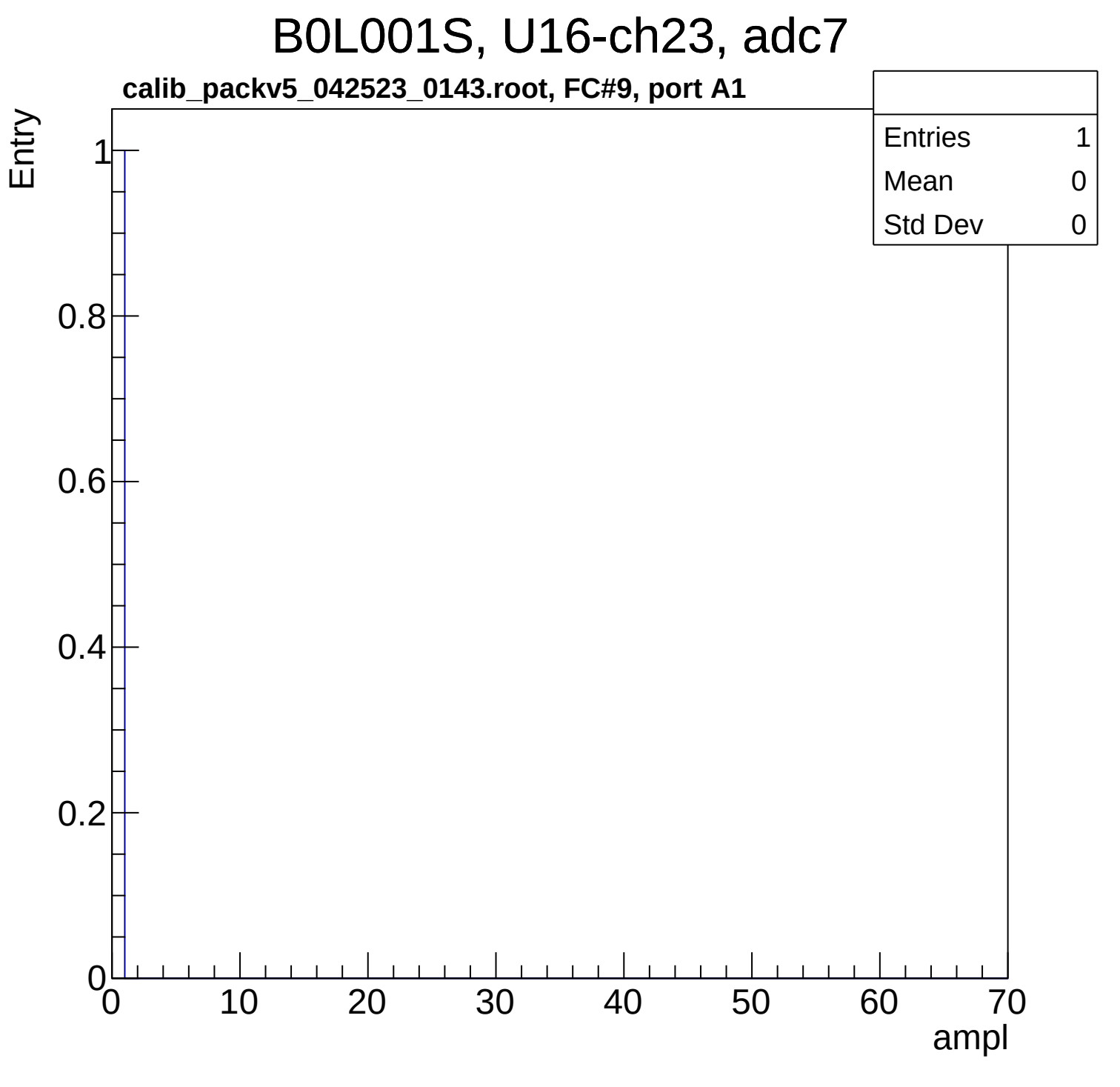

B0L001S, U16-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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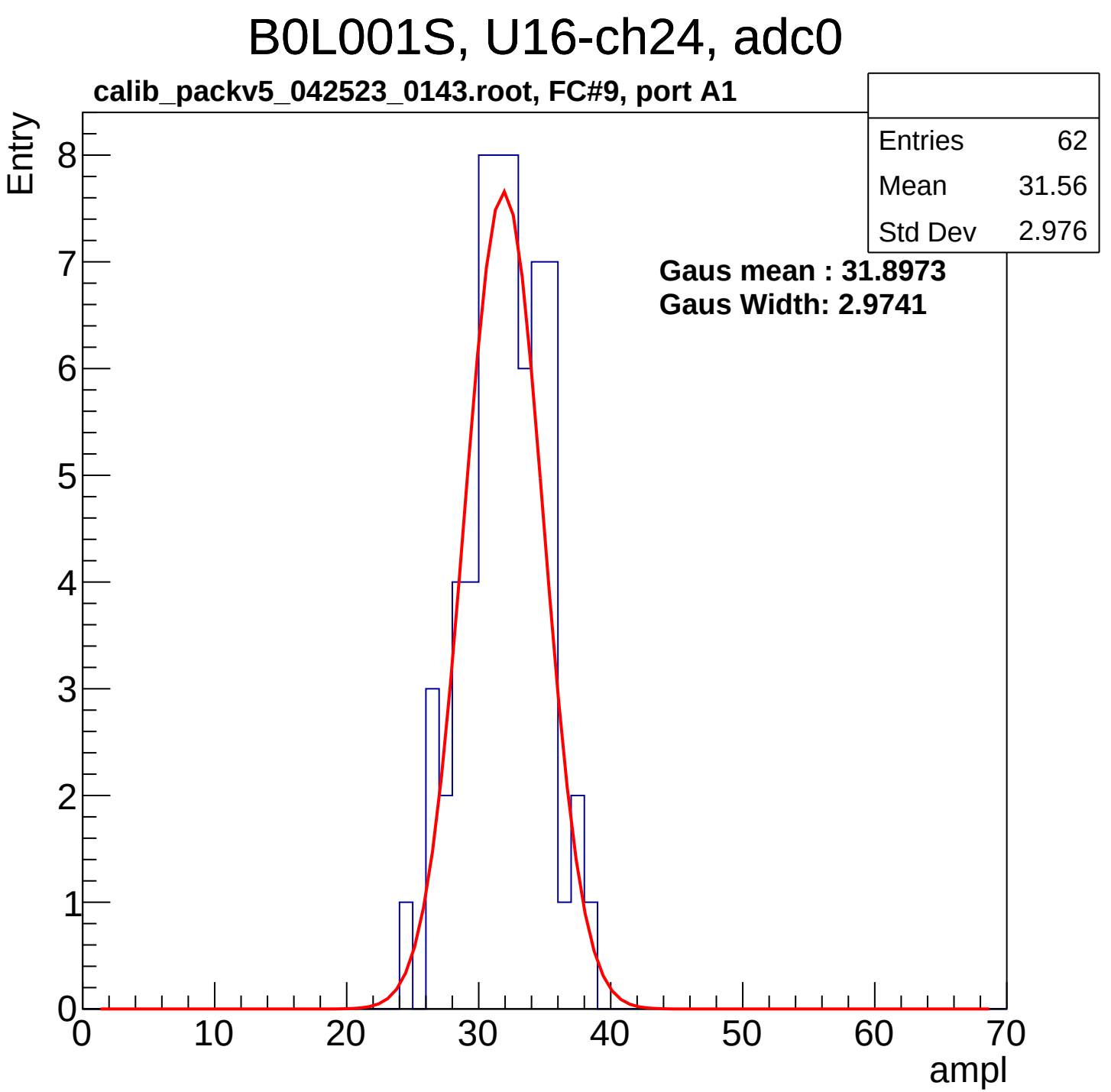
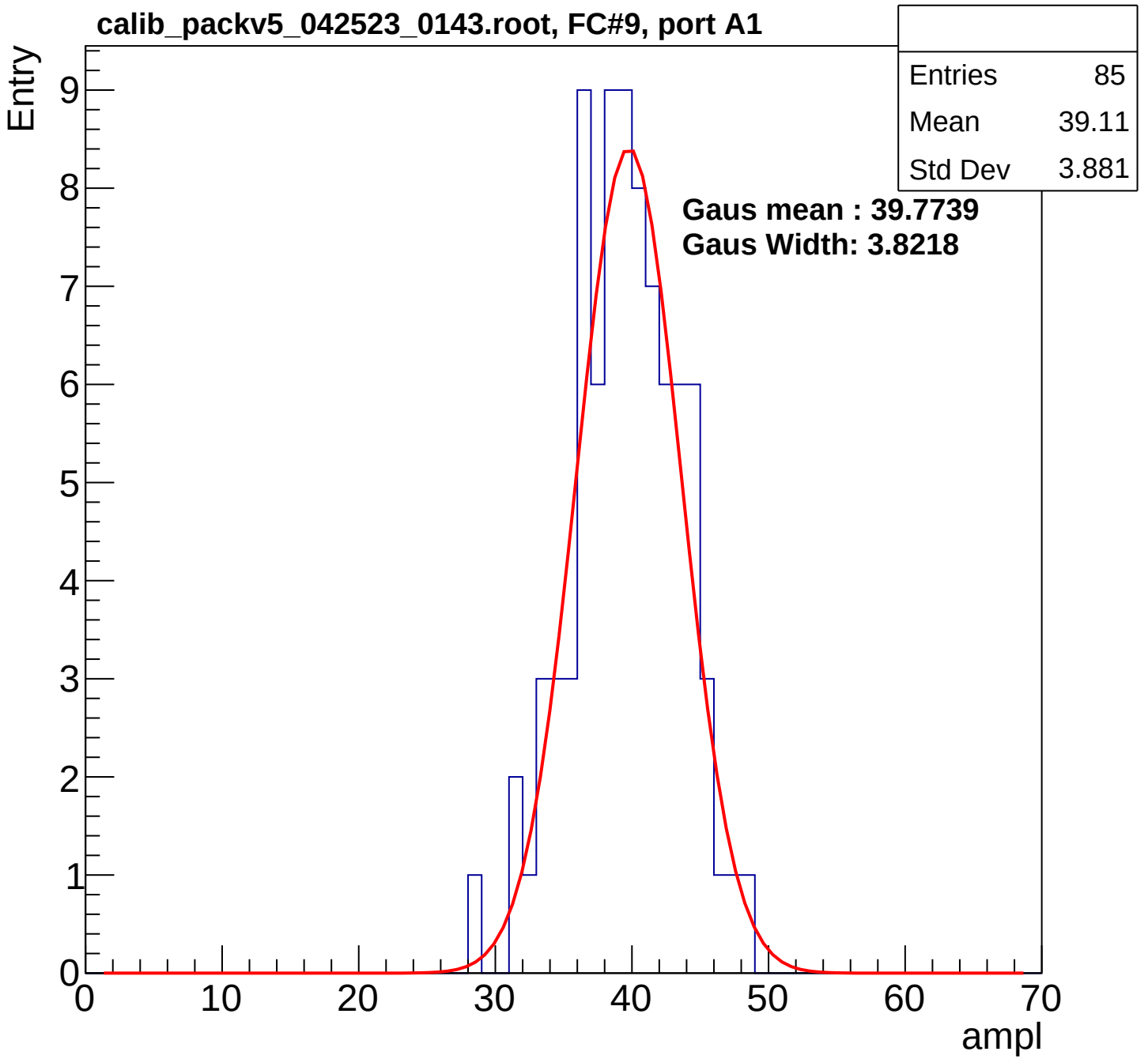
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.56
Std Dev 2.976
Gaus mean : 31.8973
Gaus Width: 2.9741
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 39.11
Std Dev 3.881
Gaus mean : 39.7739
Gaus Width: 3.8218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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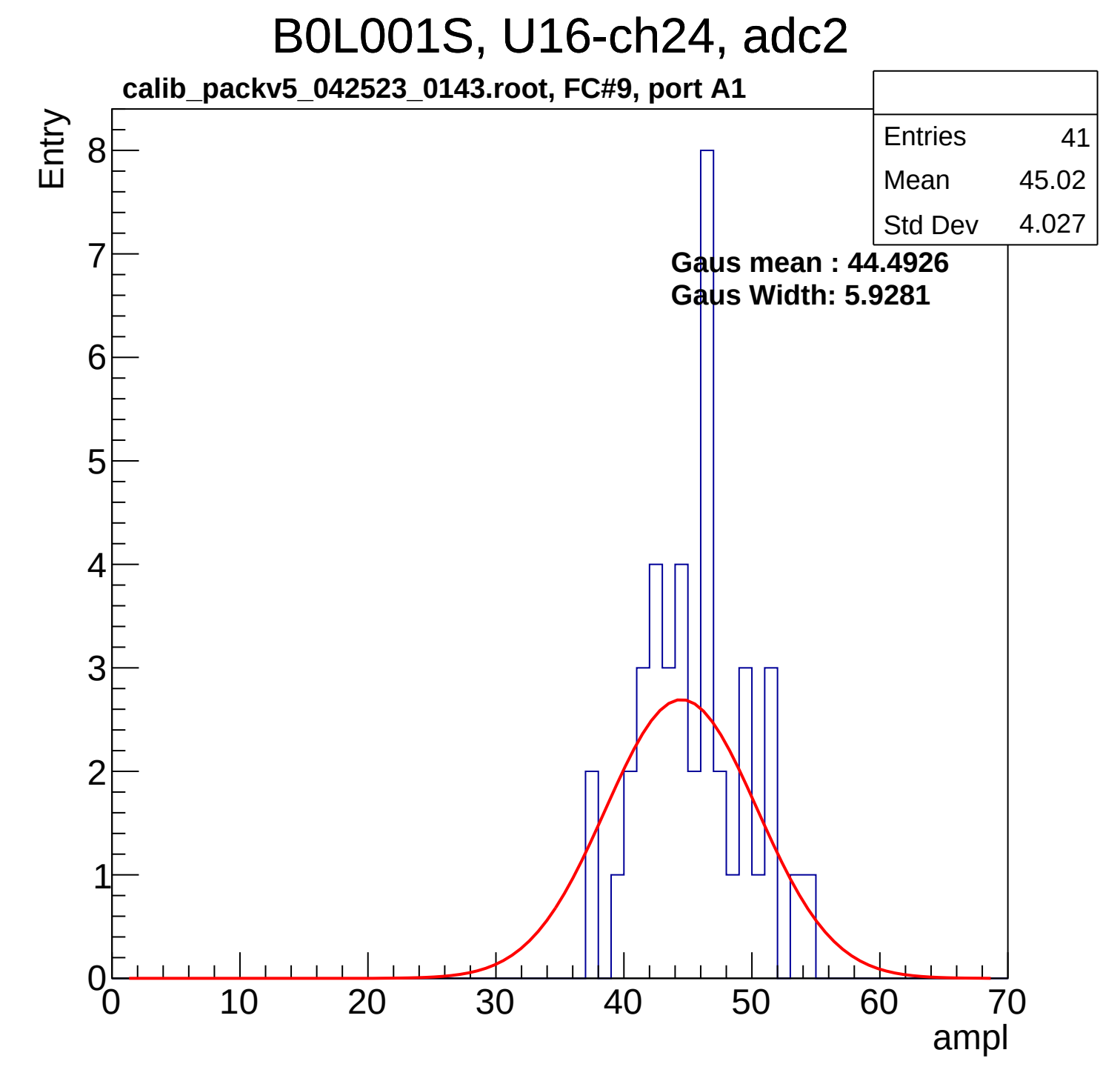
B0L001S, U16-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 45.02
Std Dev 4.027
Gaus mean : 44.4926
Gaus Width: 5.9281
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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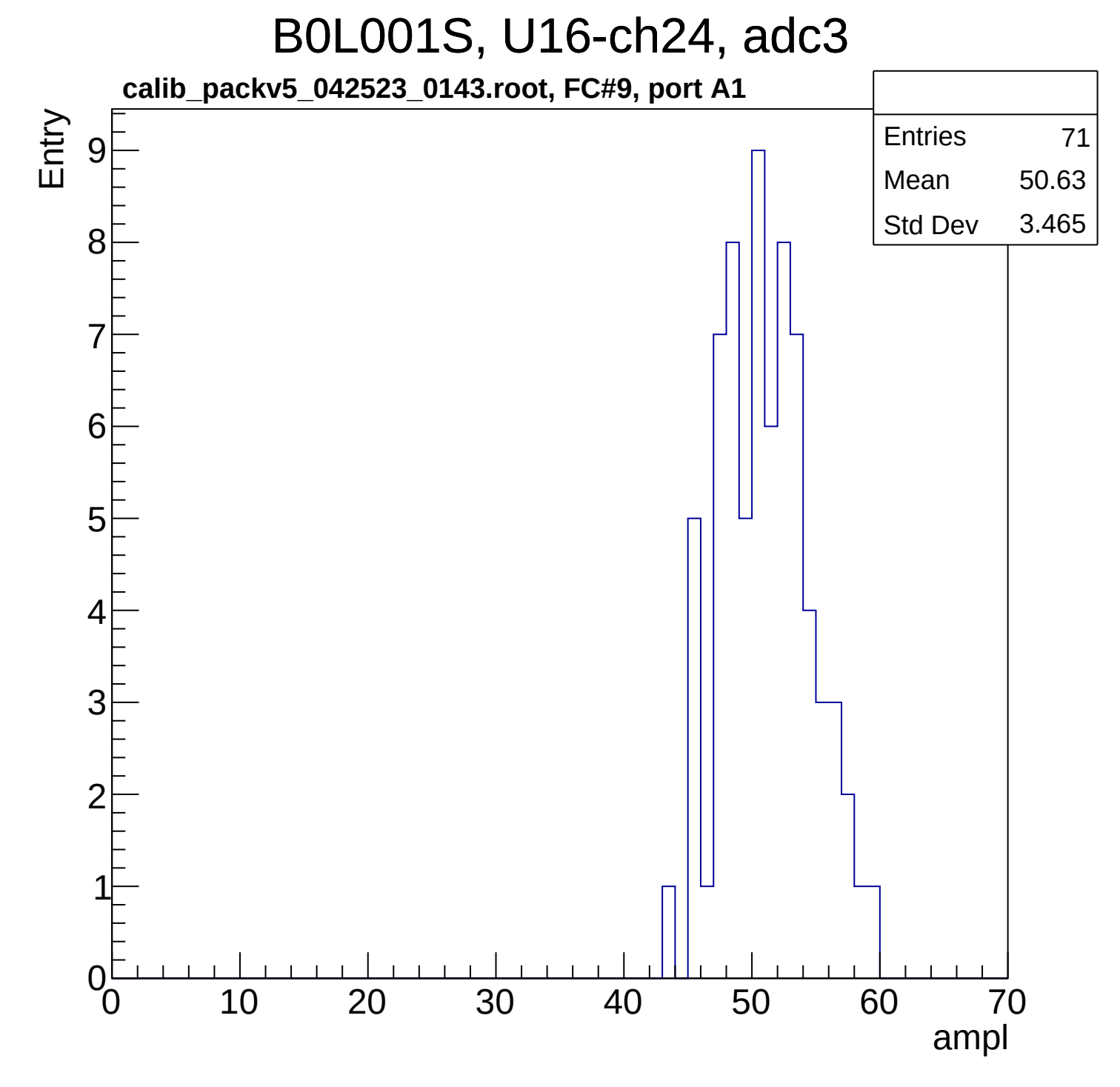
B0L001S, U16-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.63
Std Dev 3.465
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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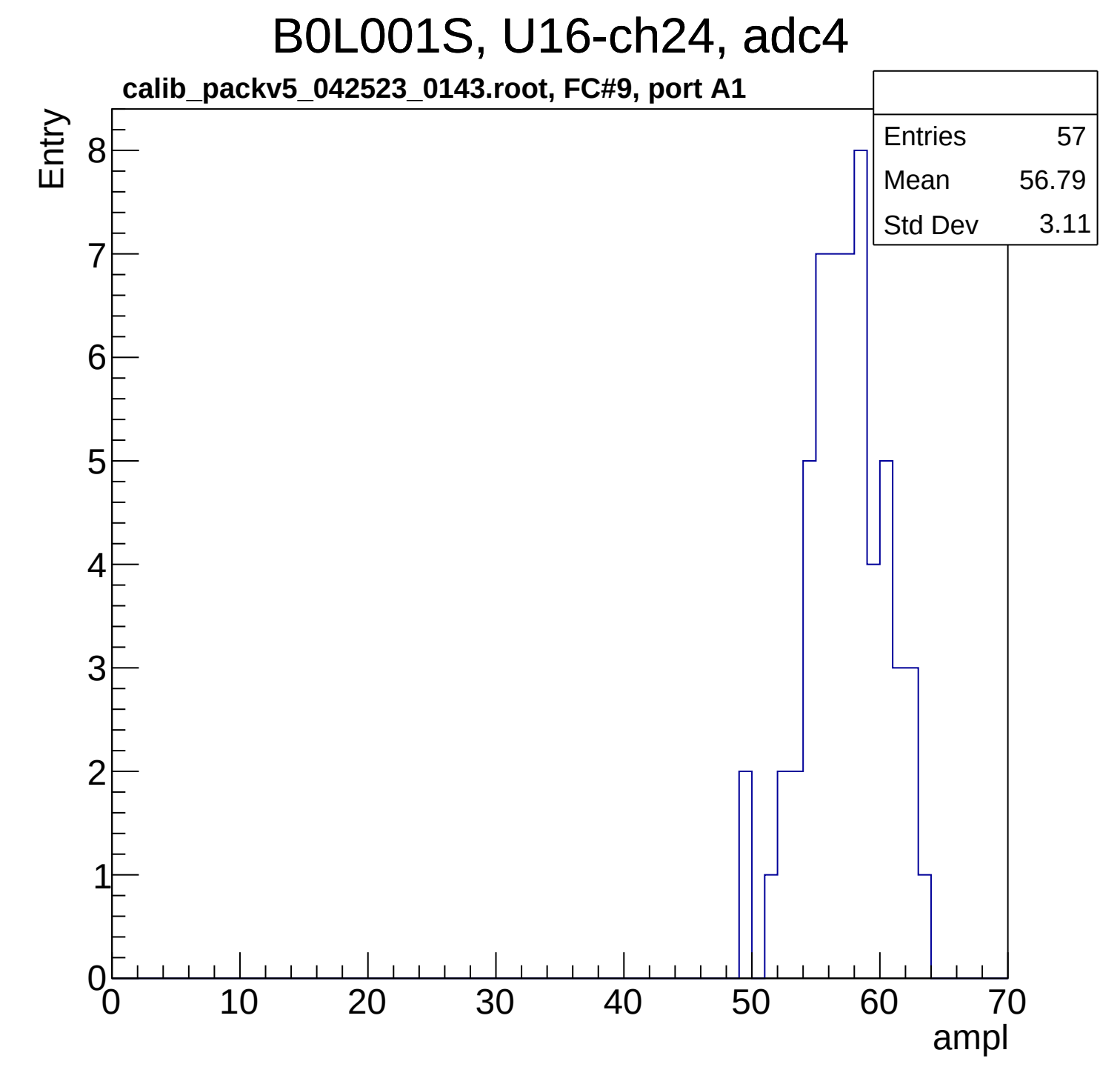
B0L001S, U16-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.79
Std Dev 3.11
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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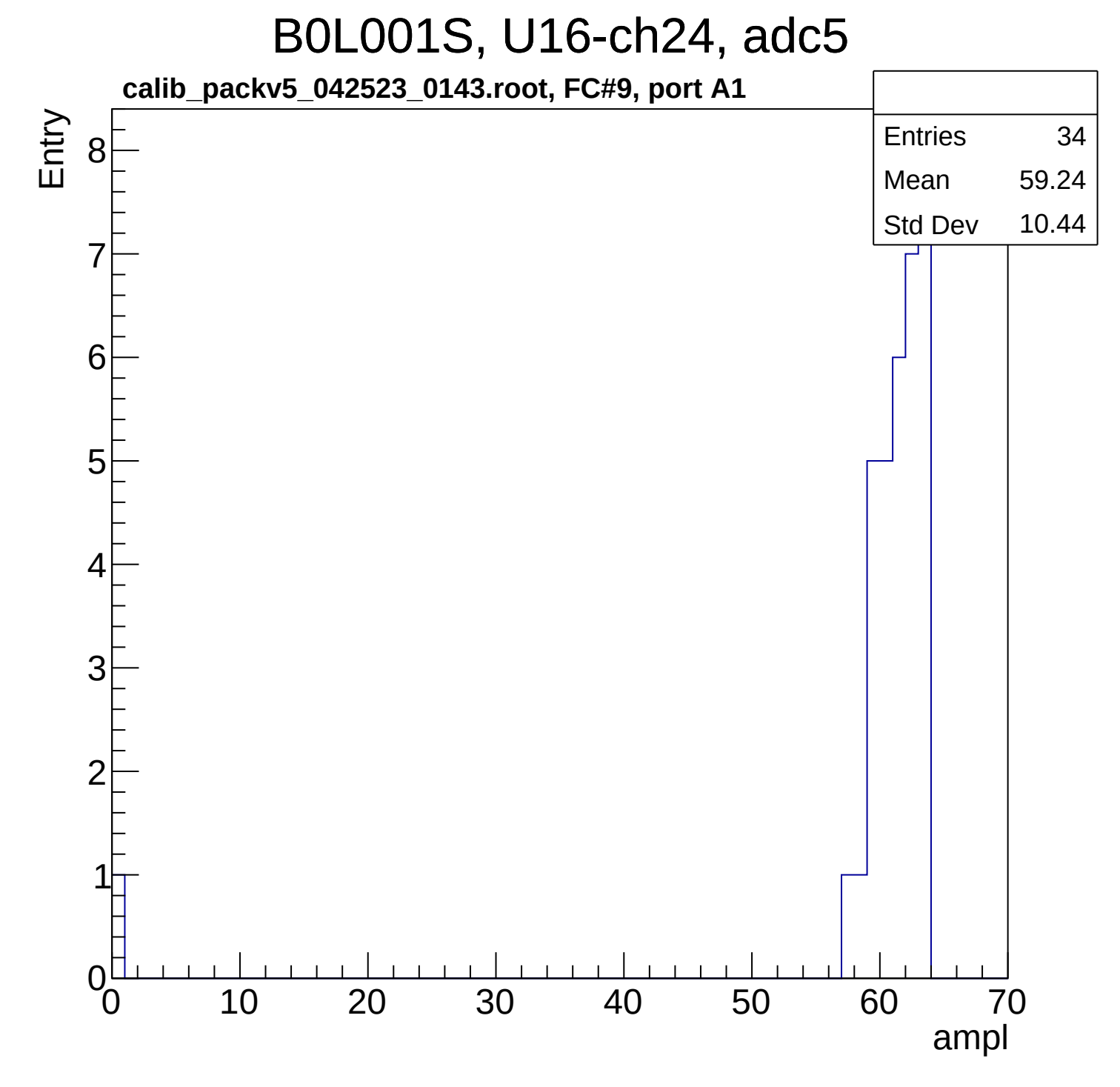
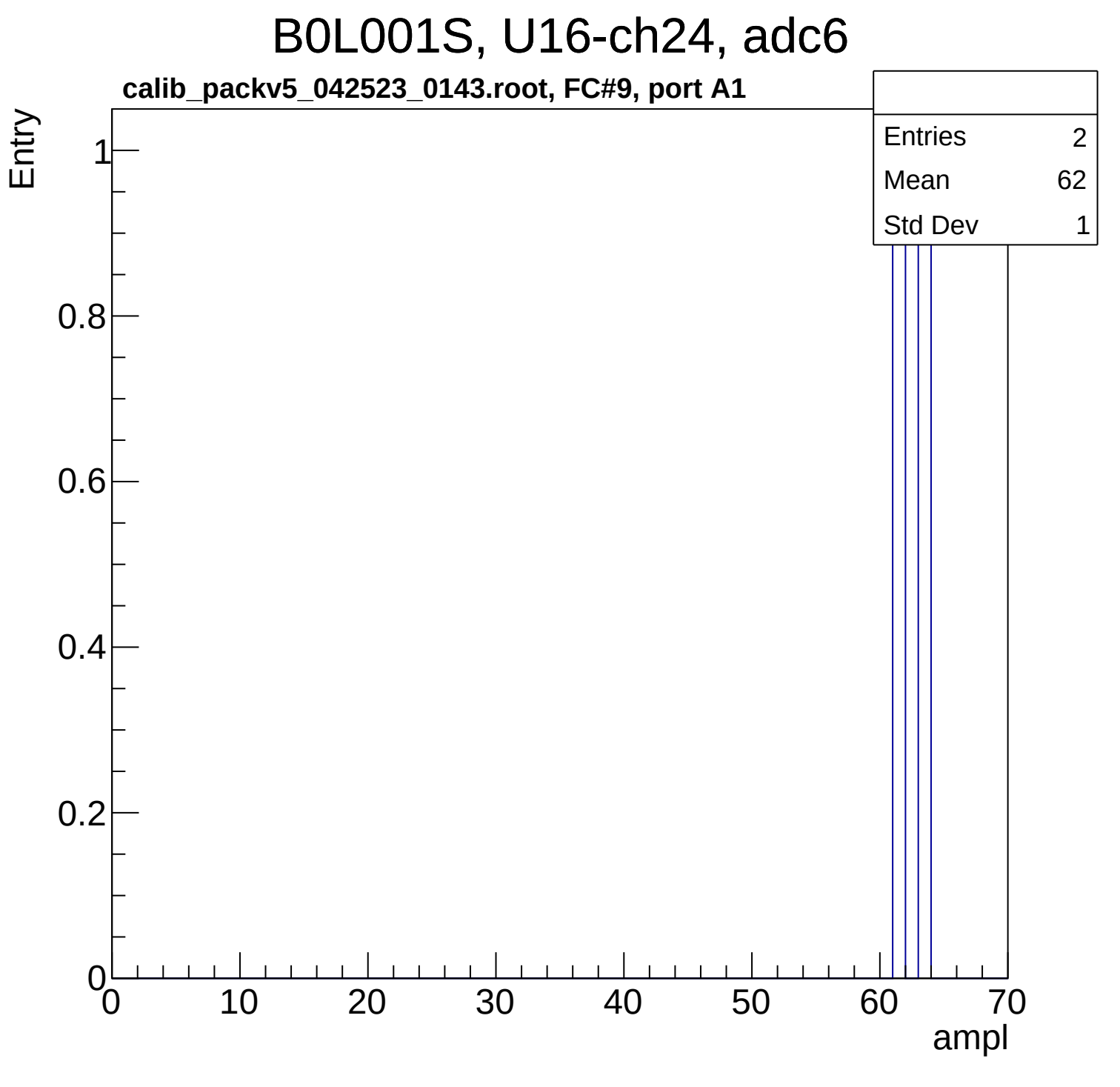
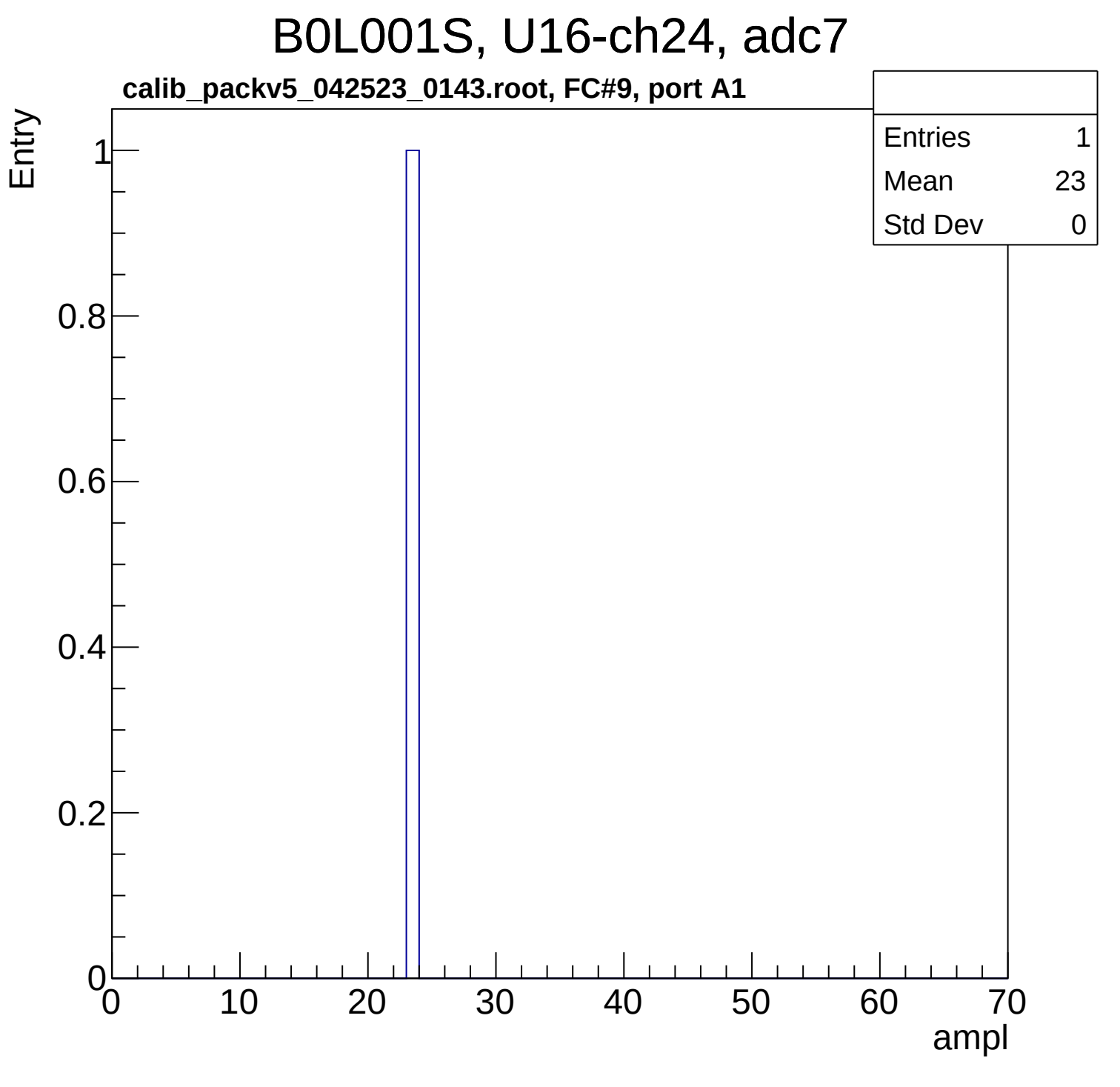
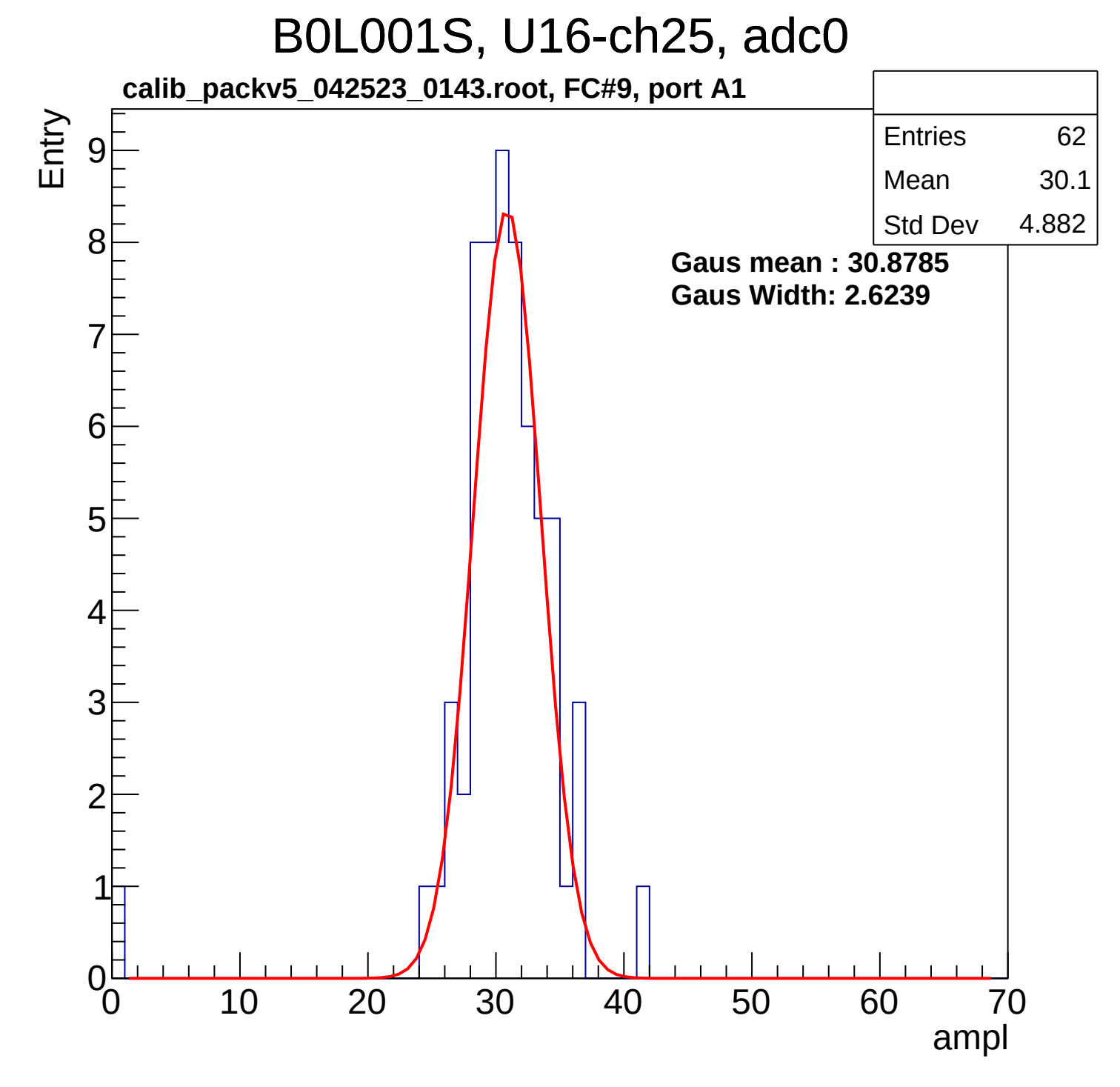
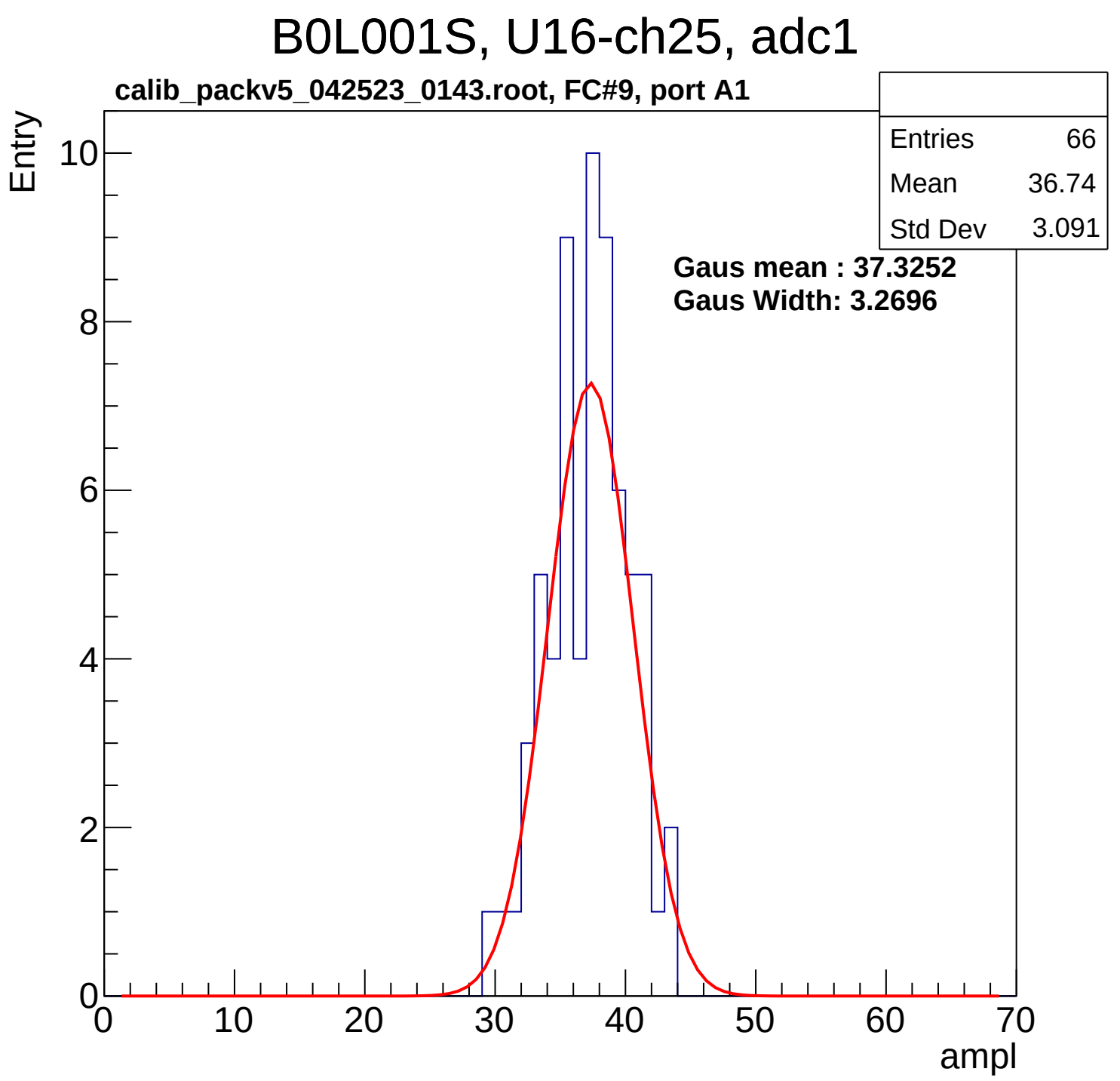
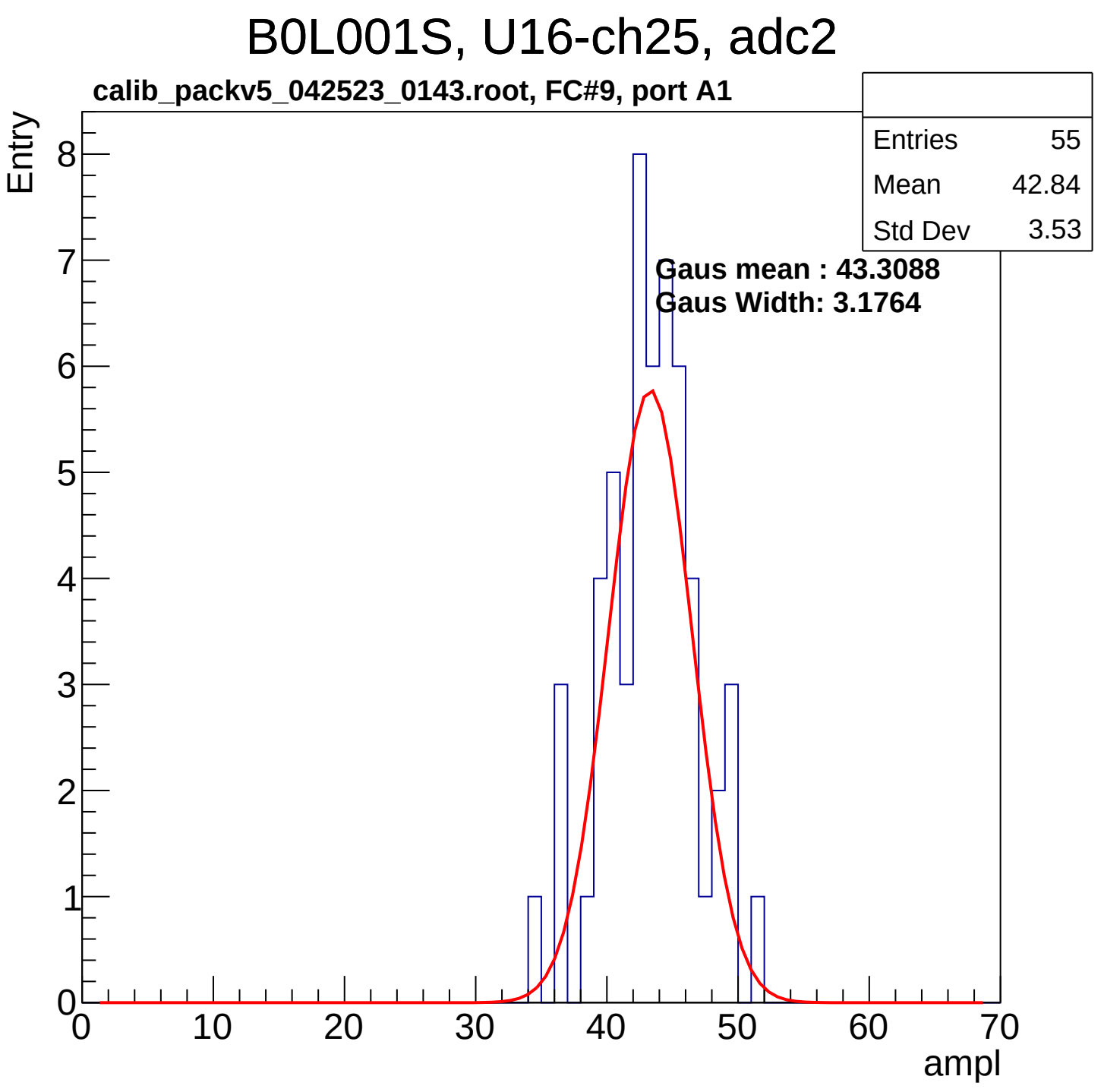

B0L001S, U16-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.24
Std Dev 10.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.1
Std Dev 4.882
Gaus mean : 30.8785
Gaus Width: 2.6239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.74
Std Dev 3.091
Gaus mean : 37.3252
Gaus Width: 3.2696
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.84
Std Dev 3.53
Gaus mean : 43.3088
Gaus Width: 3.1764
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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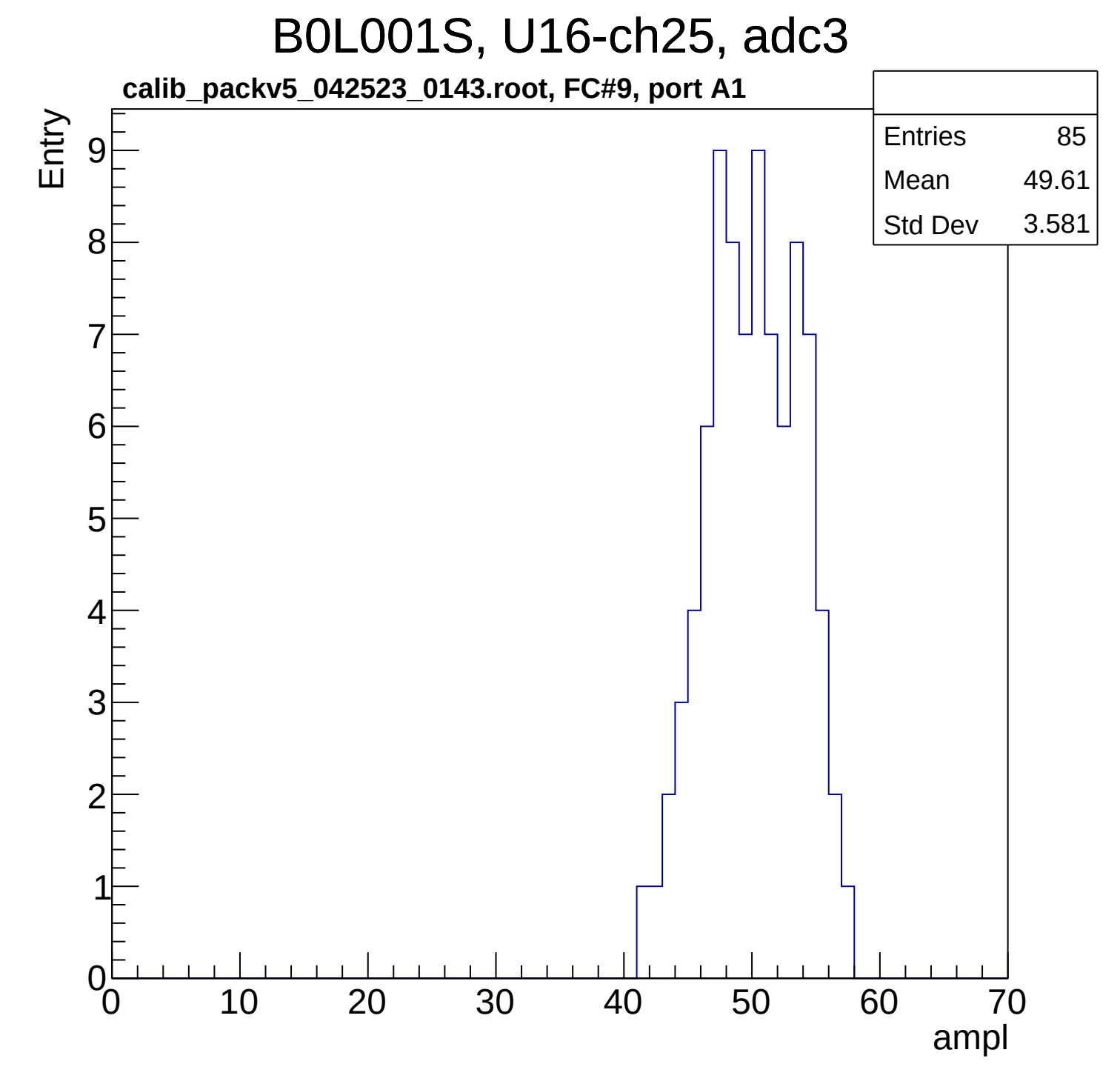
B0L001S, U16-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 49.61
Std Dev 3.581
Entry
ampl
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3
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5
6
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0
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60
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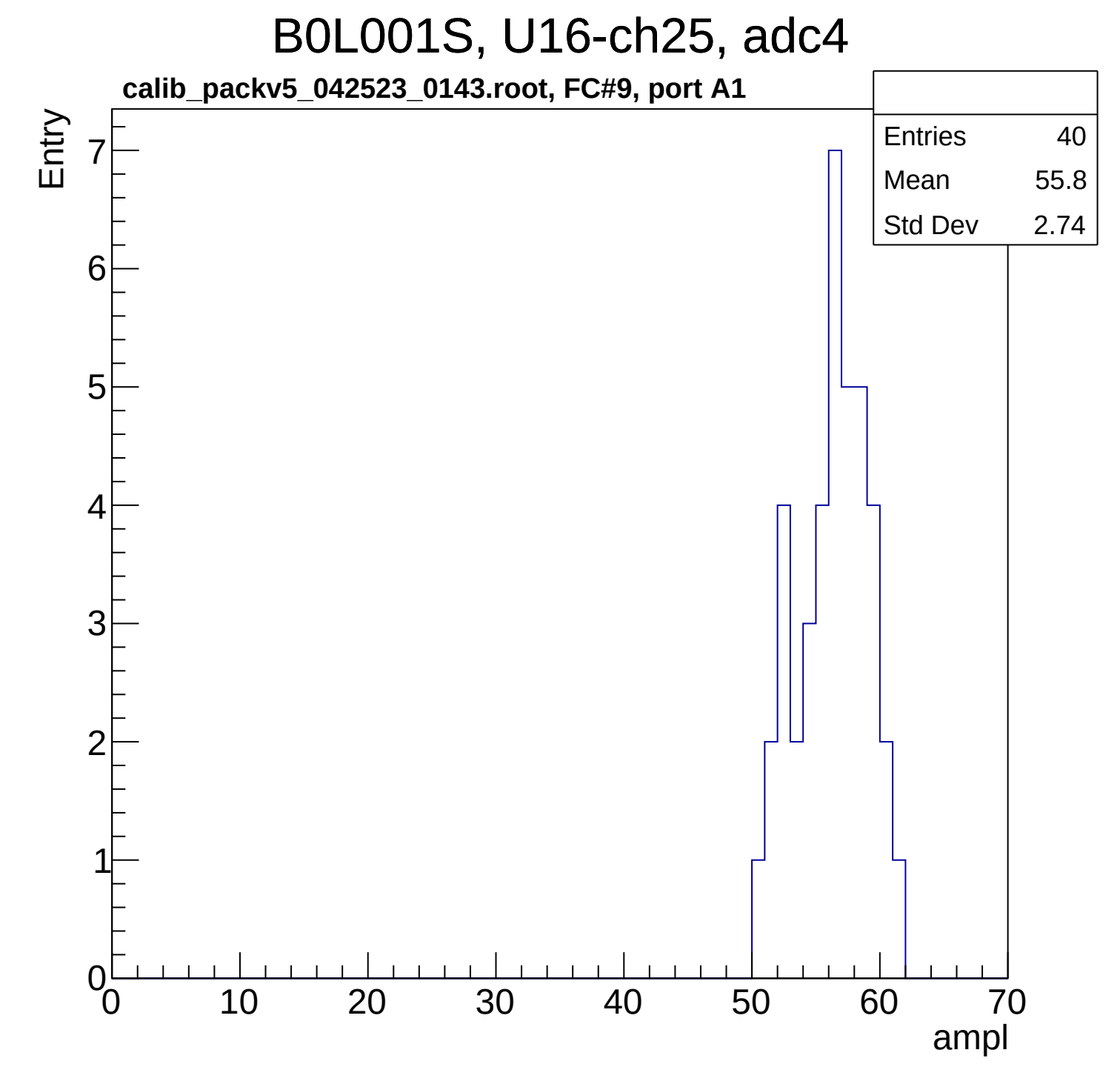
B0L001S, U16-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 55.8
Std Dev 2.74
Entry
ampl
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1
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0
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40
50
60
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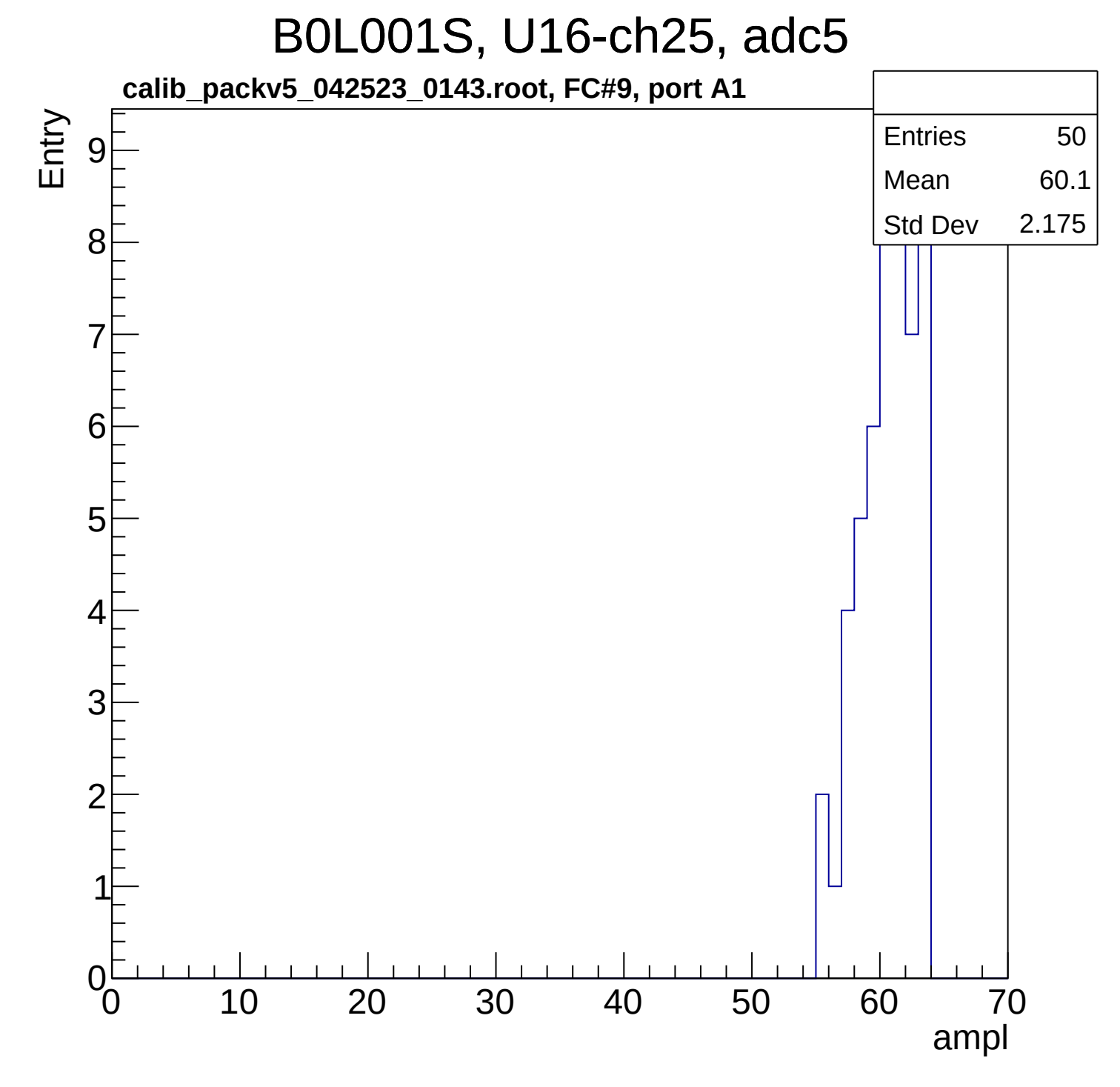
B0L001S, U16-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 60.1
Std Dev 2.175
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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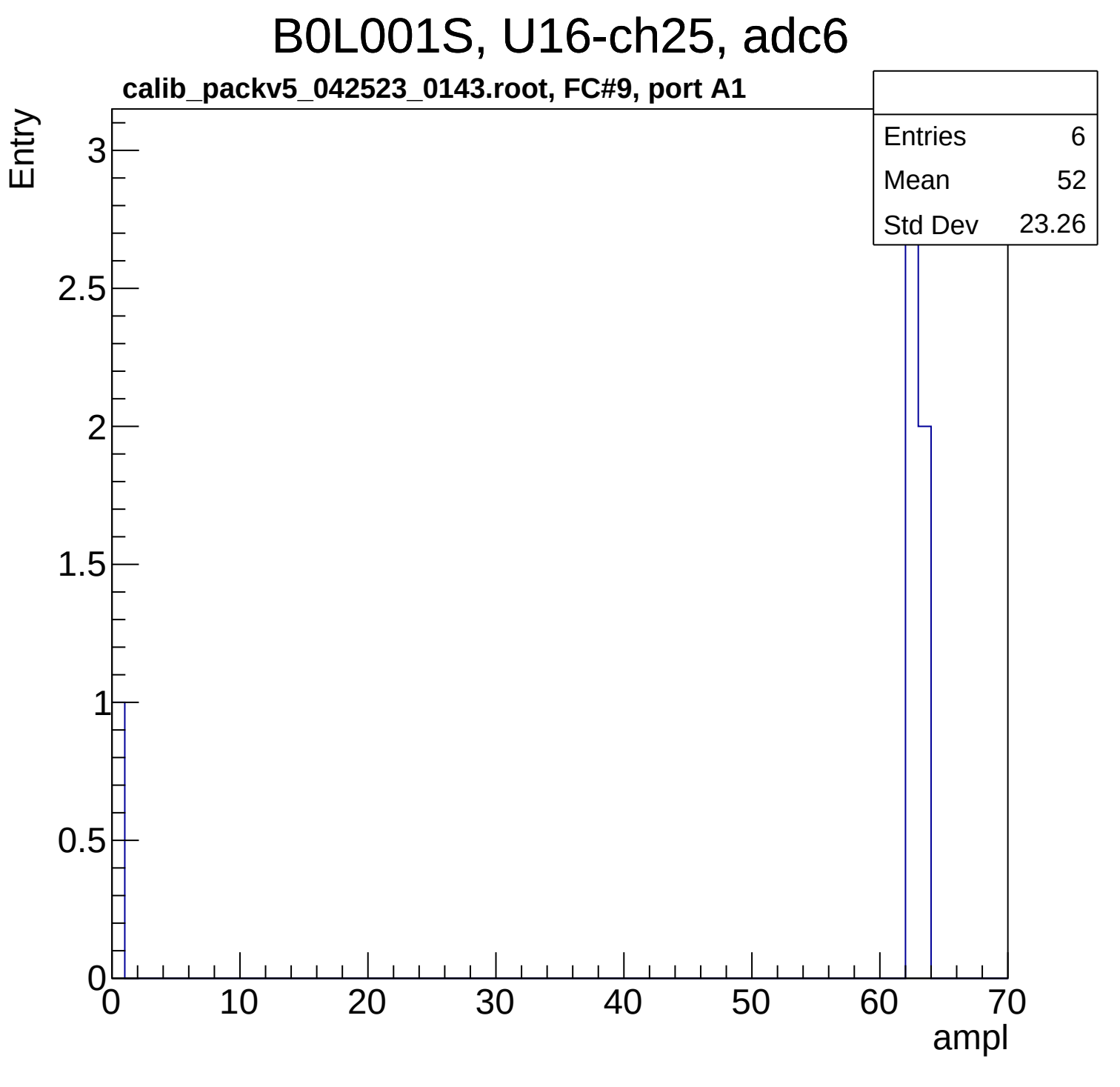
60
70


B0L001S, U16-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 52
Std Dev 23.26
Entry
ampl
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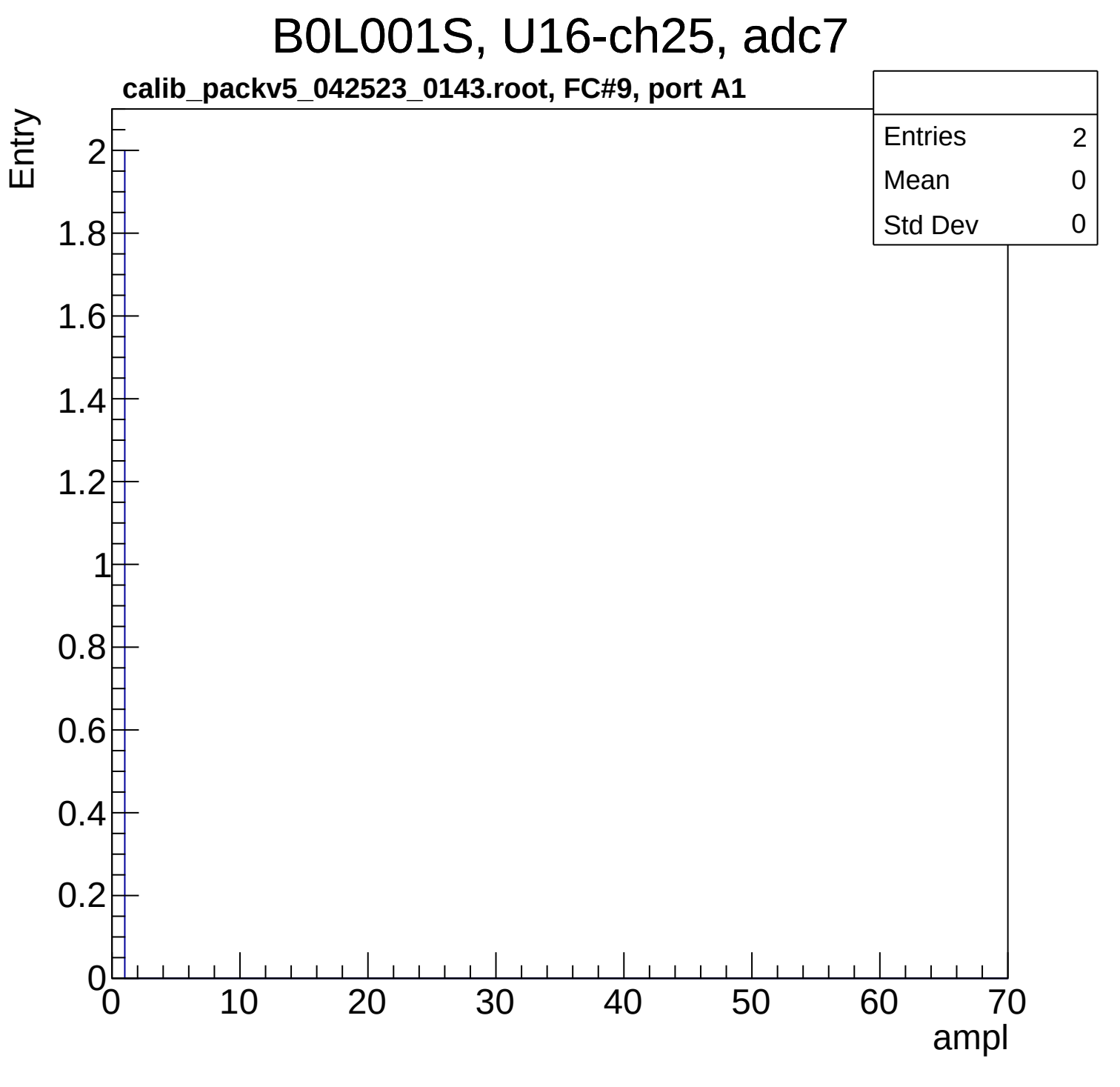
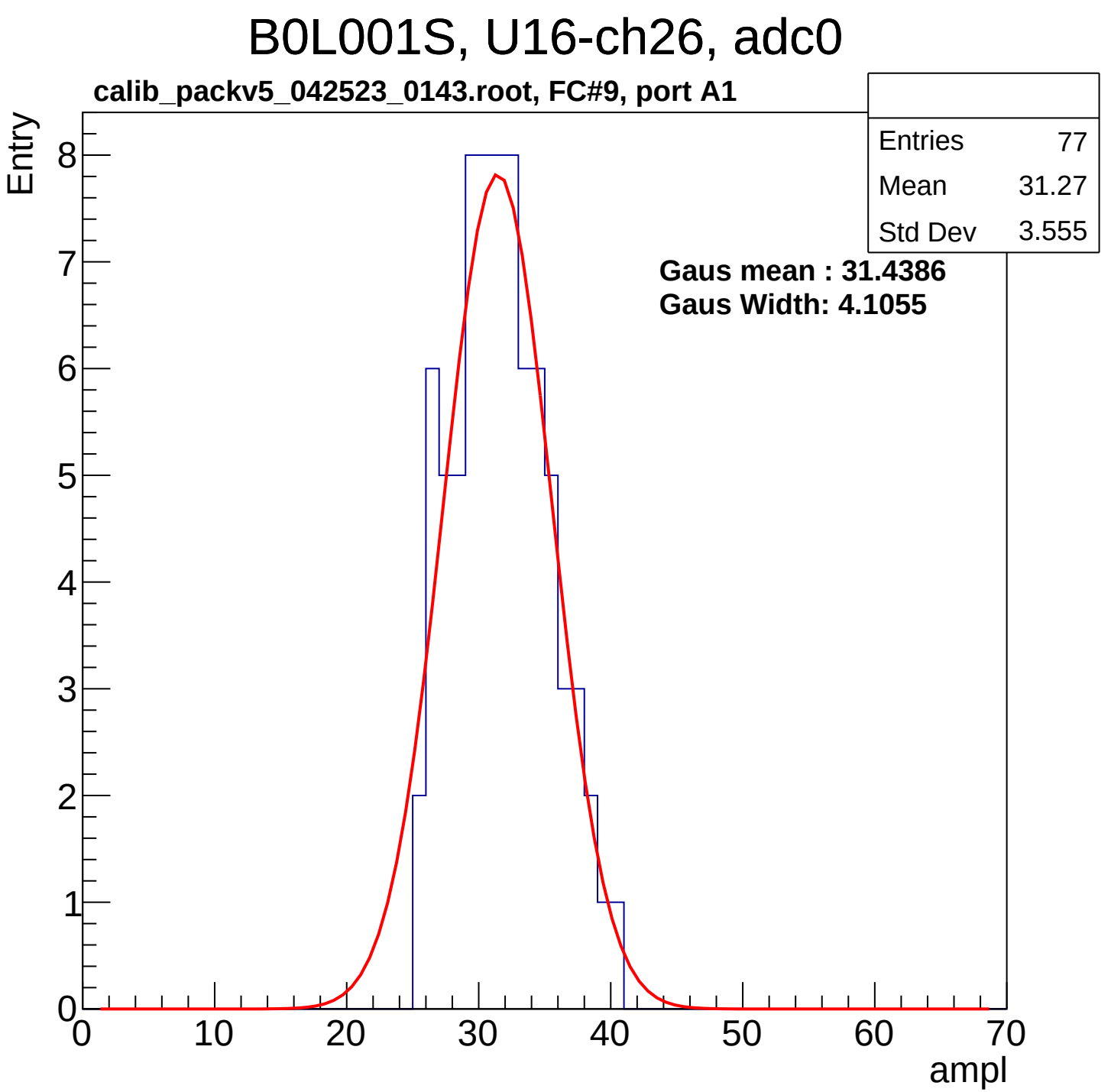
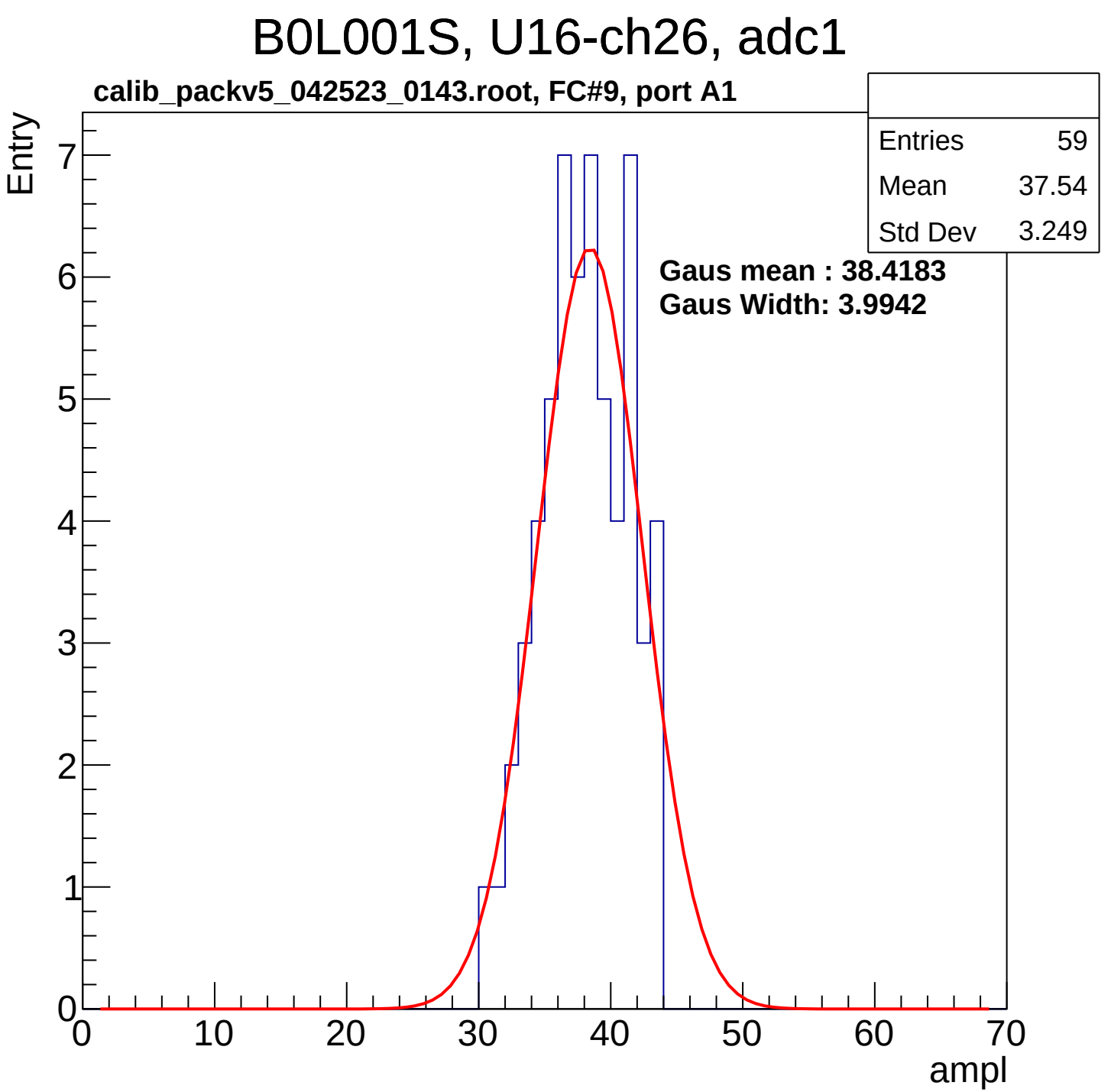
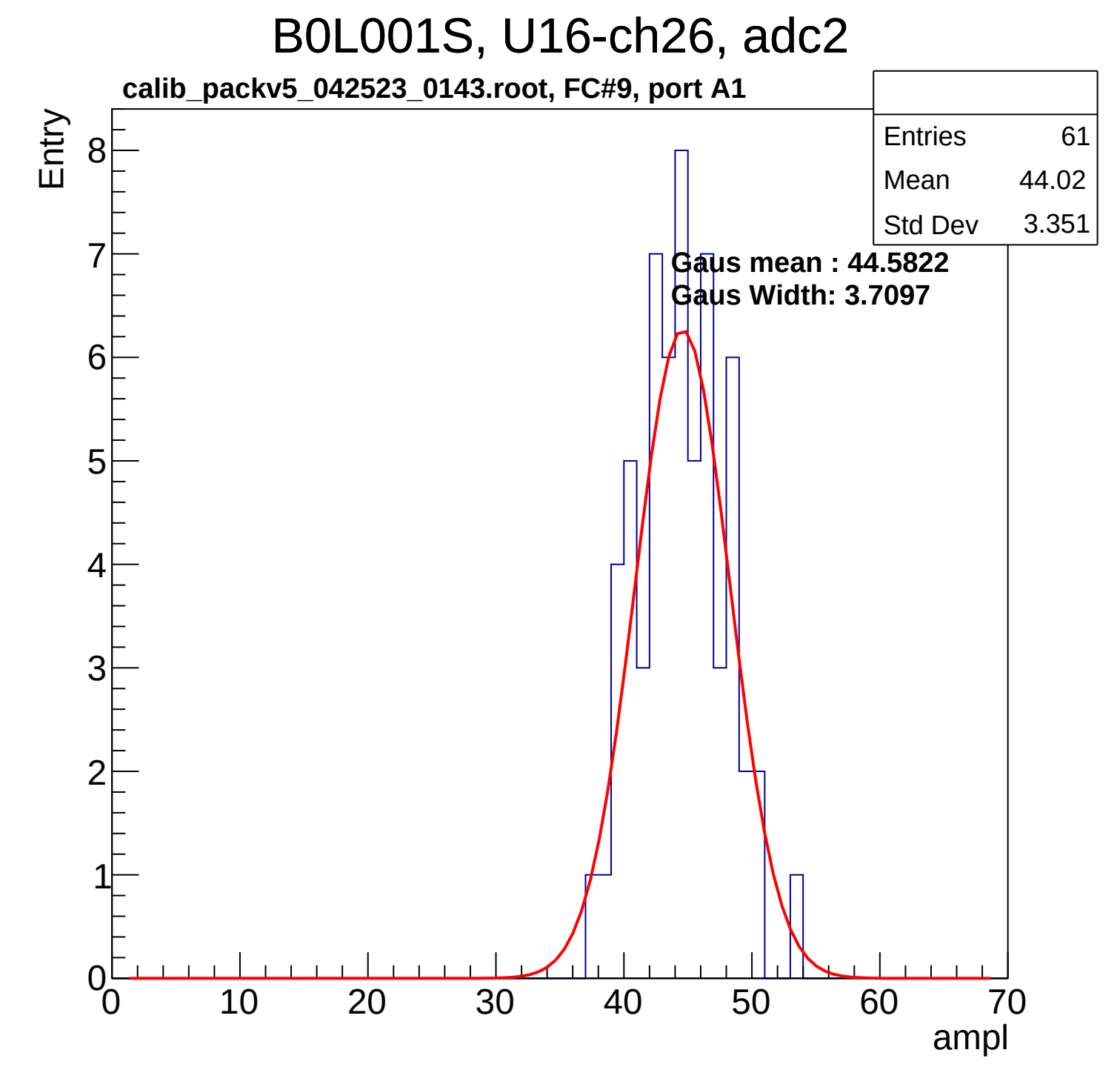
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.27
Std Dev 3.555
Gaus mean : 31.4386
Gaus Width: 4.1055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.54
Std Dev 3.249
Gaus mean : 38.4183
Gaus Width: 3.9942
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.02
Std Dev 3.351
Gaus mean : 44.5822
Gaus Width: 3.7097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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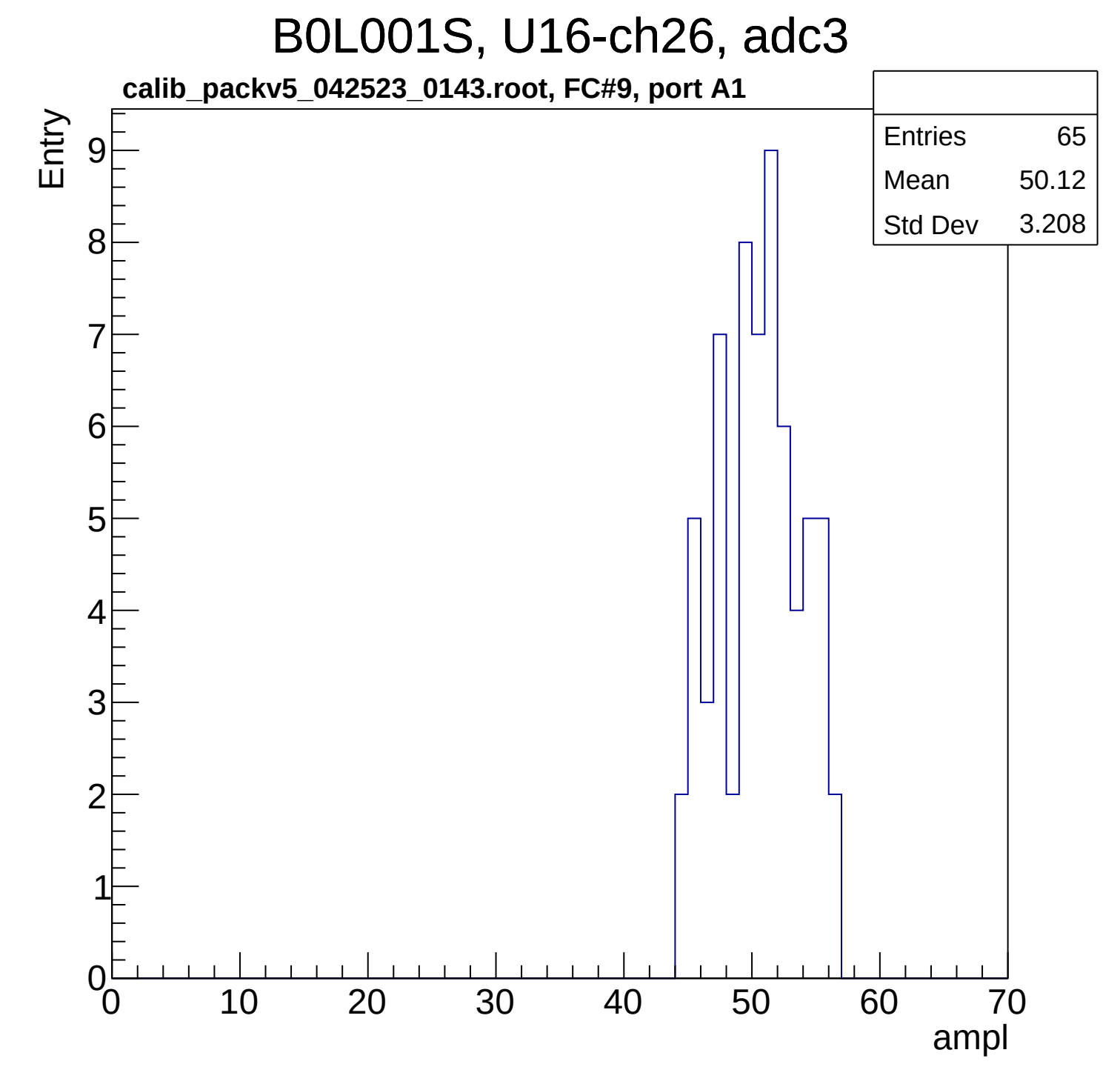
40
50
60
70


B0L001S, U16-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.12
Std Dev 3.208
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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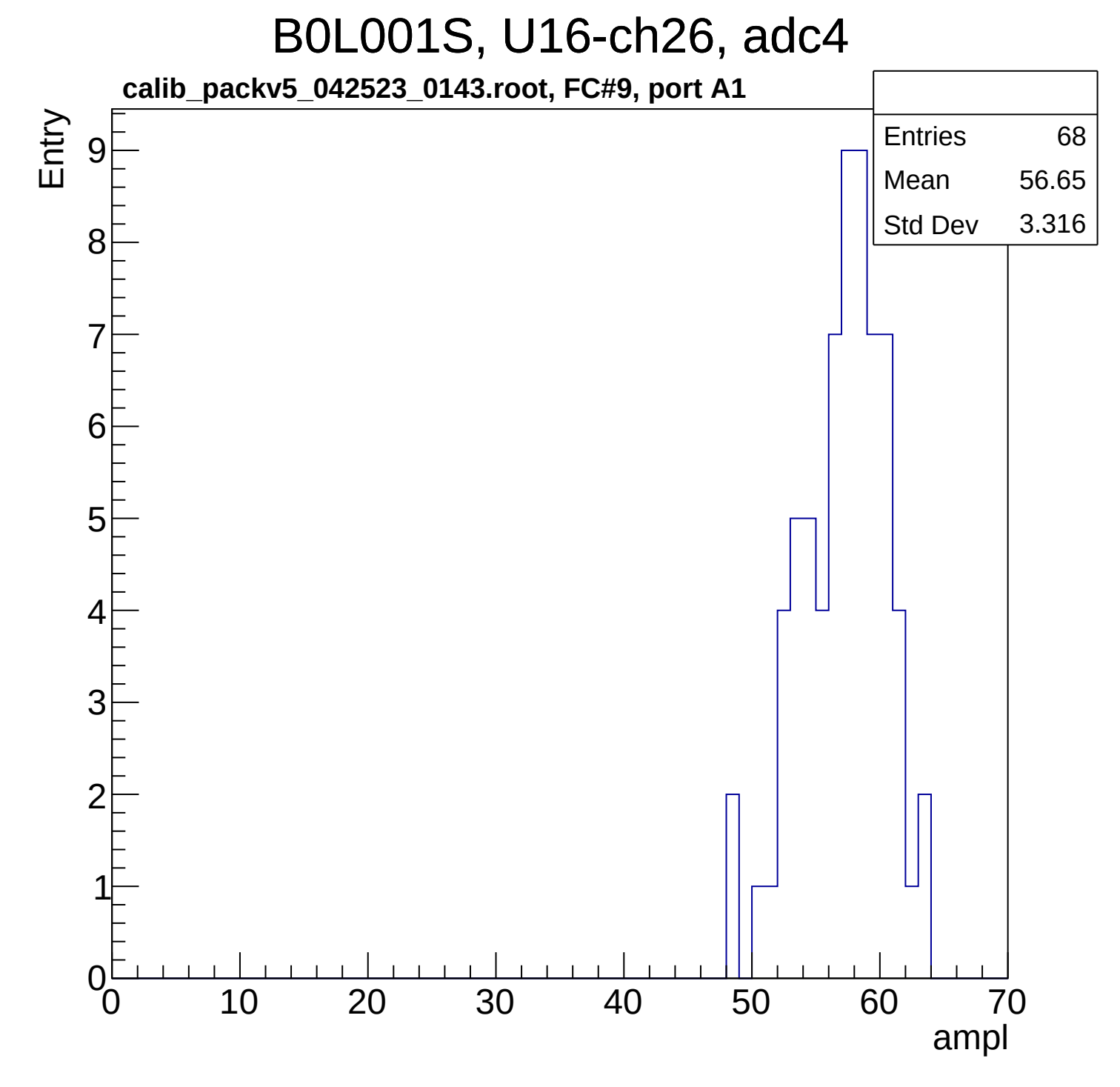
40
50
60
70


B0L001S, U16-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56.65
Std Dev 3.316
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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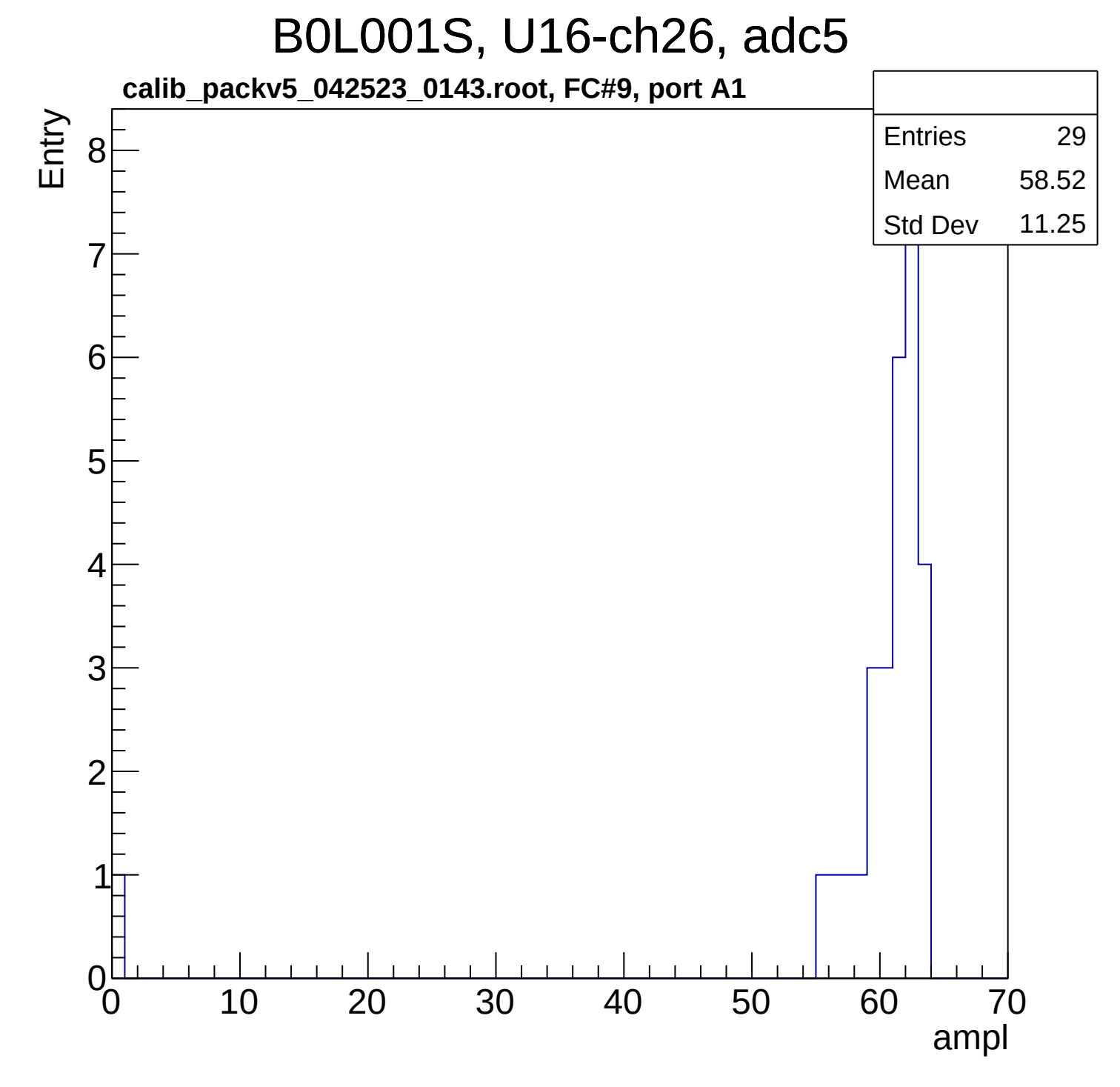
B0L001S, U16-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.52
Std Dev 11.25
Entry
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2
3
4
5
6
7
8
0
10
20
30
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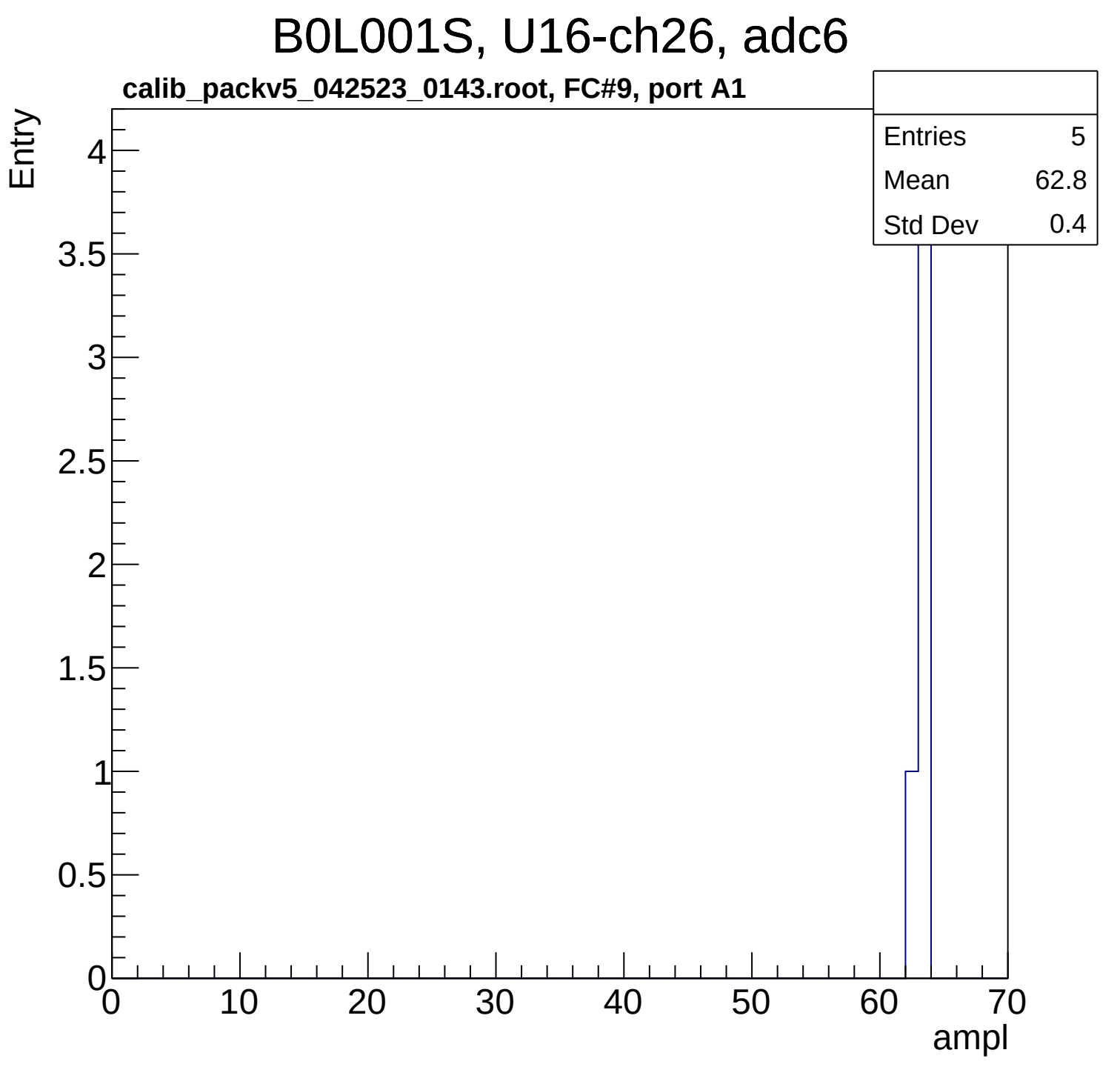
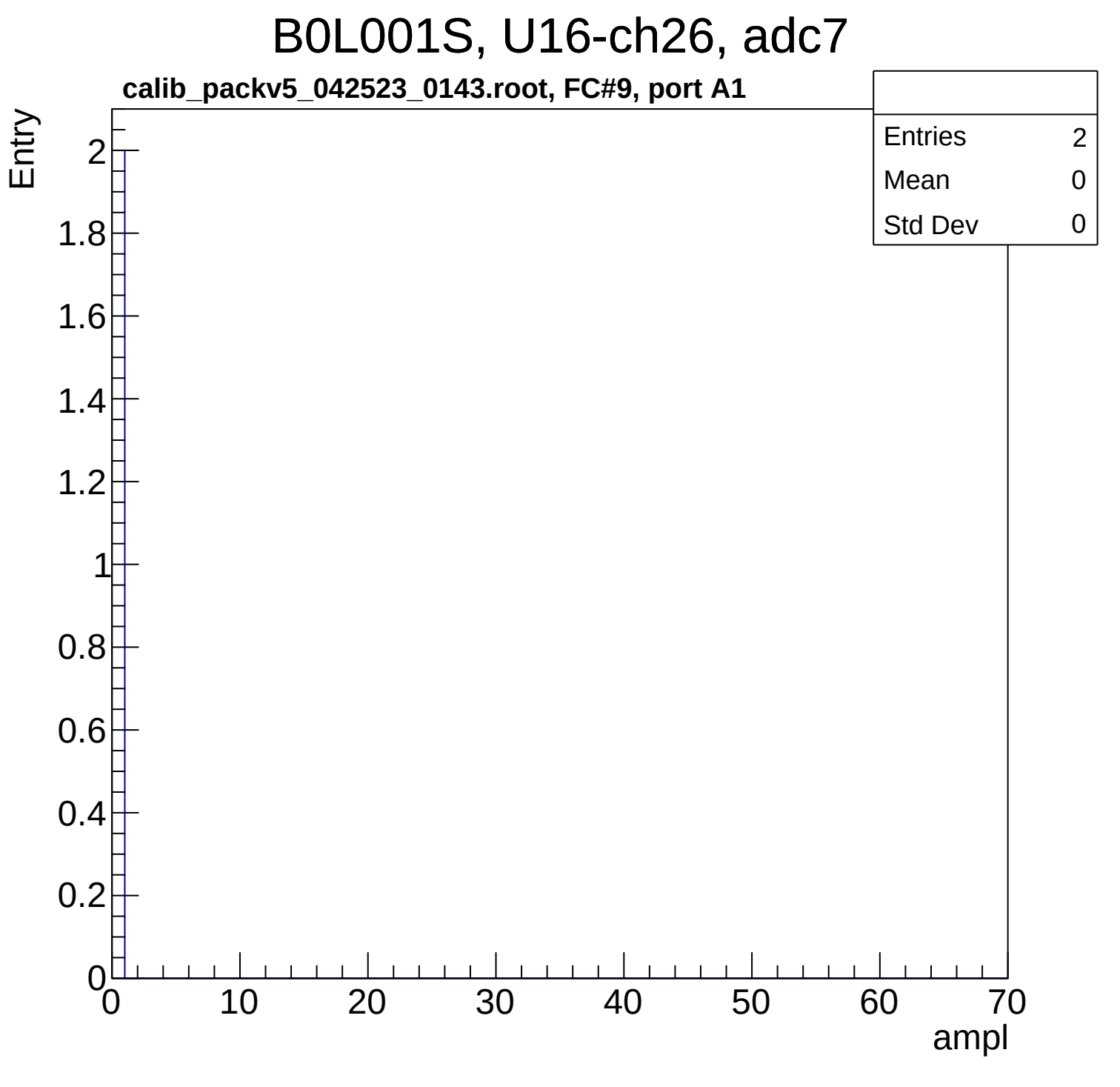
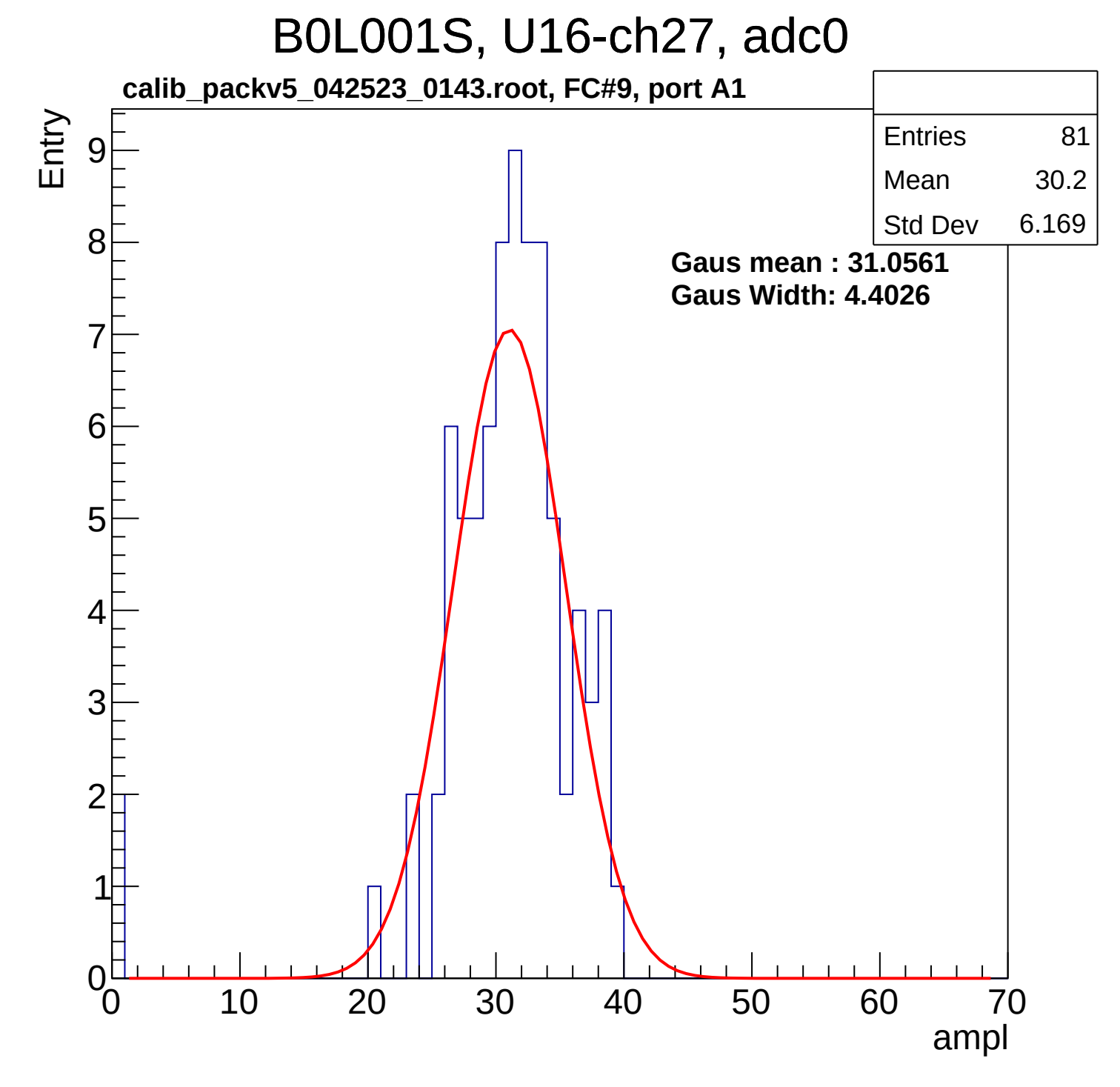
50
60
70
ampl


B0L001S, U16-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 30.2
Std Dev 6.169
Gaus mean : 31.0561
Gaus Width: 4.4026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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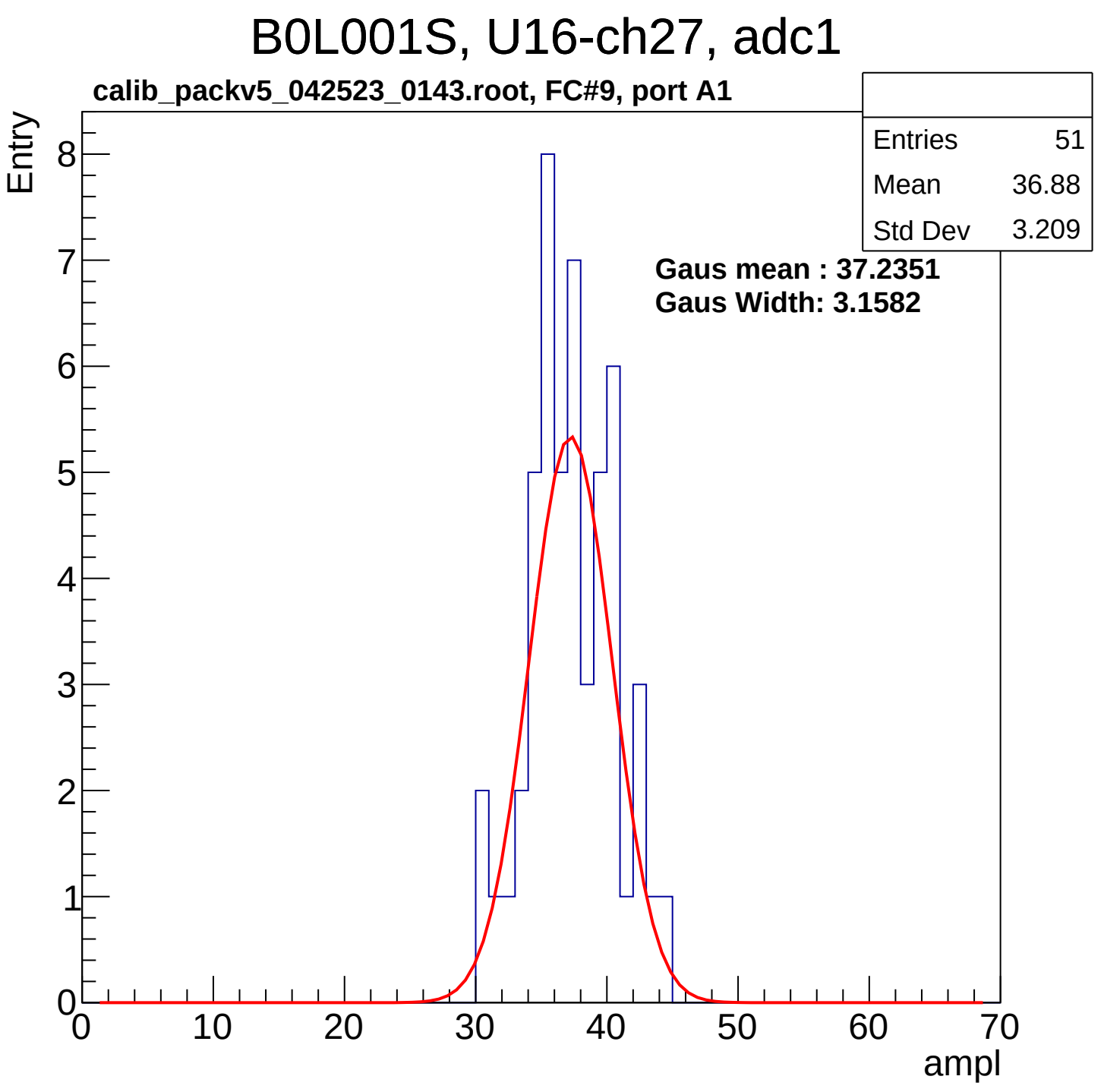
B0L001S, U16-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 36.88
Std Dev 3.209
Gaus mean : 37.2351
Gaus Width: 3.1582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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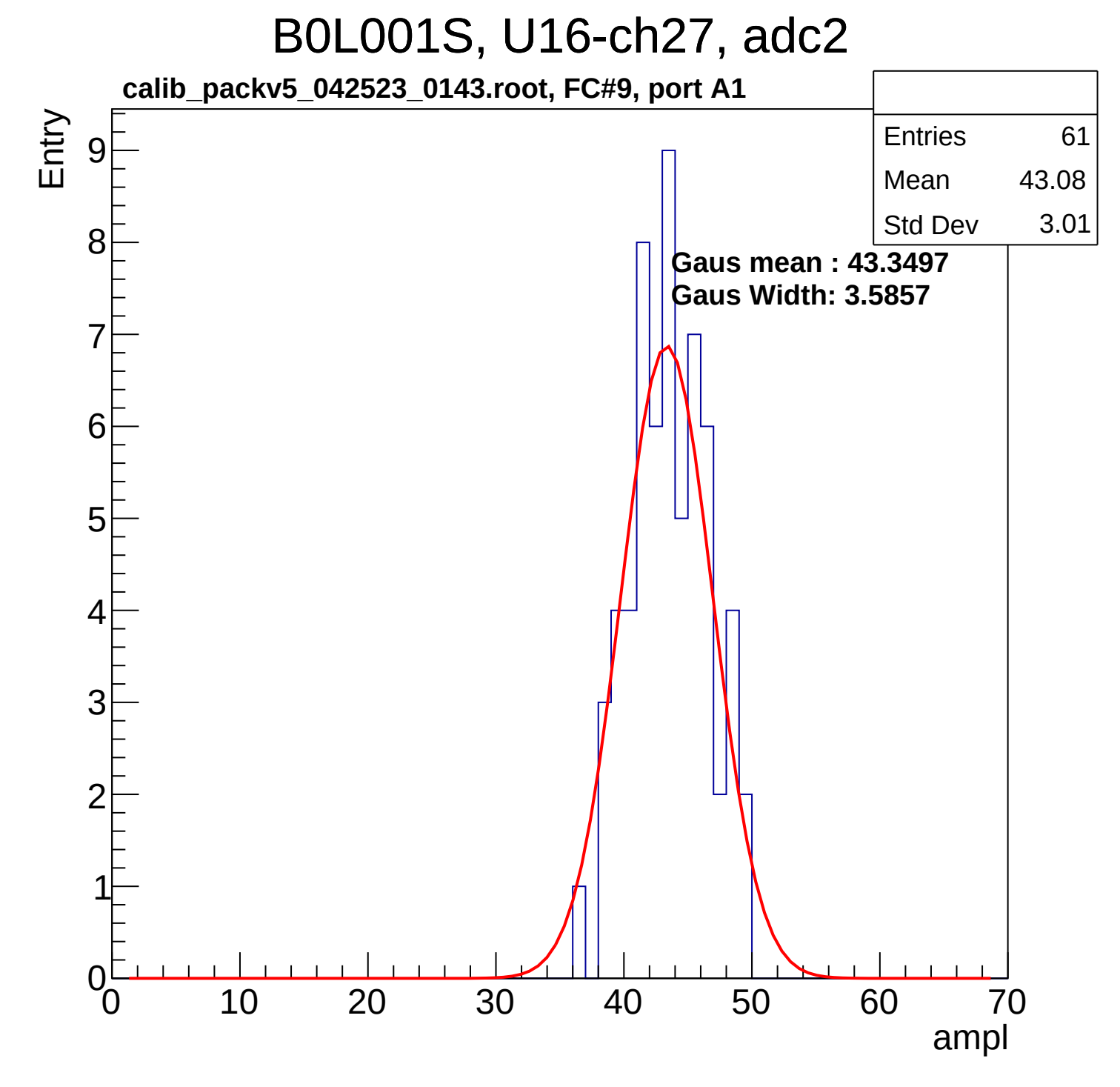
B0L001S, U16-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.08
Std Dev 3.01
Gaus mean : 43.3497
Gaus Width: 3.5857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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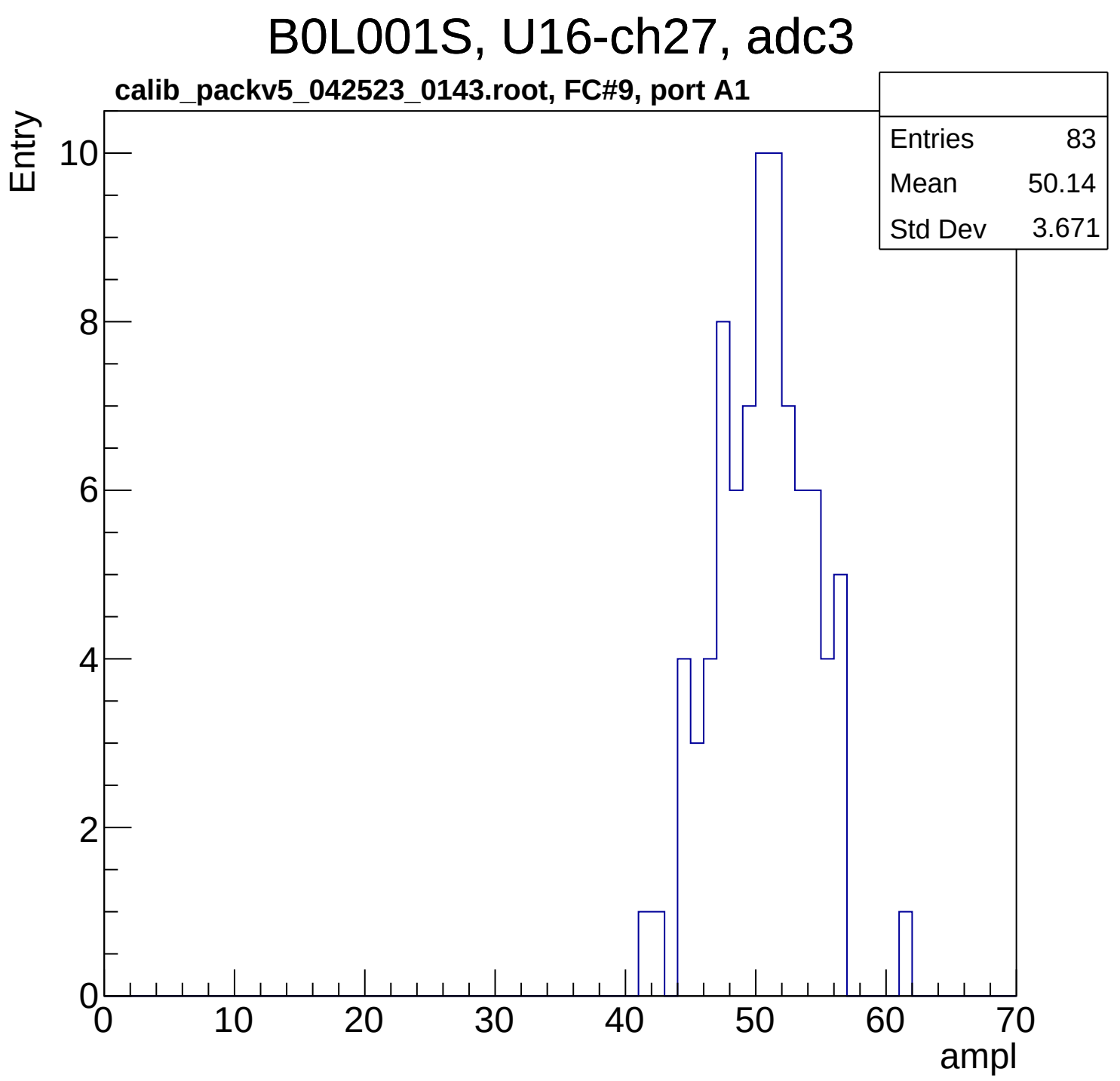
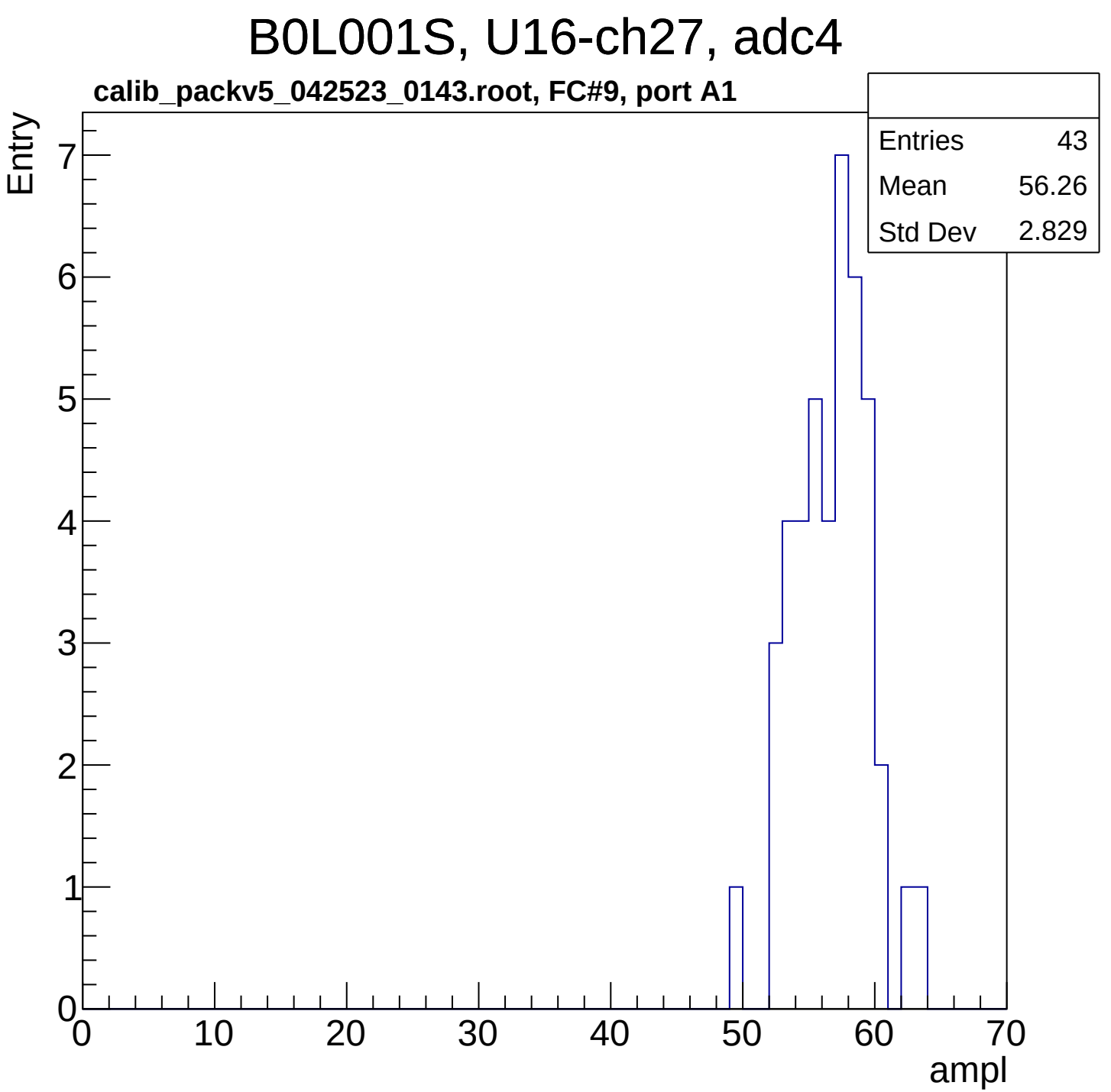
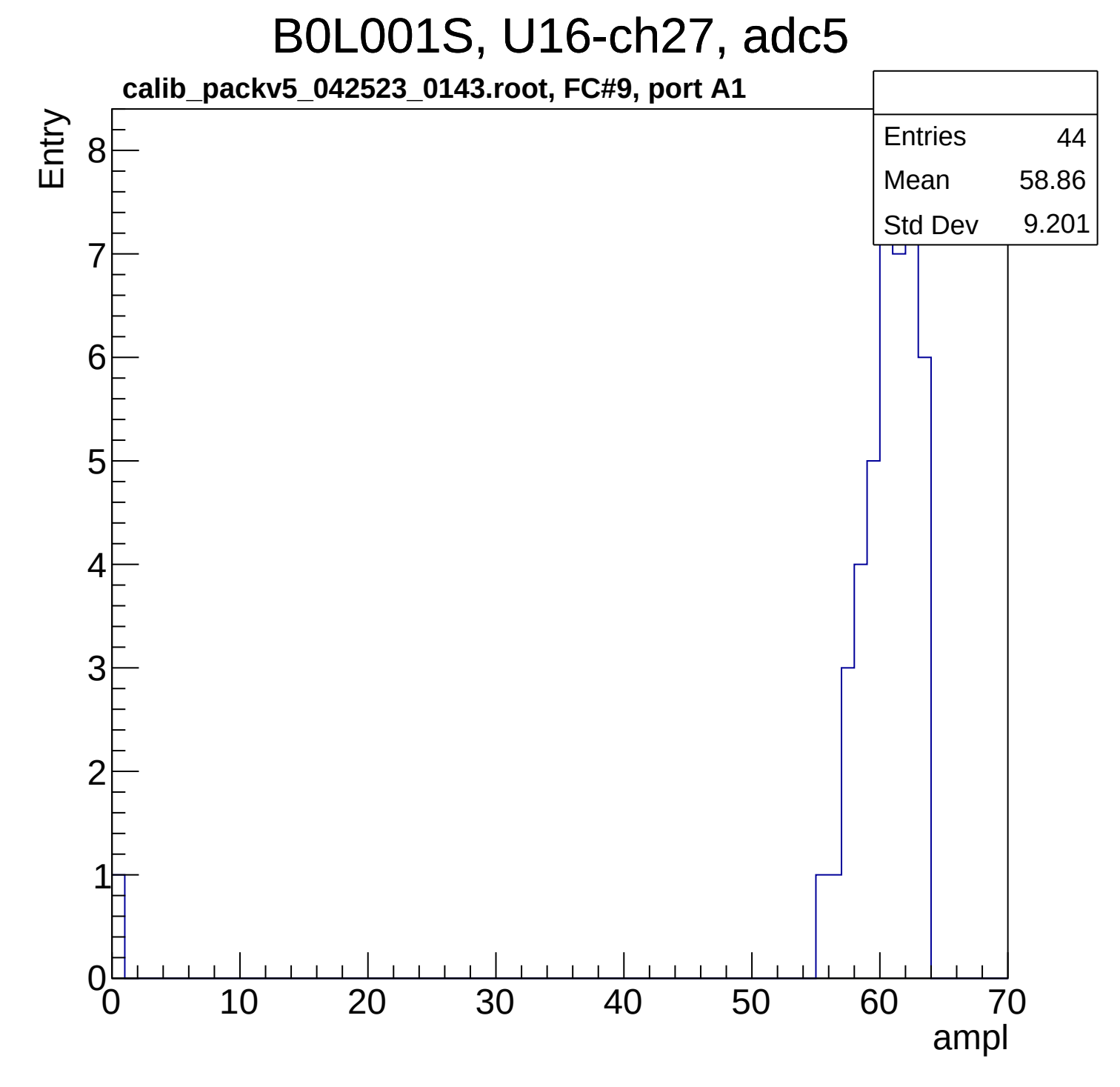

B0L001S, U16-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 50.14
Std Dev 3.671
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 56.26
Std Dev 2.829
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.86
Std Dev 9.201
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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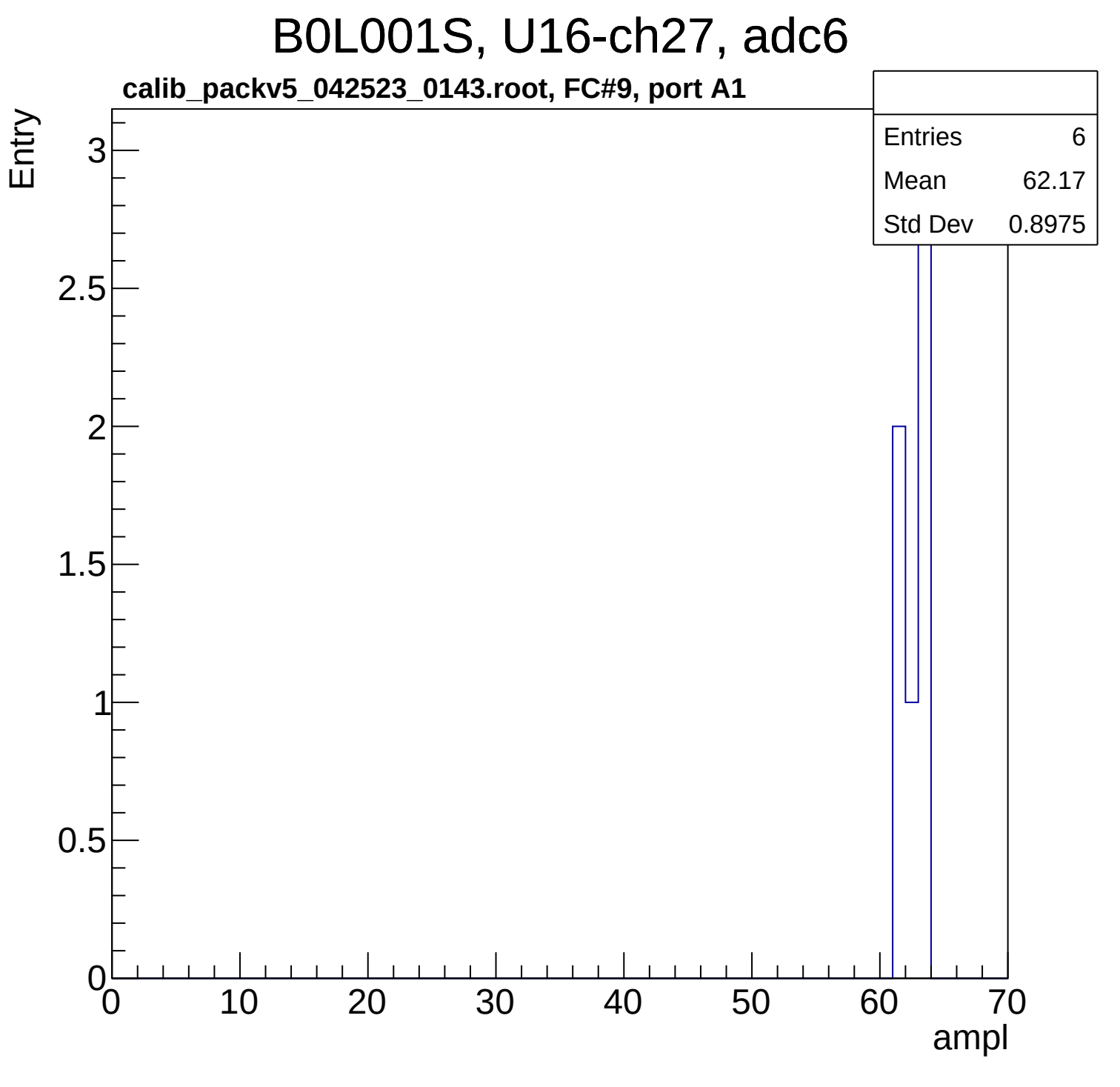
B0L001S, U16-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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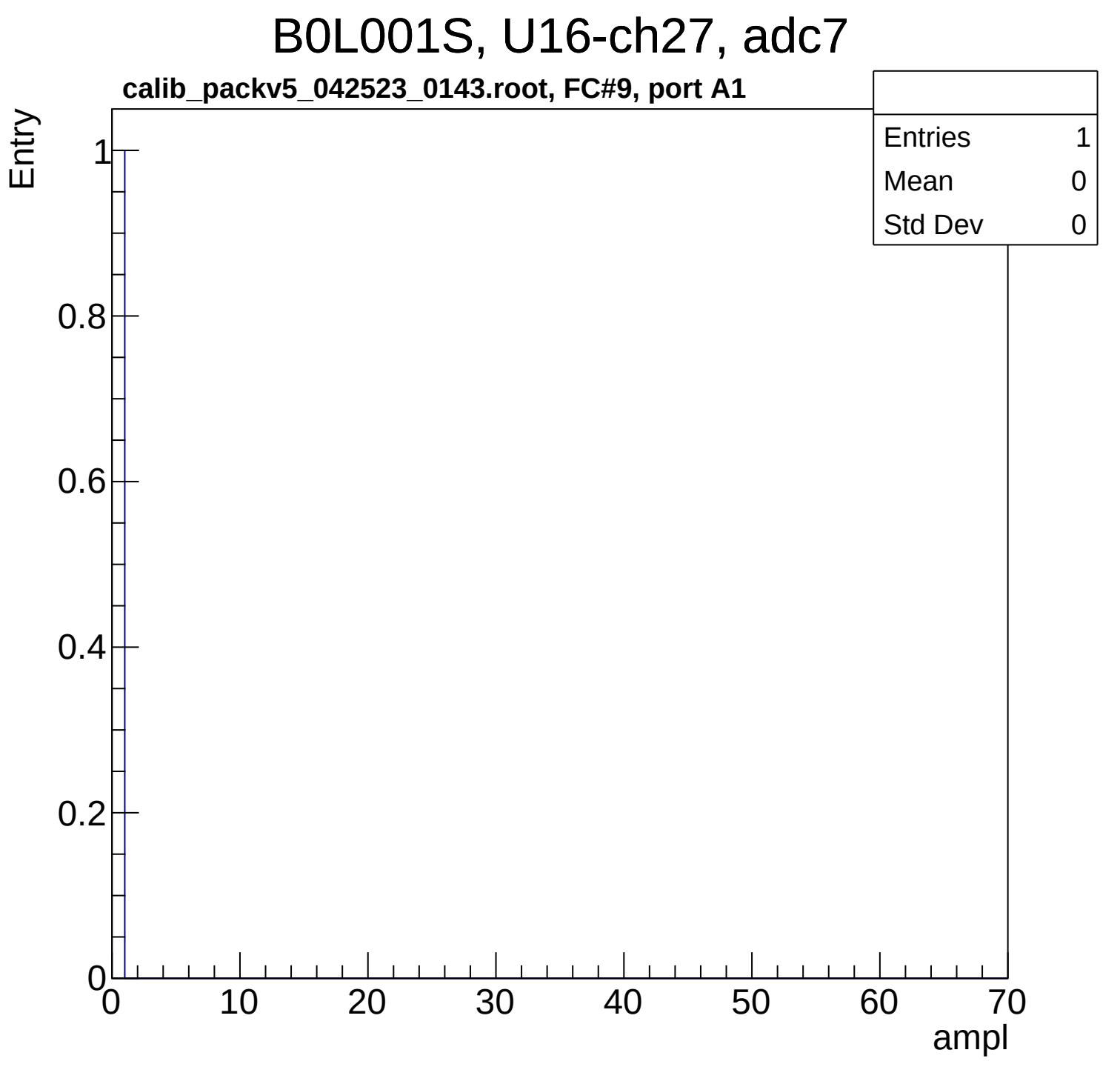
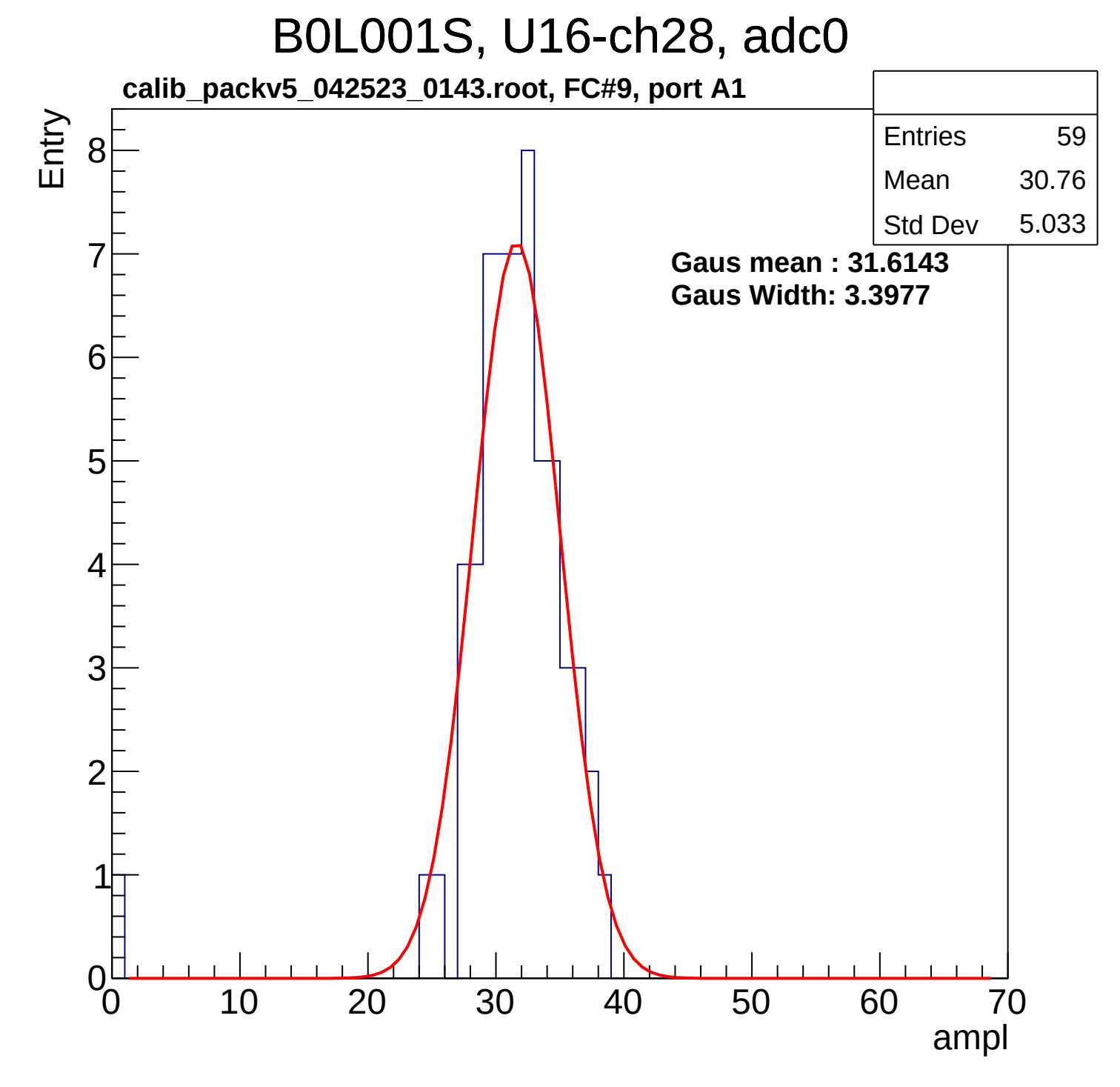
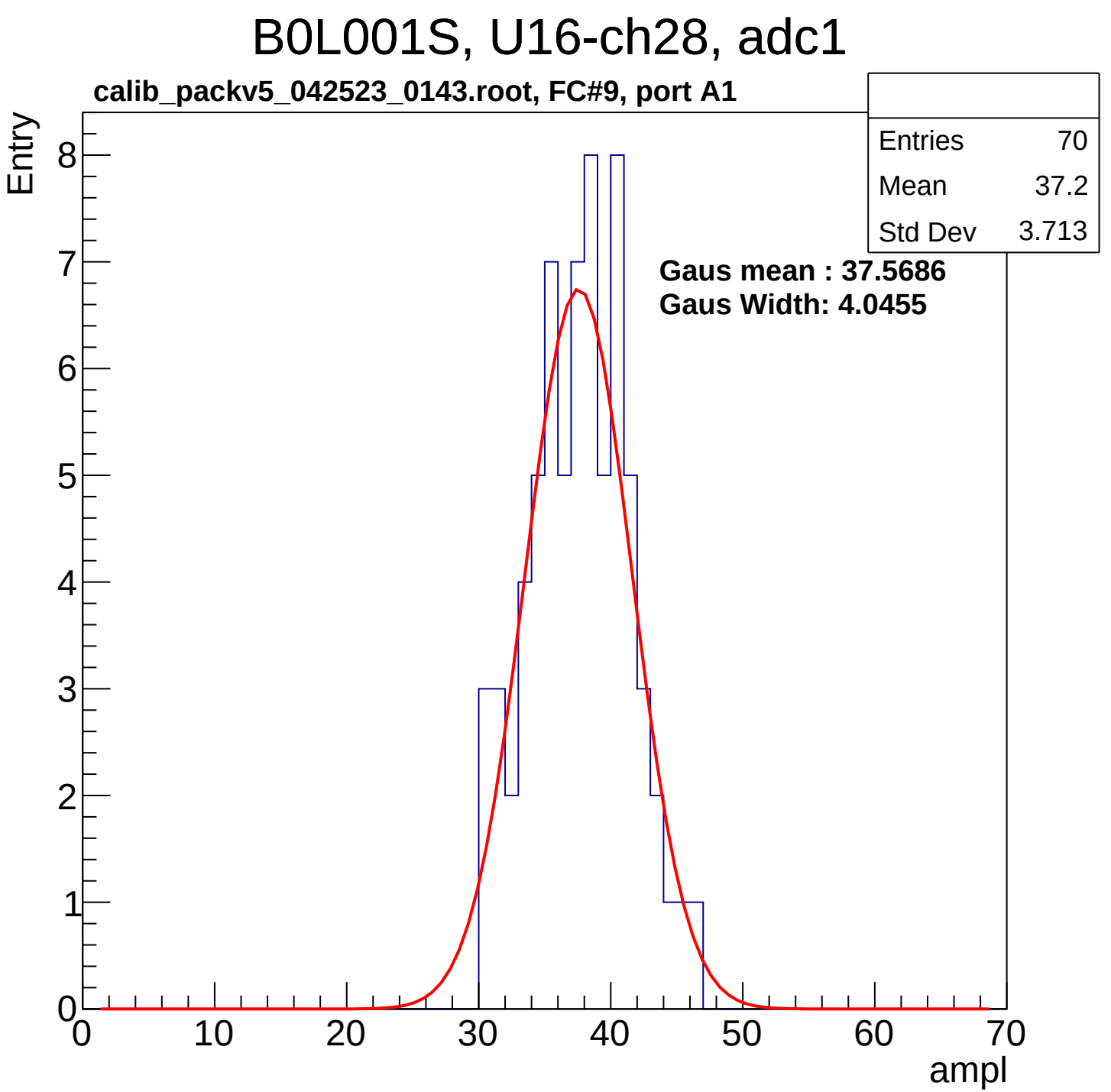
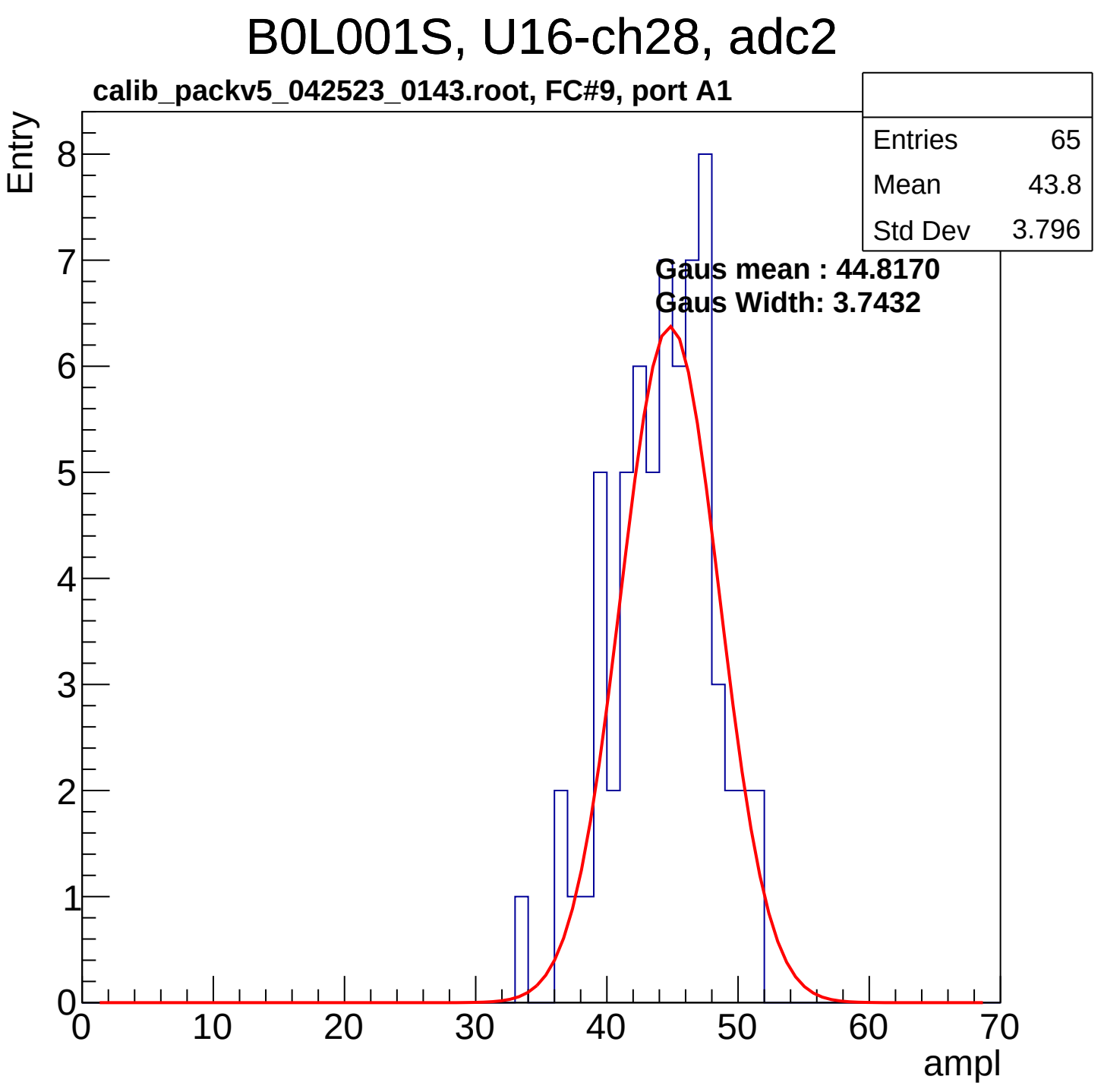
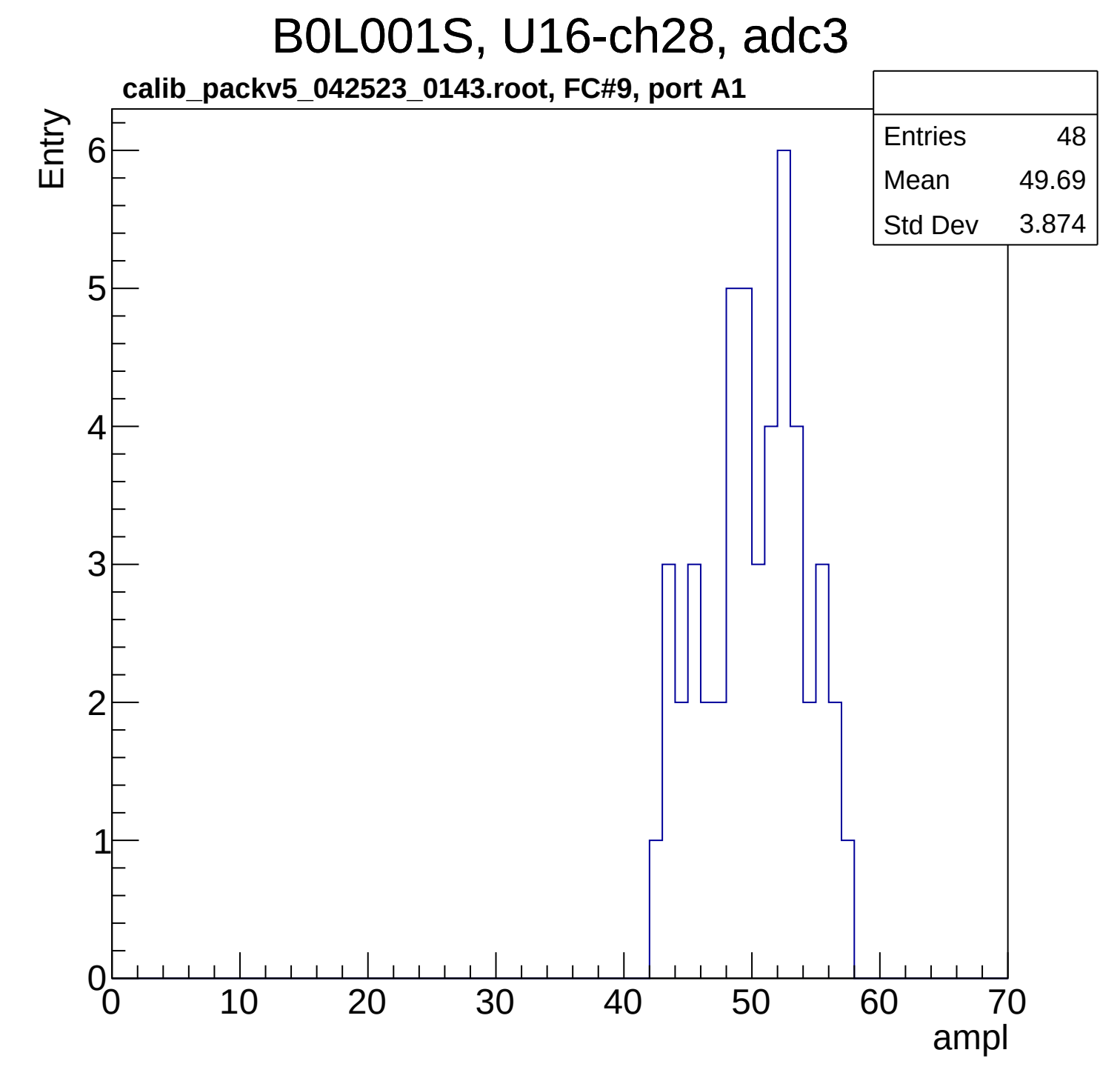
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

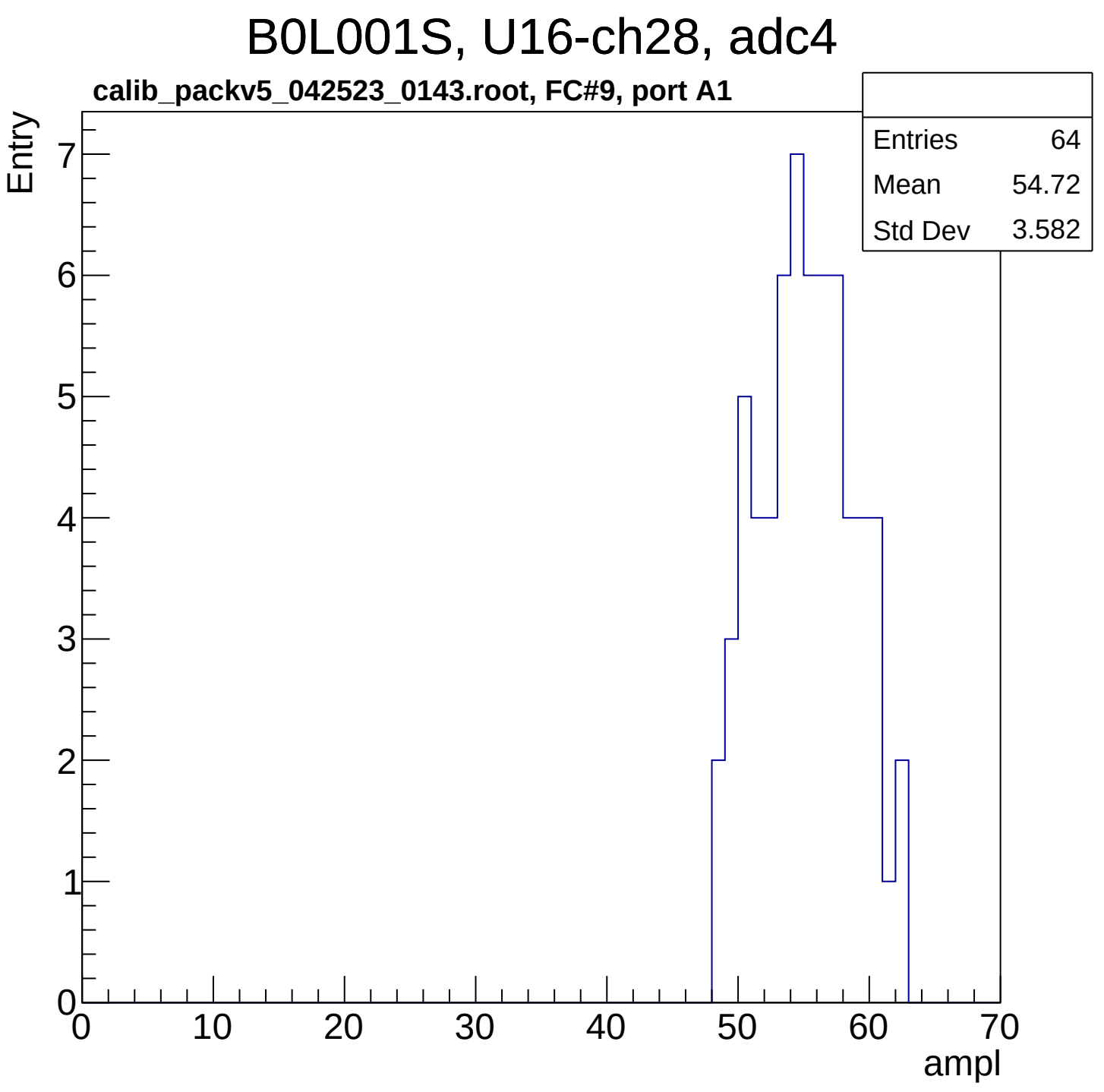

B0L001S, U16-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

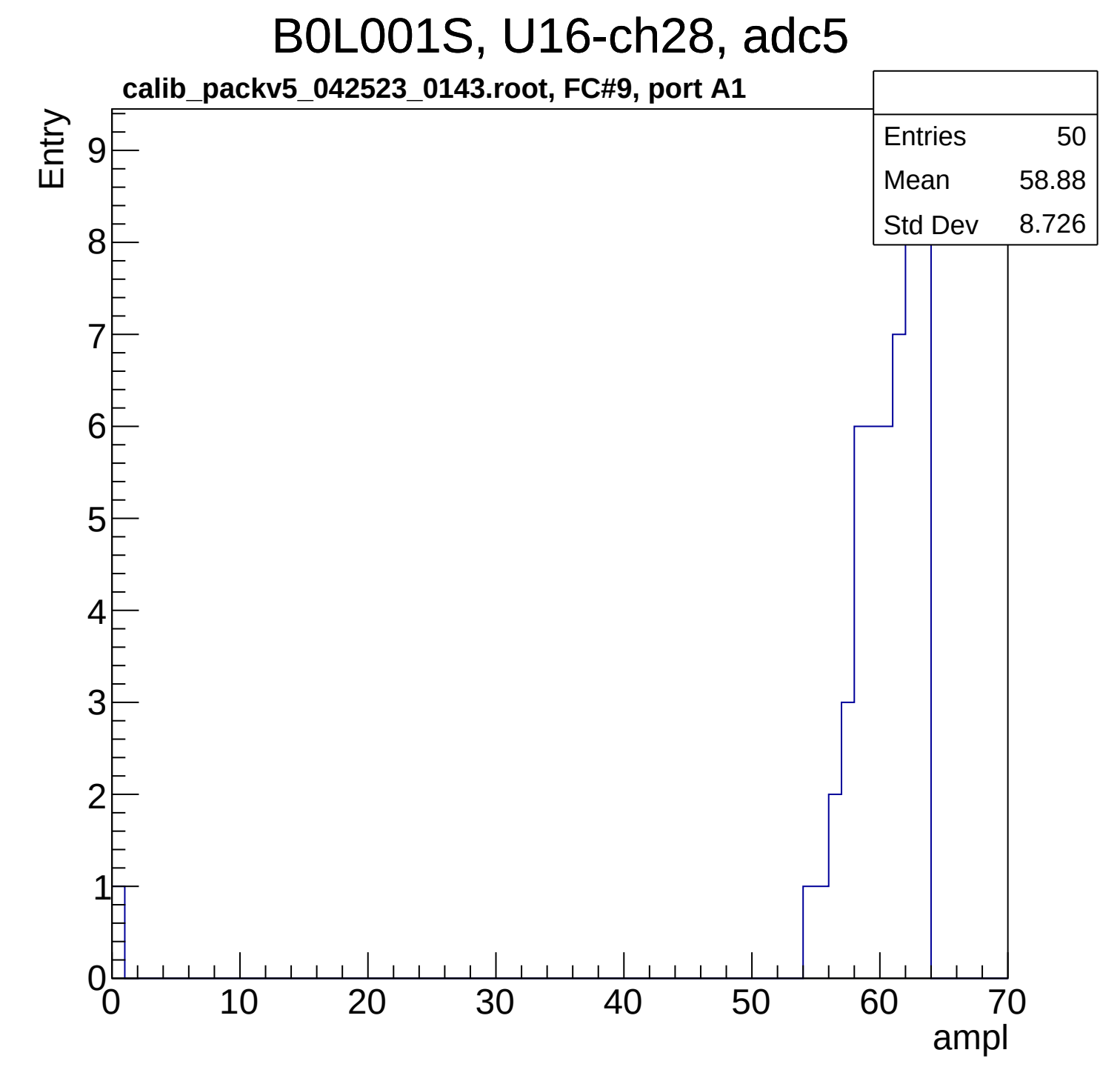

B0L001S, U16-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.76
Std Dev 5.033
Gaus mean : 31.6143
Gaus Width: 3.3977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

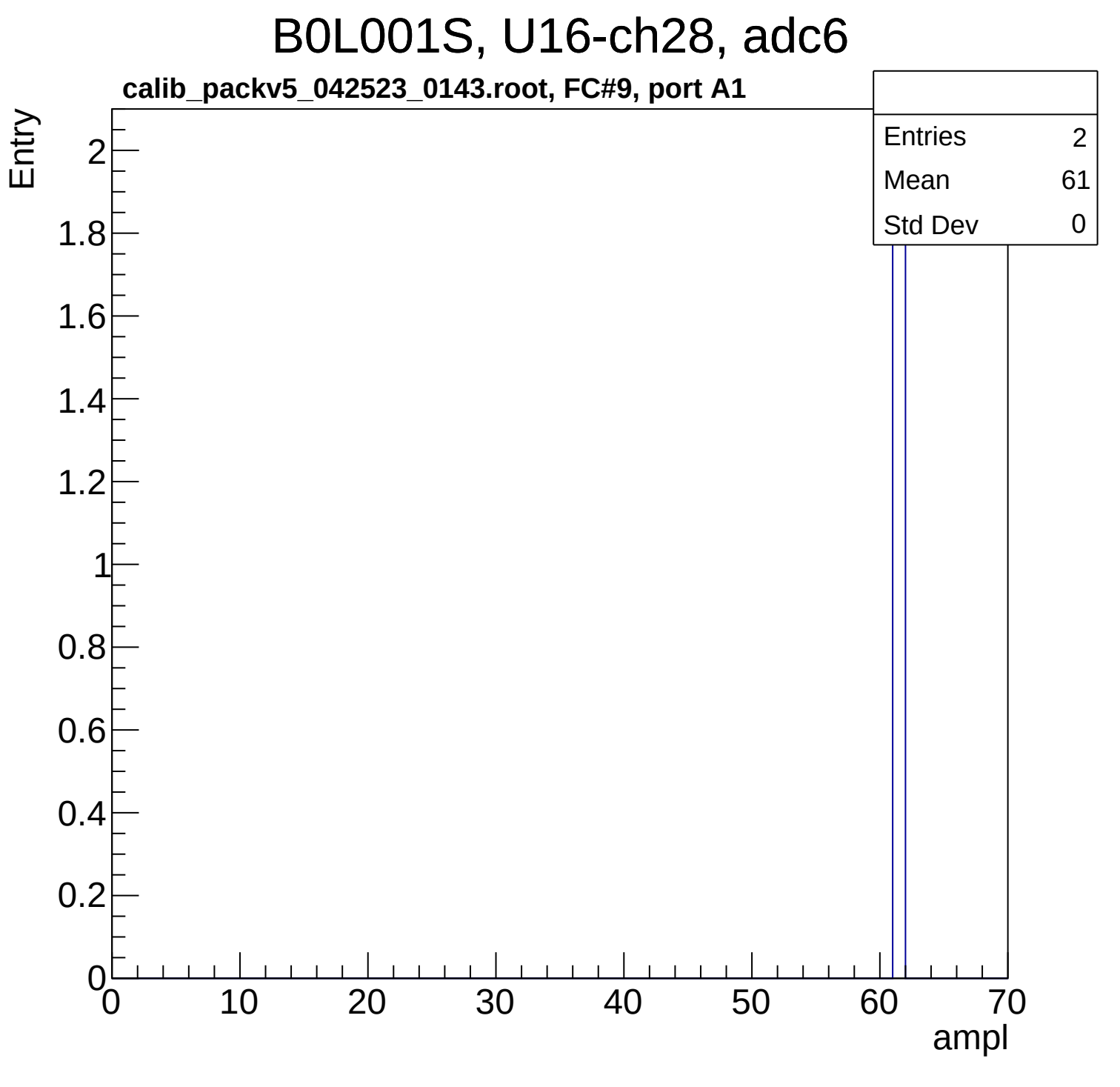

B0L001S, U16-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.2
Std Dev 3.713
Gaus mean : 37.5686
Gaus Width: 4.0455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

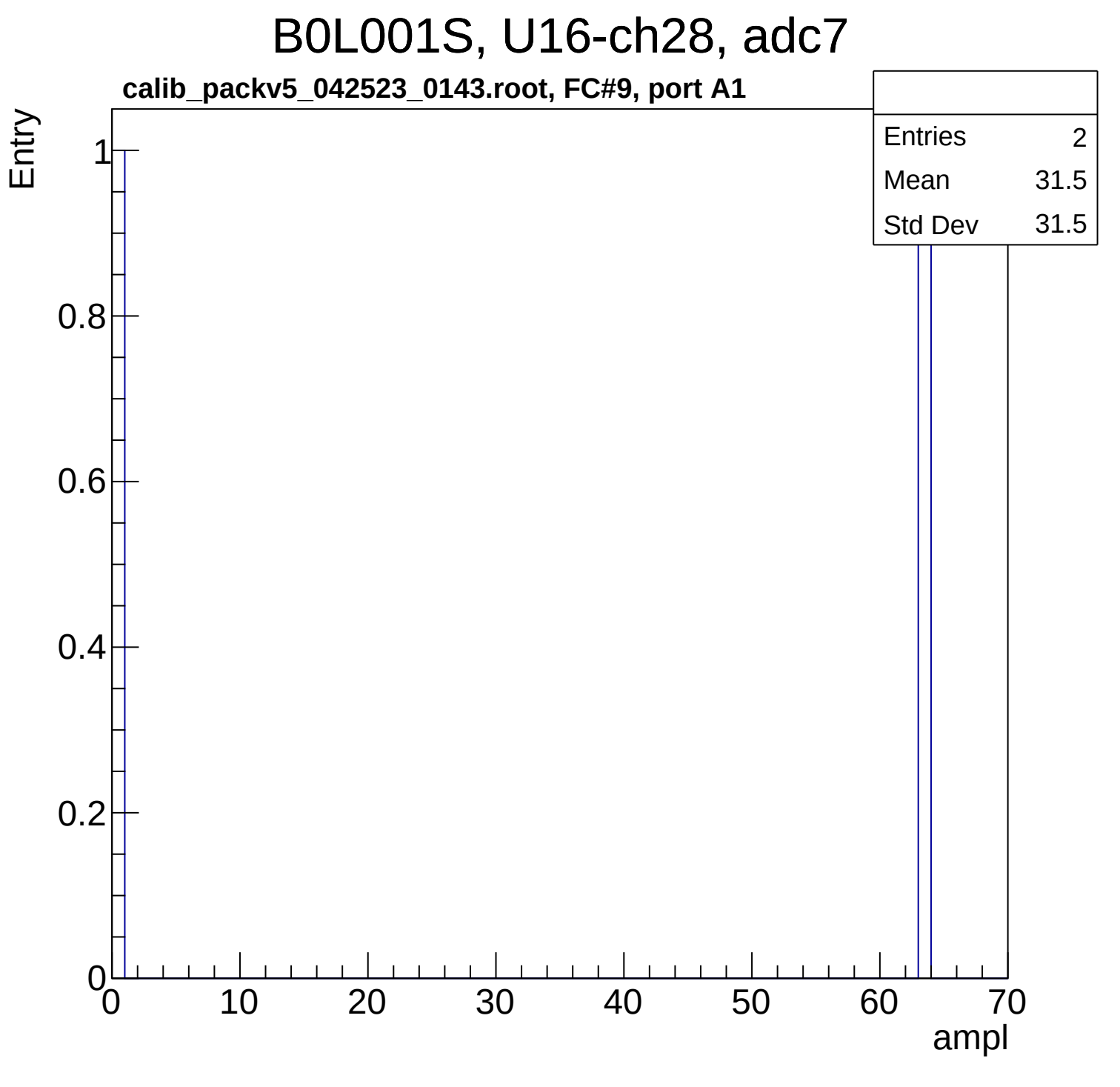

B0L001S, U16-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.8
Std Dev 3.796
Gaus mean : 44.8170
Gaus Width: 3.7432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

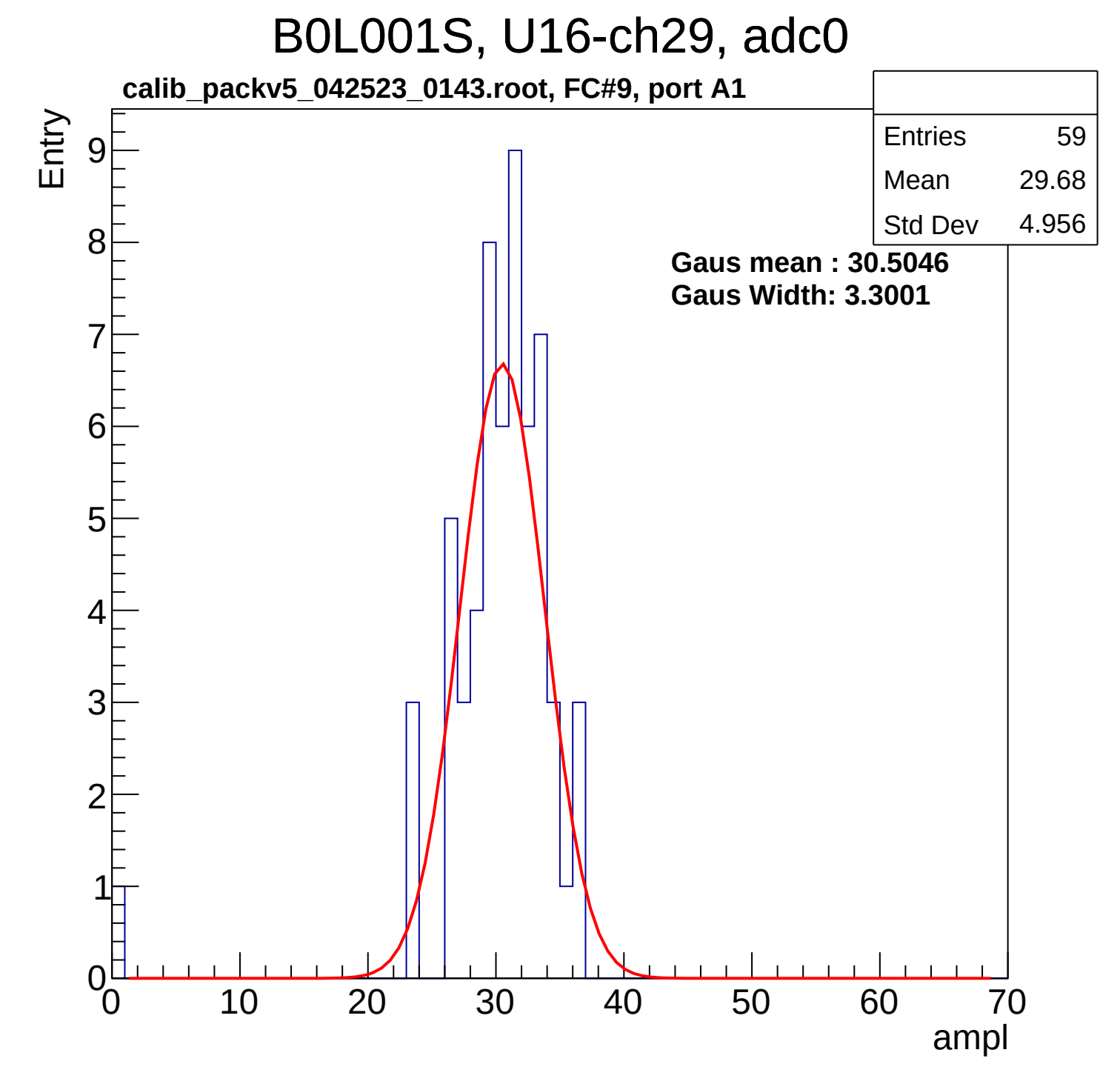

B0L001S, U16-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 49.69
Std Dev 3.874
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

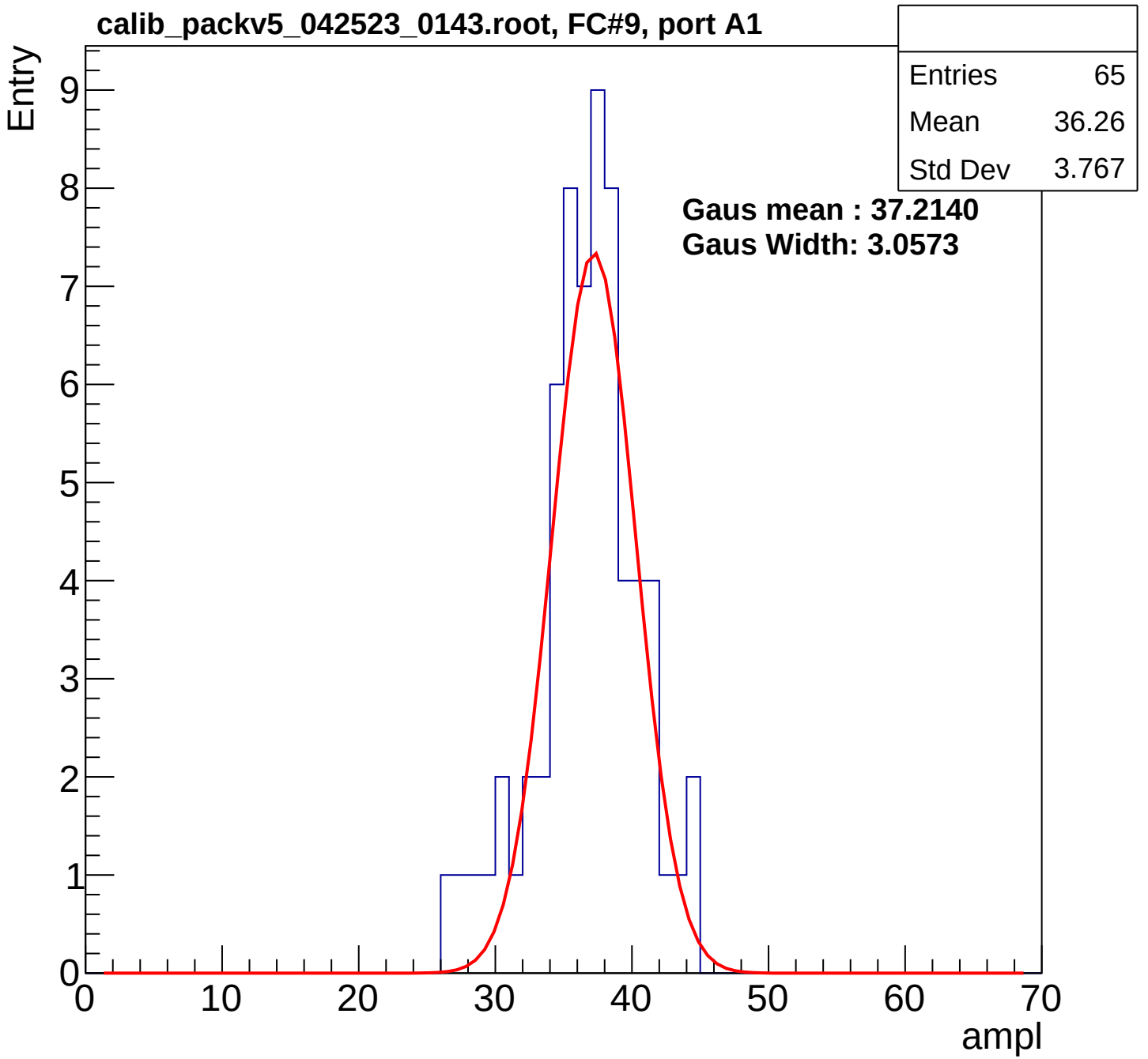

B0L001S, U16-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 54.72
Std Dev 3.582
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

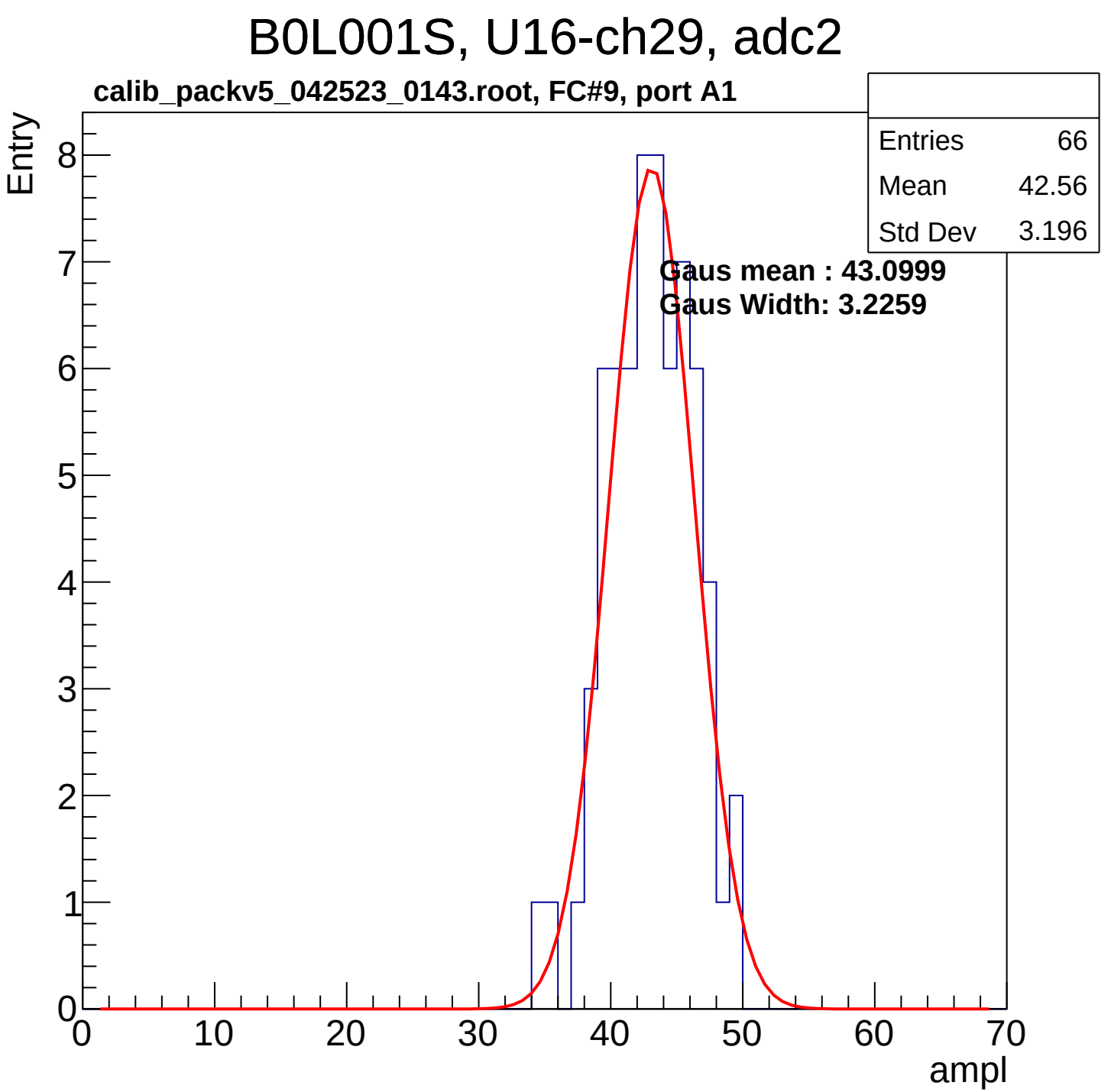

B0L001S, U16-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58.88
Std Dev 8.726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

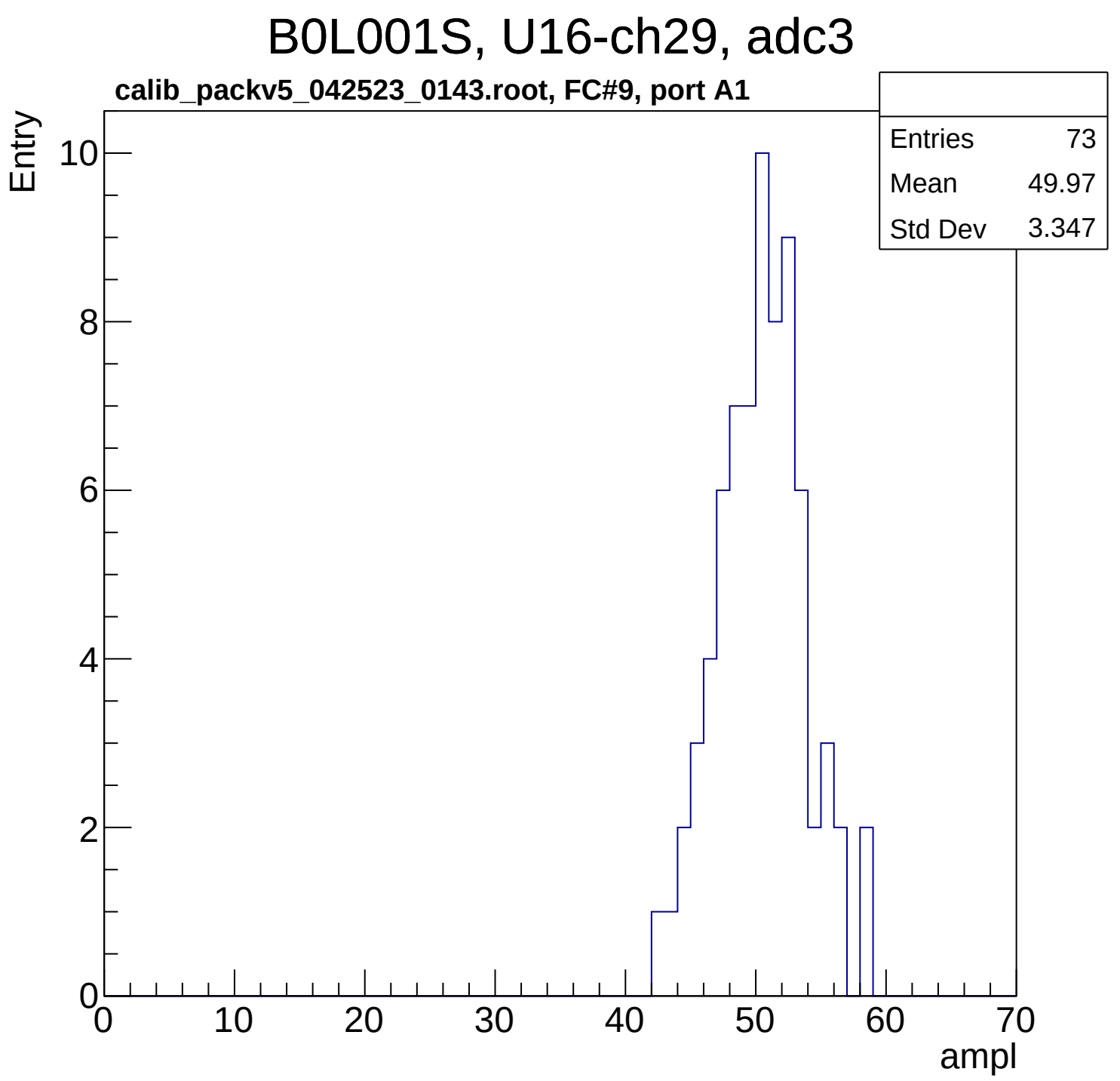

B0L001S, U16-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

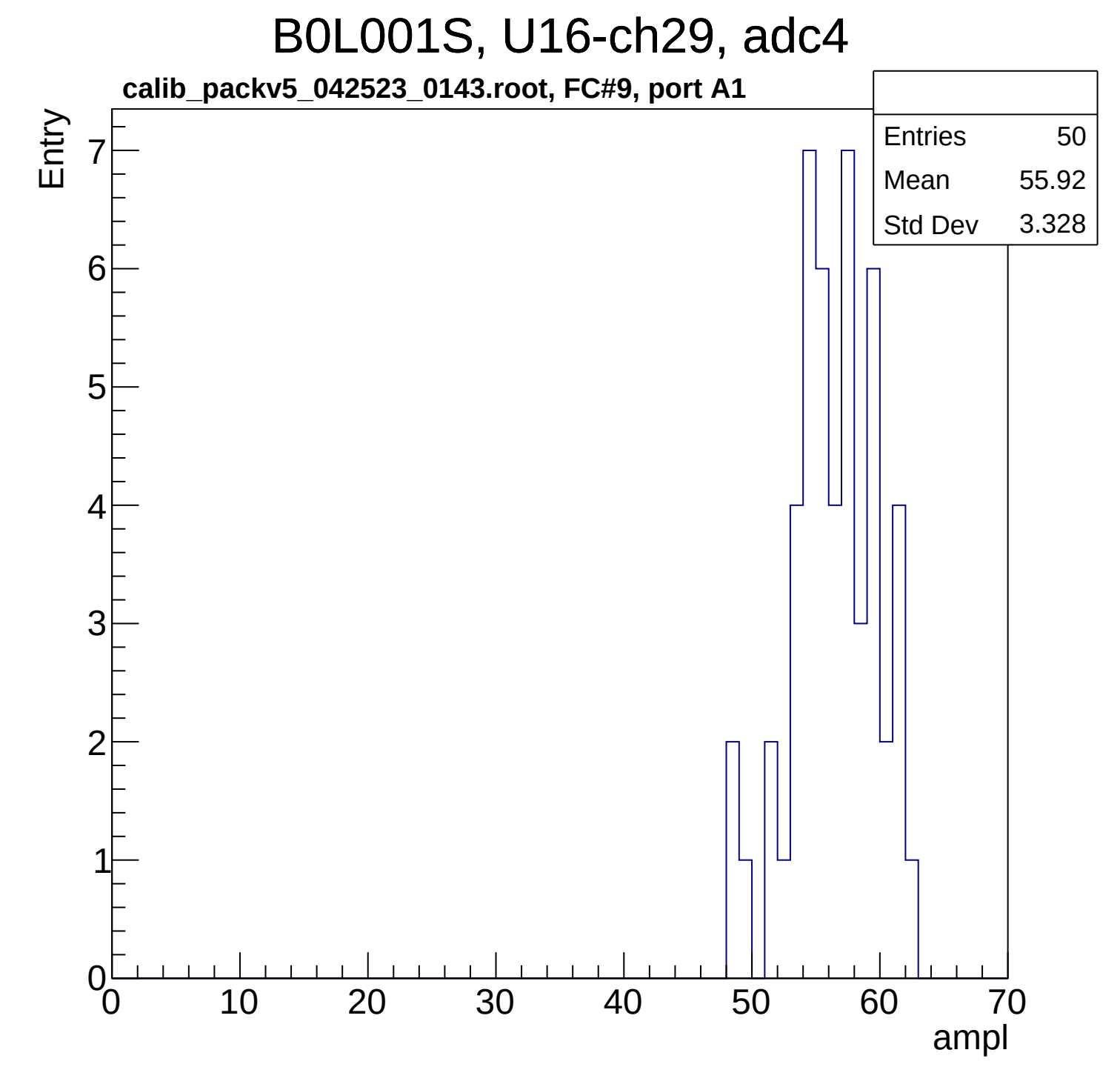

B0L001S, U16-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

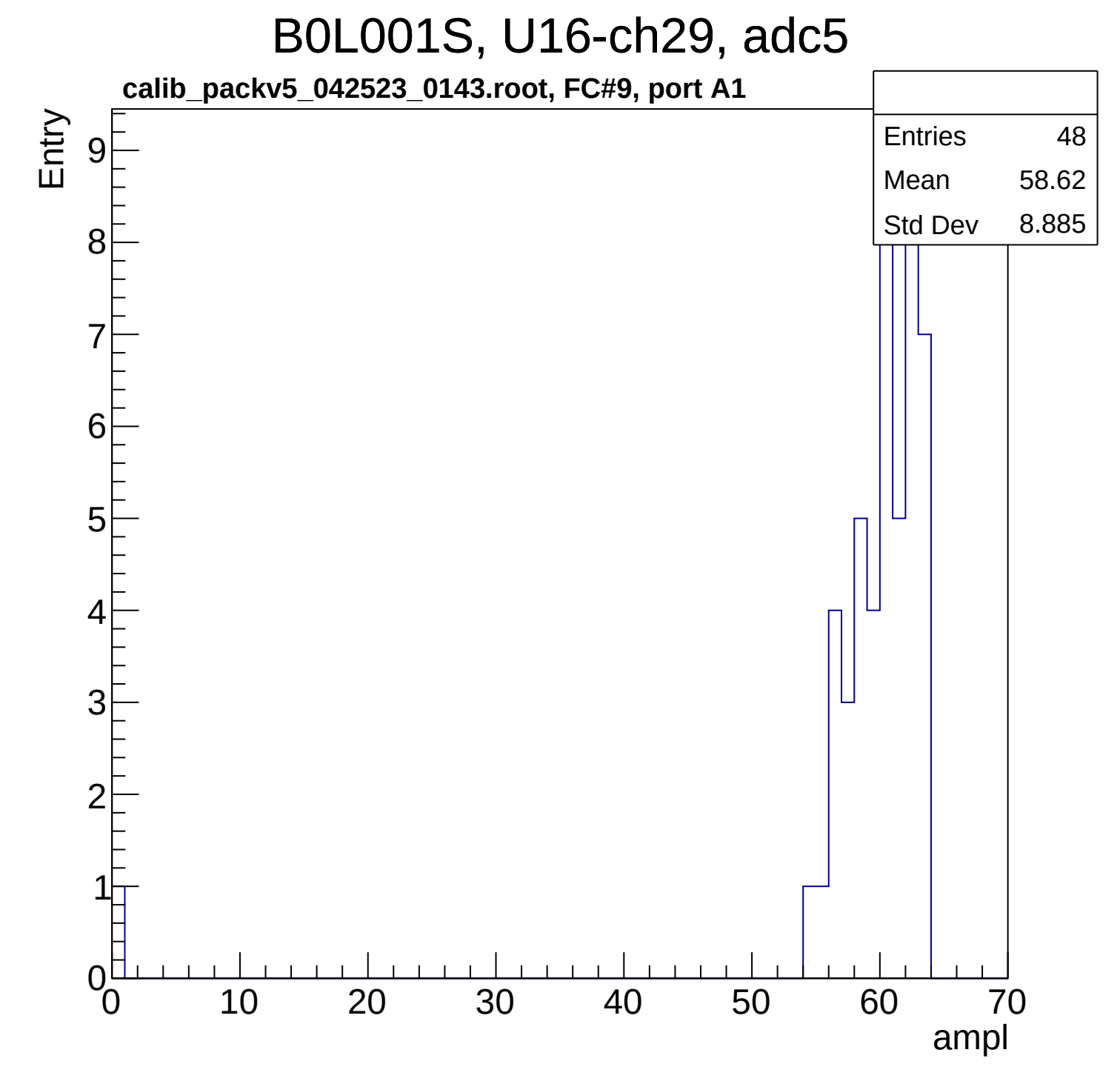

B0L001S, U16-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.68
Std Dev 4.956
Gaus mean : 30.5046
Gaus Width: 3.3001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.26
Std Dev 3.767
Gaus mean : 37.2140
Gaus Width: 3.0573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.56
Std Dev 3.196
Gaus mean : 43.0999
Gaus Width: 3.2259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 49.97
Std Dev 3.347
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 55.92
Std Dev 3.328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.62
Std Dev 8.885
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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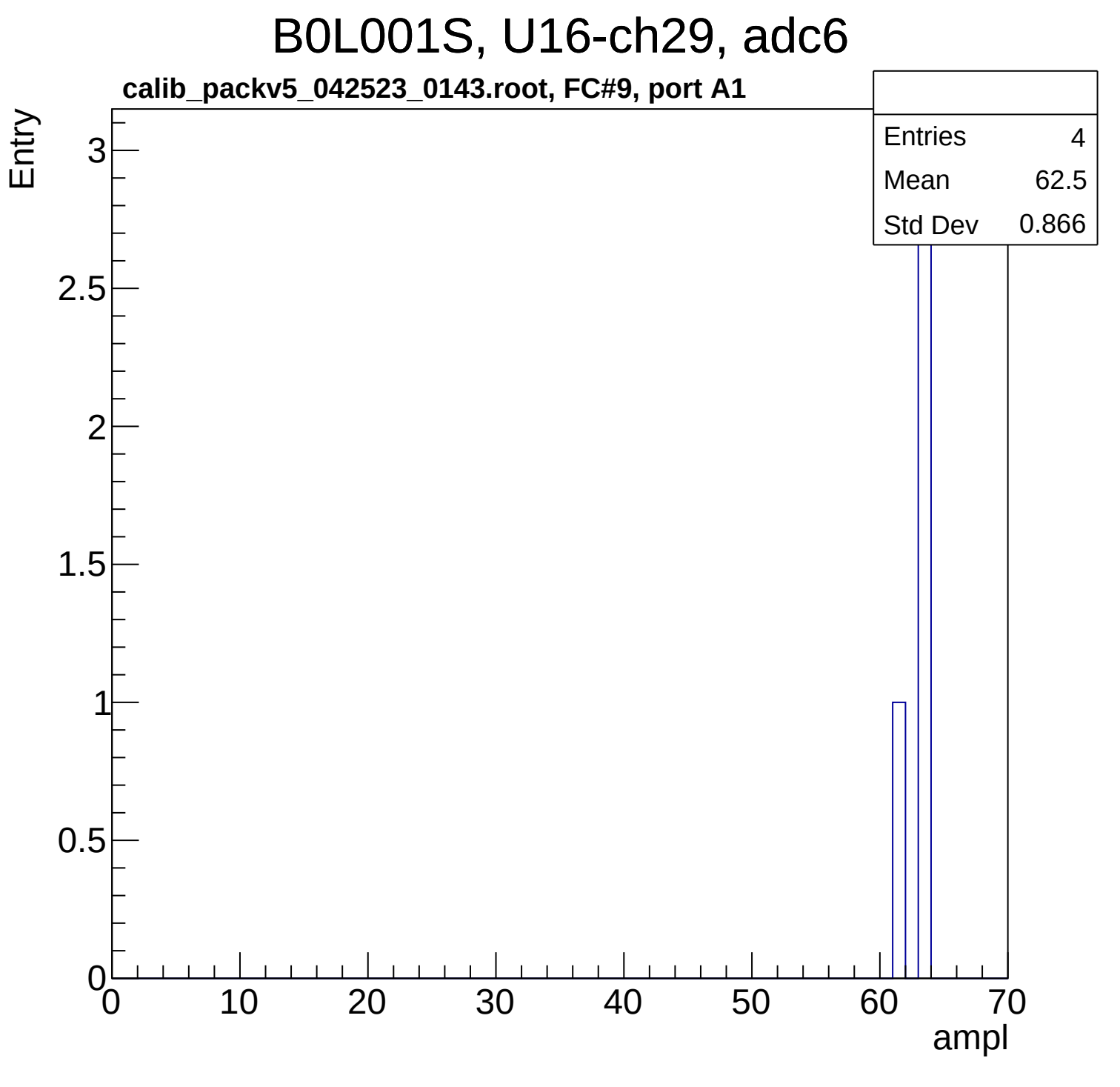
B0L001S, U16-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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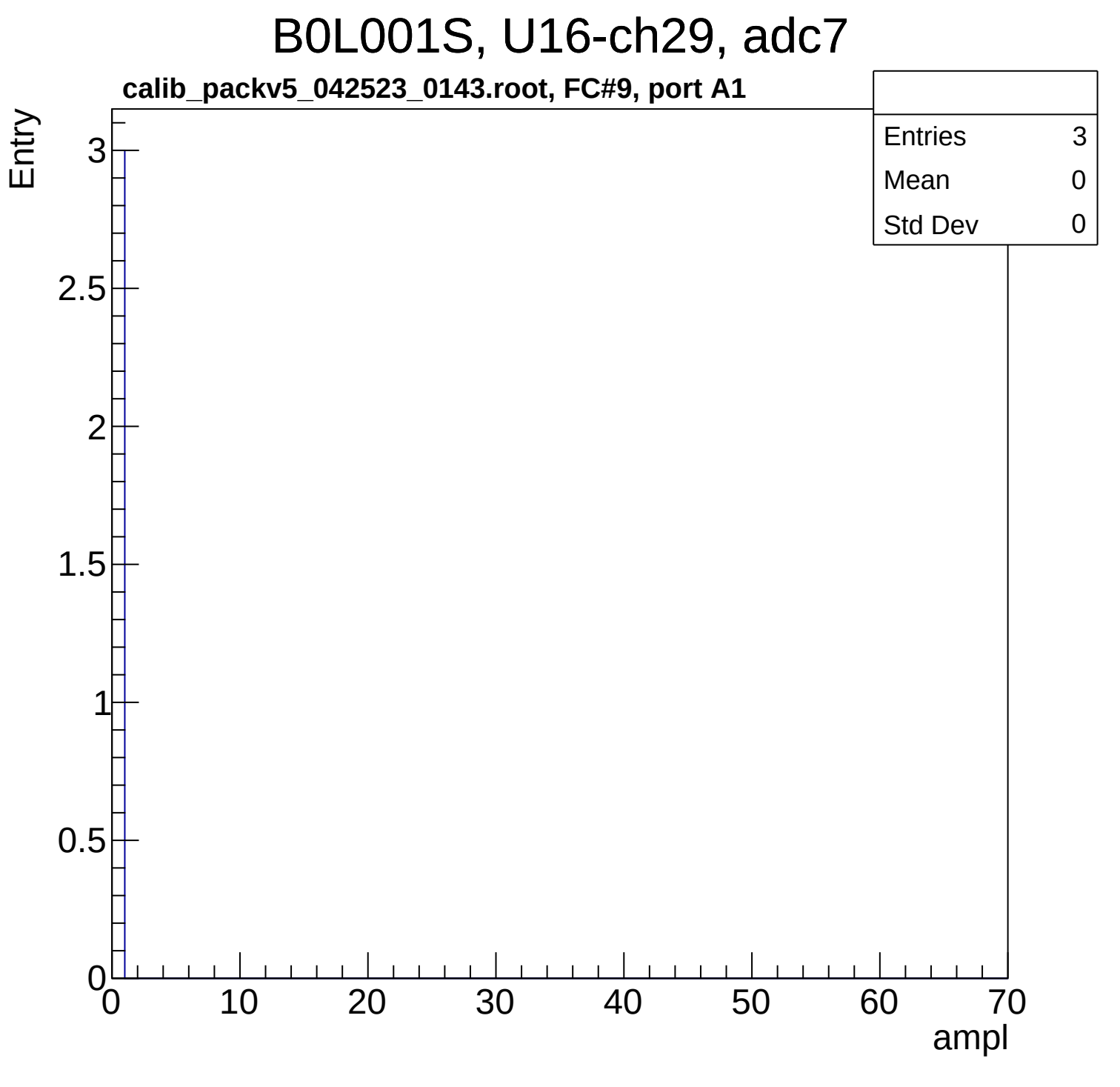
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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1
1.5
2
2.5
3
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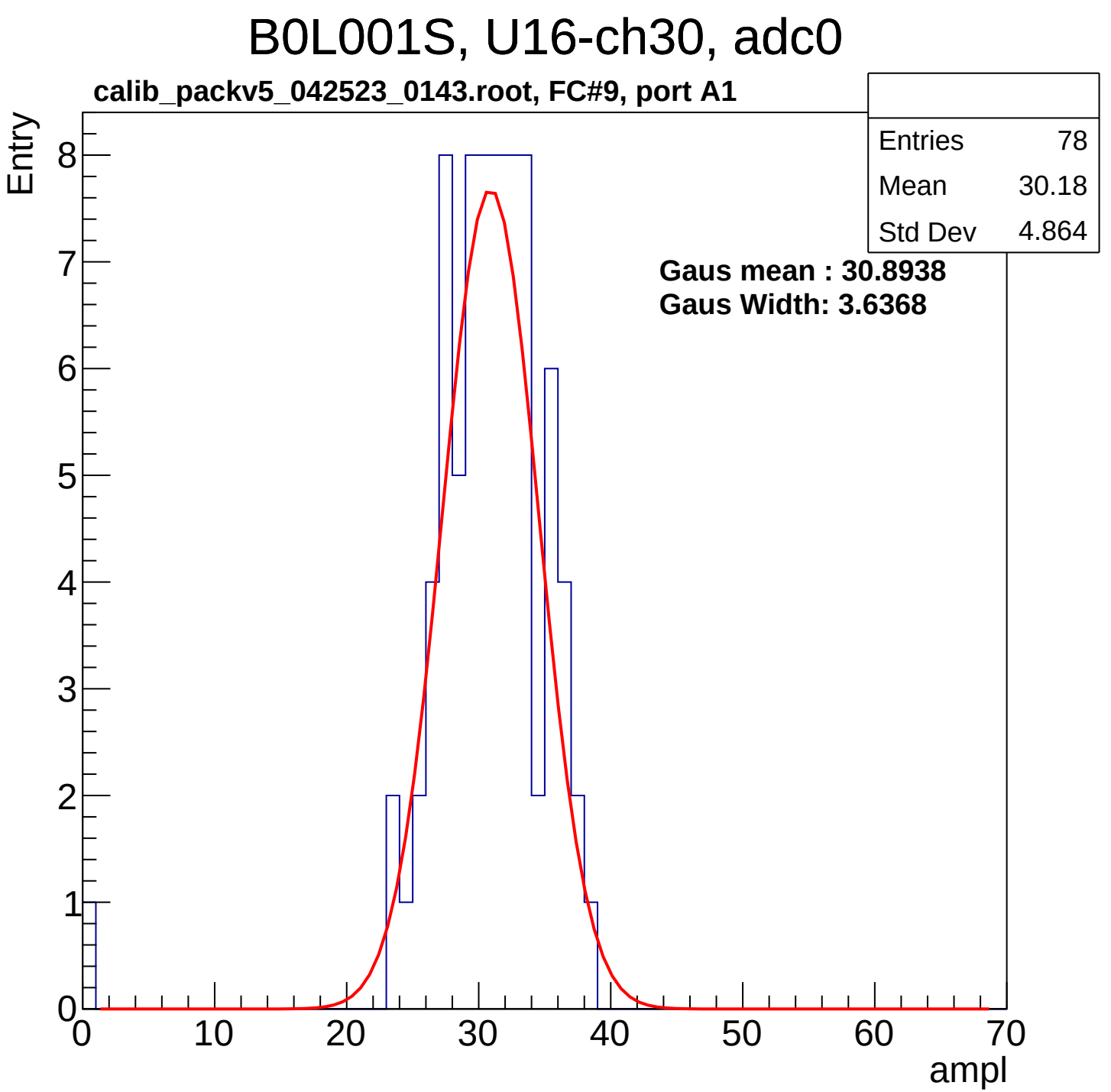
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.18
Std Dev 4.864
Gaus mean : 30.8938
Gaus Width: 3.6368
Entry
ampl
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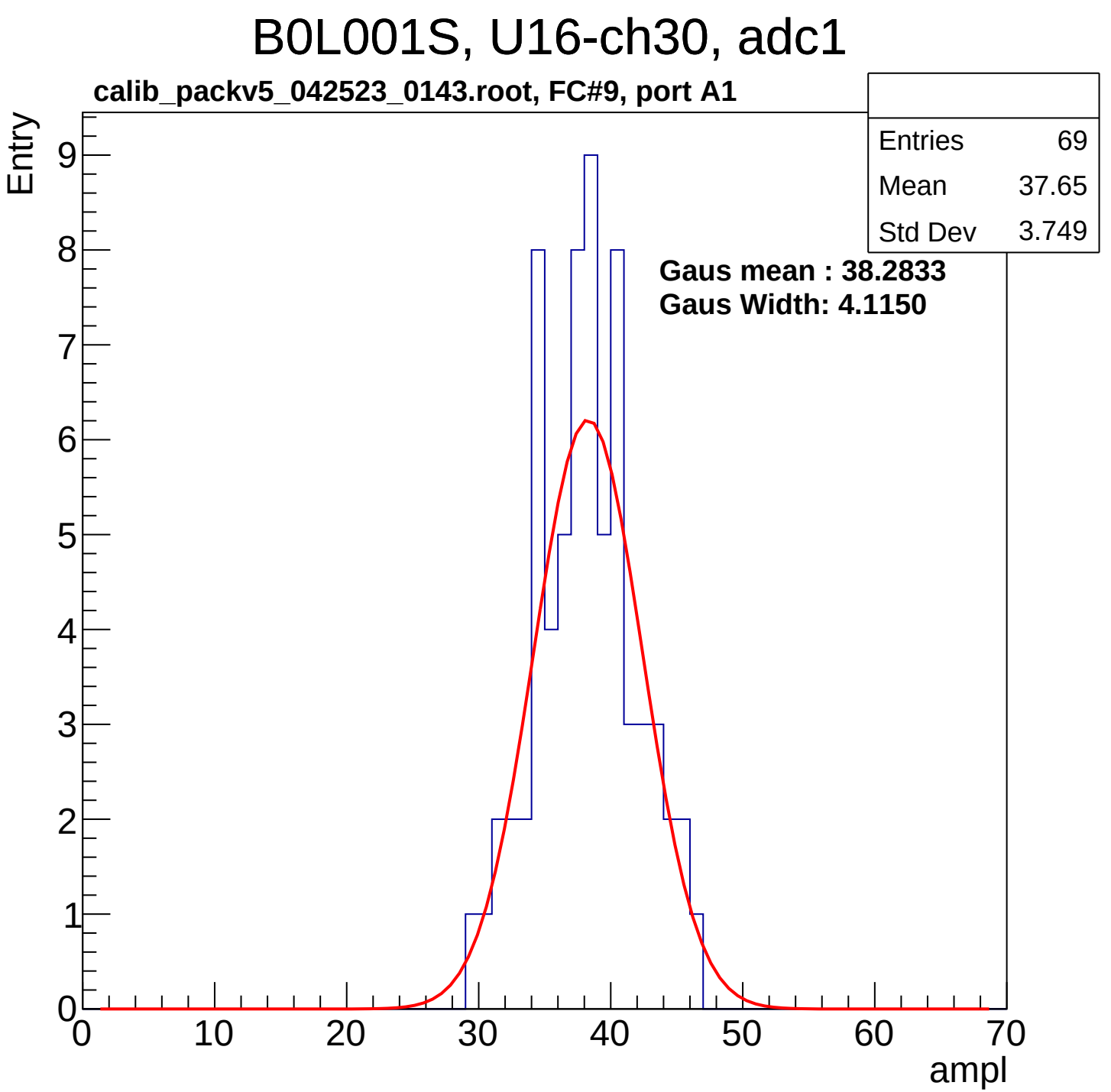
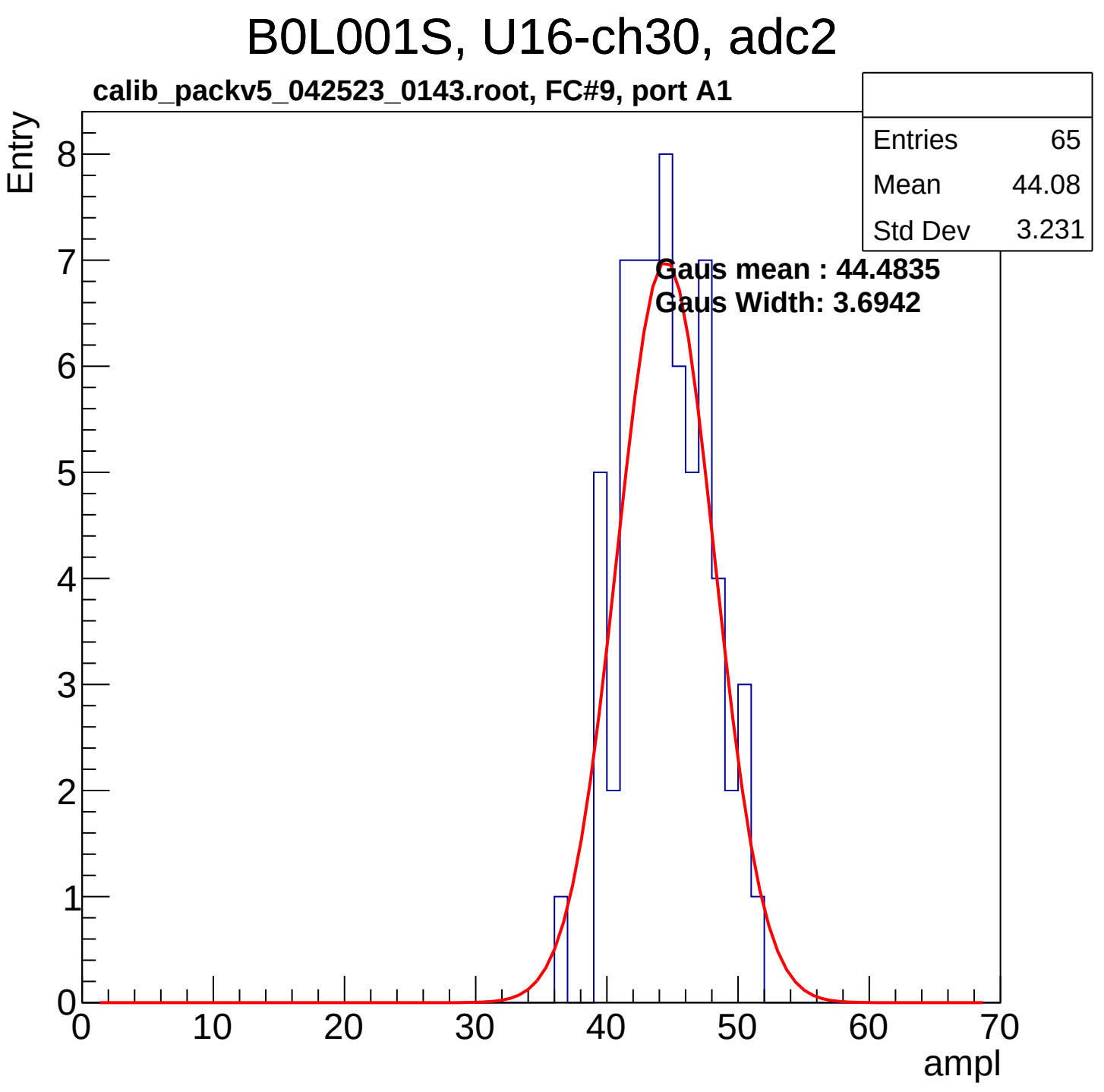
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.65
Std Dev 3.749
Gaus mean : 38.2833
Gaus Width: 4.1150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.08
Std Dev 3.231
Gaus mean : 44.4835
Gaus Width: 3.6942
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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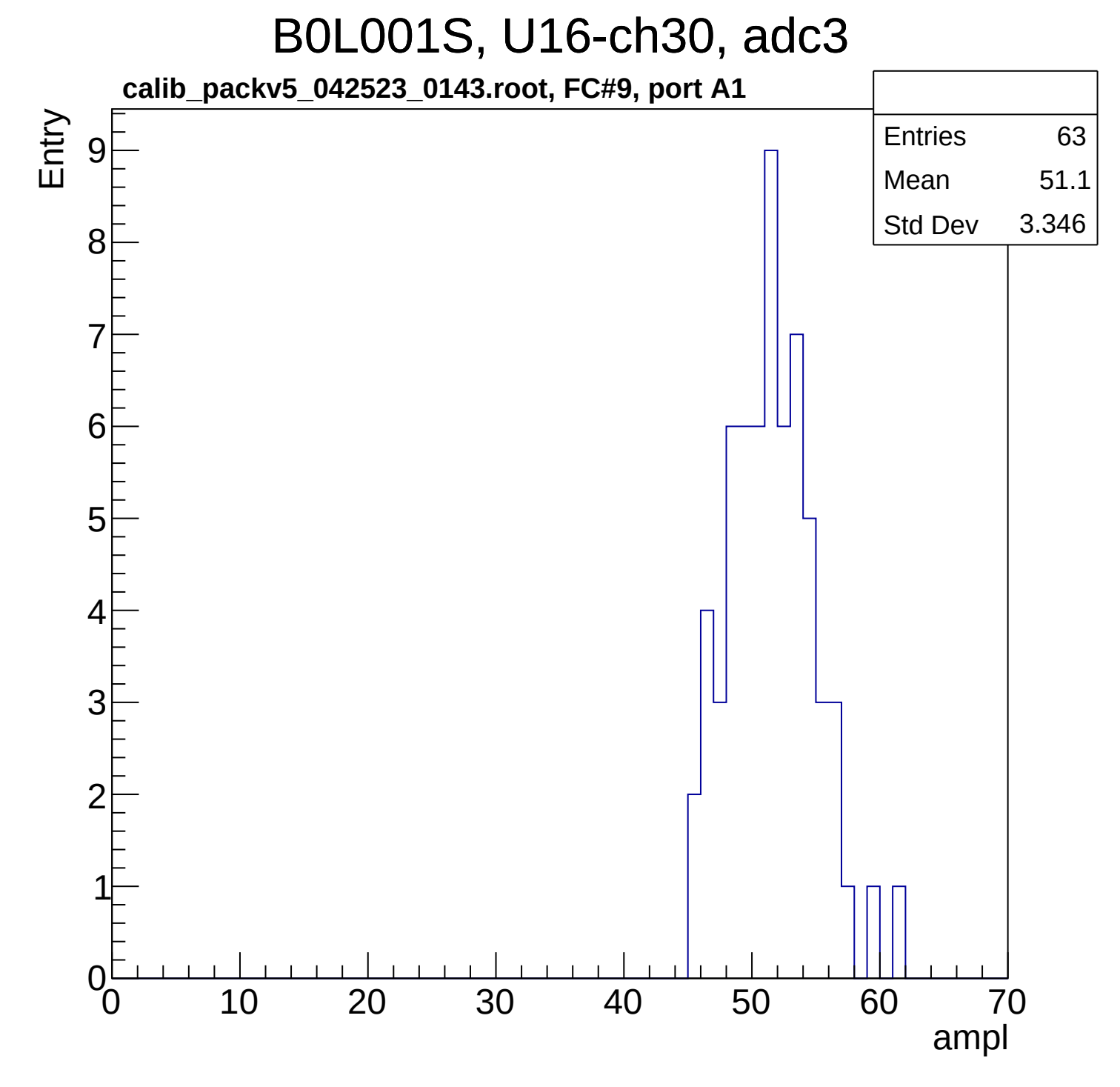
B0L001S, U16-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.1
Std Dev 3.346
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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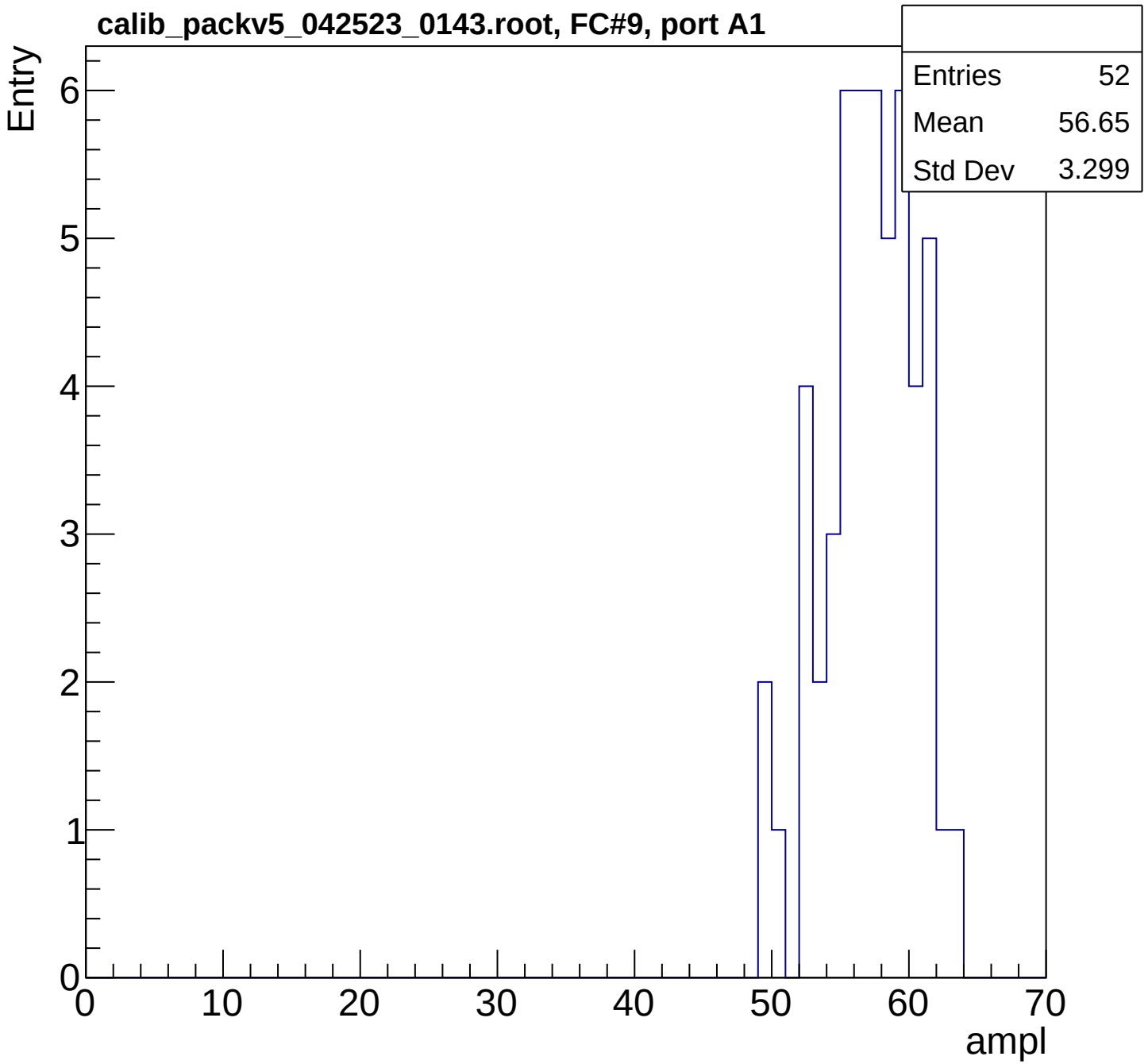
B0L001S, U16-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.65
Std Dev 3.299
Entry
ampl
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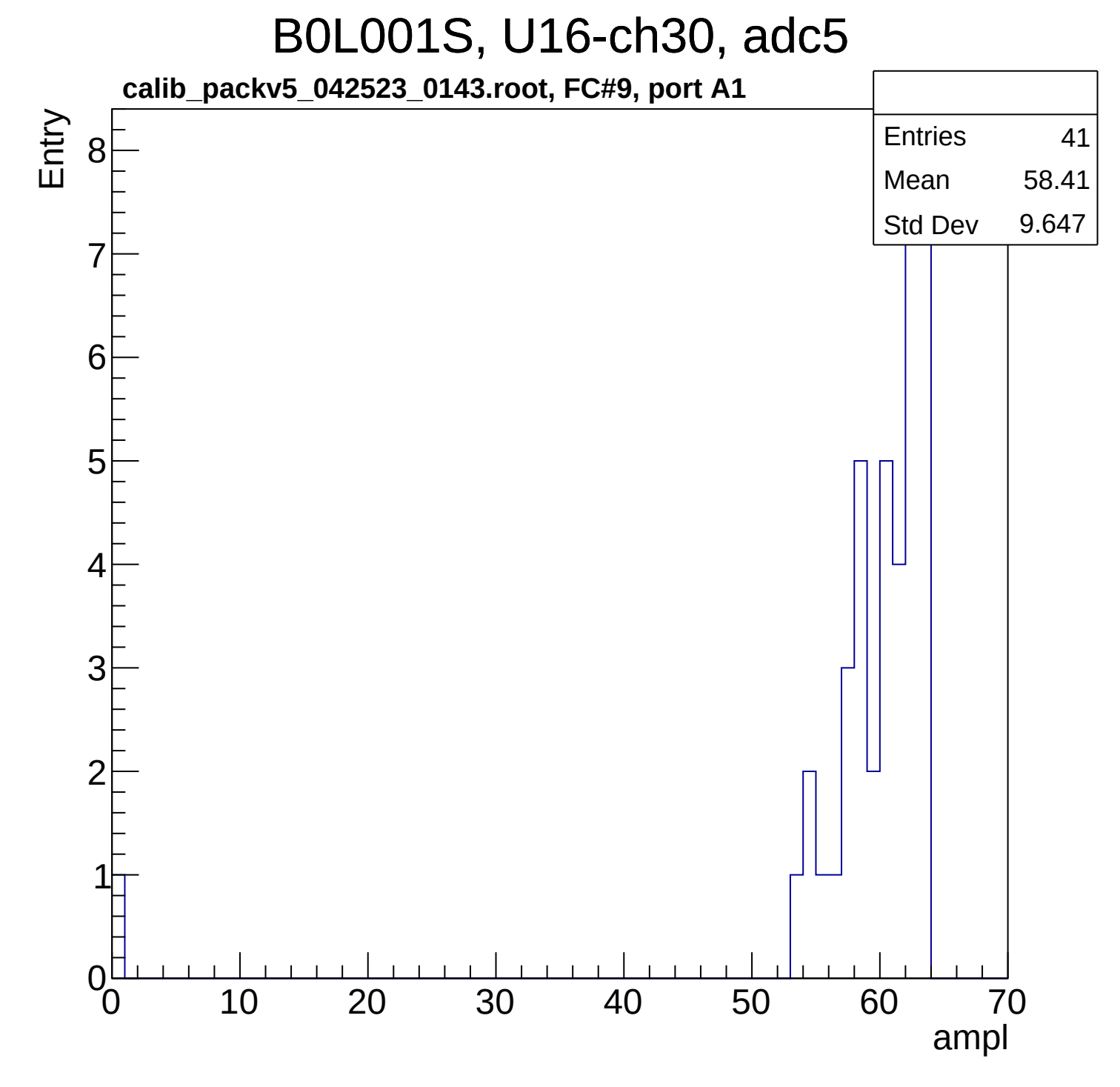
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.41
Std Dev 9.647
Entry
ampl
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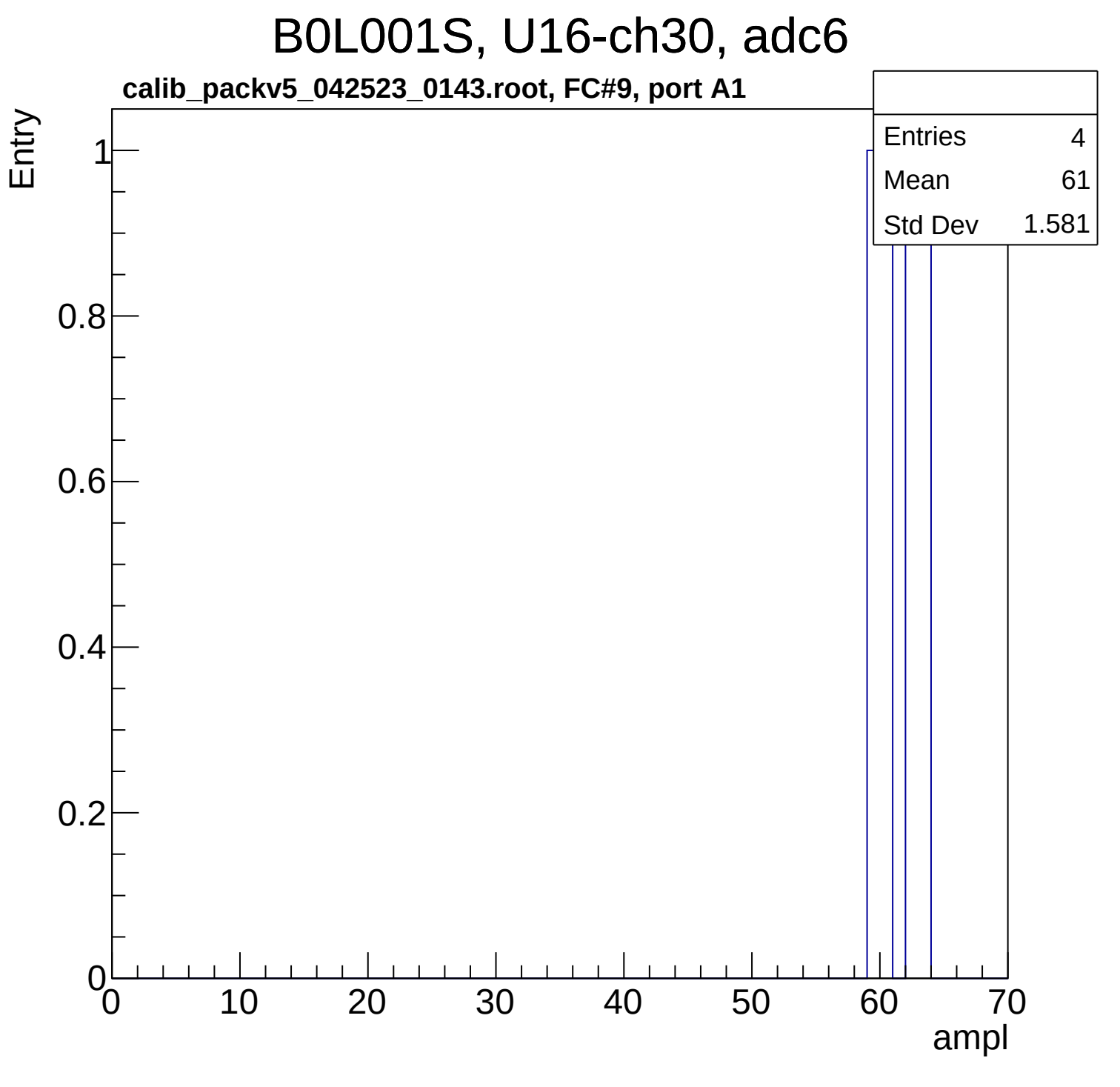
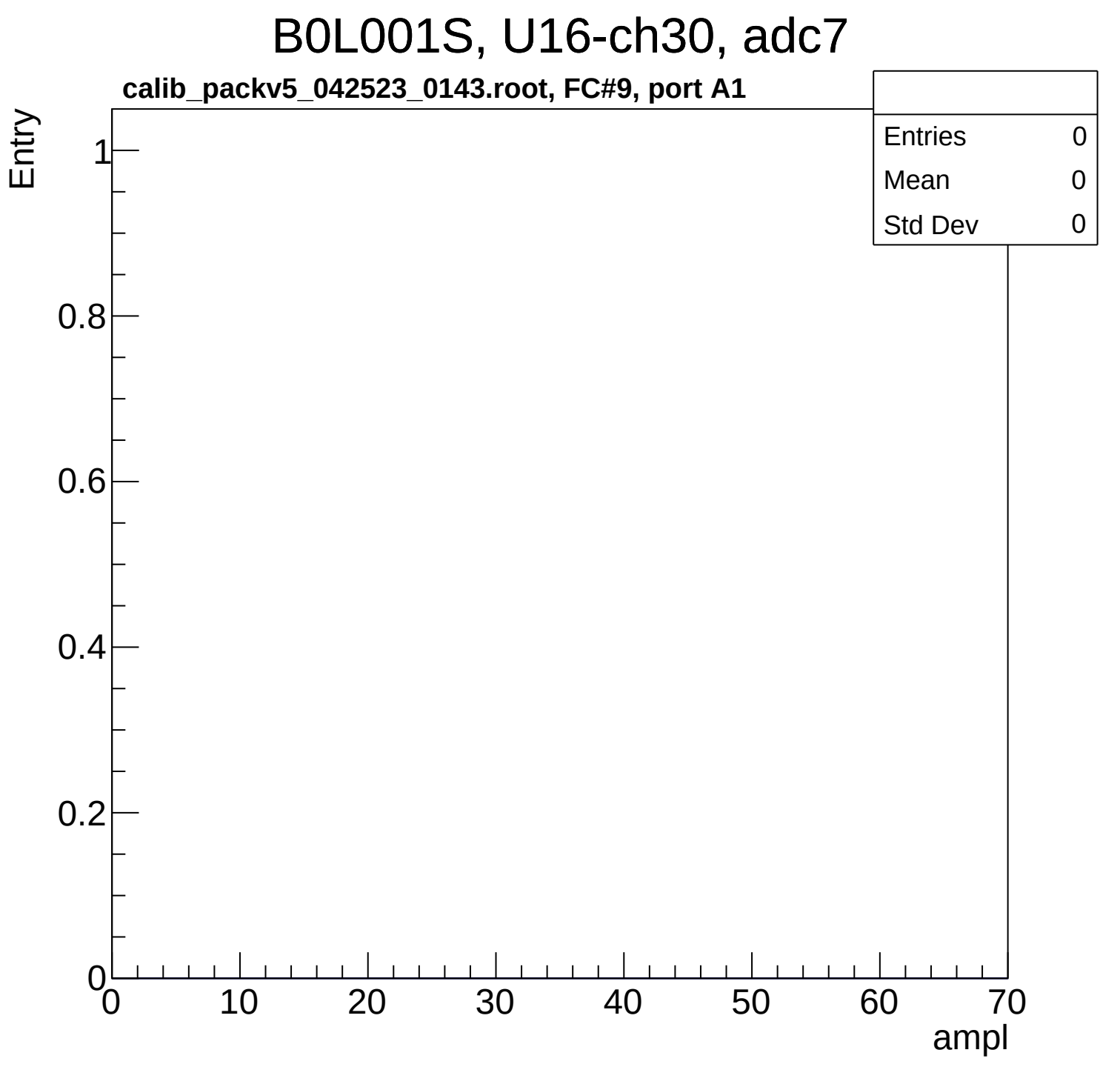
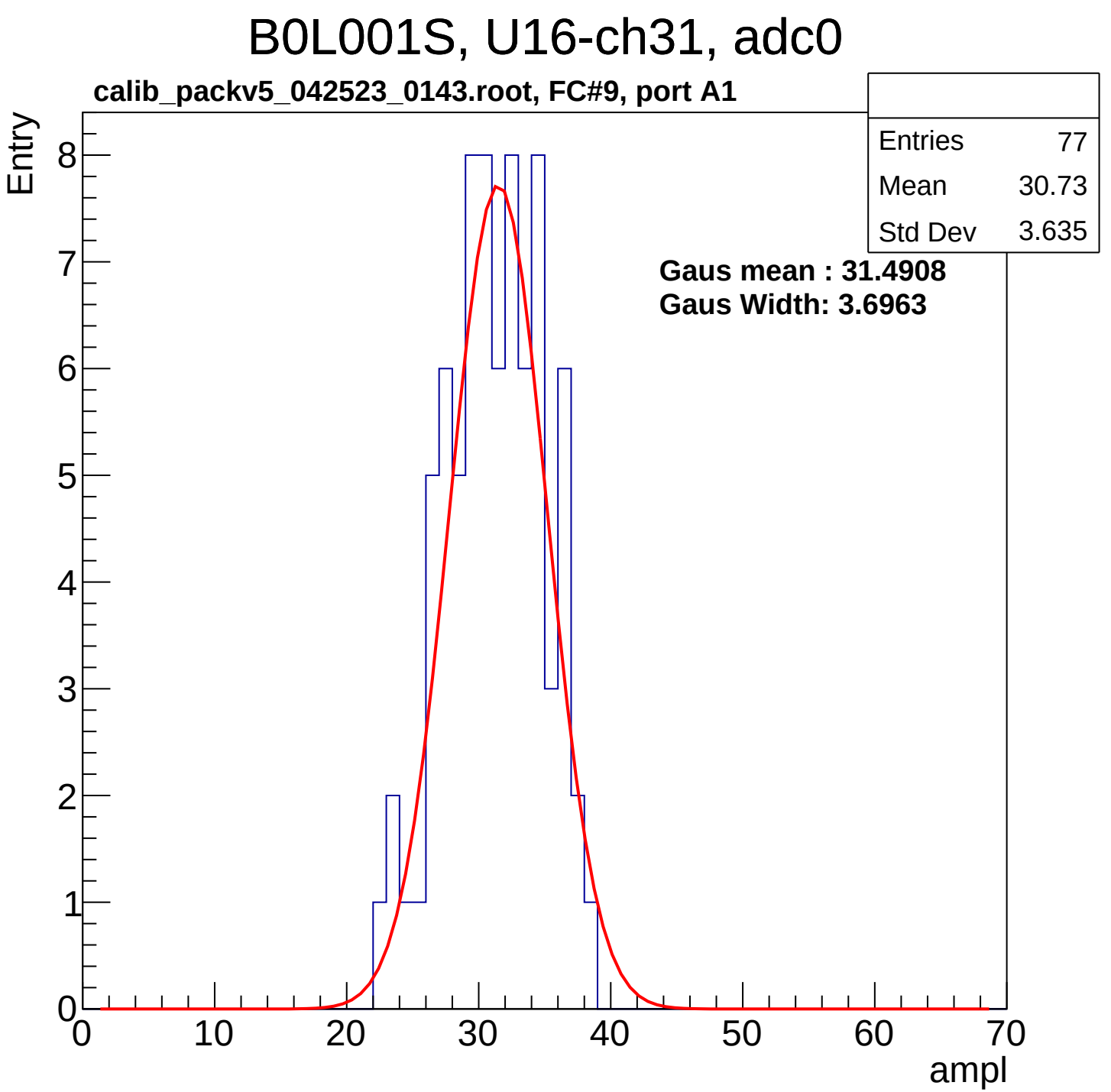
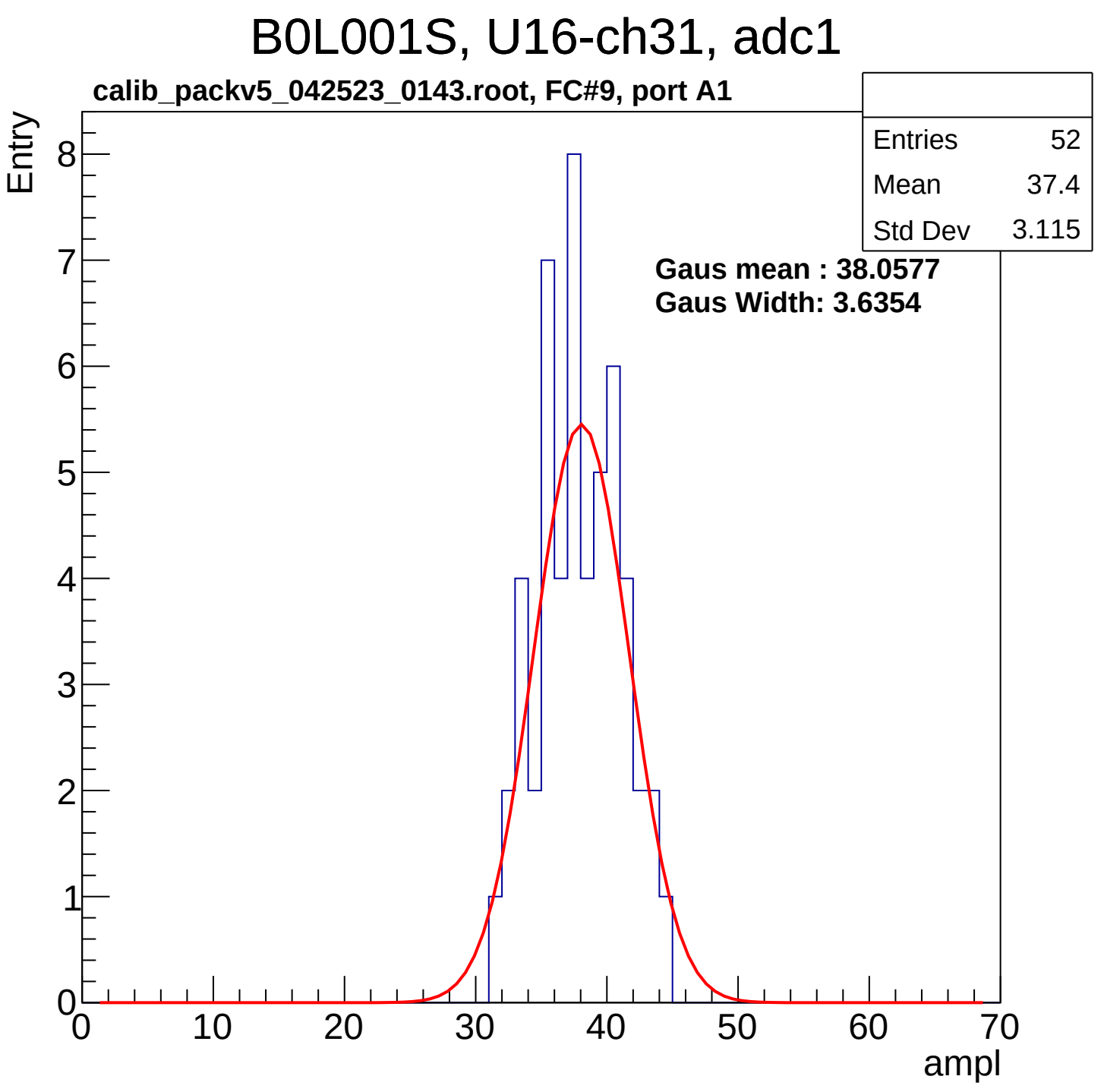
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61
Std Dev 1.581
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.73
Std Dev 3.635
Gaus mean : 31.4908
Gaus Width: 3.6963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 37.4
Std Dev 3.115
Gaus mean : 38.0577
Gaus Width: 3.6354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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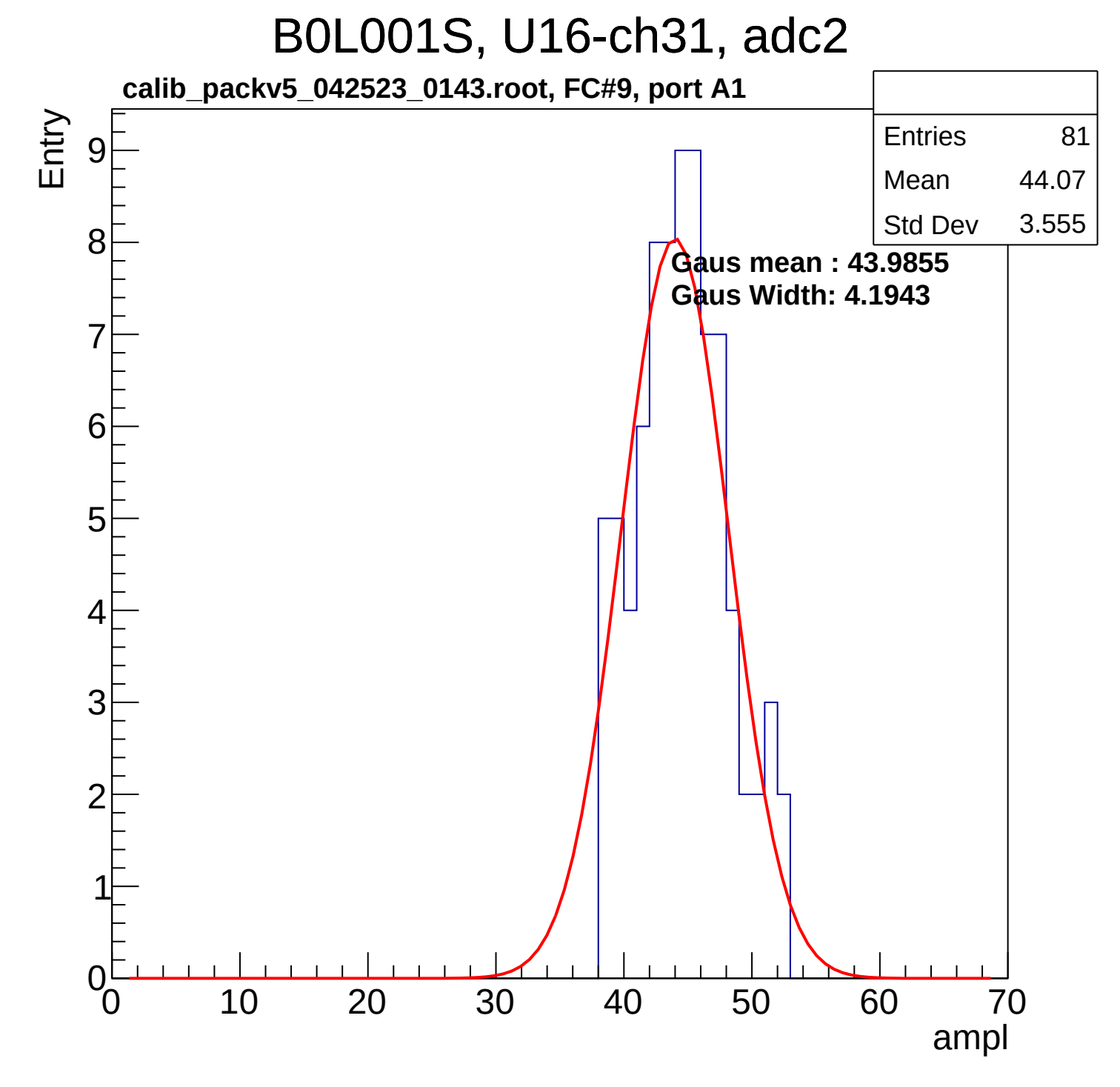
B0L001S, U16-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 44.07
Std Dev 3.555
Gaus mean : 43.9855
Gaus Width: 4.1943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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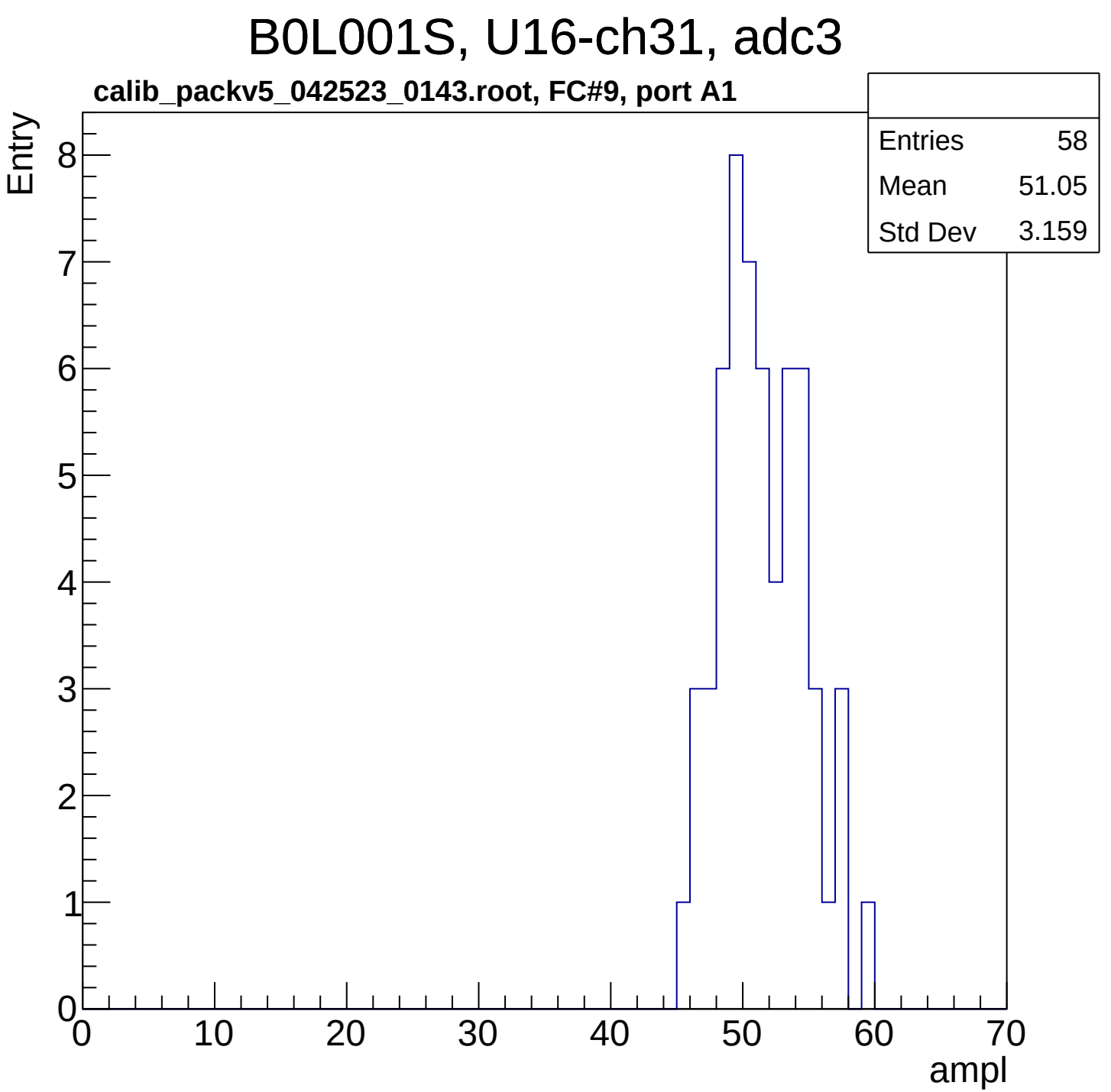
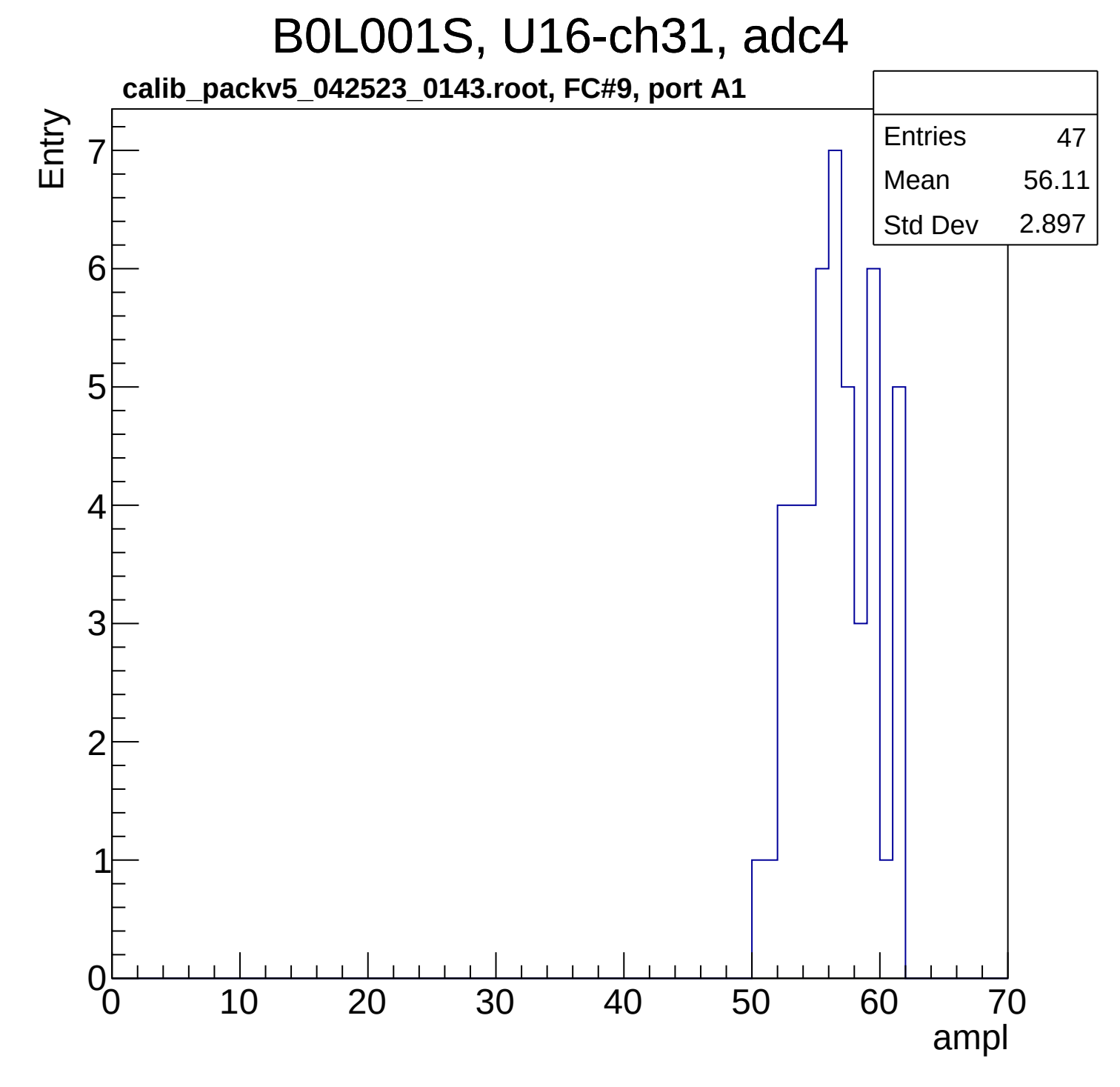
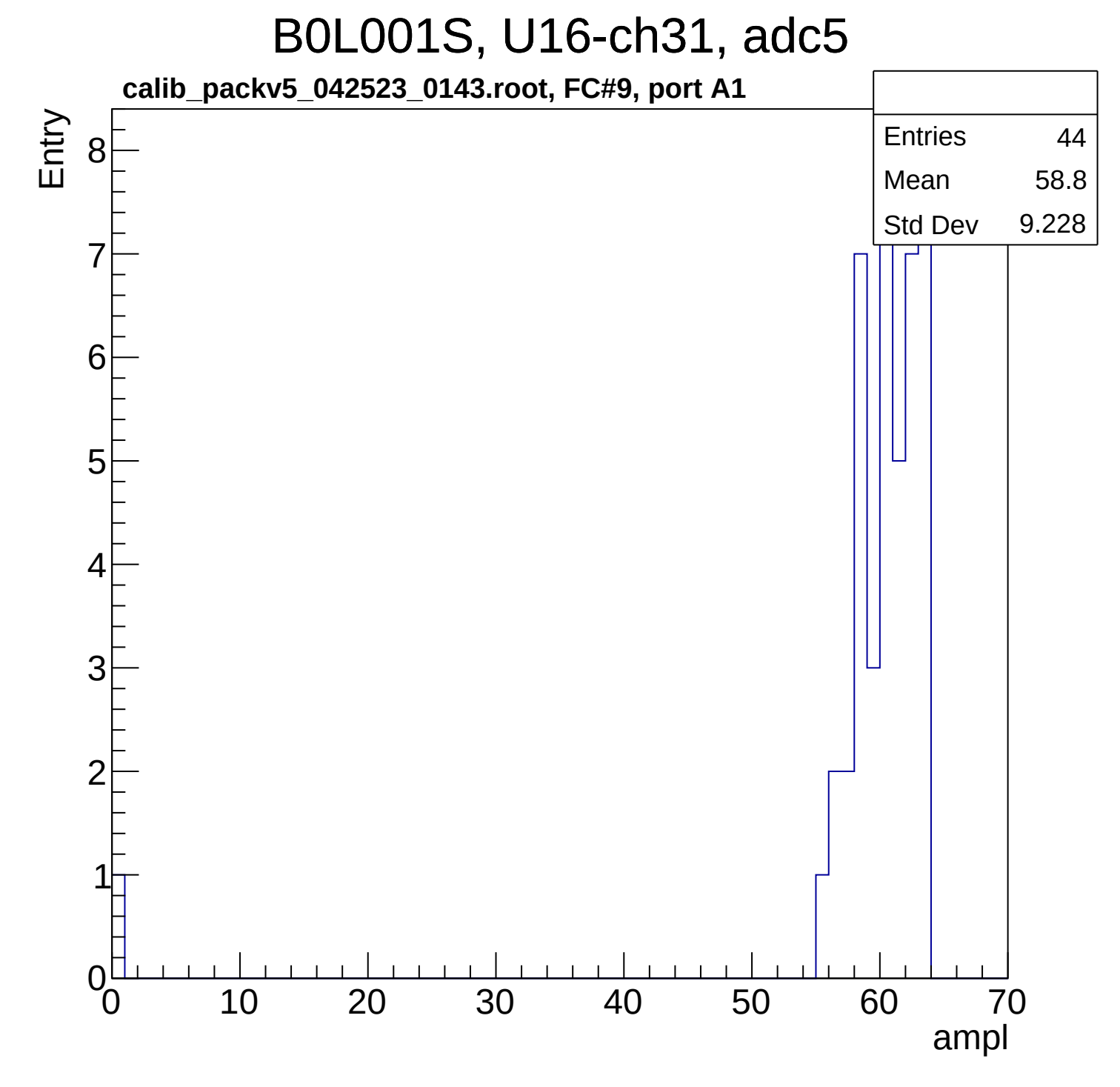
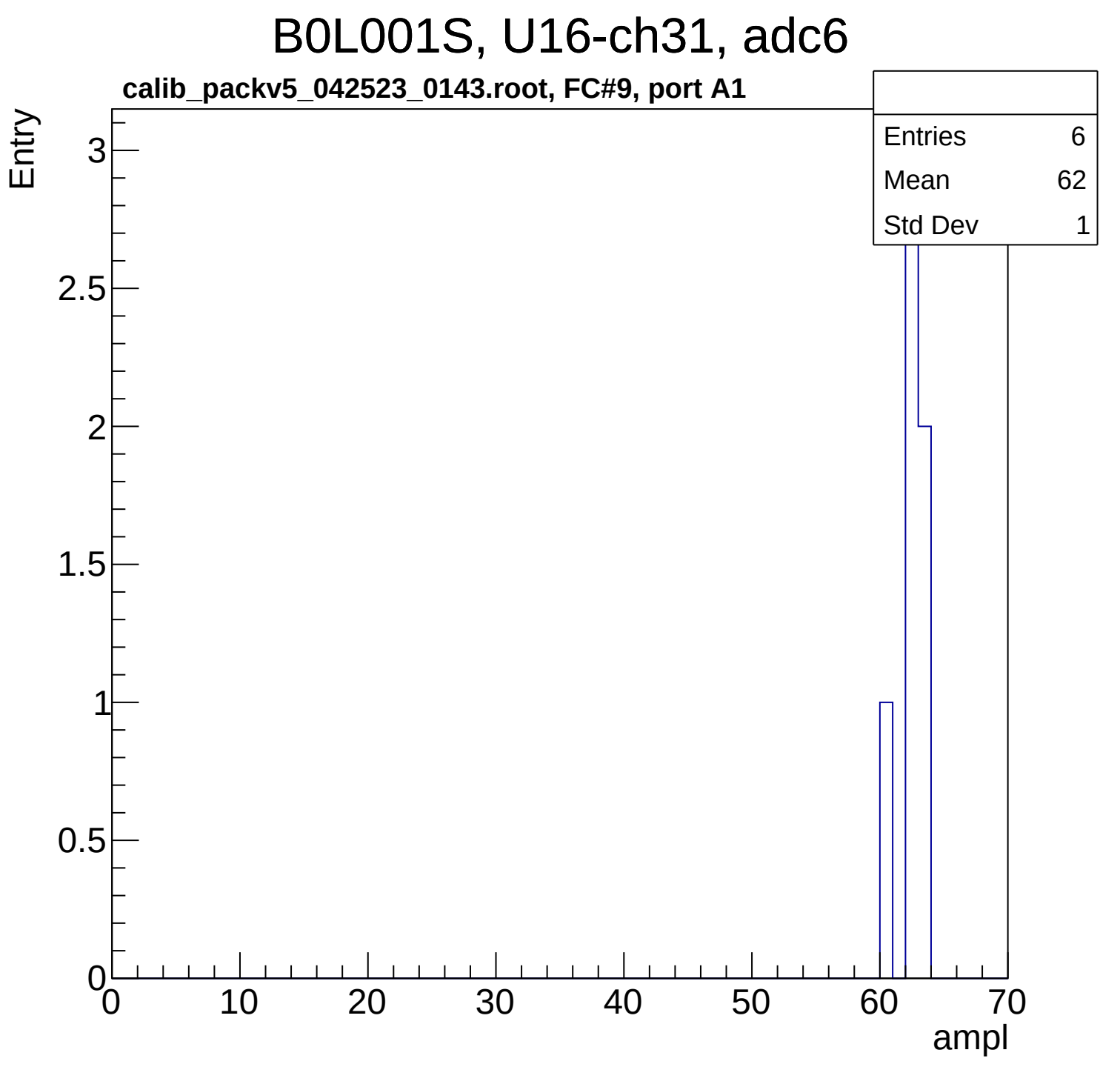
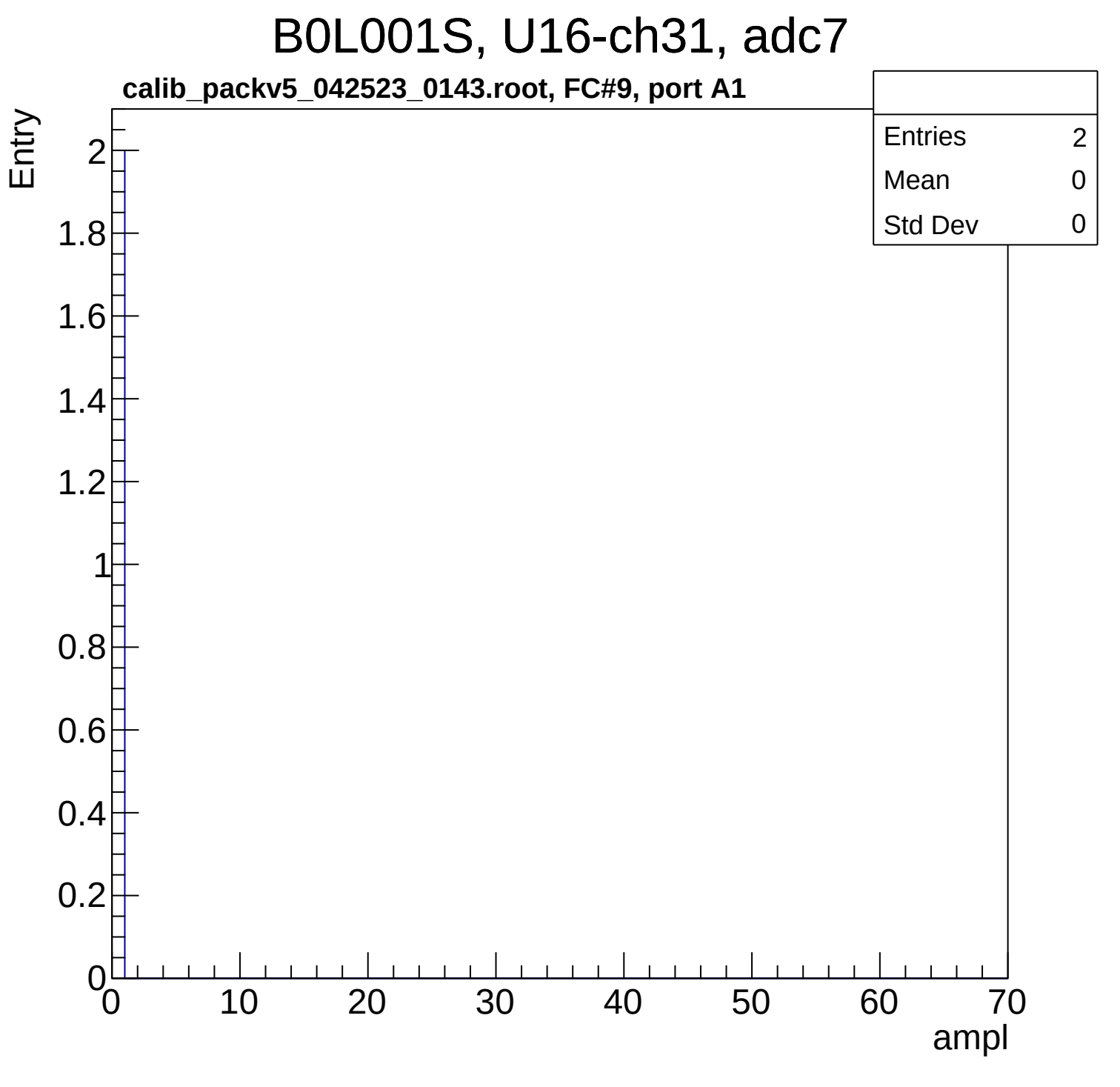
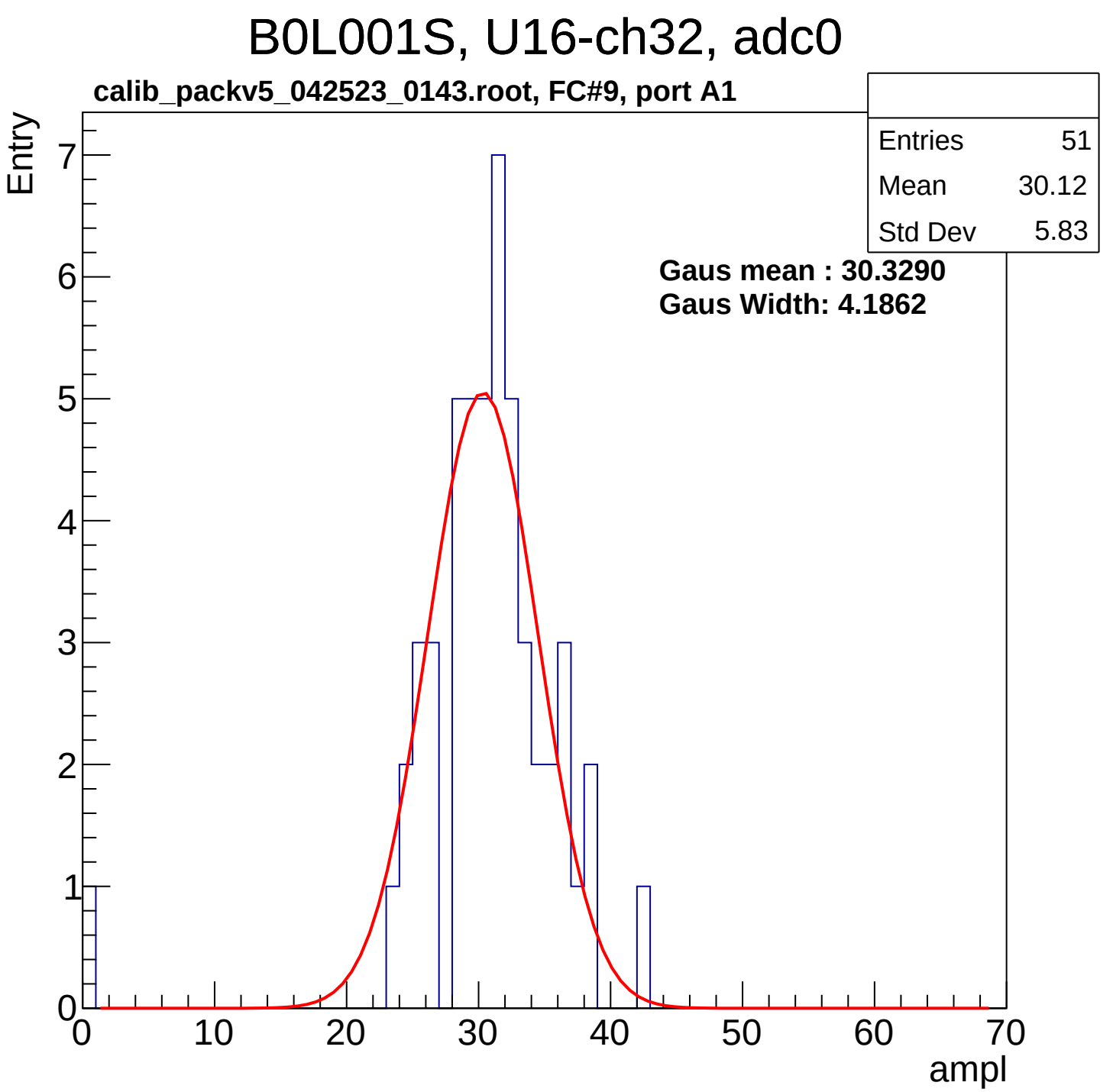

B0L001S, U16-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 51.05
Std Dev 3.159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

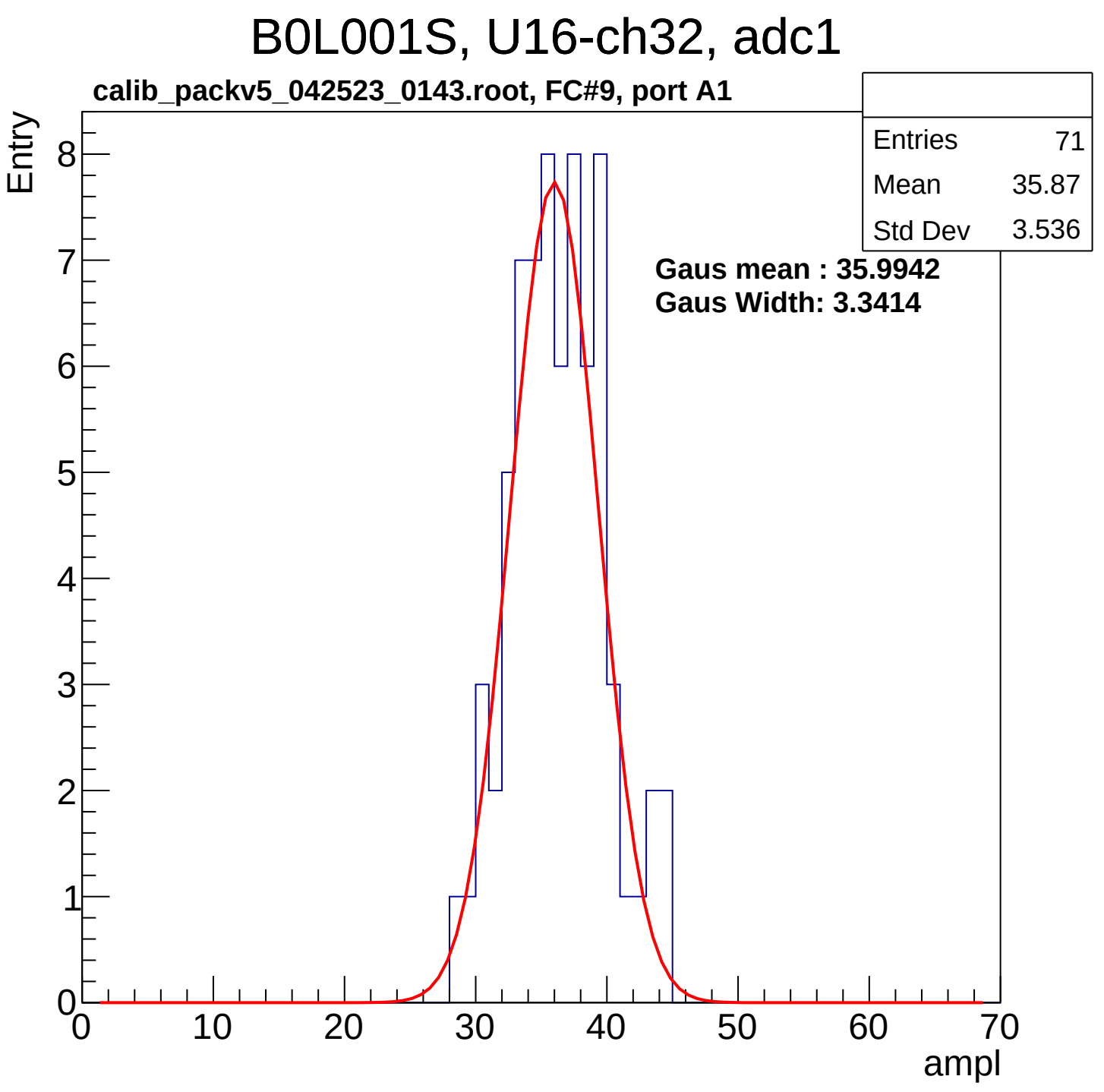

B0L001S, U16-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.11
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

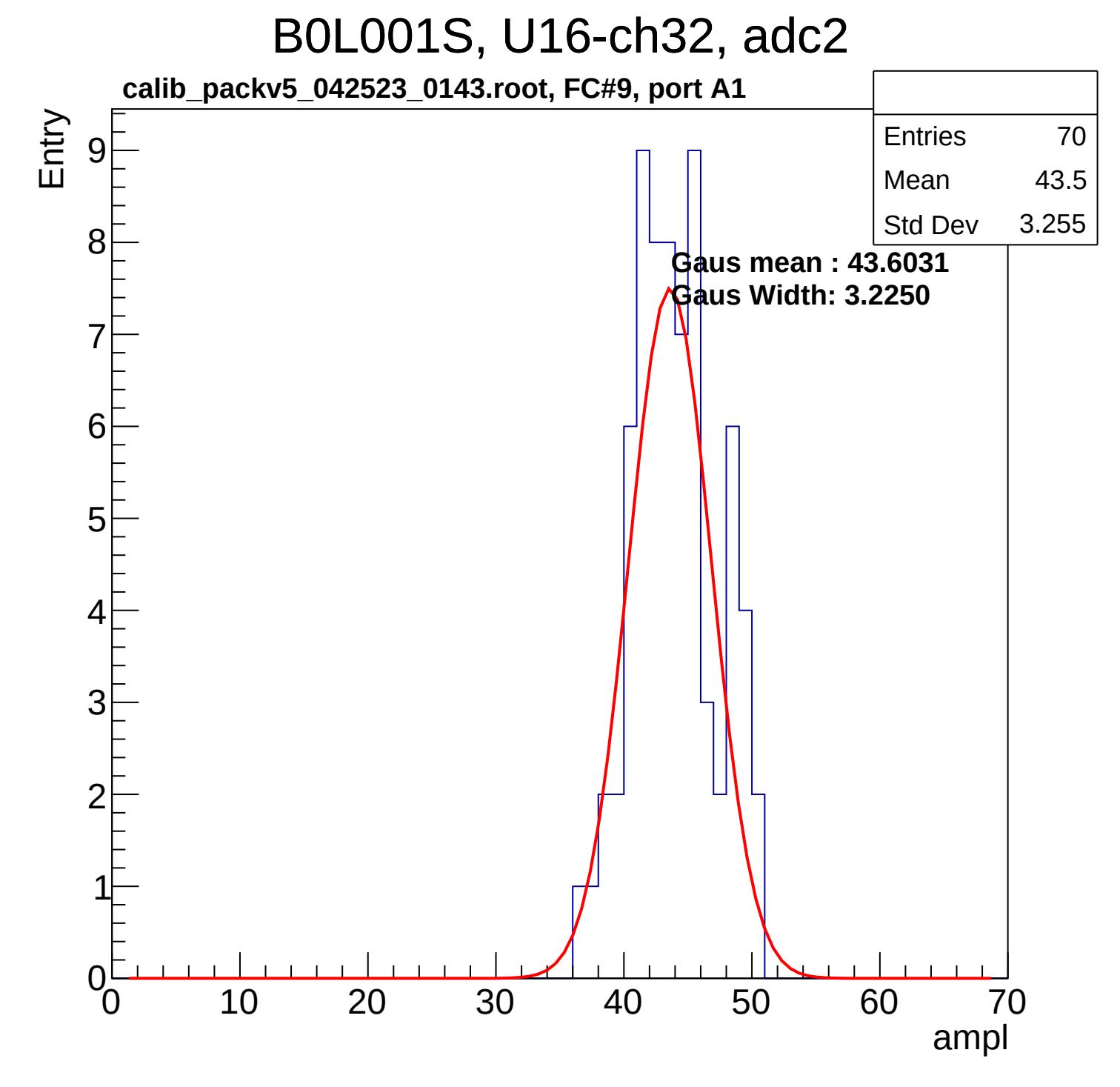

B0L001S, U16-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.8
Std Dev 9.228
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

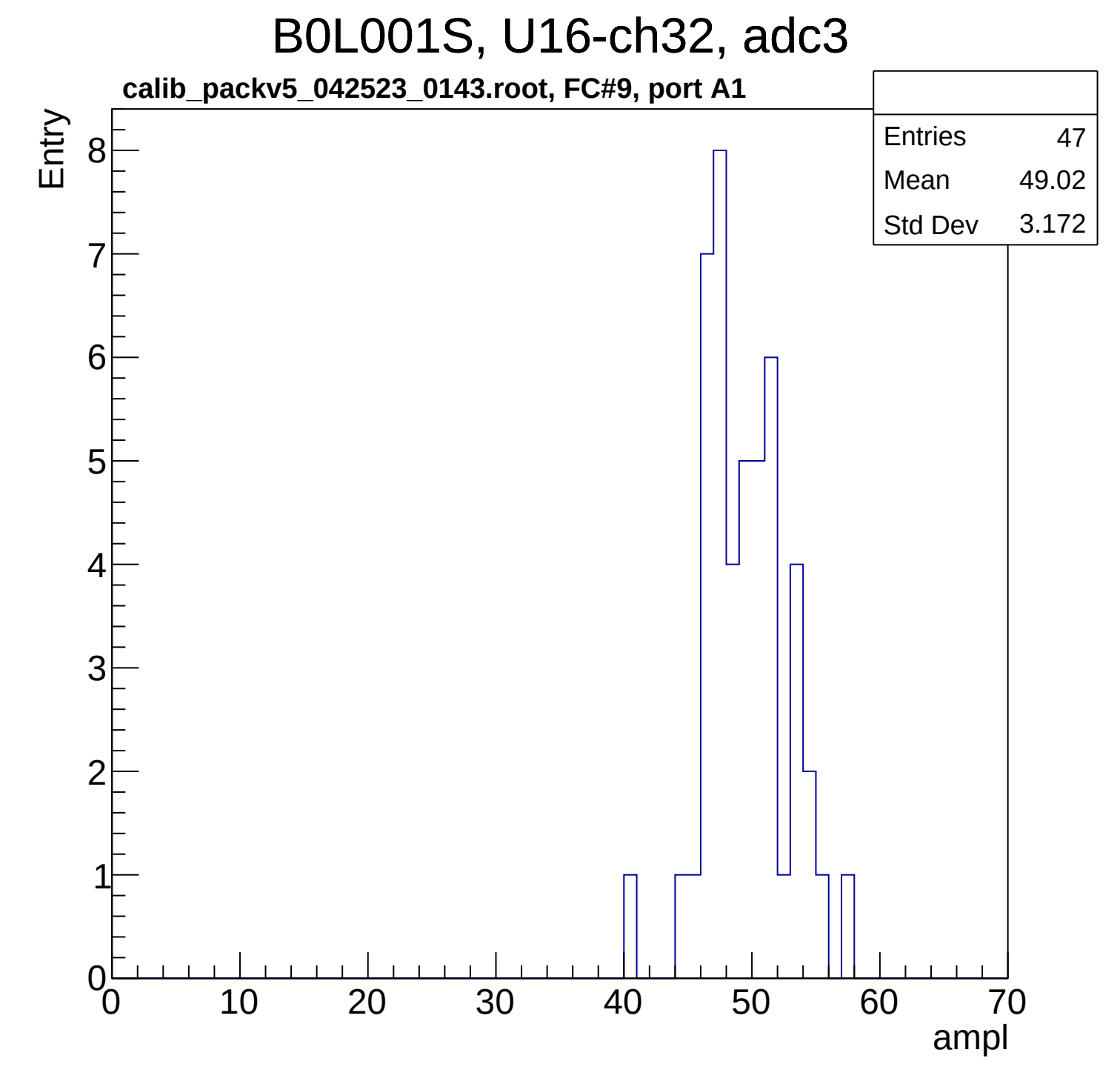

B0L001S, U16-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

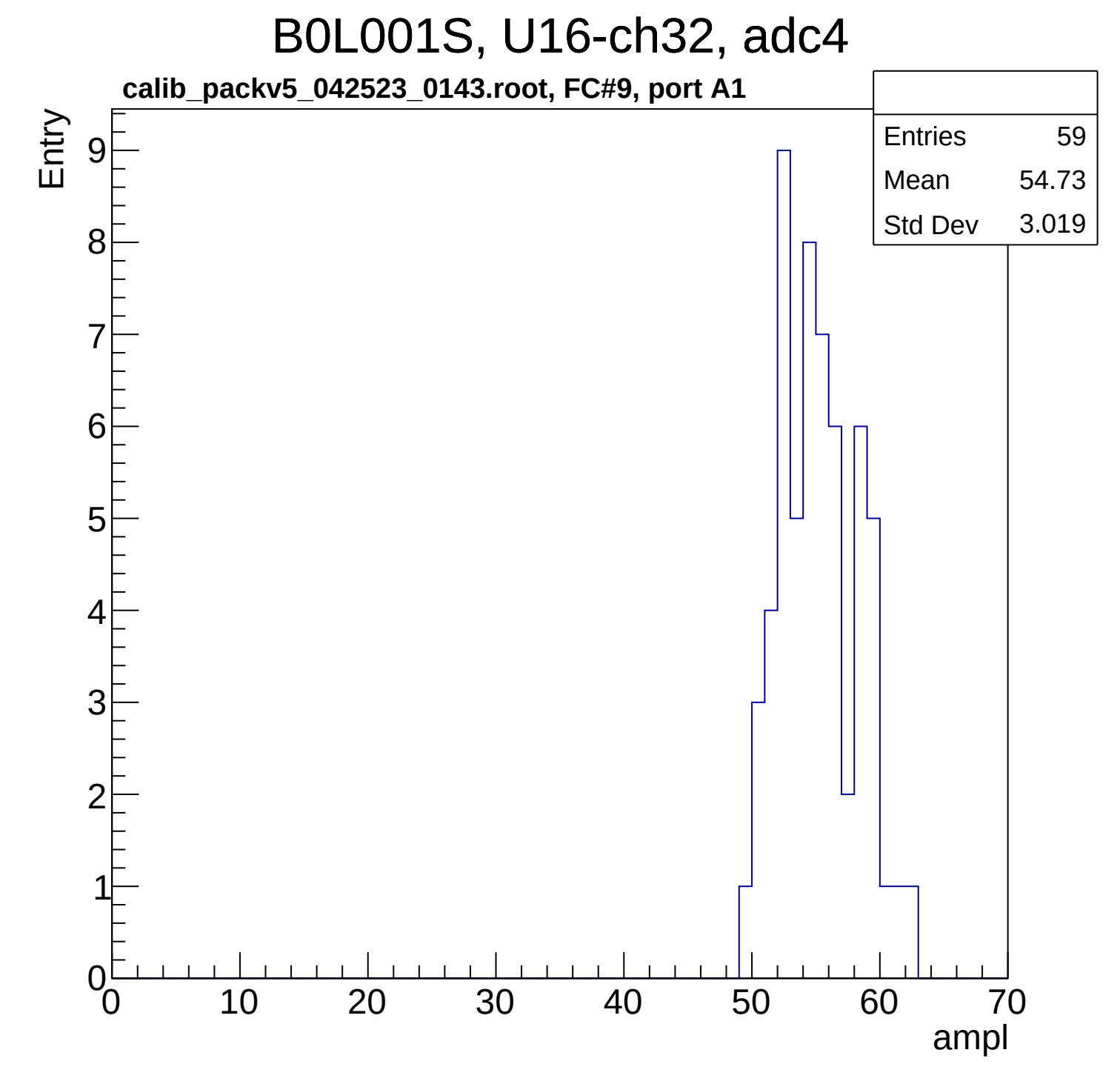

B0L001S, U16-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

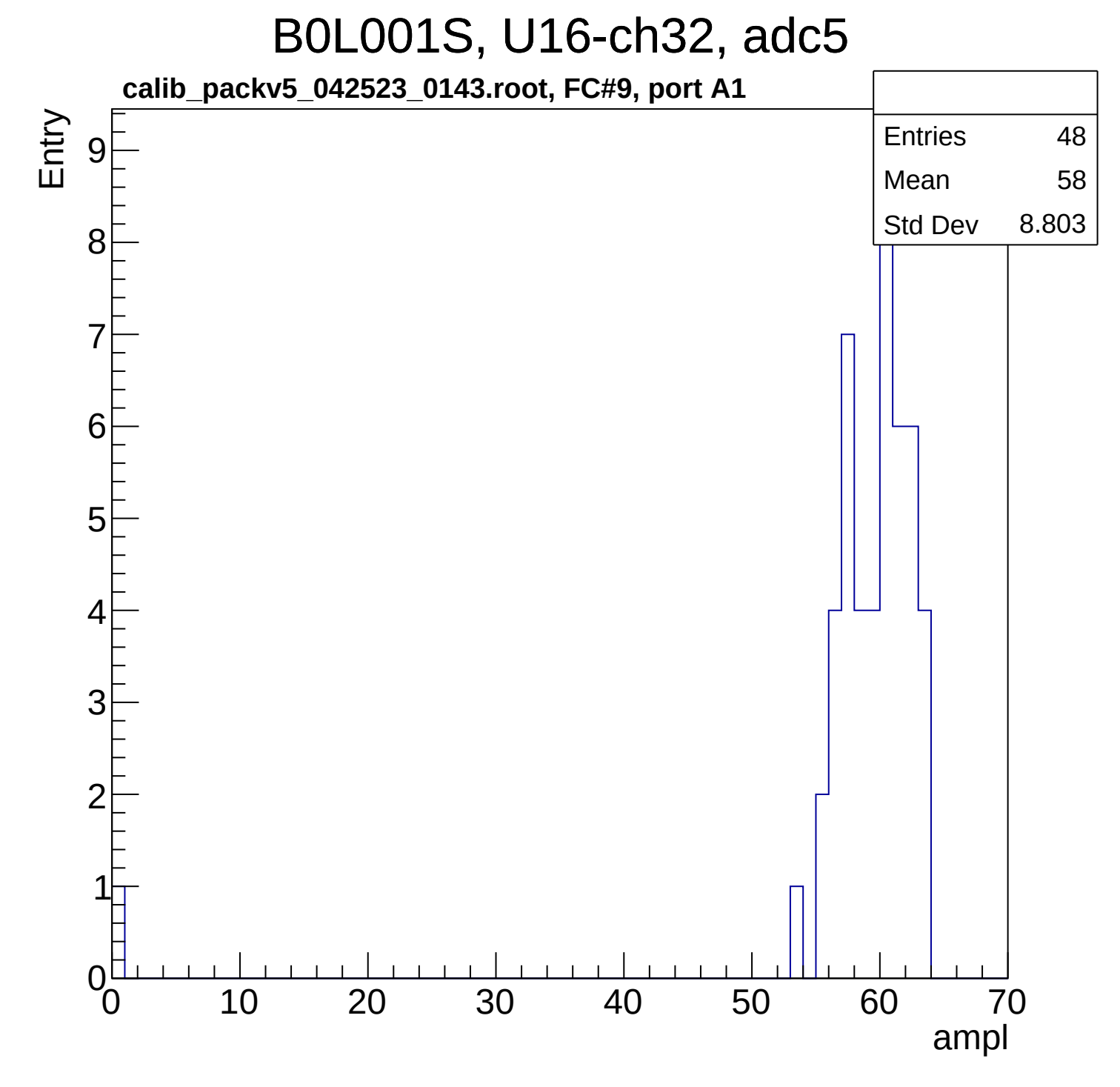

B0L001S, U16-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 30.12
Std Dev 5.83
Gaus mean : 30.3290
Gaus Width: 4.1862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

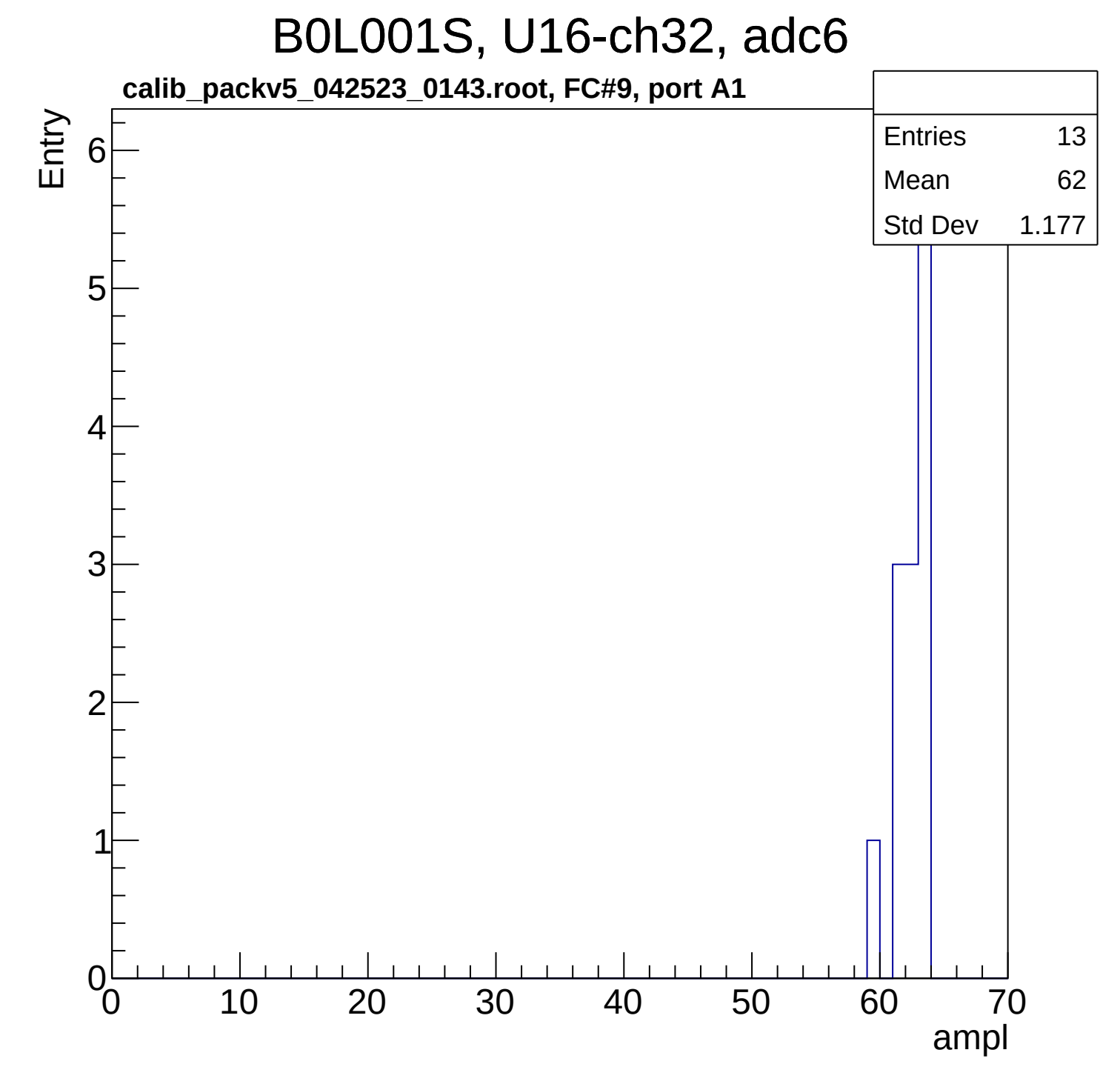

B0L001S, U16-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 35.87
Std Dev 3.536
Gaus mean : 35.9942
Gaus Width: 3.3414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

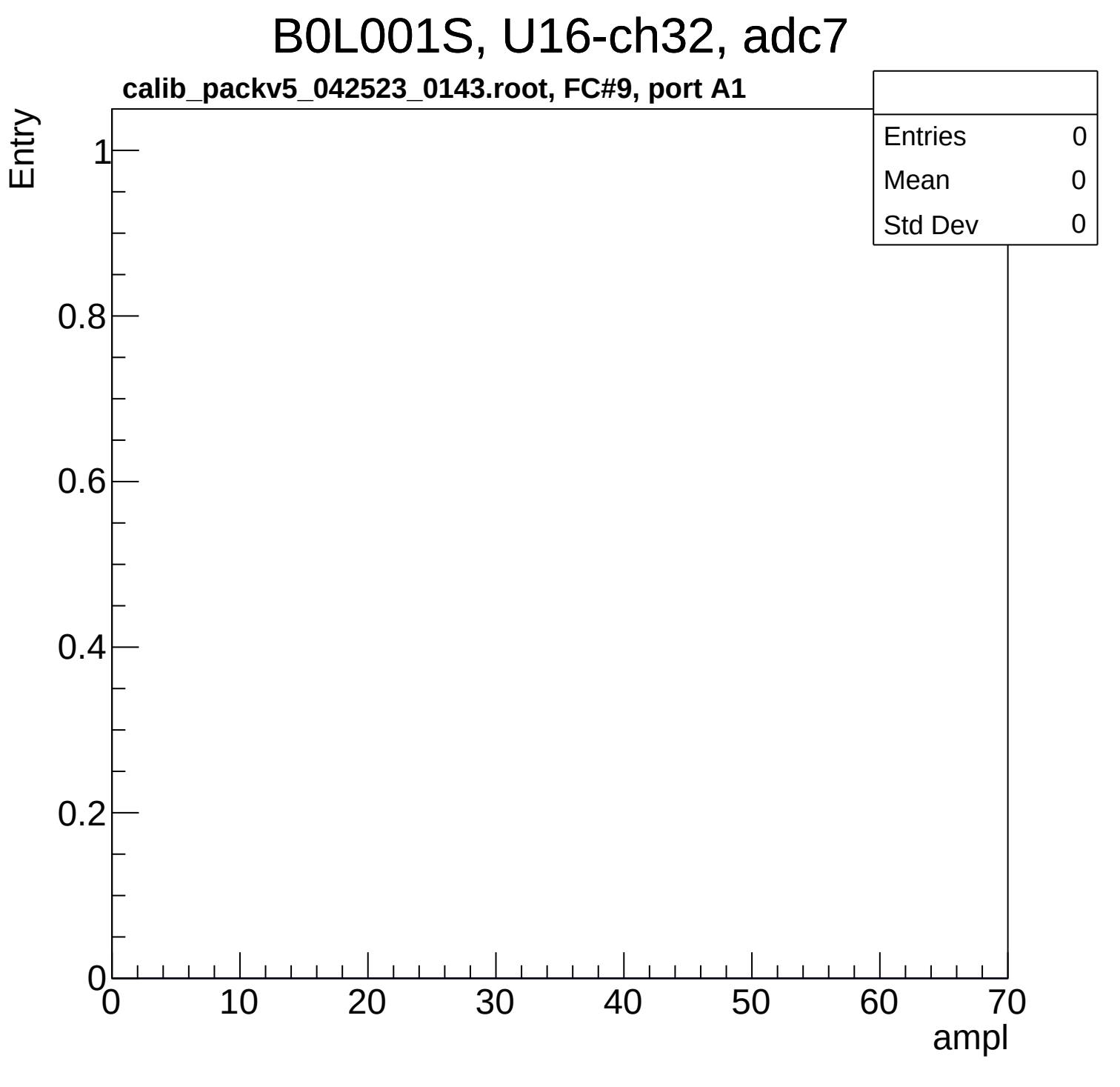

B0L001S, U16-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.5
Std Dev 3.255
Gaus mean : 43.6031
Gaus Width: 3.2250
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

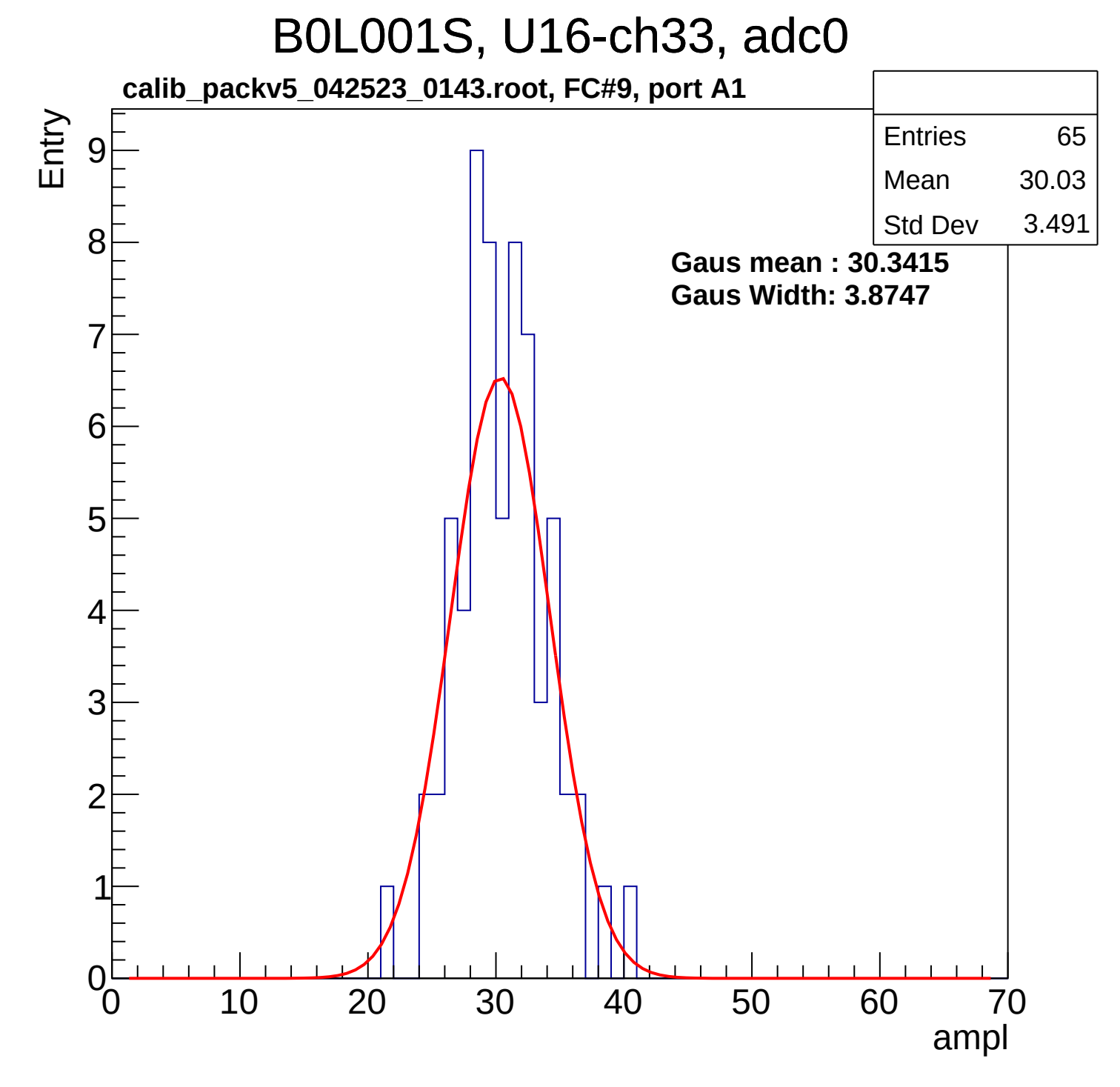

B0L001S, U16-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 49.02
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

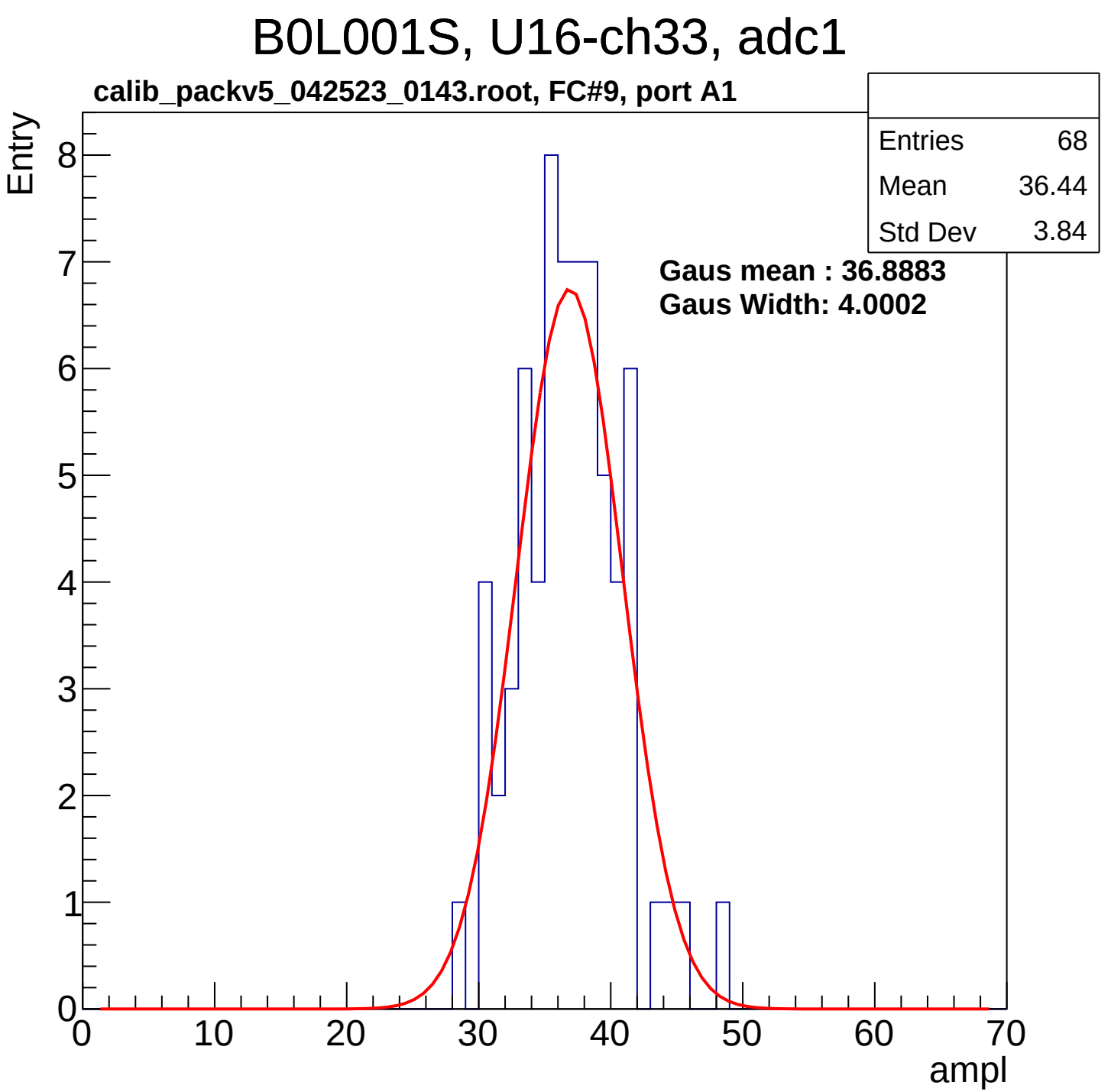

B0L001S, U16-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 54.73
Std Dev 3.019
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

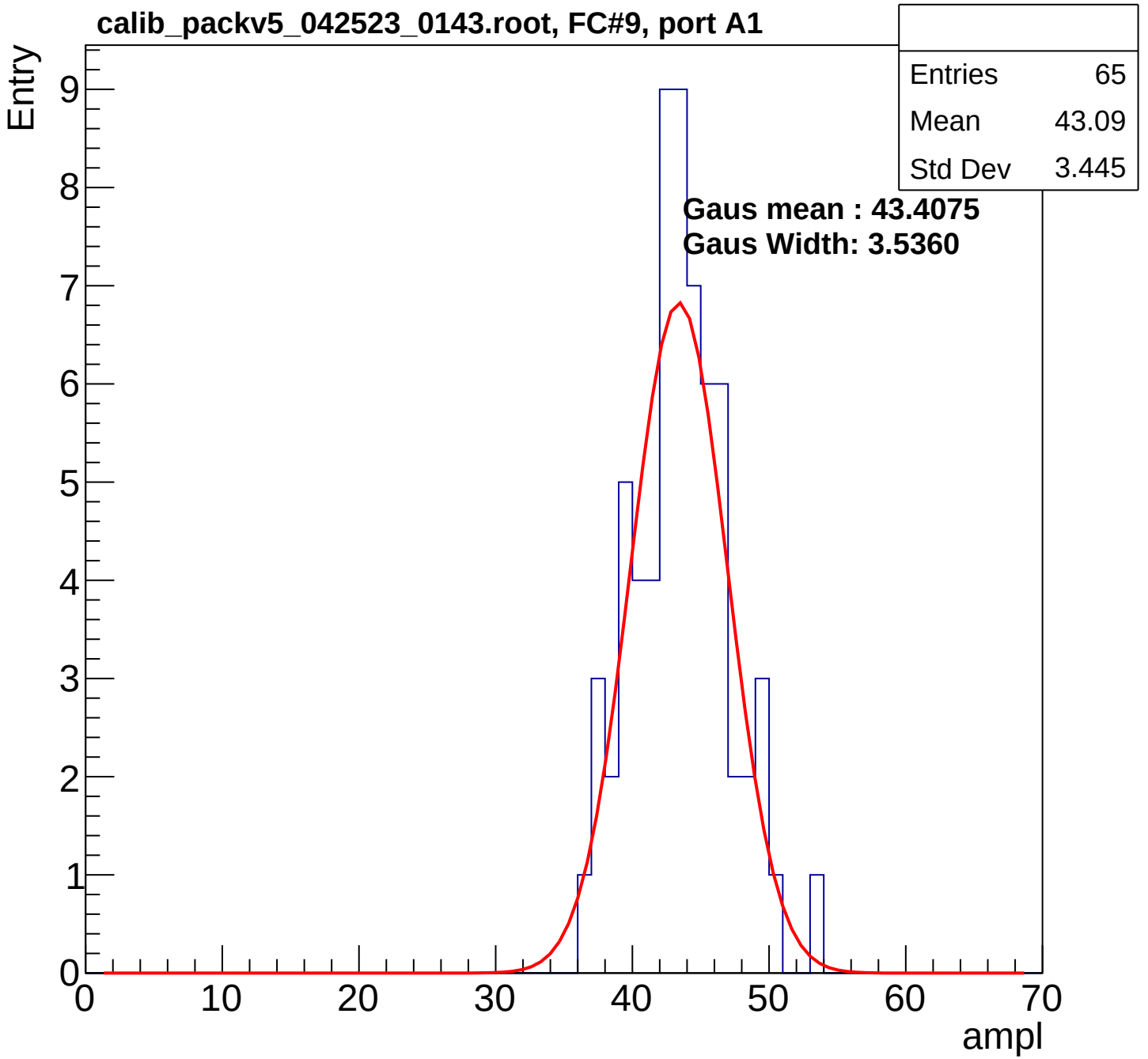

B0L001S, U16-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58
Std Dev 8.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 62
Std Dev 1.177
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.03
Std Dev 3.491
Gaus mean : 30.3415
Gaus Width: 3.8747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.44
Std Dev 3.84
Gaus mean : 36.8883
Gaus Width: 4.0002
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.09
Std Dev 3.445
Gaus mean : 43.4075
Gaus Width: 3.5360
Entry
ampl
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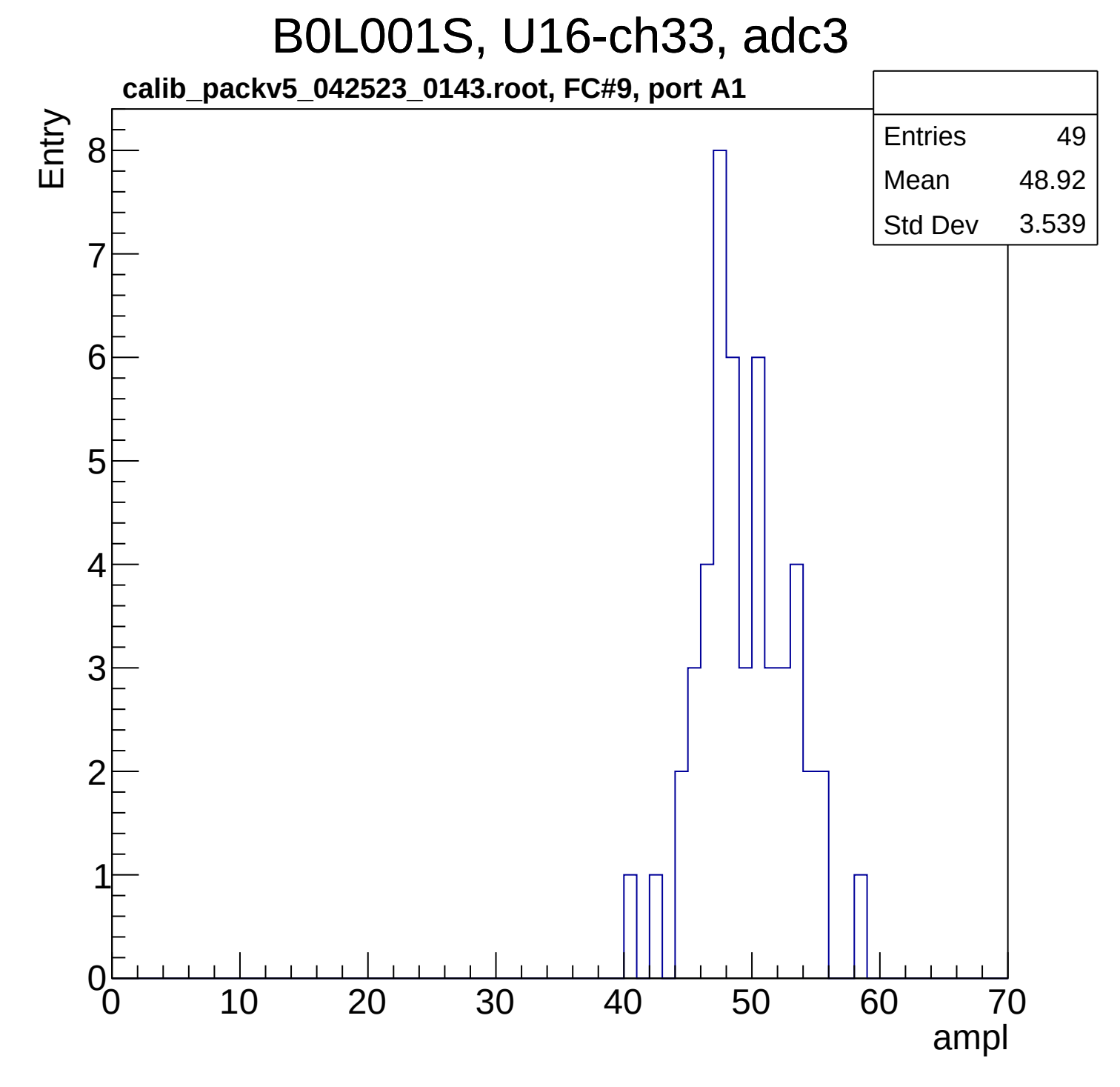
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 48.92
Std Dev 3.539
Entry
ampl
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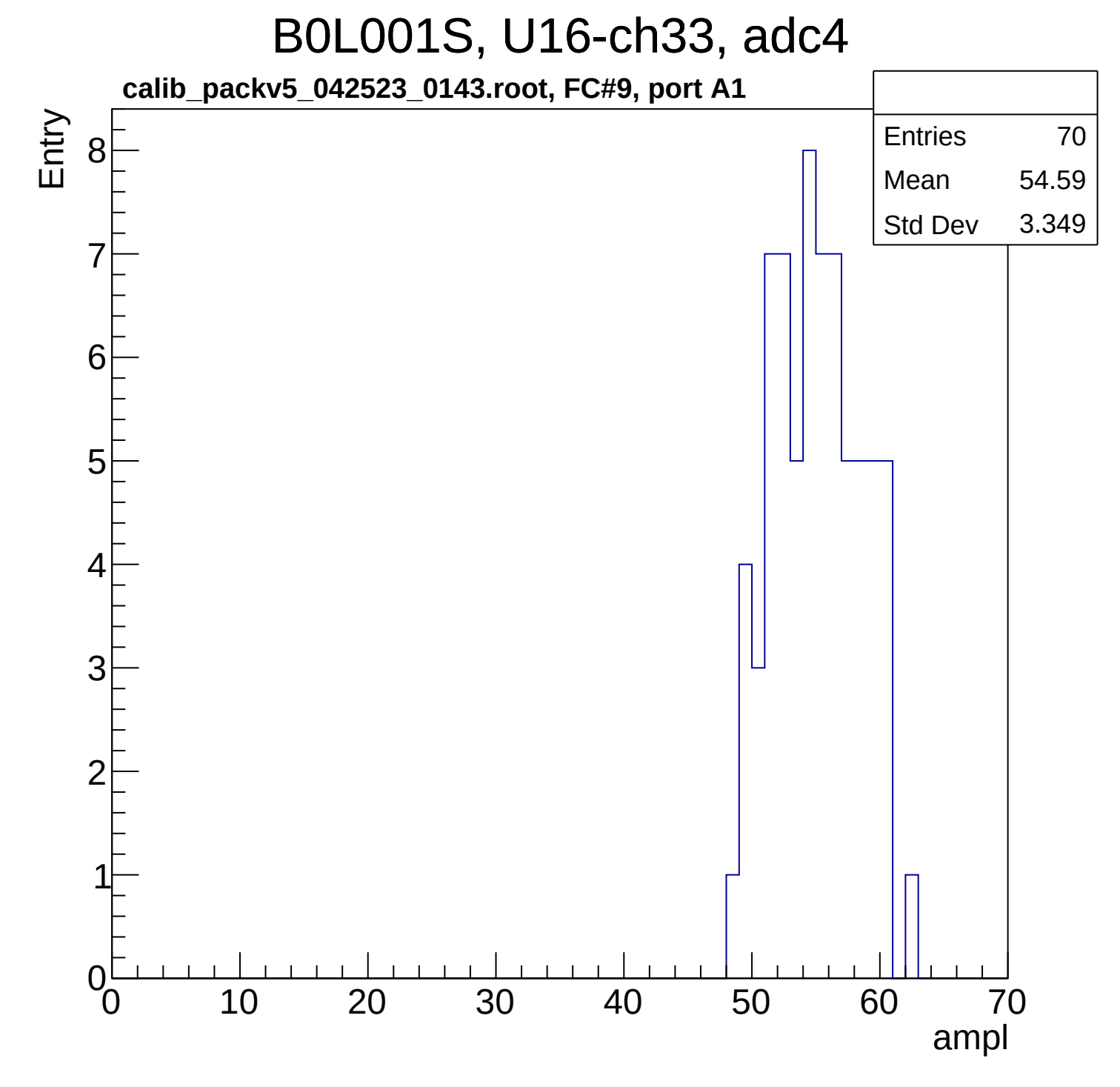
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.59
Std Dev 3.349
Entry
ampl
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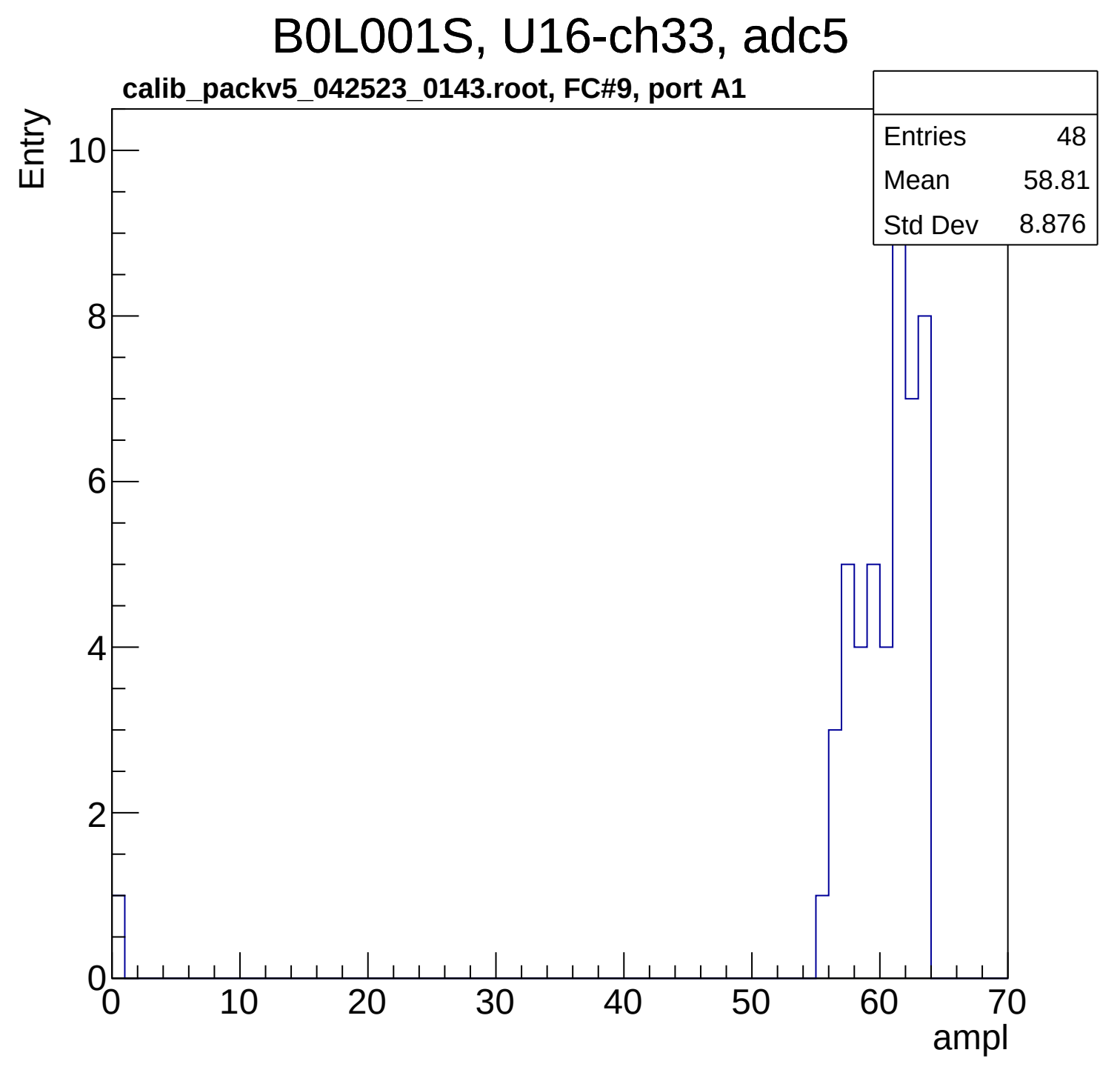
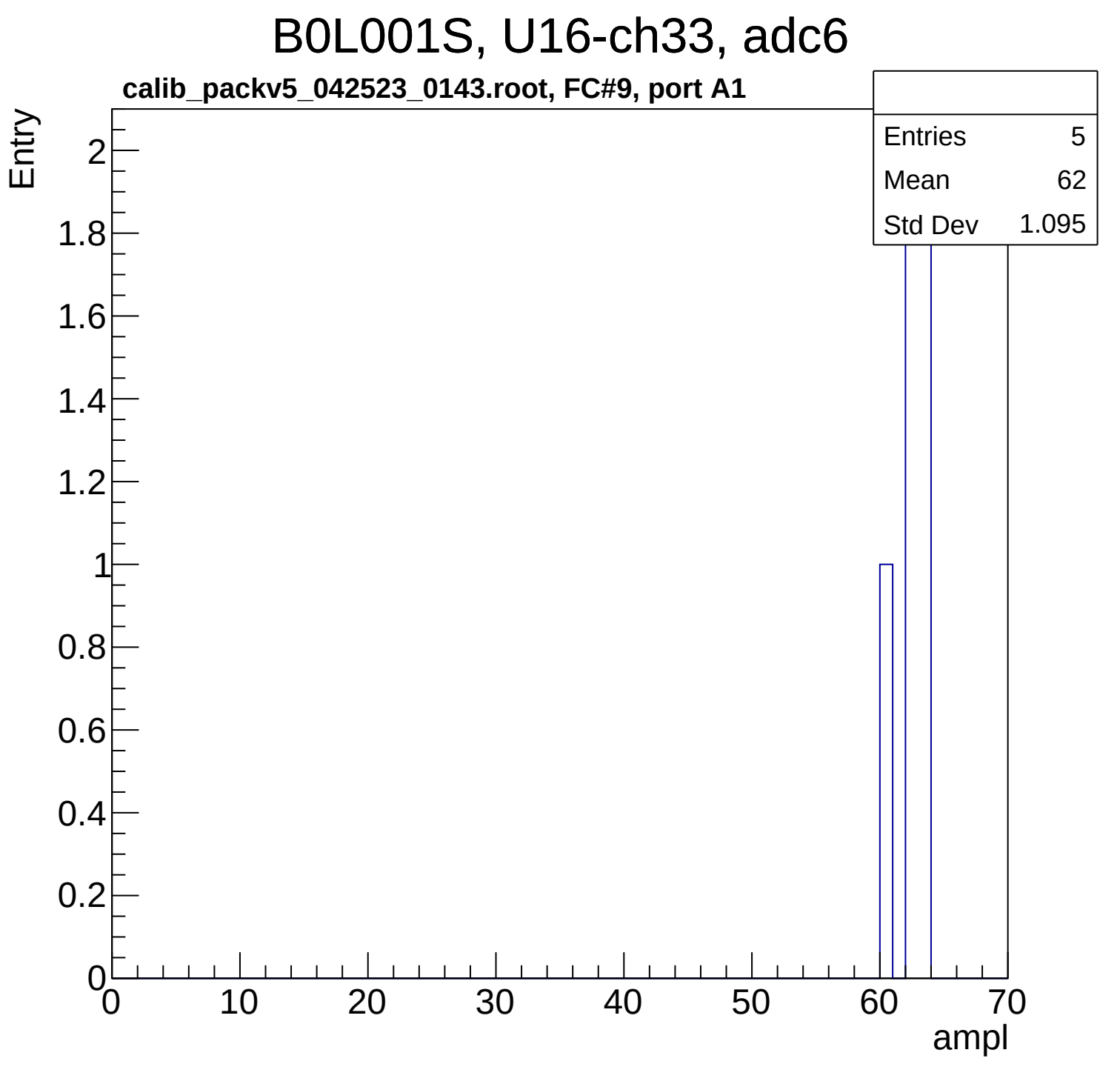
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.81
Std Dev 8.876
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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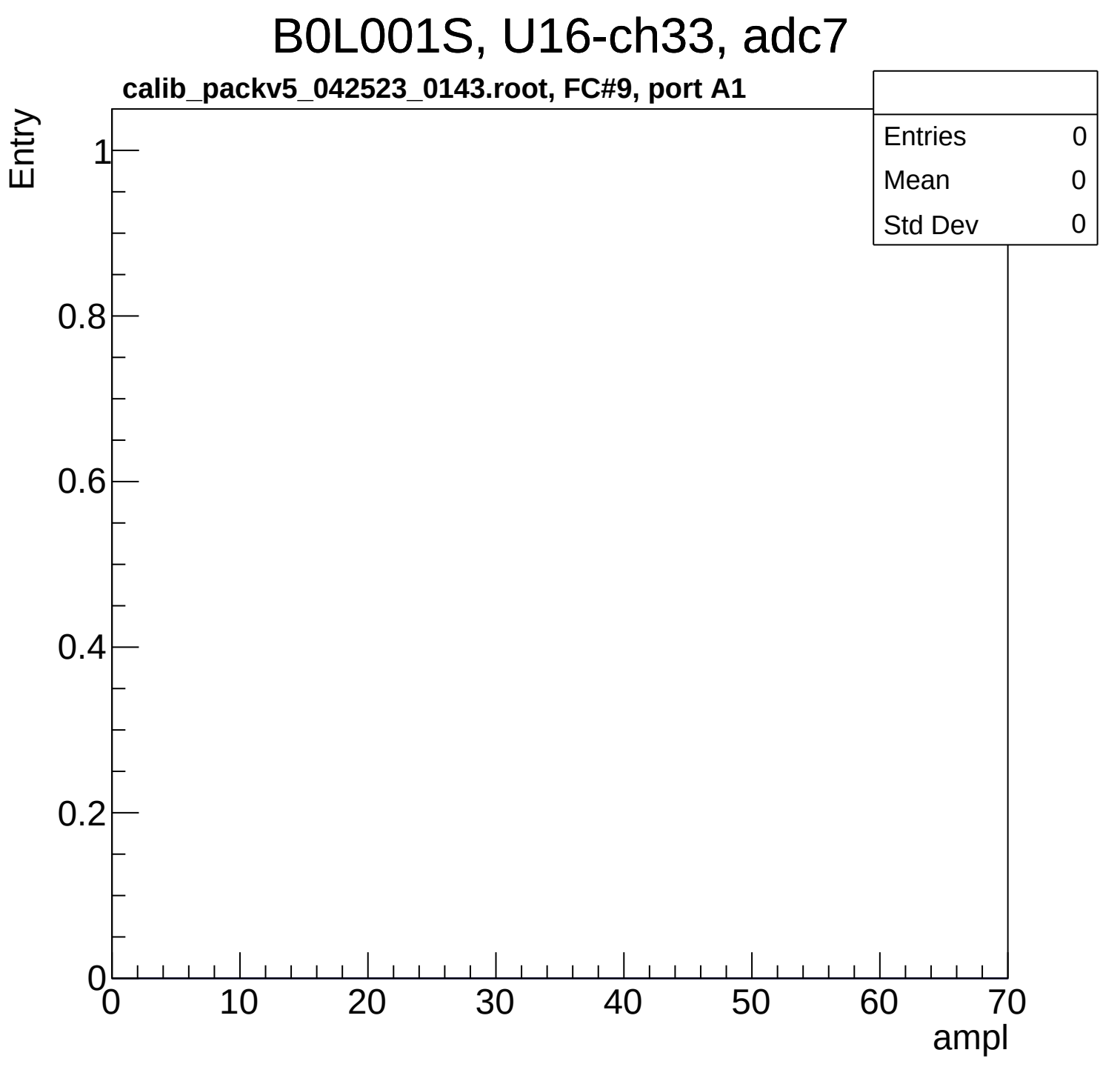
60
70


B0L001S, U16-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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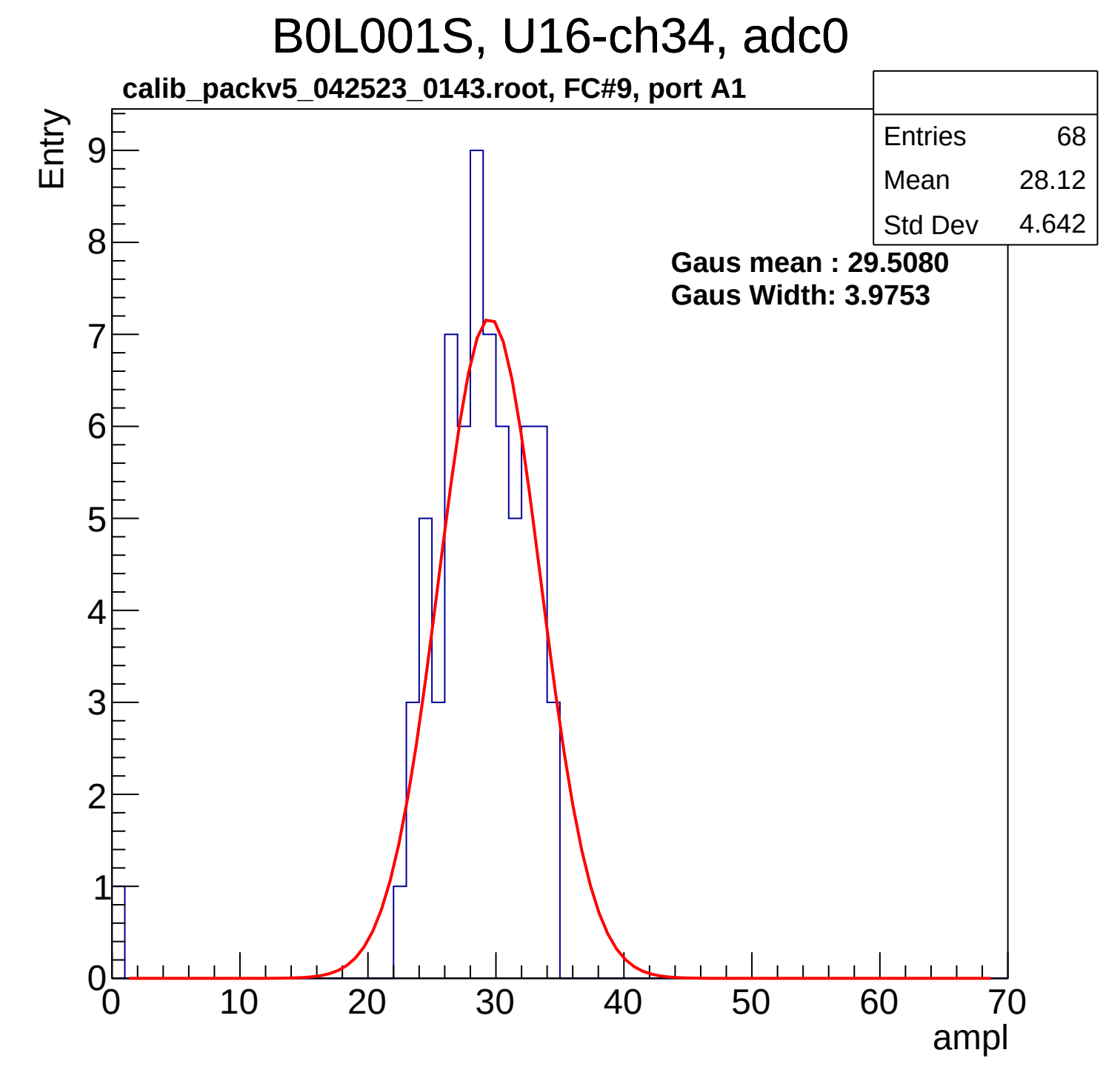
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 28.12
Std Dev 4.642
Gaus mean : 29.5080
Gaus Width: 3.9753
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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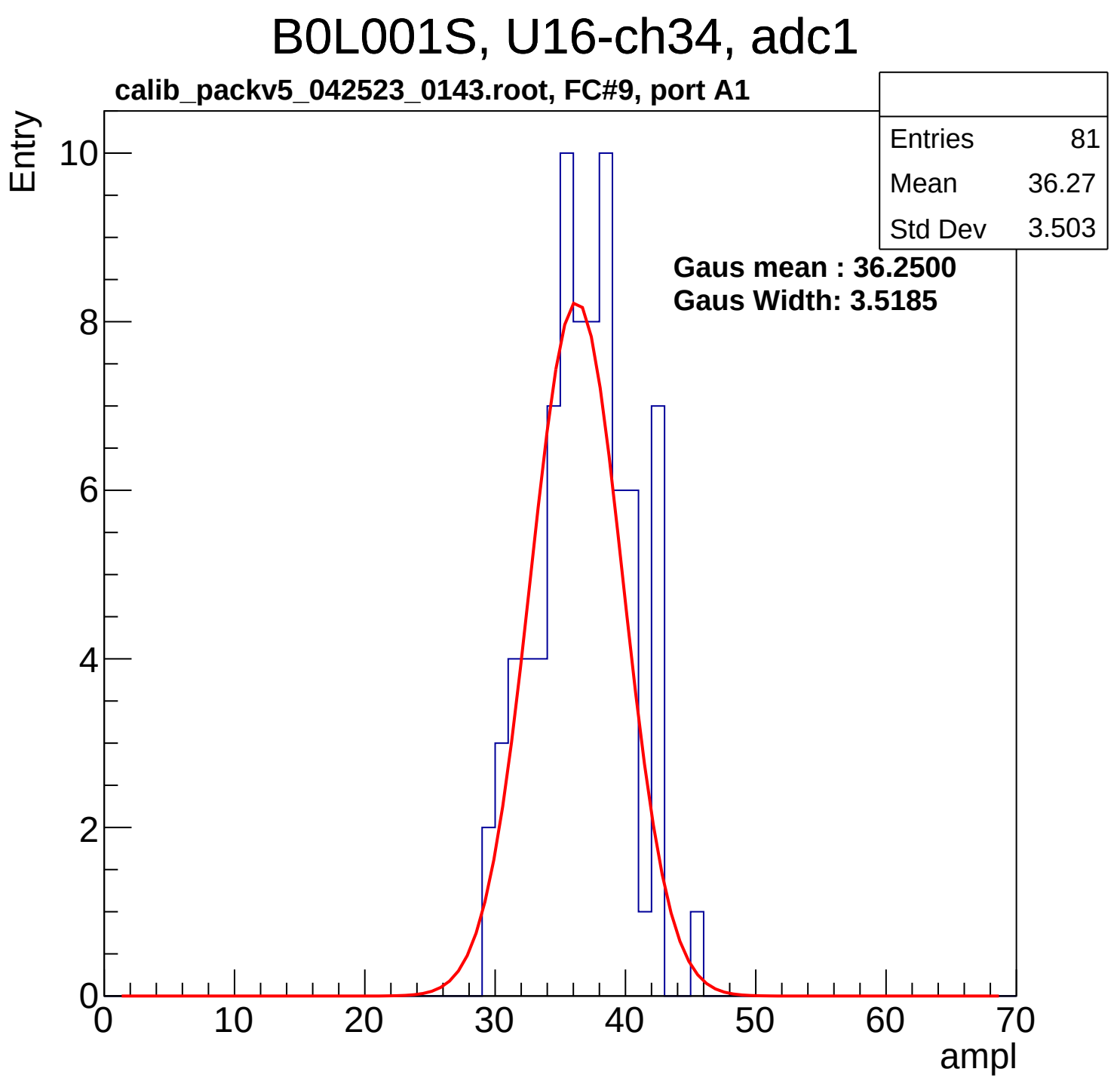
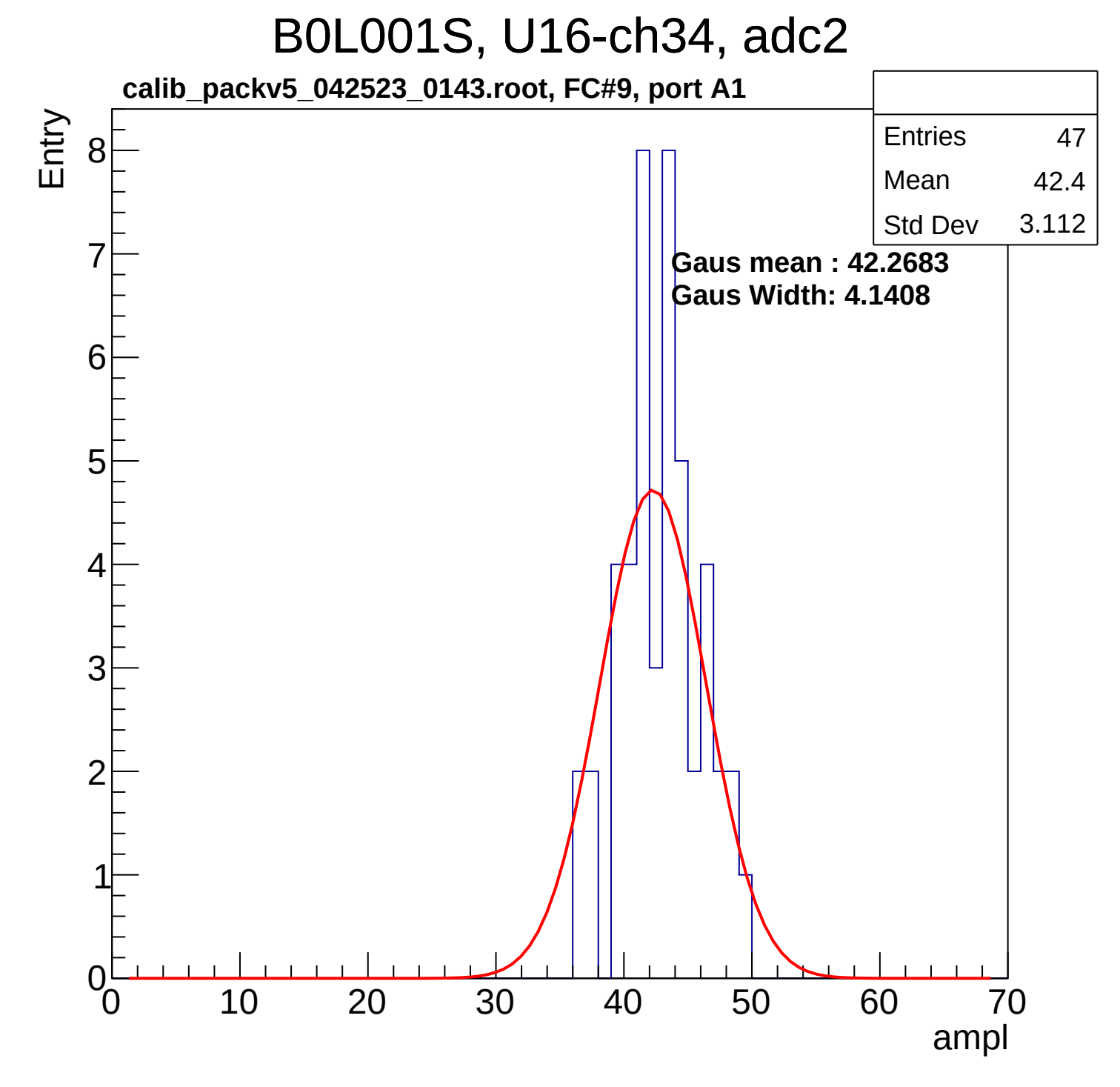
40
50
60
70


B0L001S, U16-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 36.27
Std Dev 3.503
Gaus mean : 36.2500
Gaus Width: 3.5185
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 42.4
Std Dev 3.112
Gaus mean : 42.2683
Gaus Width: 4.1408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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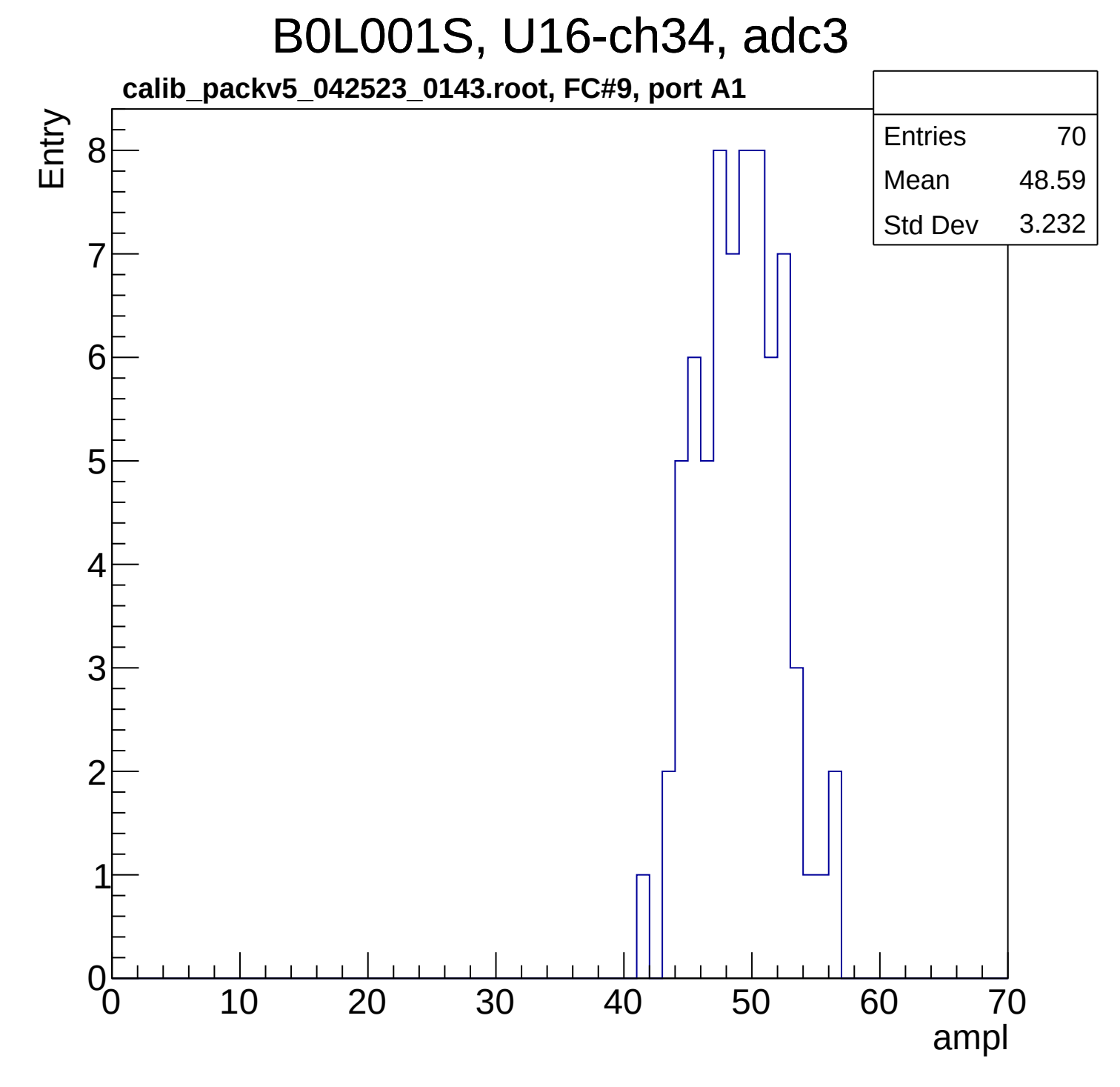
B0L001S, U16-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 48.59
Std Dev 3.232
Entry
ampl
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2
3
4
5
6
7
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0
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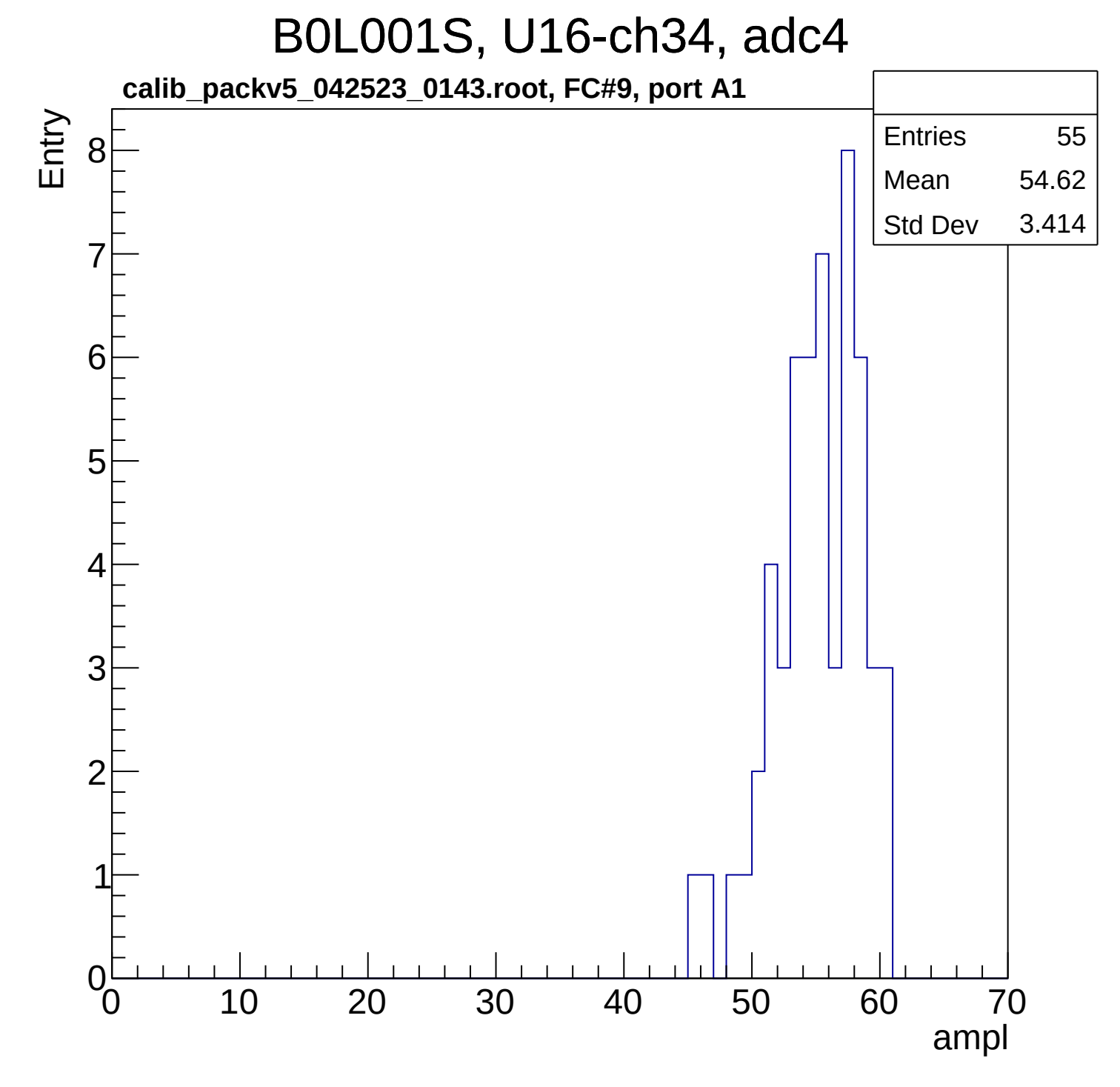
B0L001S, U16-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.62
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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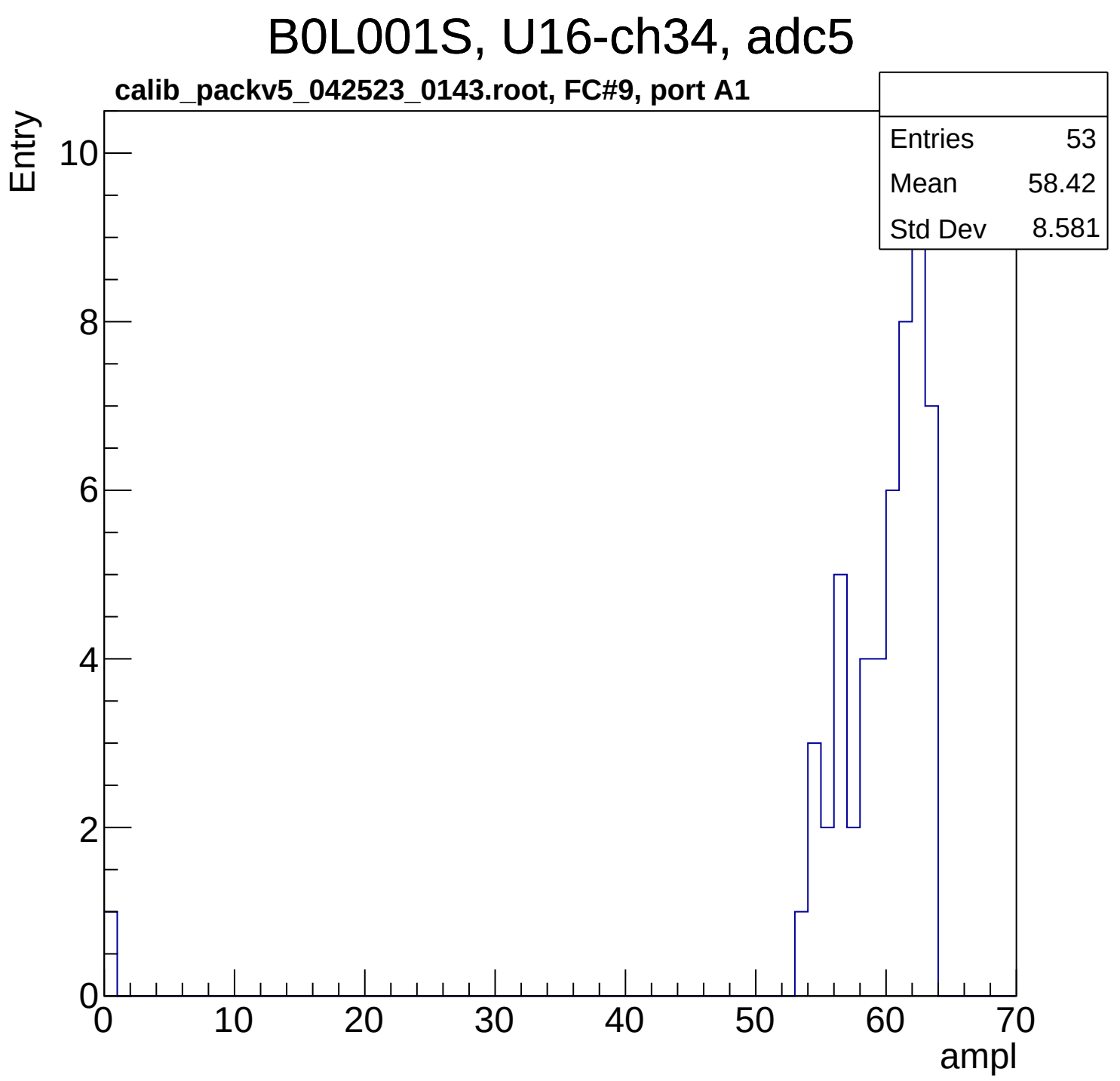
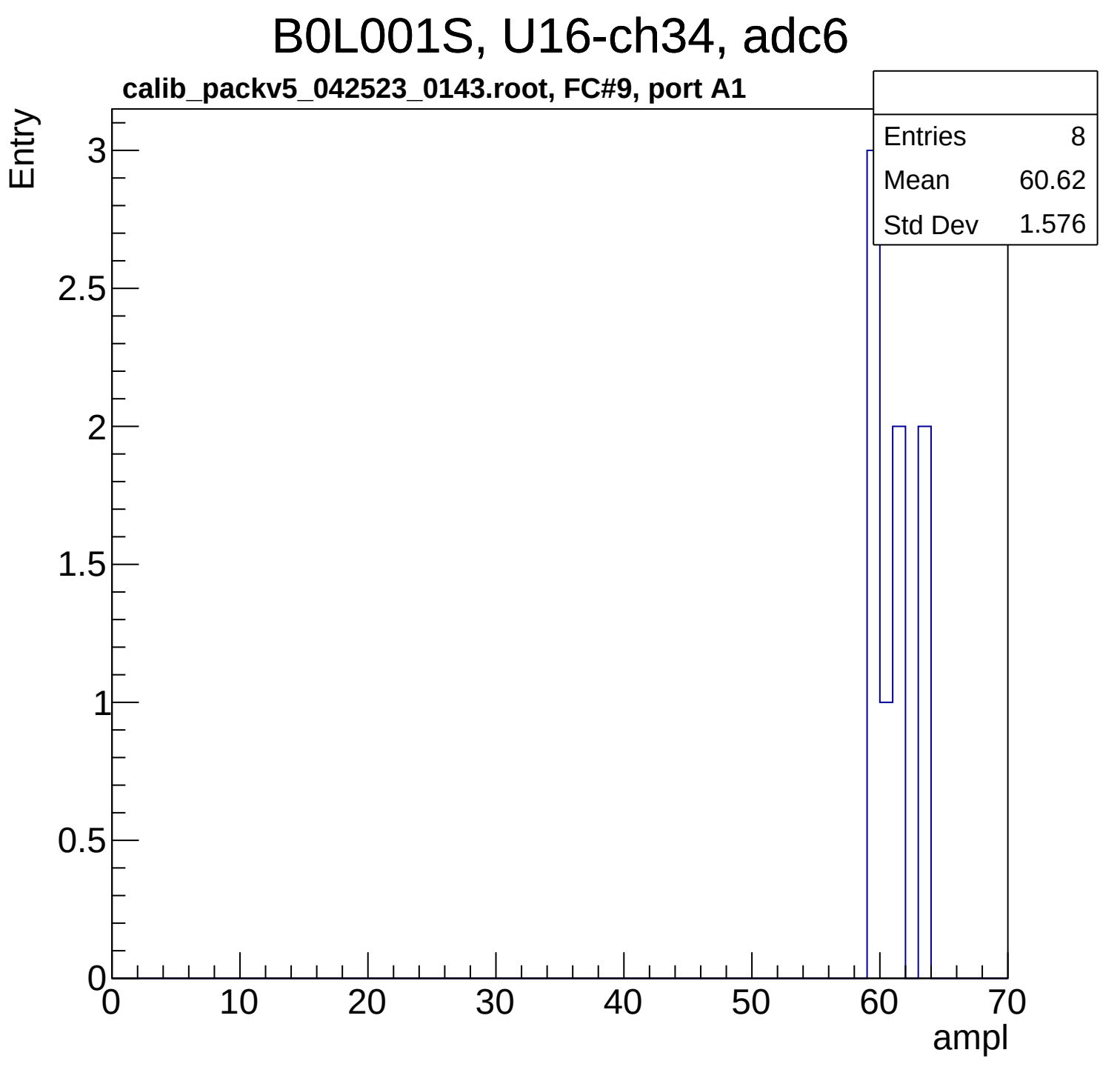
40
50
60
70


B0L001S, U16-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.42
Std Dev 8.581
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 60.62
Std Dev 1.576
Entry
ampl
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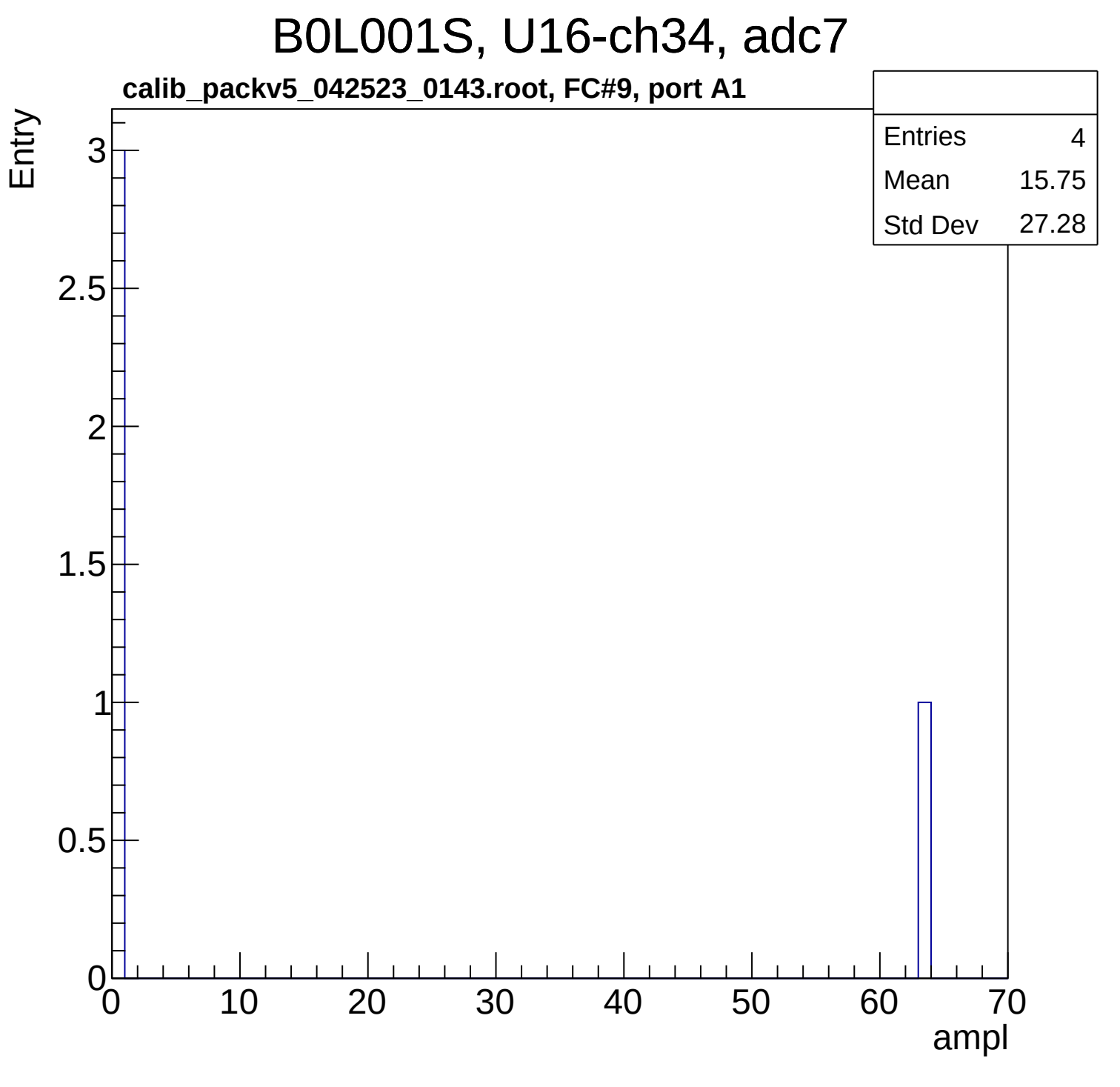
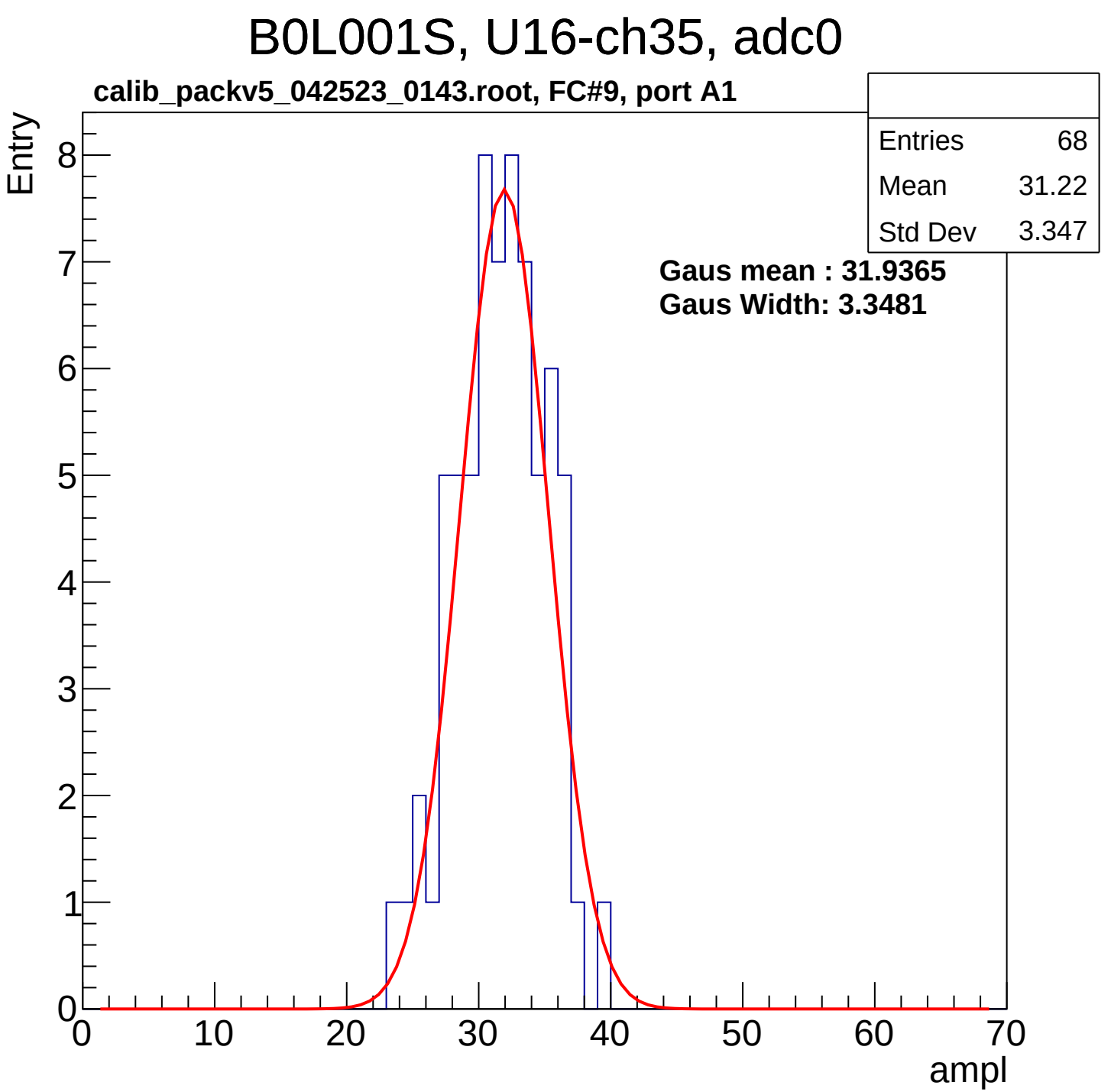
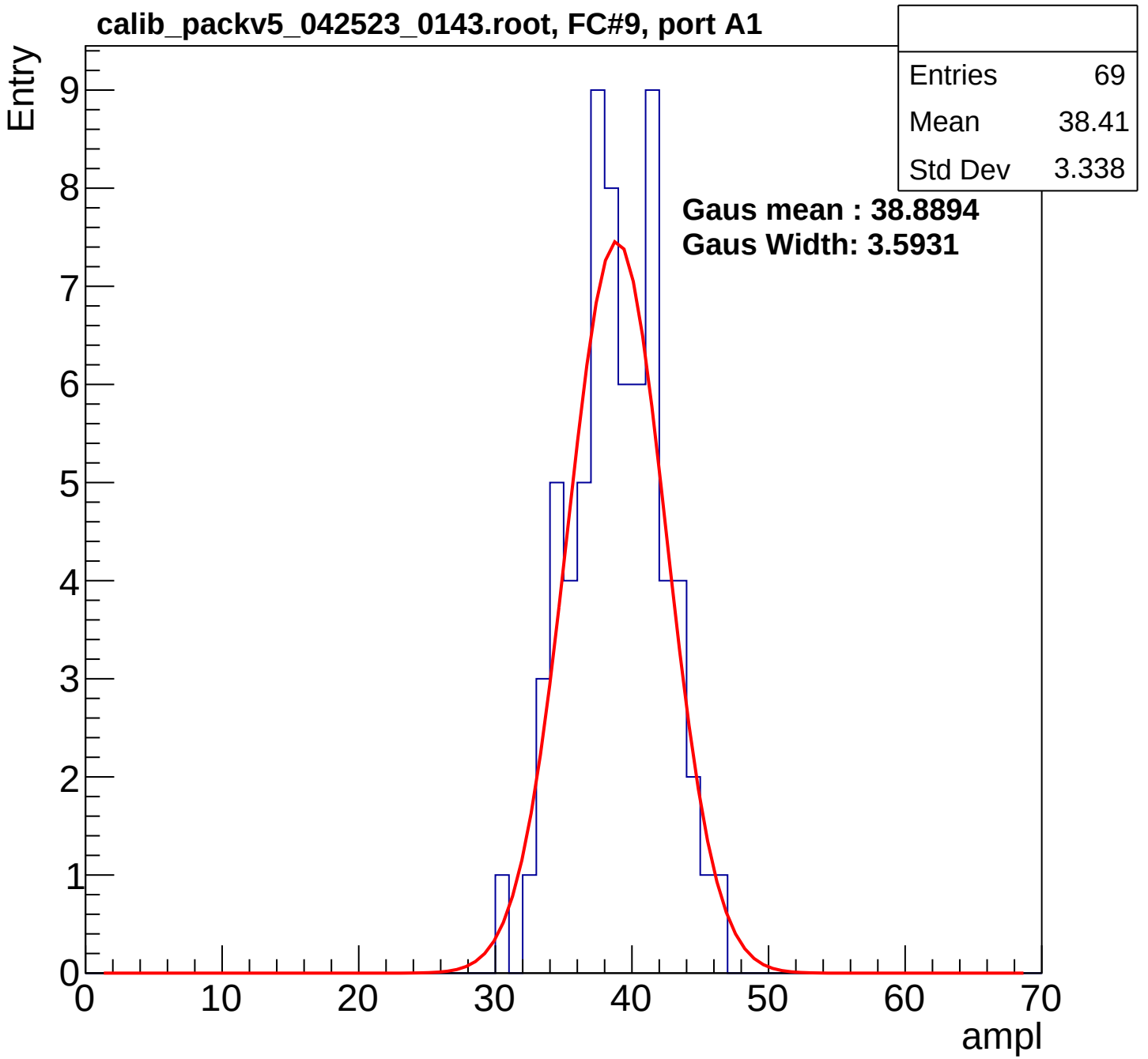
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.22
Std Dev 3.347
Gaus mean : 31.9365
Gaus Width: 3.3481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.41
Std Dev 3.338
Gaus mean : 38.8894
Gaus Width: 3.5931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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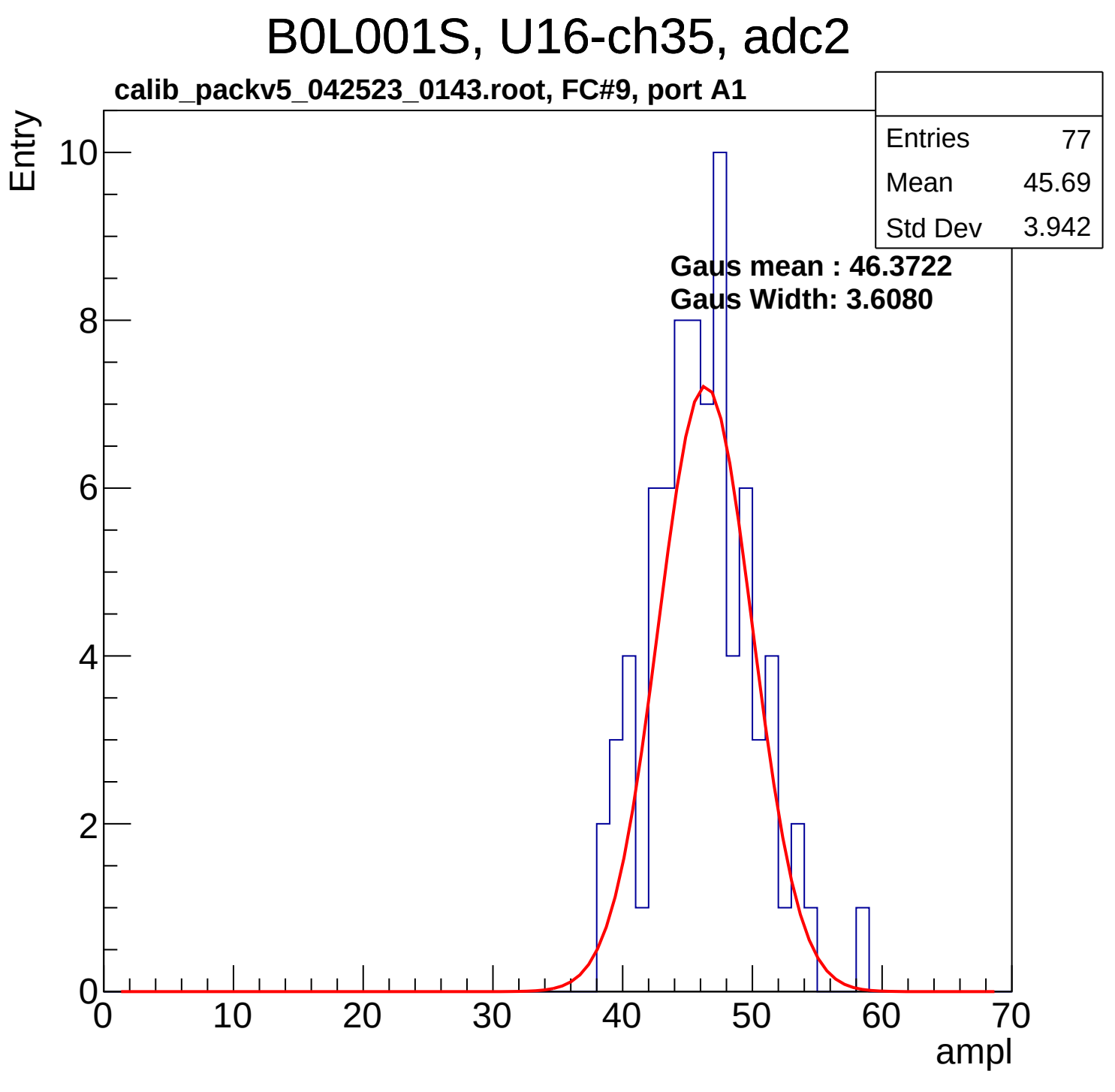
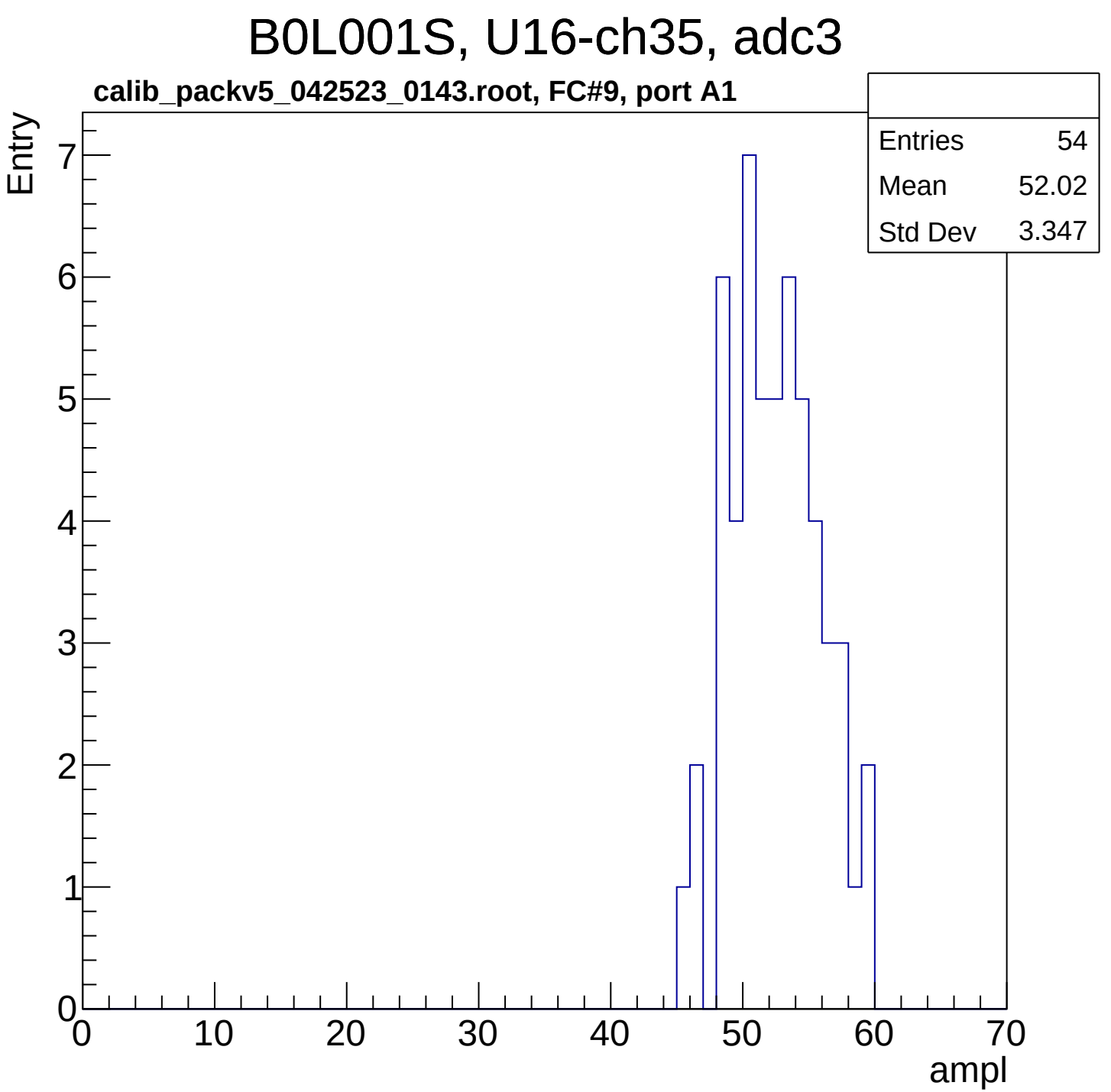
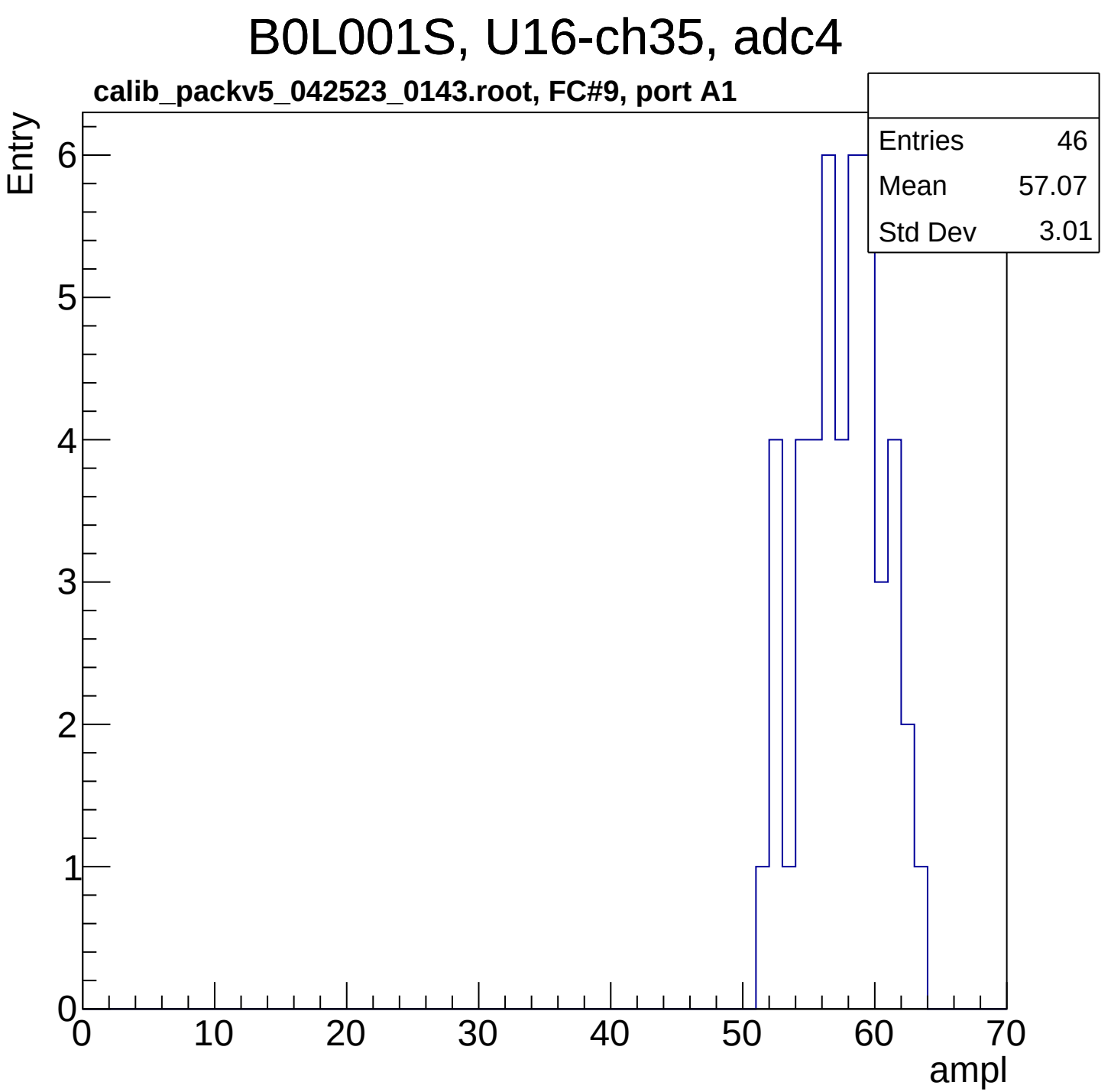
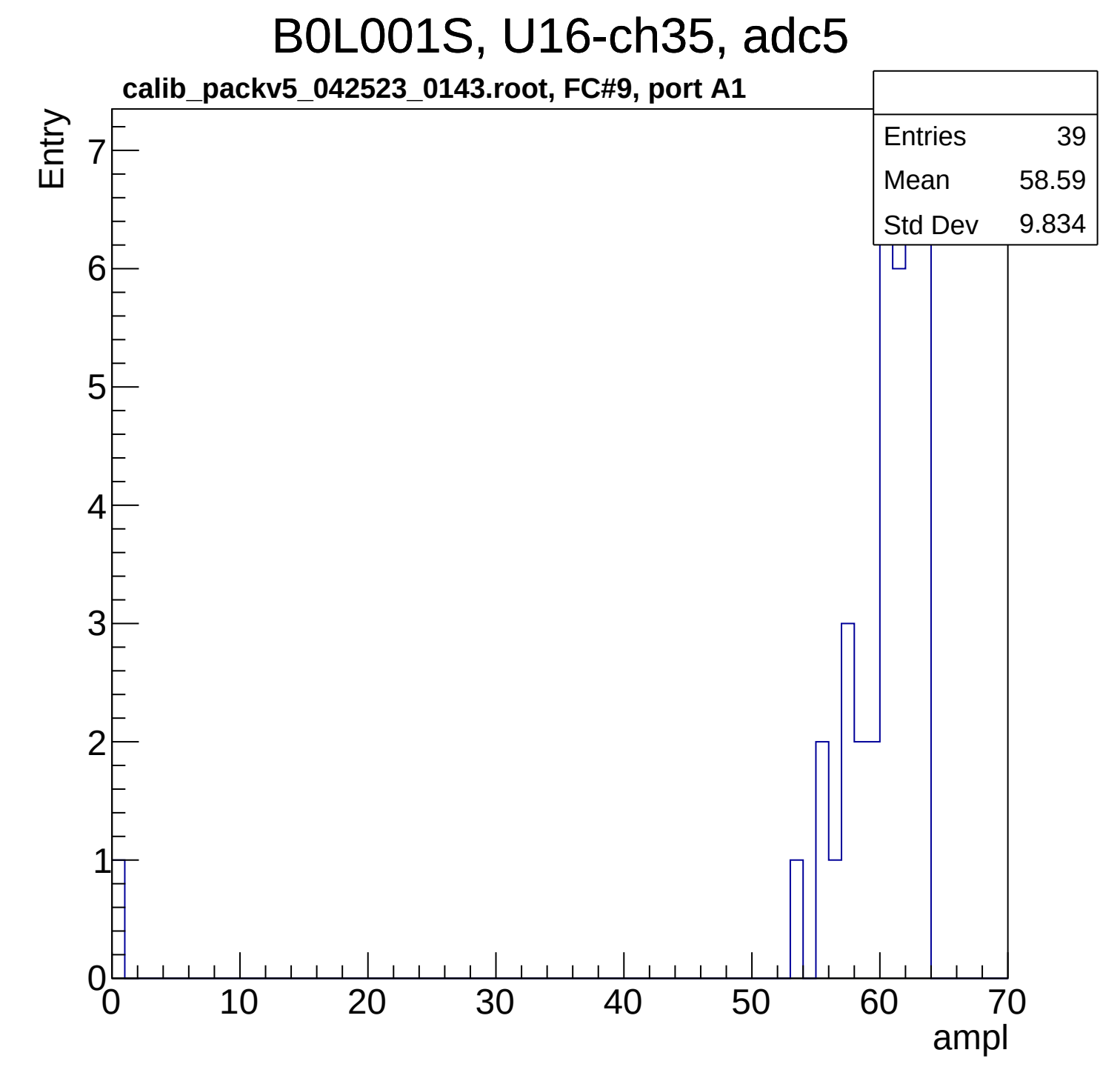
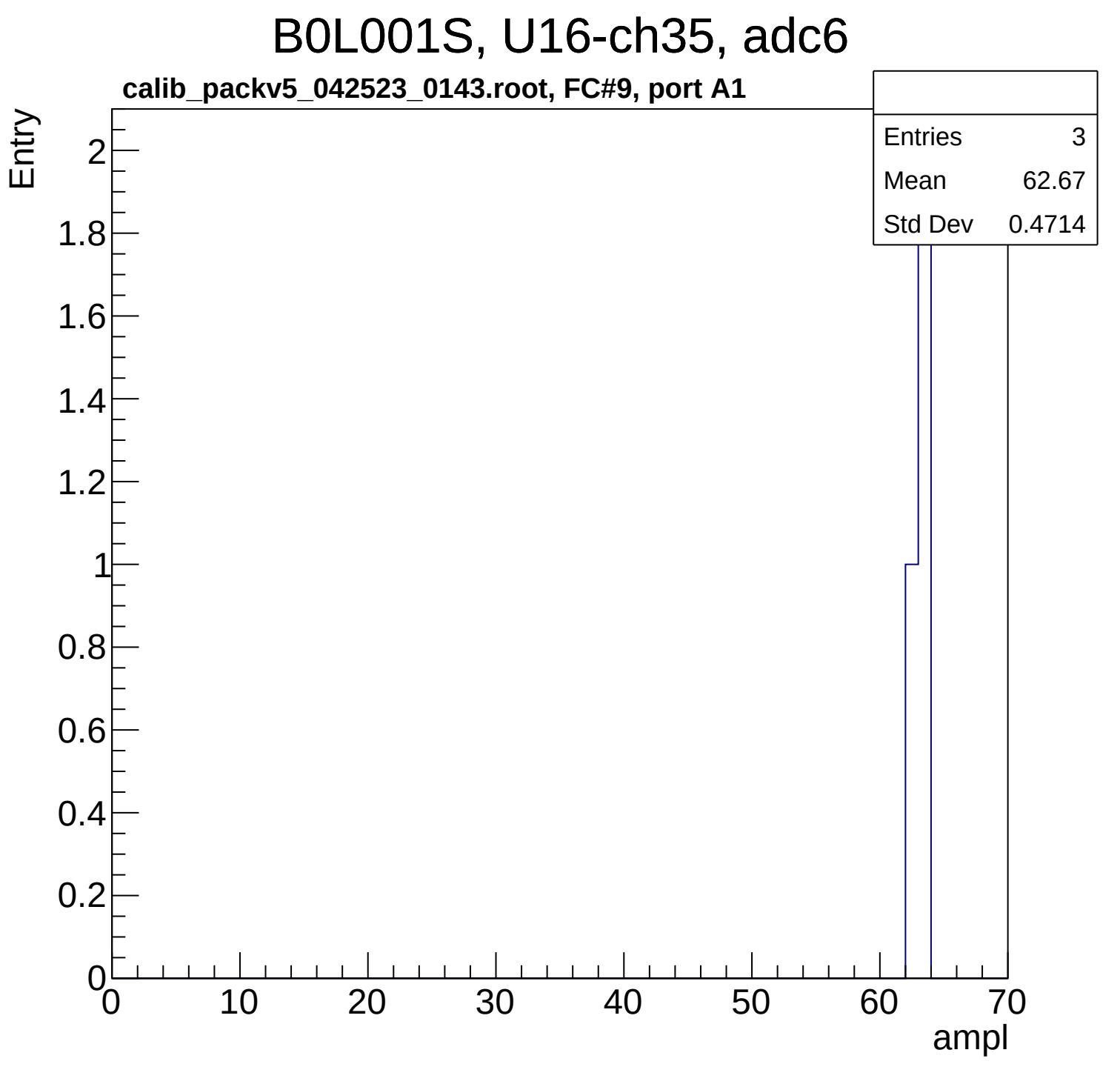
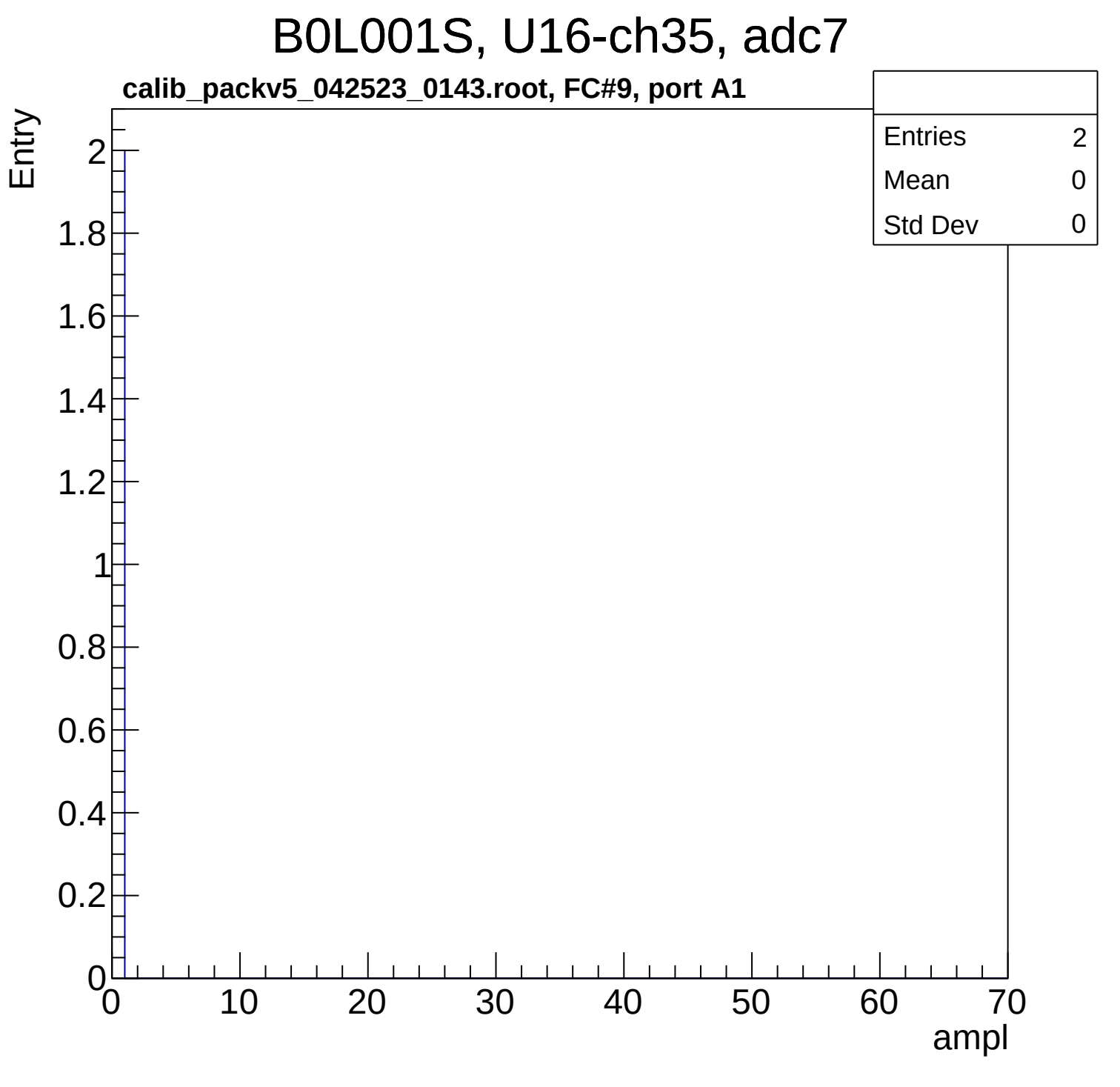

B0L001S, U16-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 45.69
Std Dev 3.942
Gaus mean : 46.3722
Gaus Width: 3.6080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

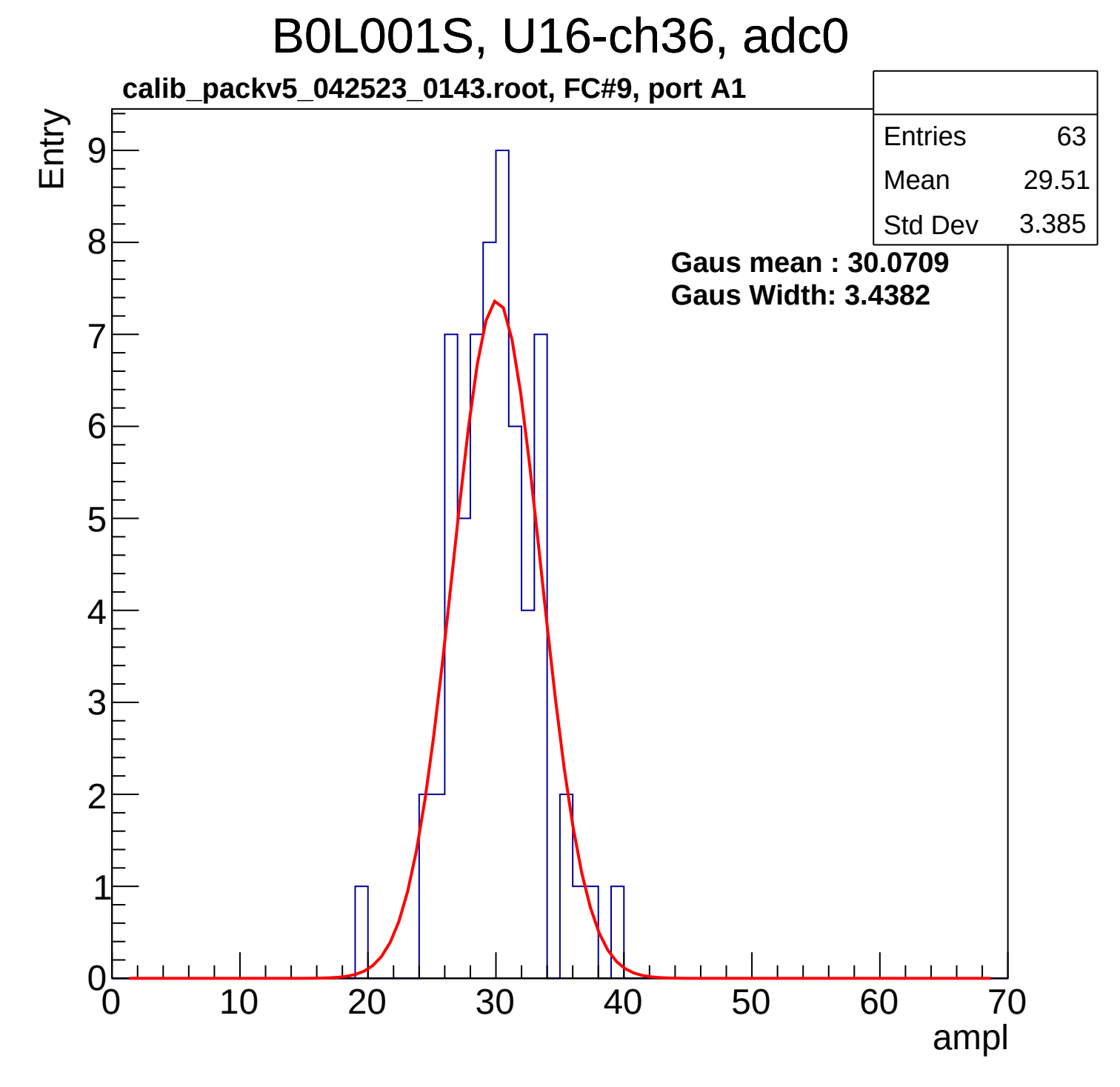

B0L001S, U16-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 52.02
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

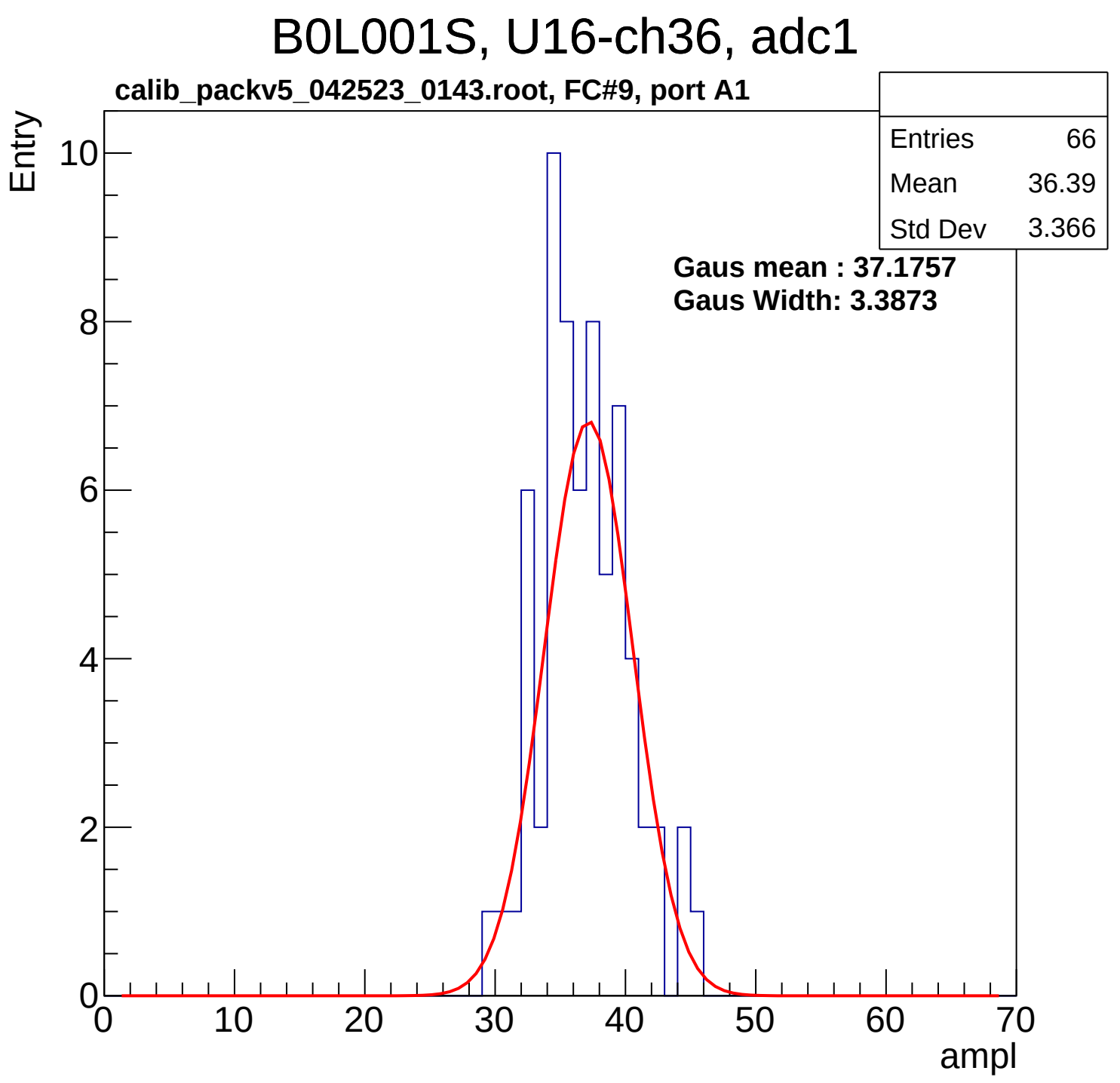

B0L001S, U16-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.07
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

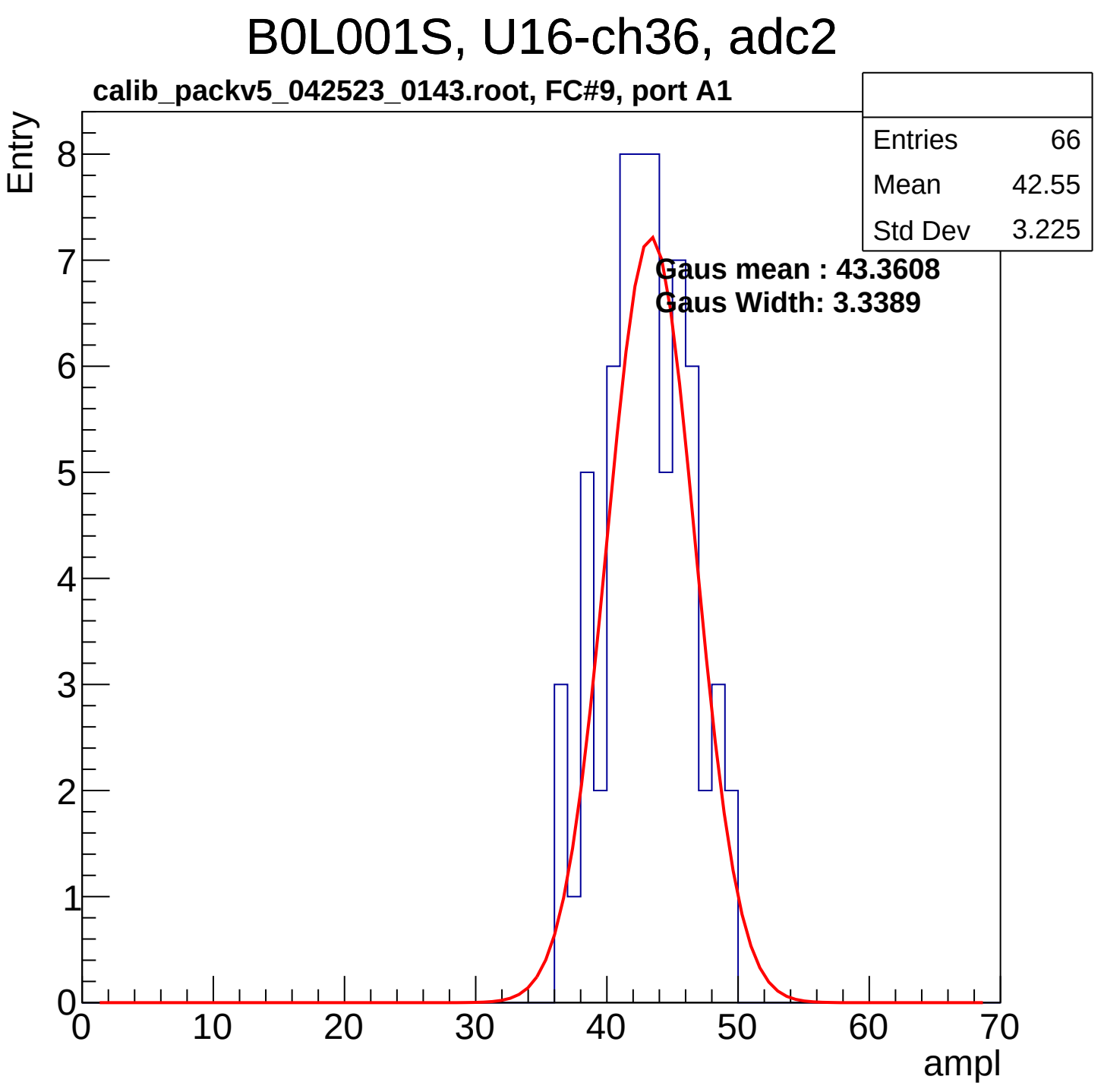

B0L001S, U16-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.59
Std Dev 9.834
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.51
Std Dev 3.385
Gaus mean : 30.0709
Gaus Width: 3.4382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.39
Std Dev 3.366
Gaus mean : 37.1757
Gaus Width: 3.3873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.55
Std Dev 3.225
Gaus mean : 43.3608
Gaus Width: 3.3389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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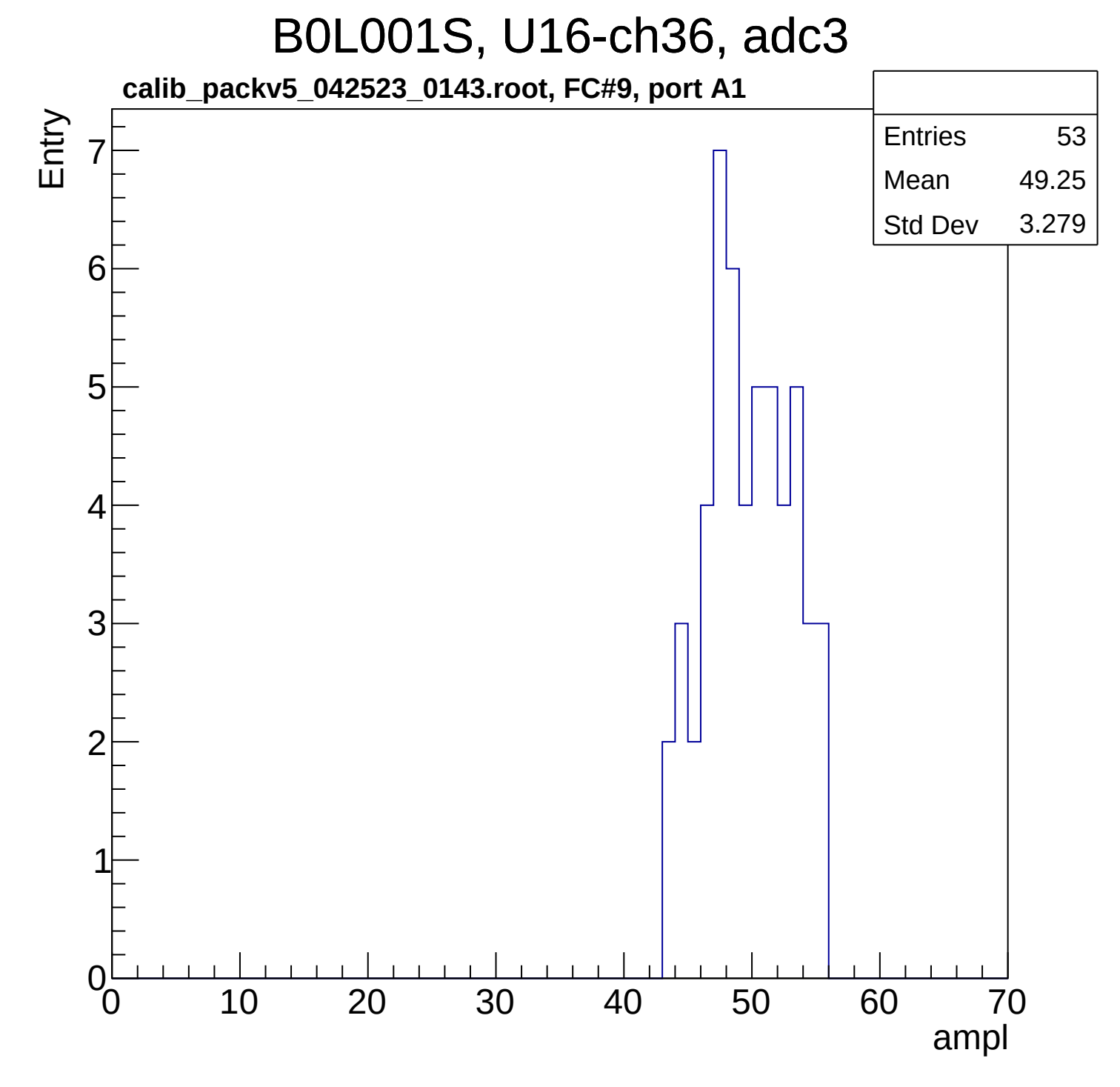
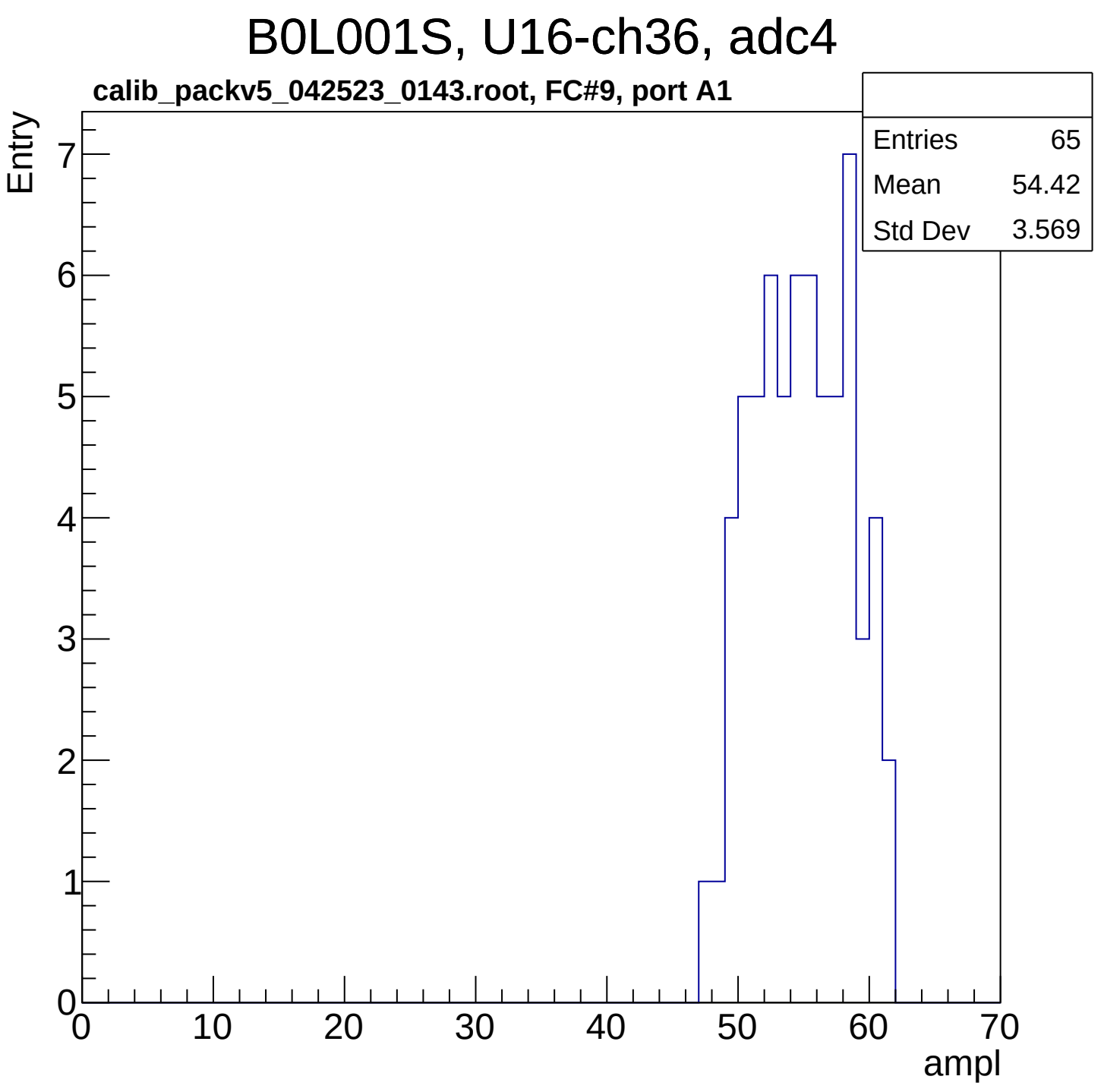
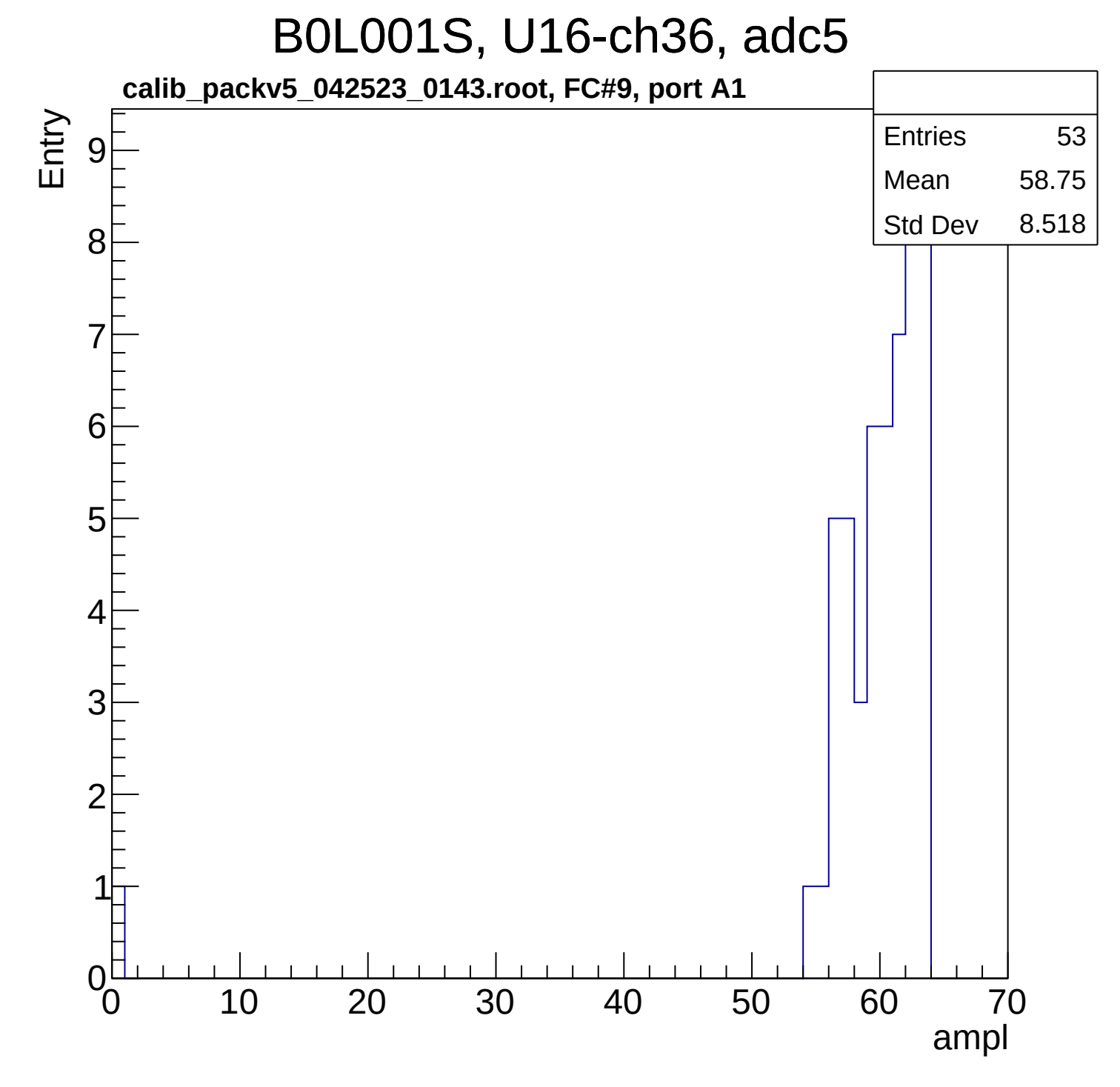
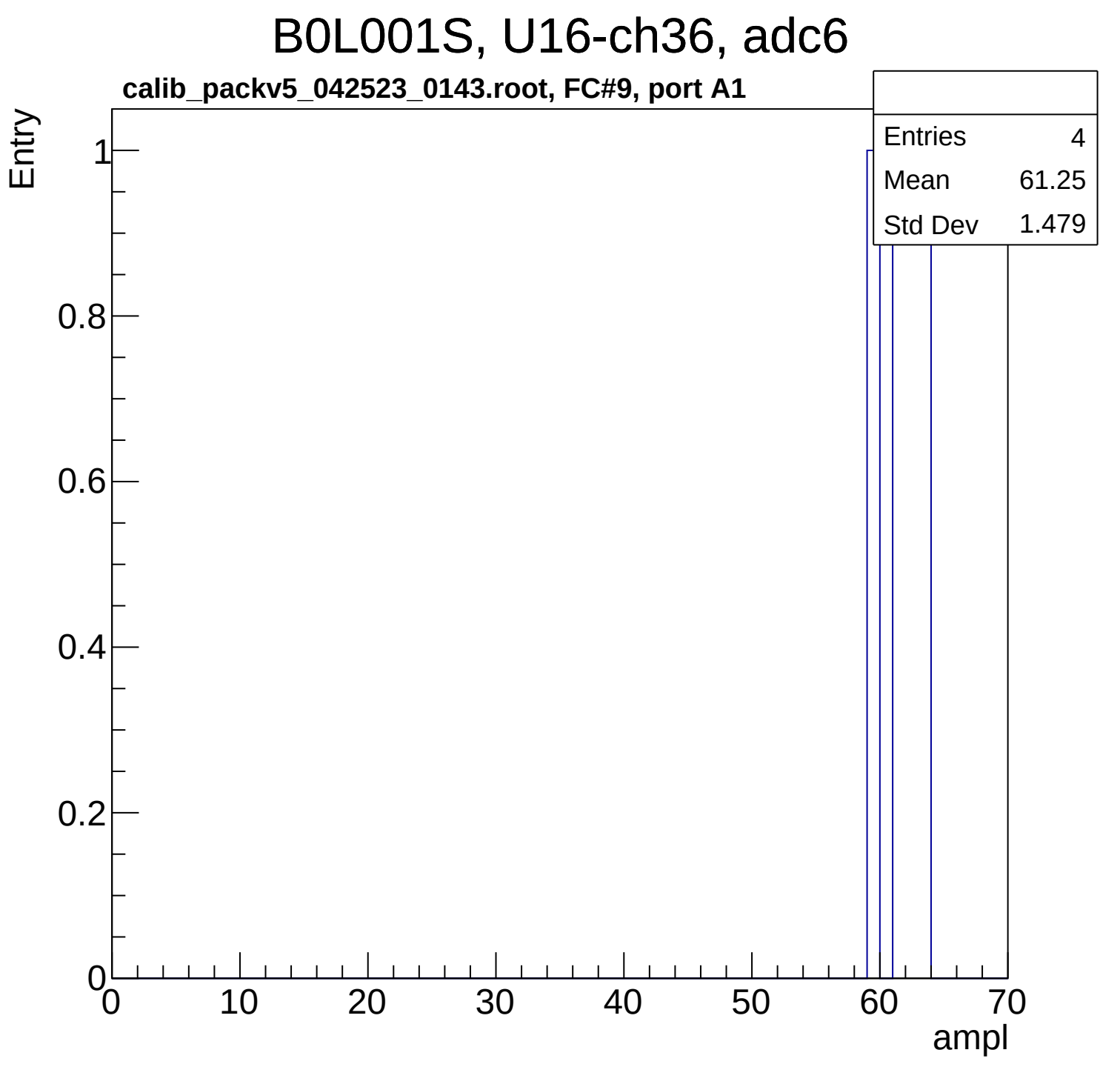

B0L001S, U16-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 49.25
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 54.42
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 58.75
Std Dev 8.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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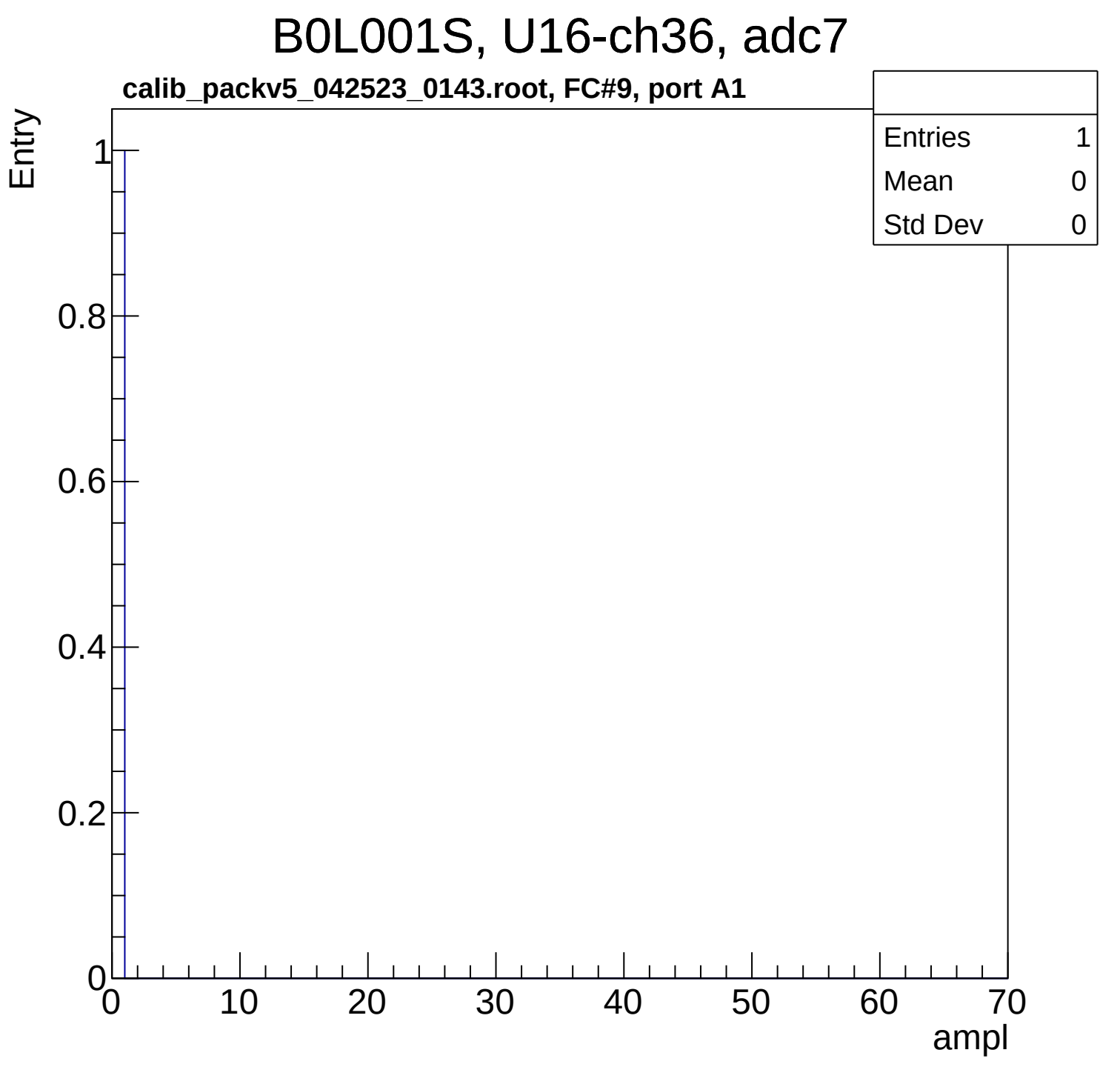
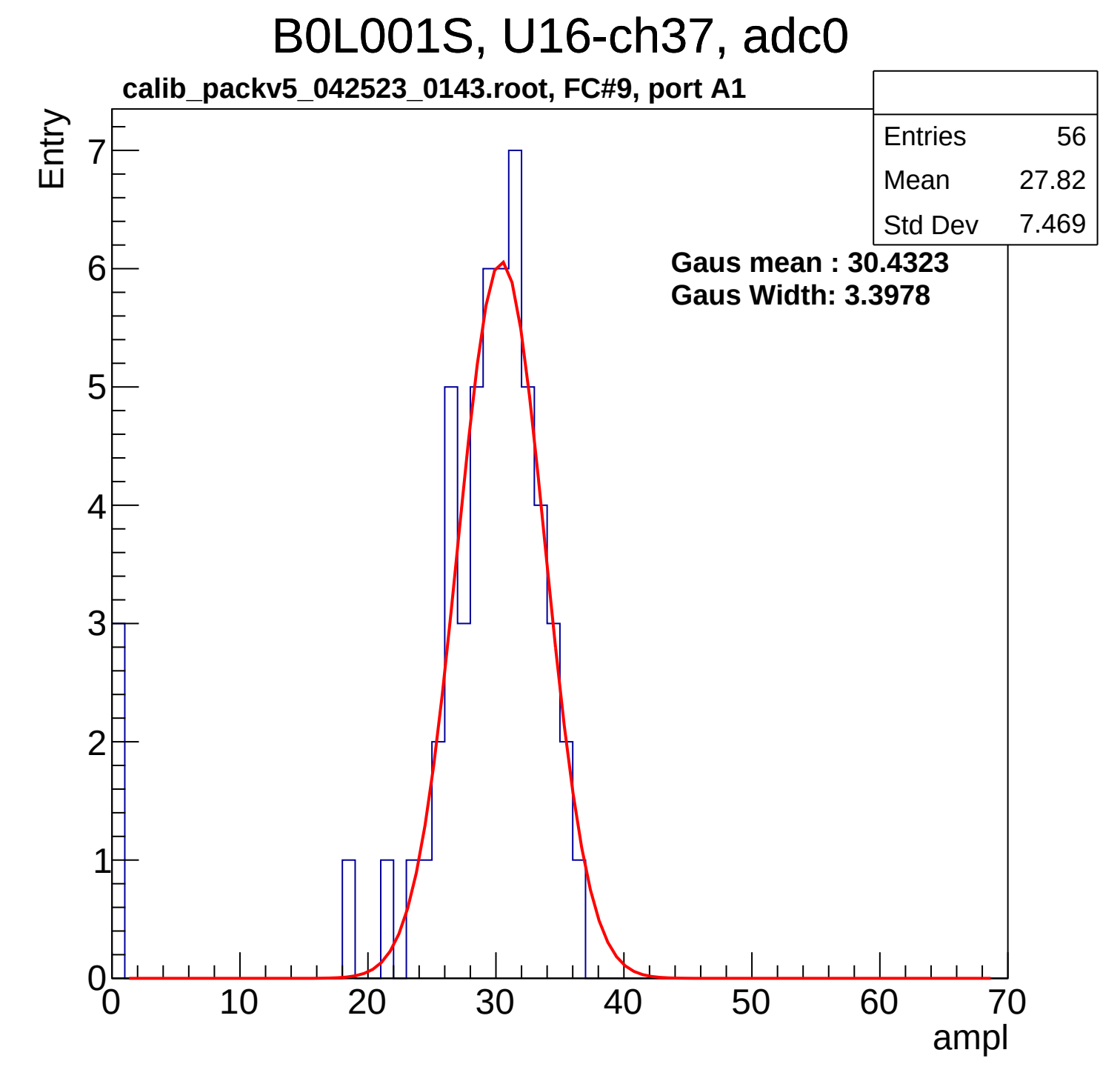
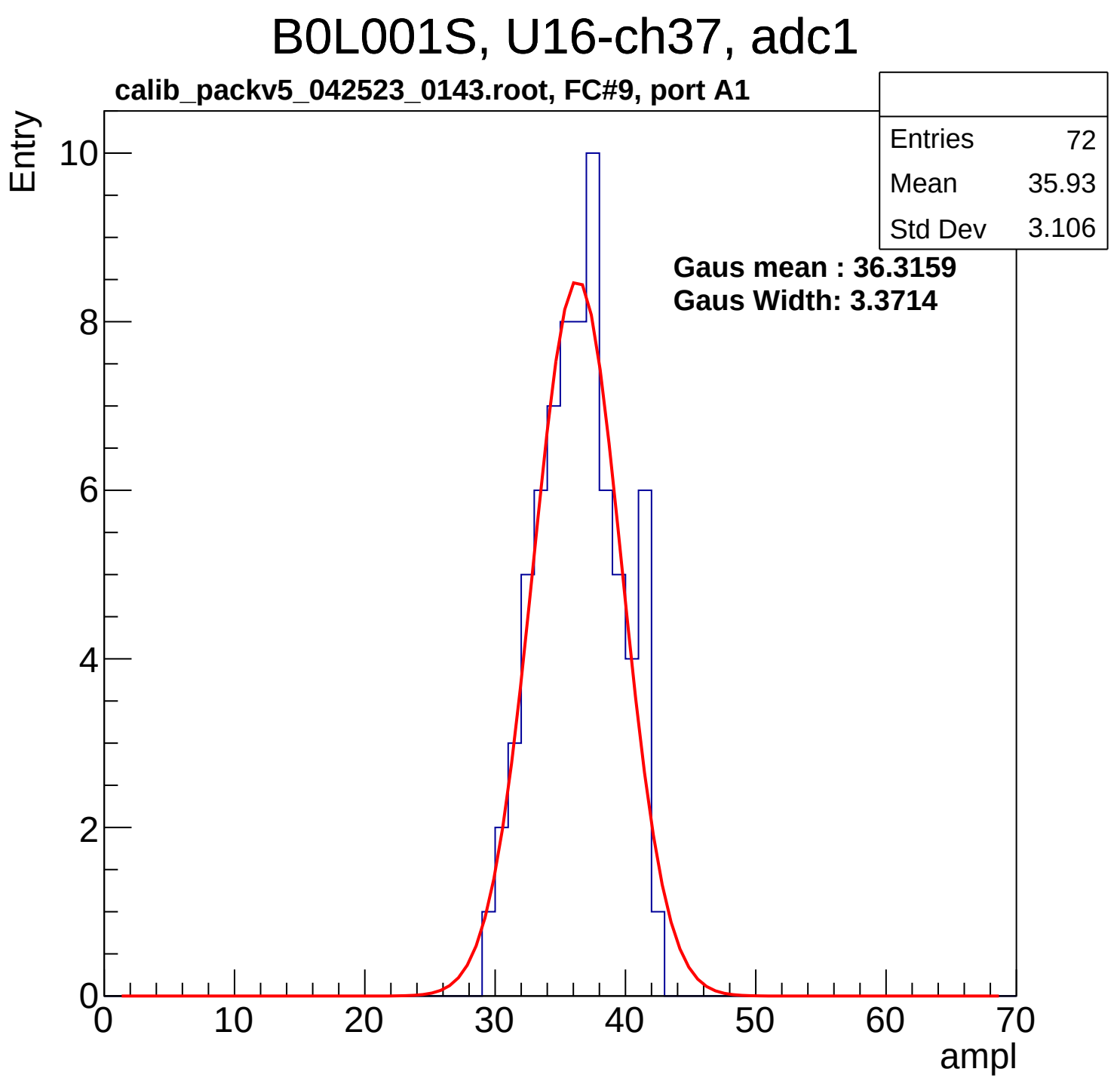
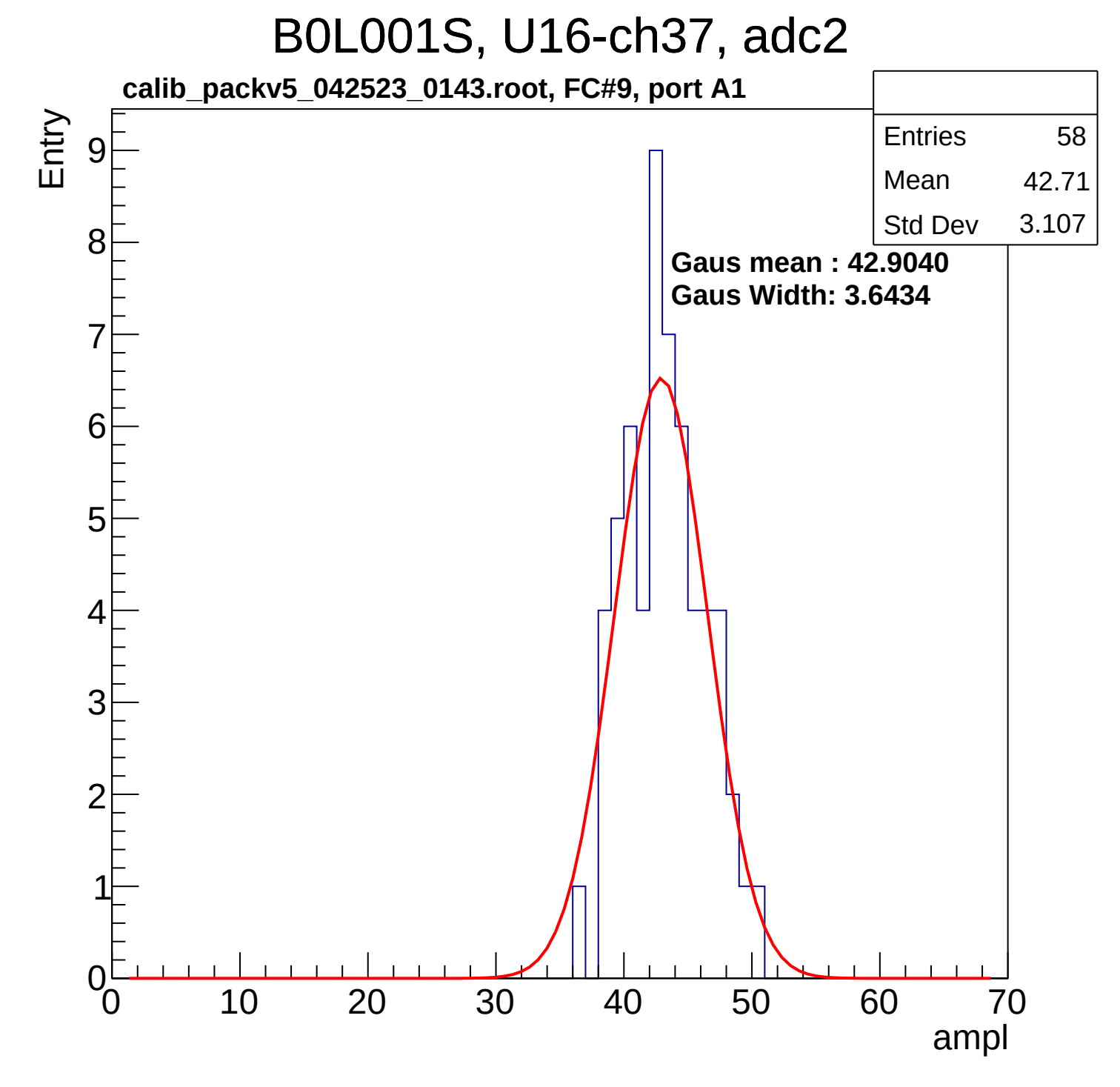
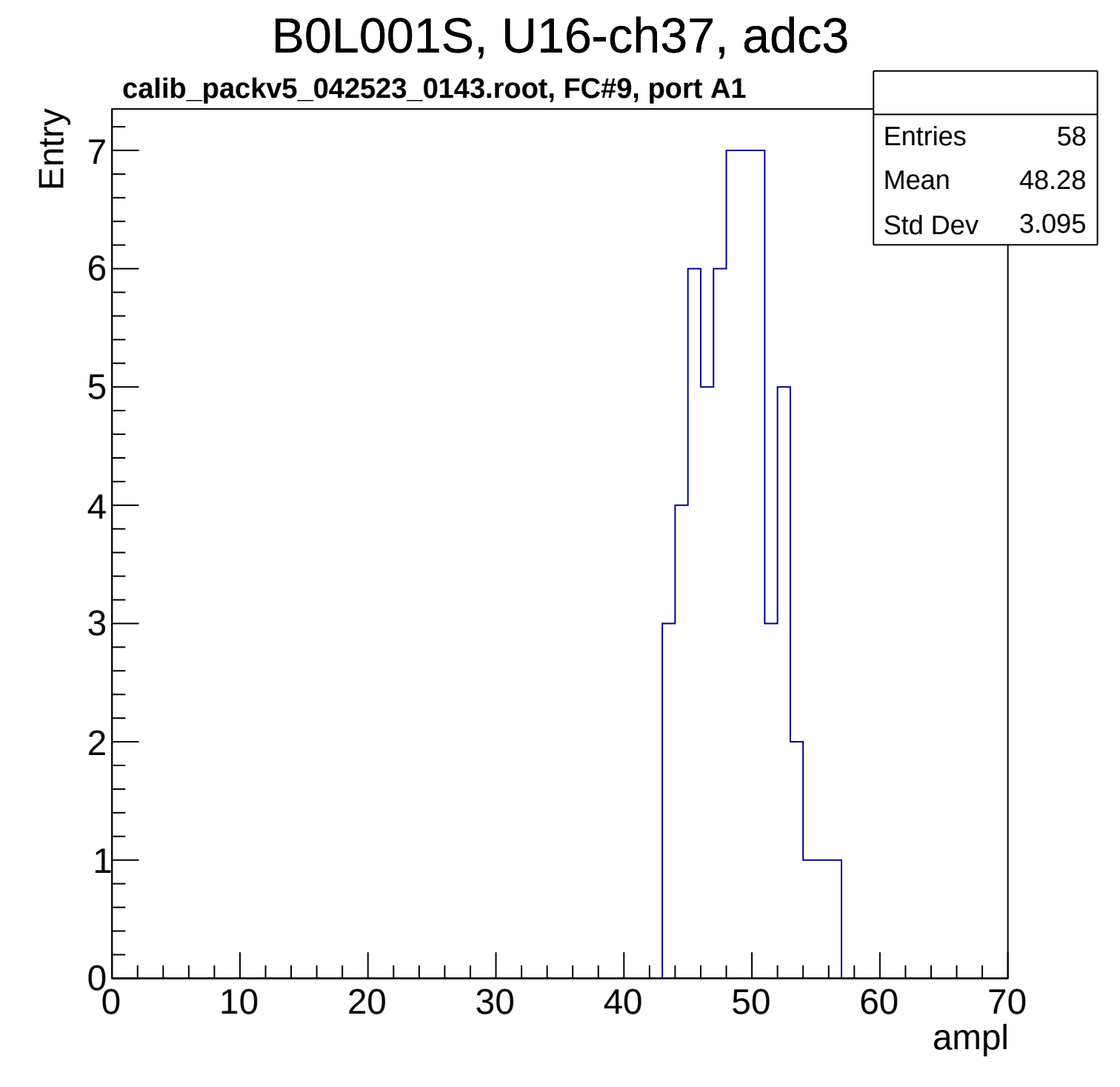
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

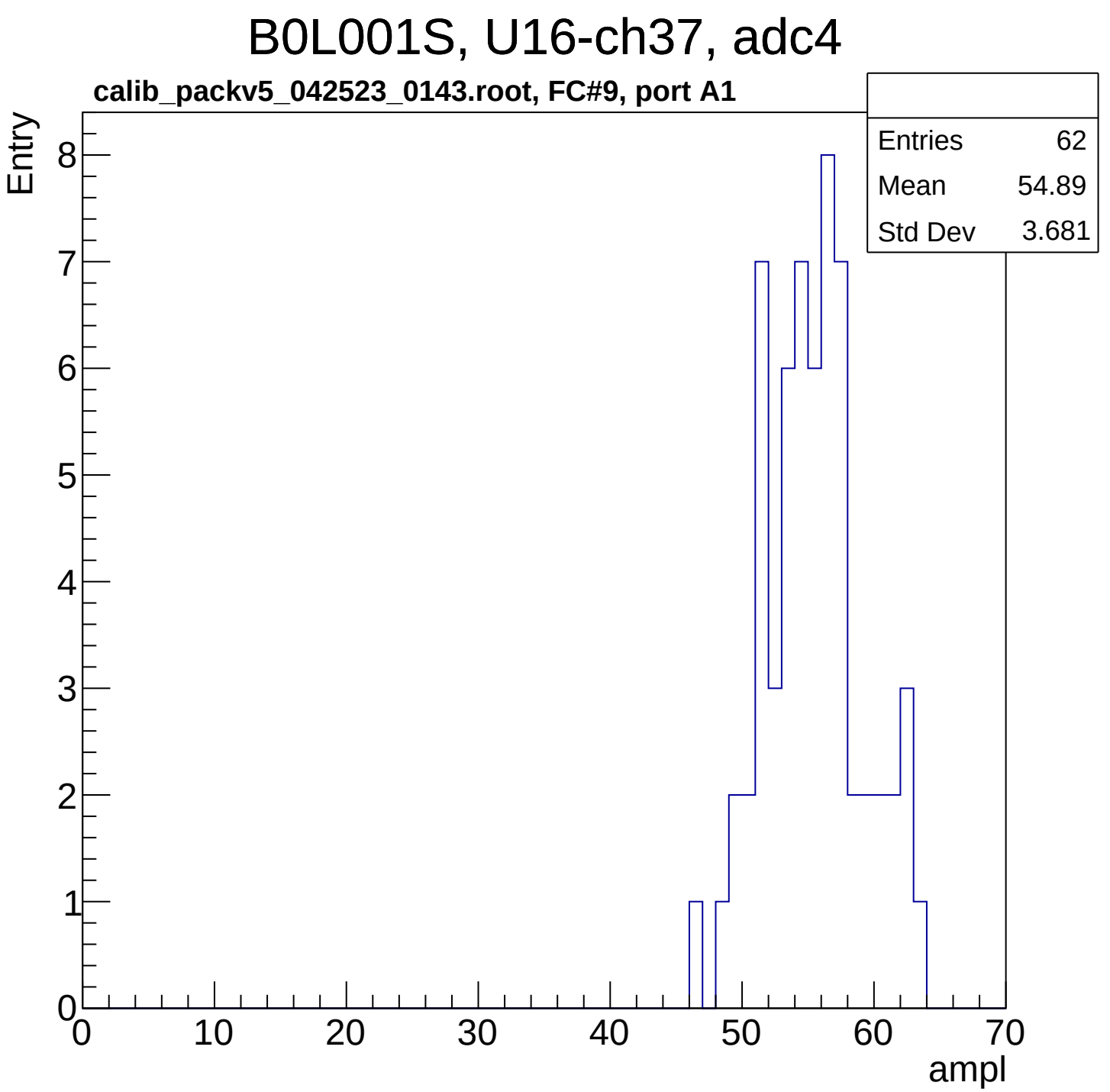

B0L001S, U16-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 27.82
Std Dev 7.469
Gaus mean : 30.4323
Gaus Width: 3.3978
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 35.93
Std Dev 3.106
Gaus mean : 36.3159
Gaus Width: 3.3714
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 42.71
Std Dev 3.107
Gaus mean : 42.9040
Gaus Width: 3.6434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 48.28
Std Dev 3.095
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 54.89
Std Dev 3.681
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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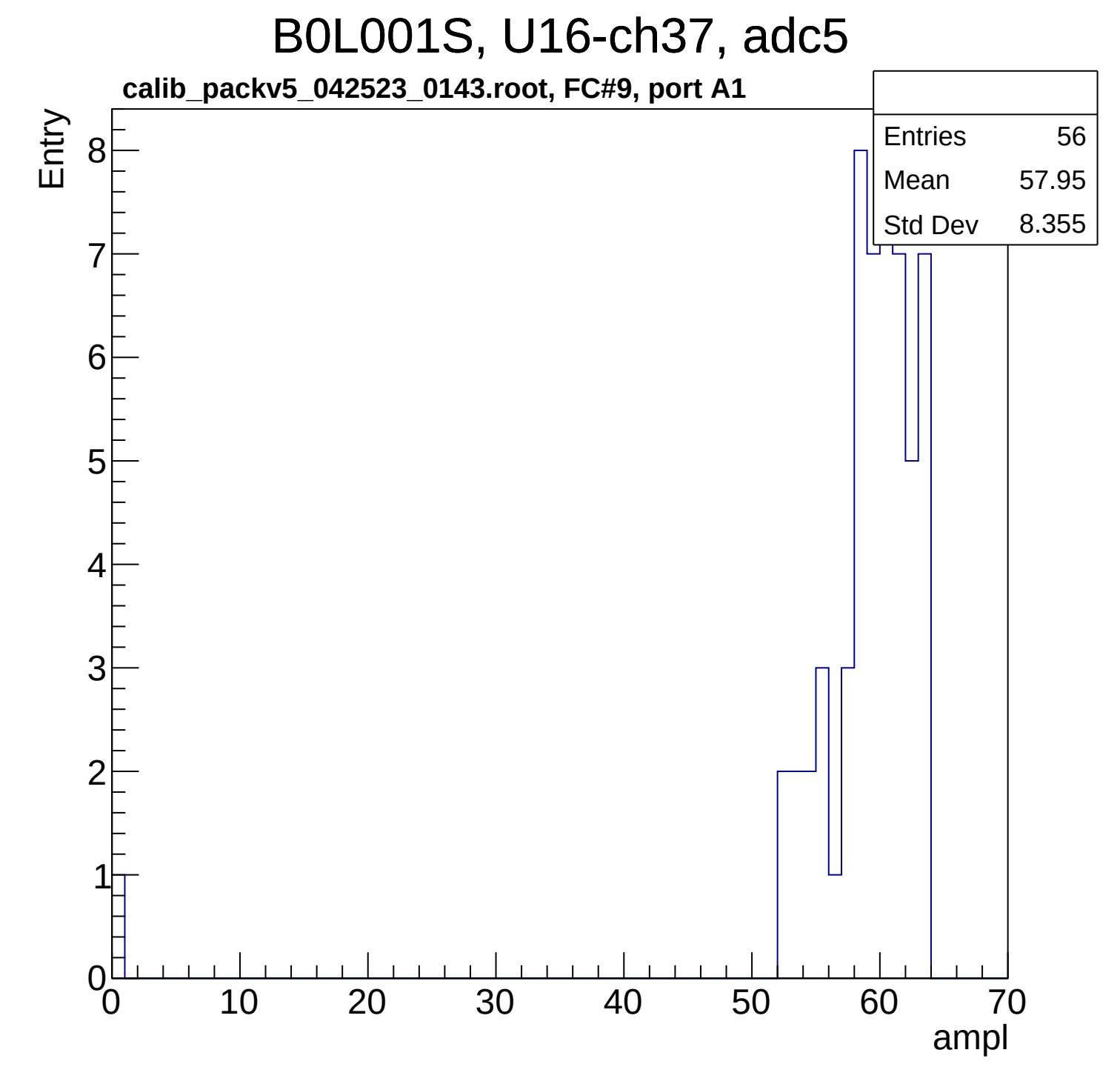
40
50
60
70


B0L001S, U16-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.95
Std Dev 8.355
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
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60
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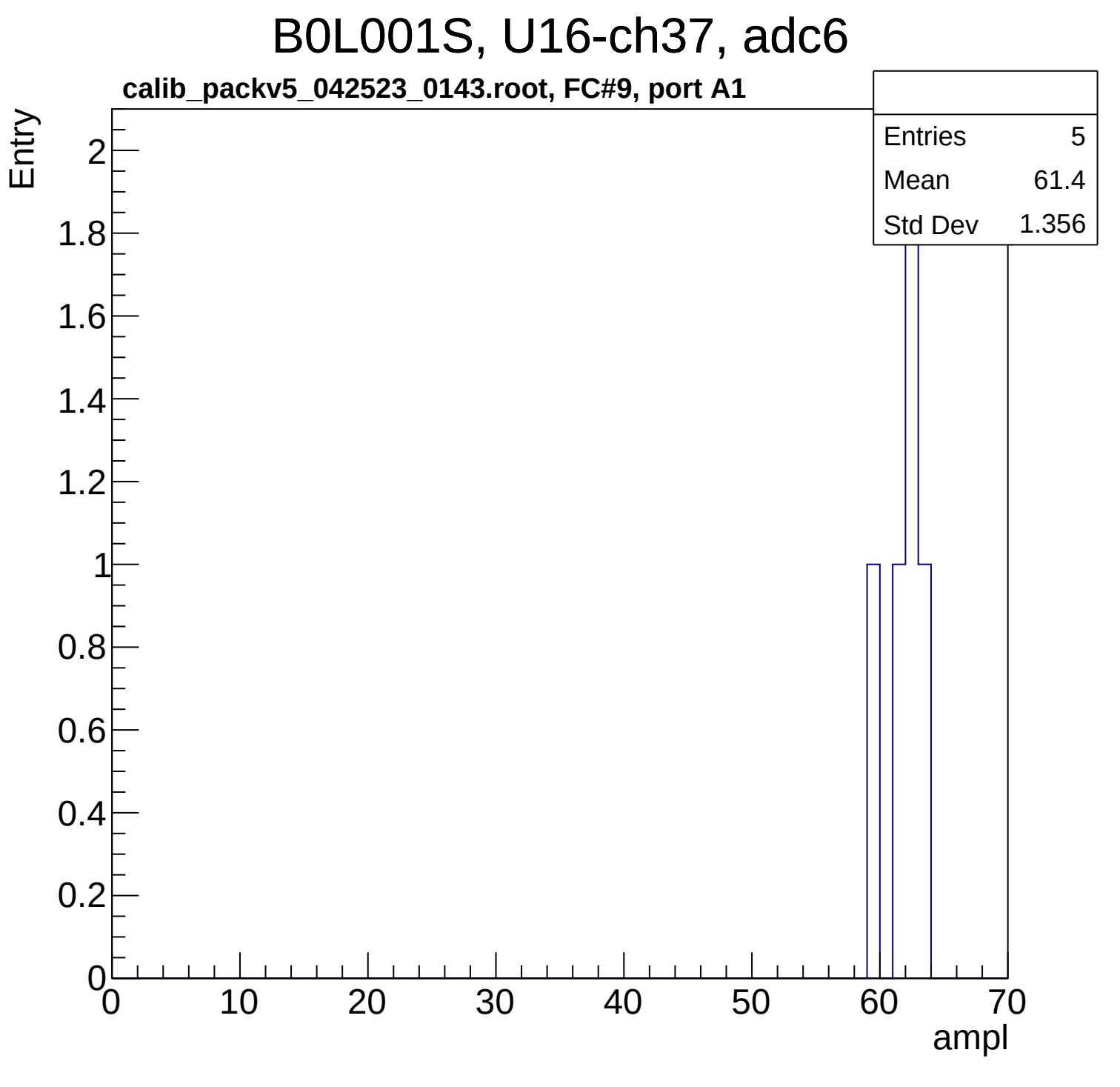
B0L001S, U16-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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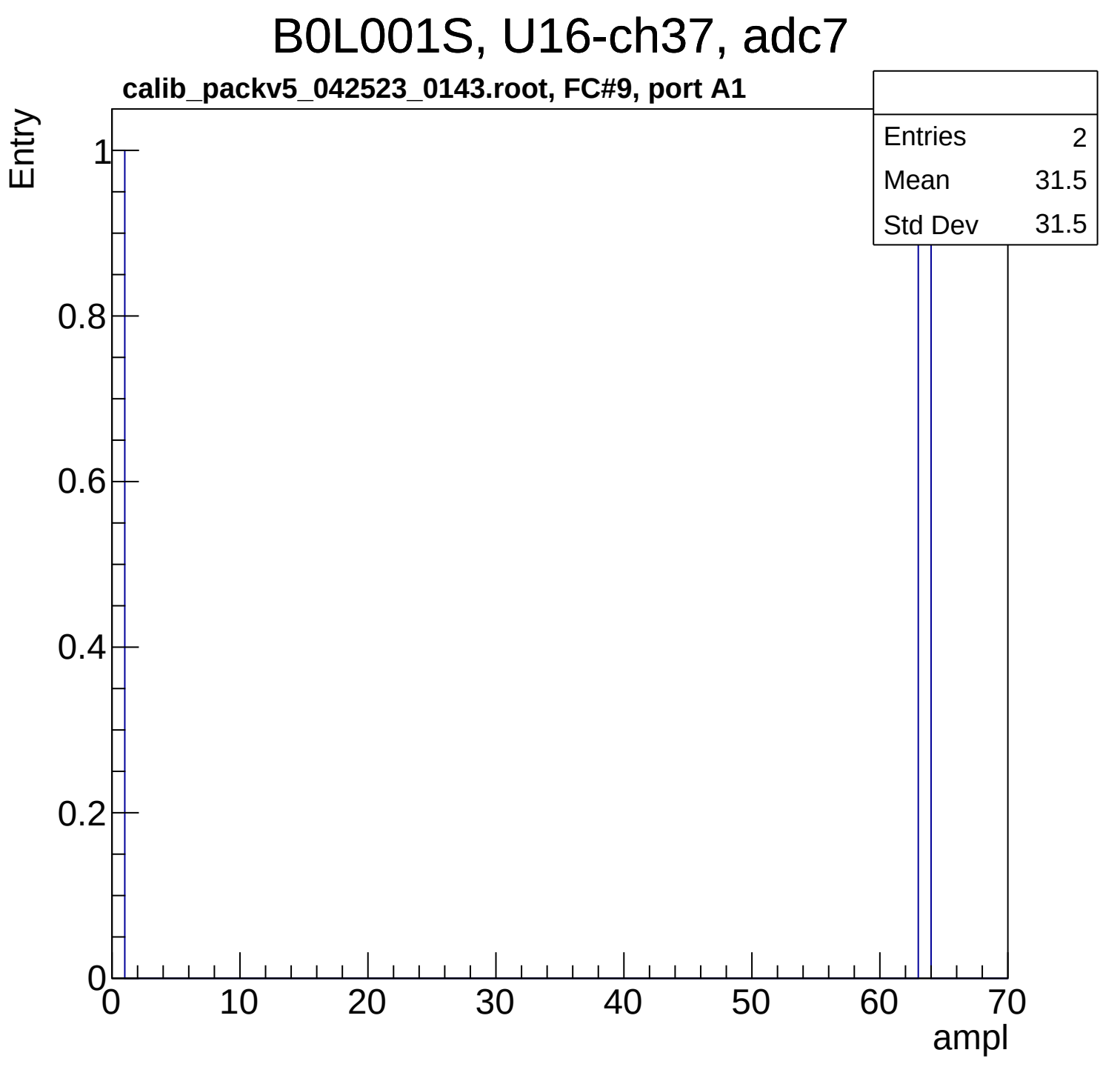
40
50
60
70


B0L001S, U16-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
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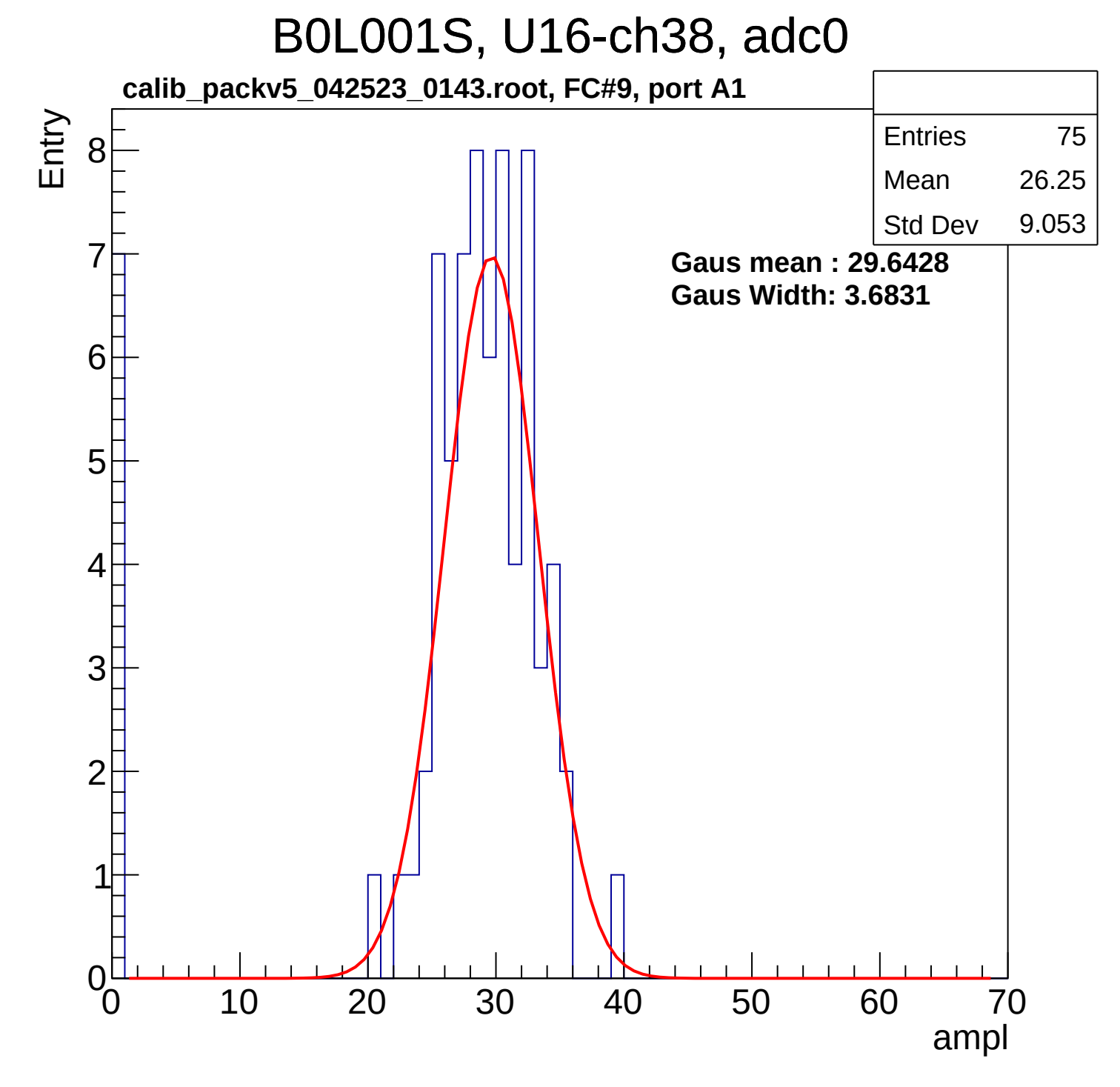
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 26.25
Std Dev 9.053
Gaus mean : 29.6428
Gaus Width: 3.6831
Entry
ampl
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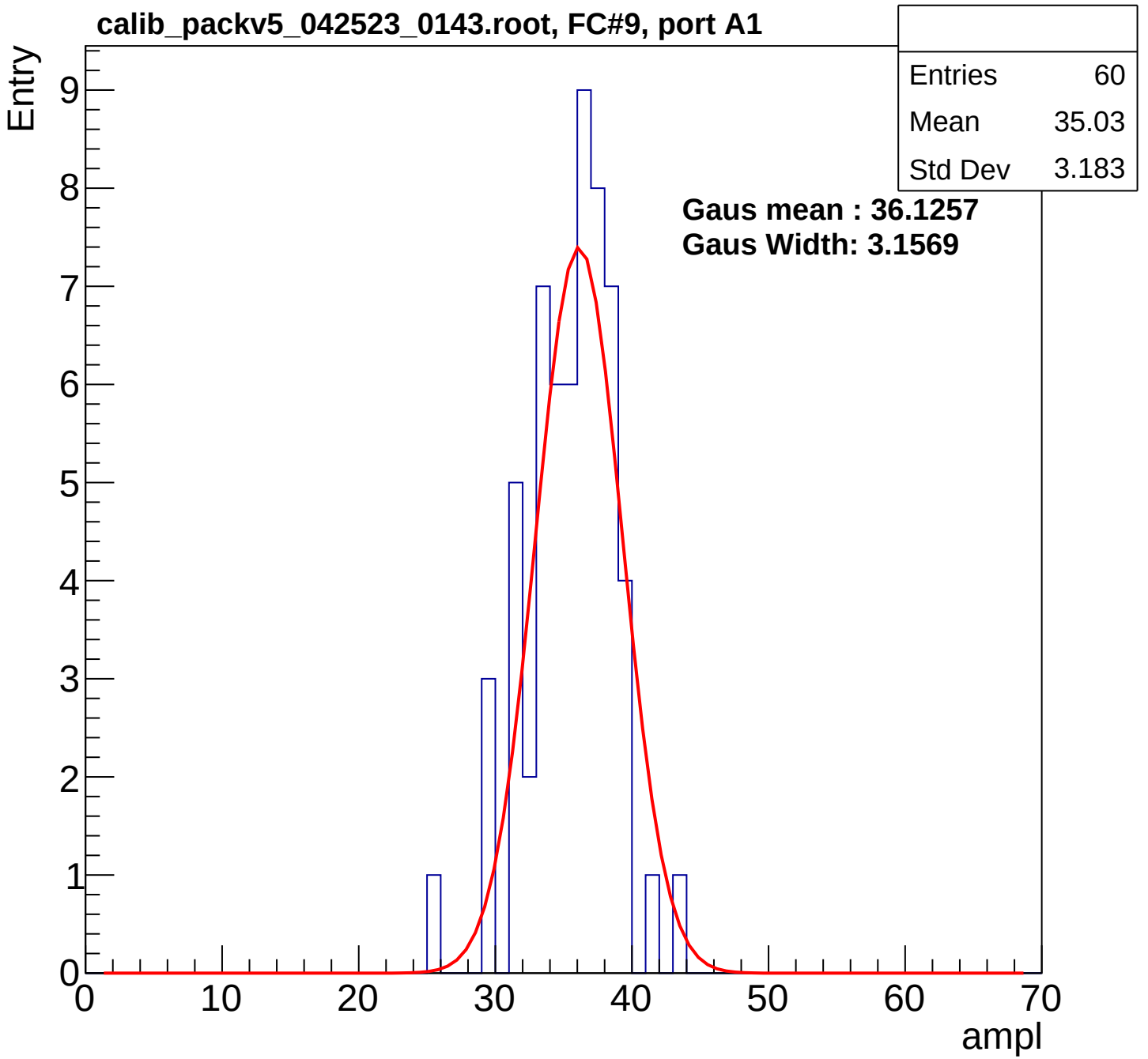
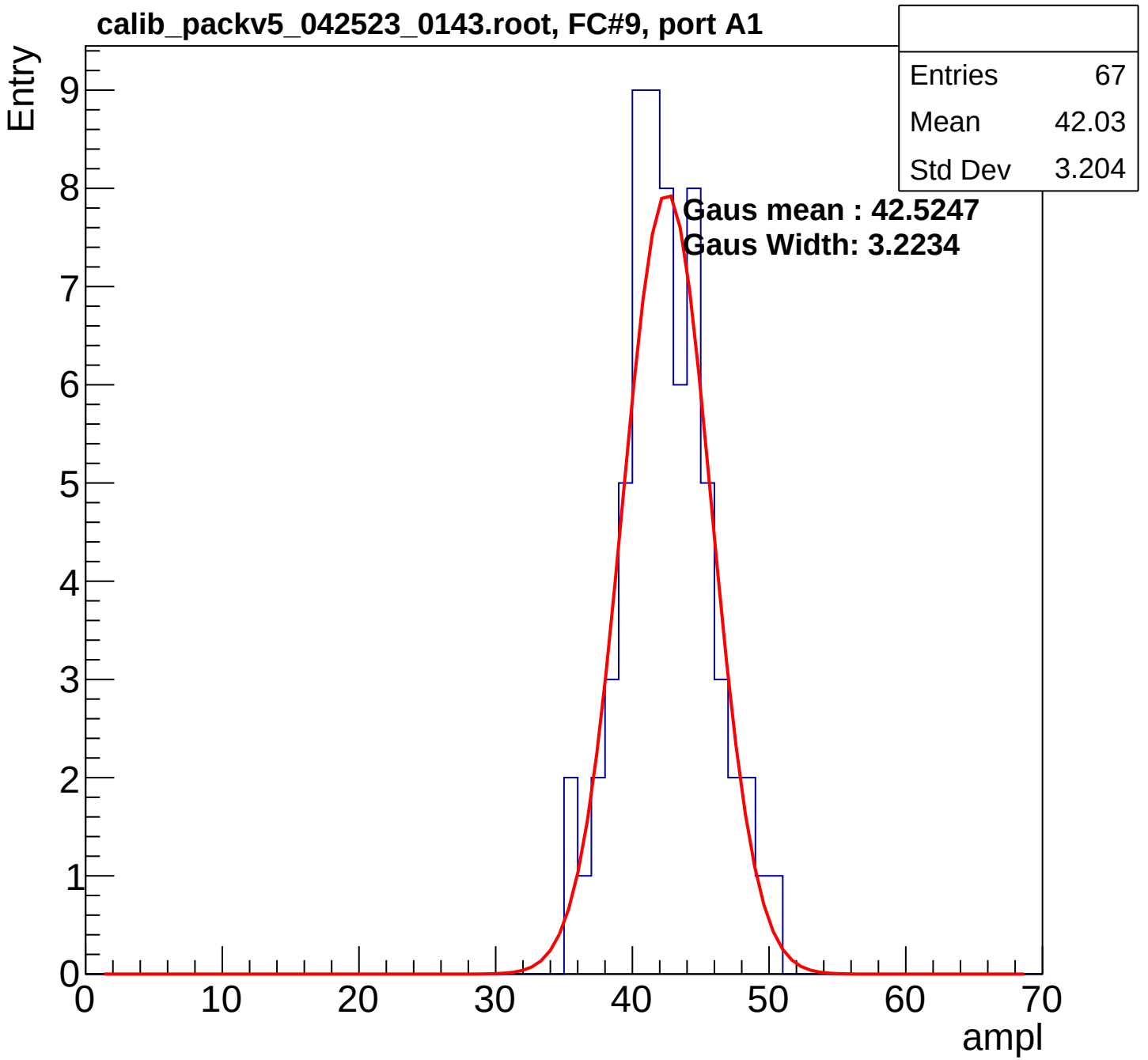
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 35.03
Std Dev 3.183
Gaus mean : 36.1257
Gaus Width: 3.1569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.03
Std Dev 3.204
Gaus mean : 42.5247
Gaus Width: 3.2234
Entry
ampl
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1
2
3
4
5
6
7
8
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0
10
20
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40
50
60
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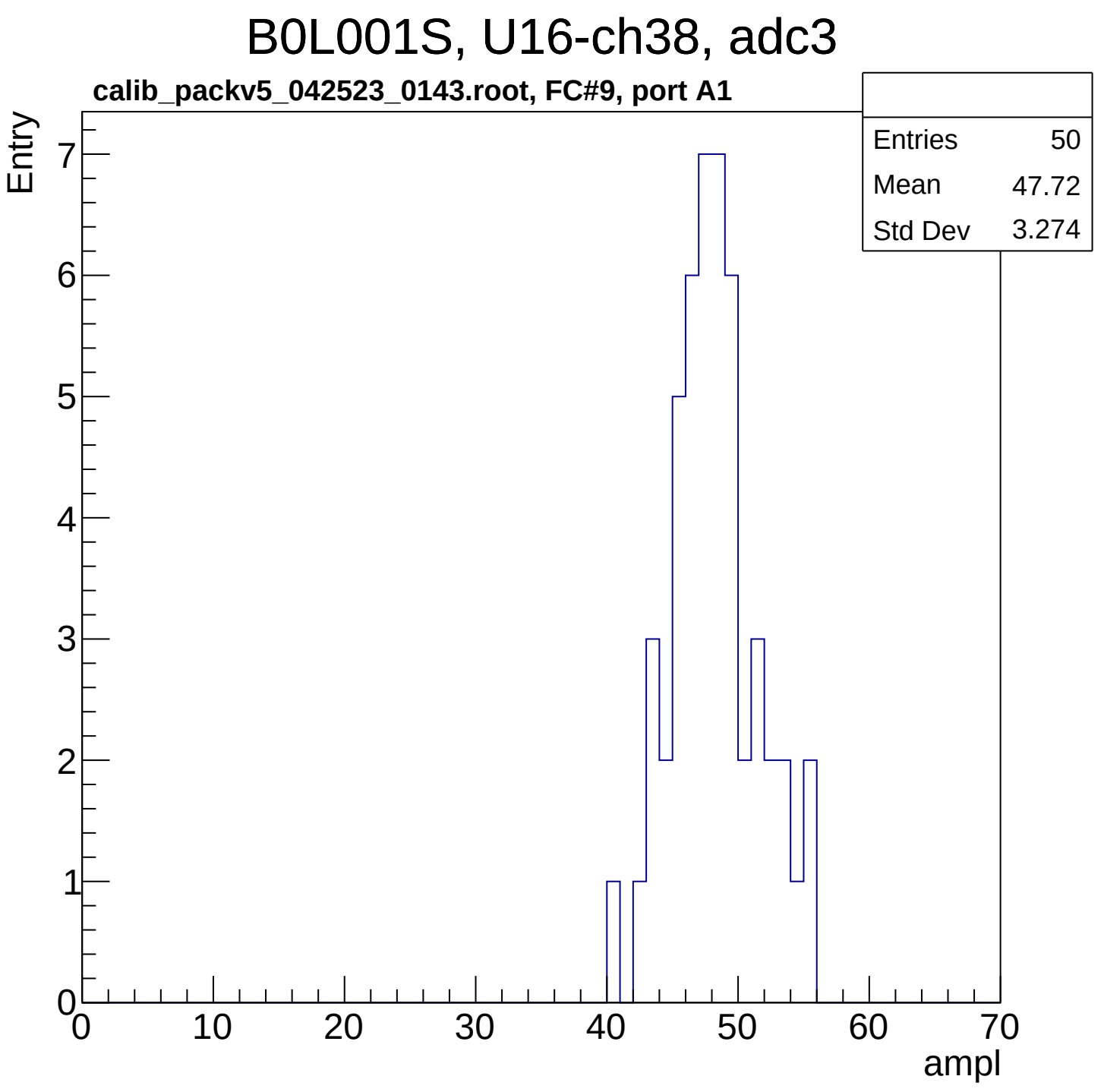
B0L001S, U16-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 47.72
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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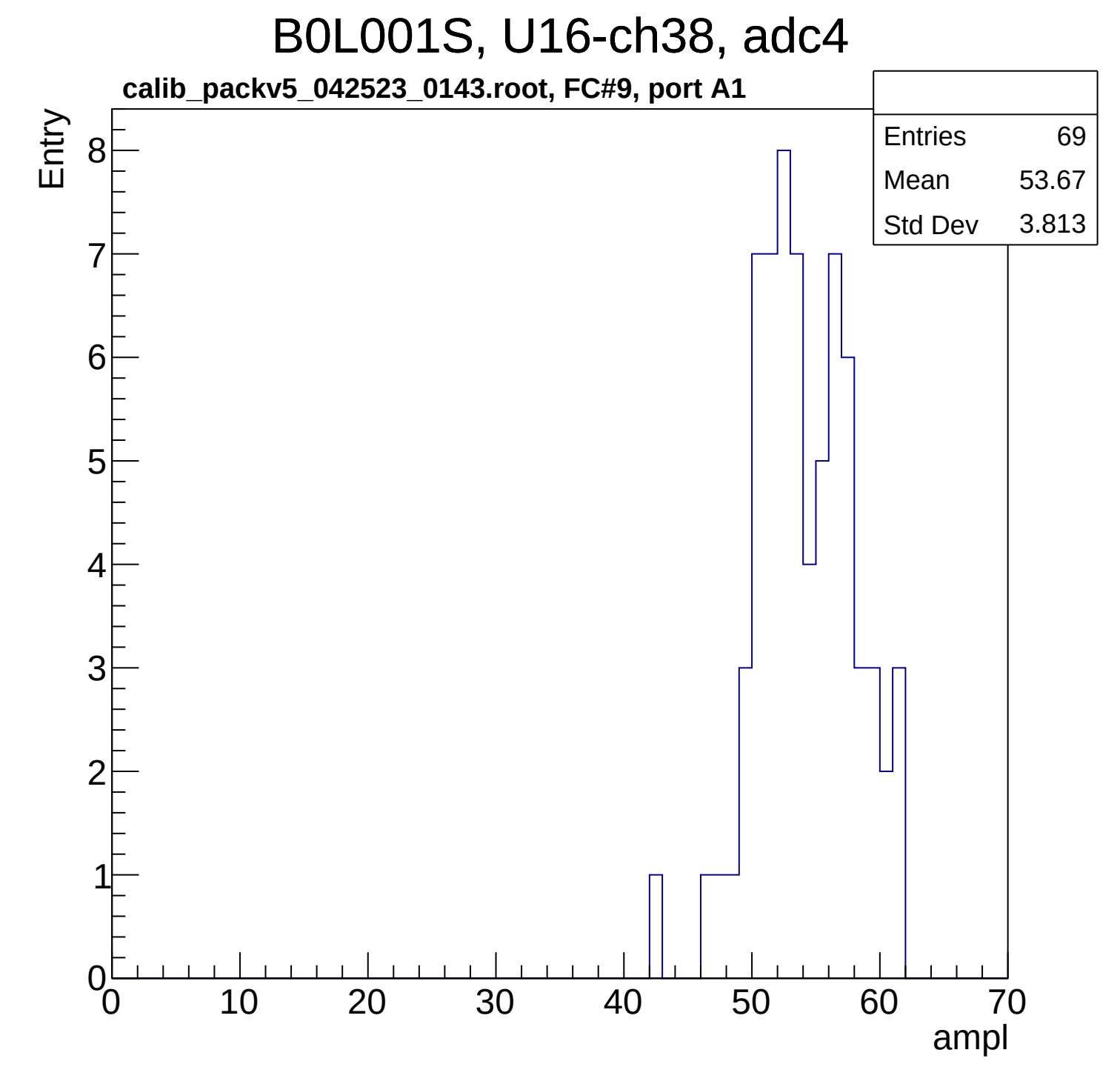
40
50
60
70


B0L001S, U16-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 53.67
Std Dev 3.813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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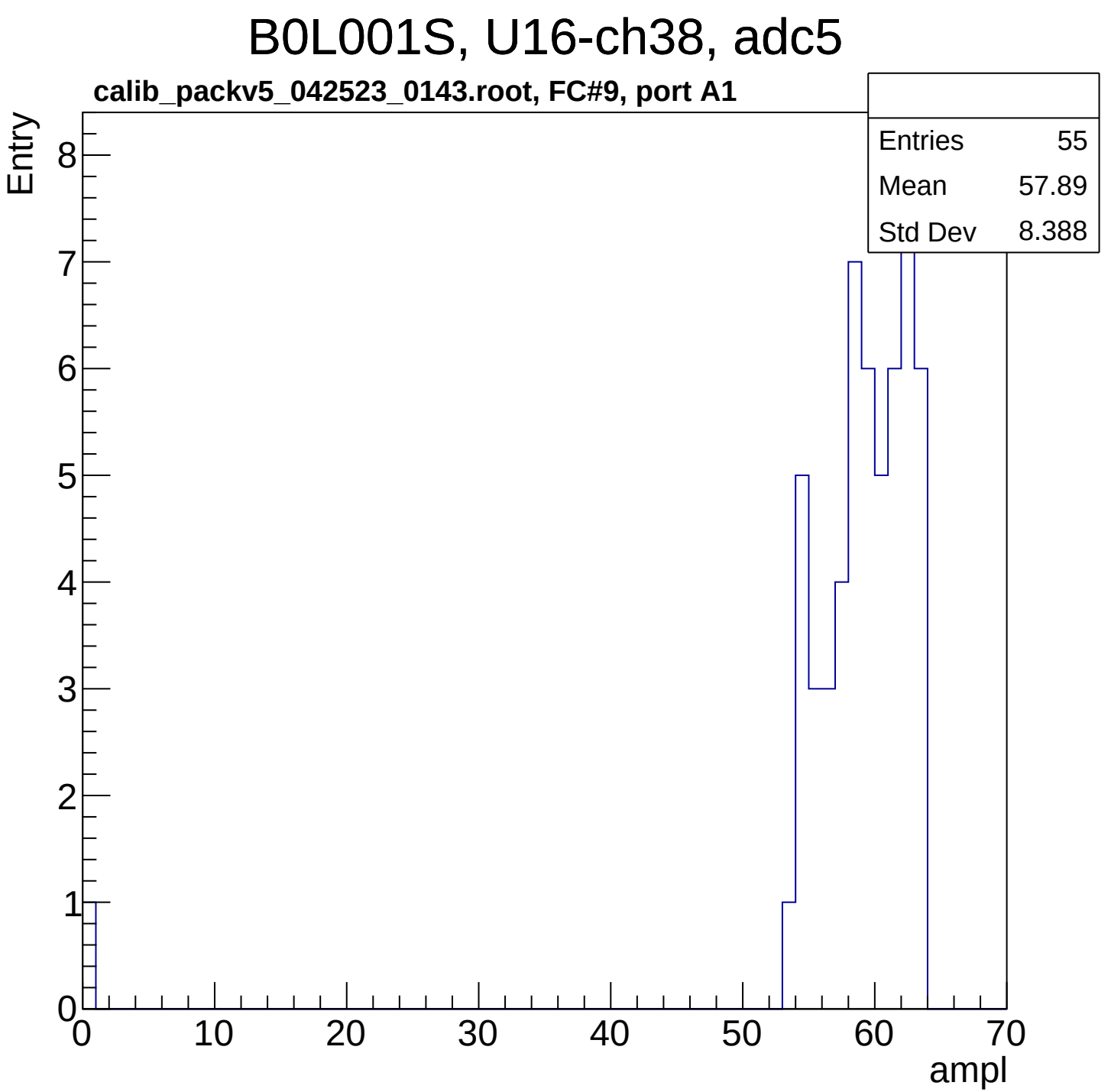
B0L001S, U16-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.89
Std Dev 8.388
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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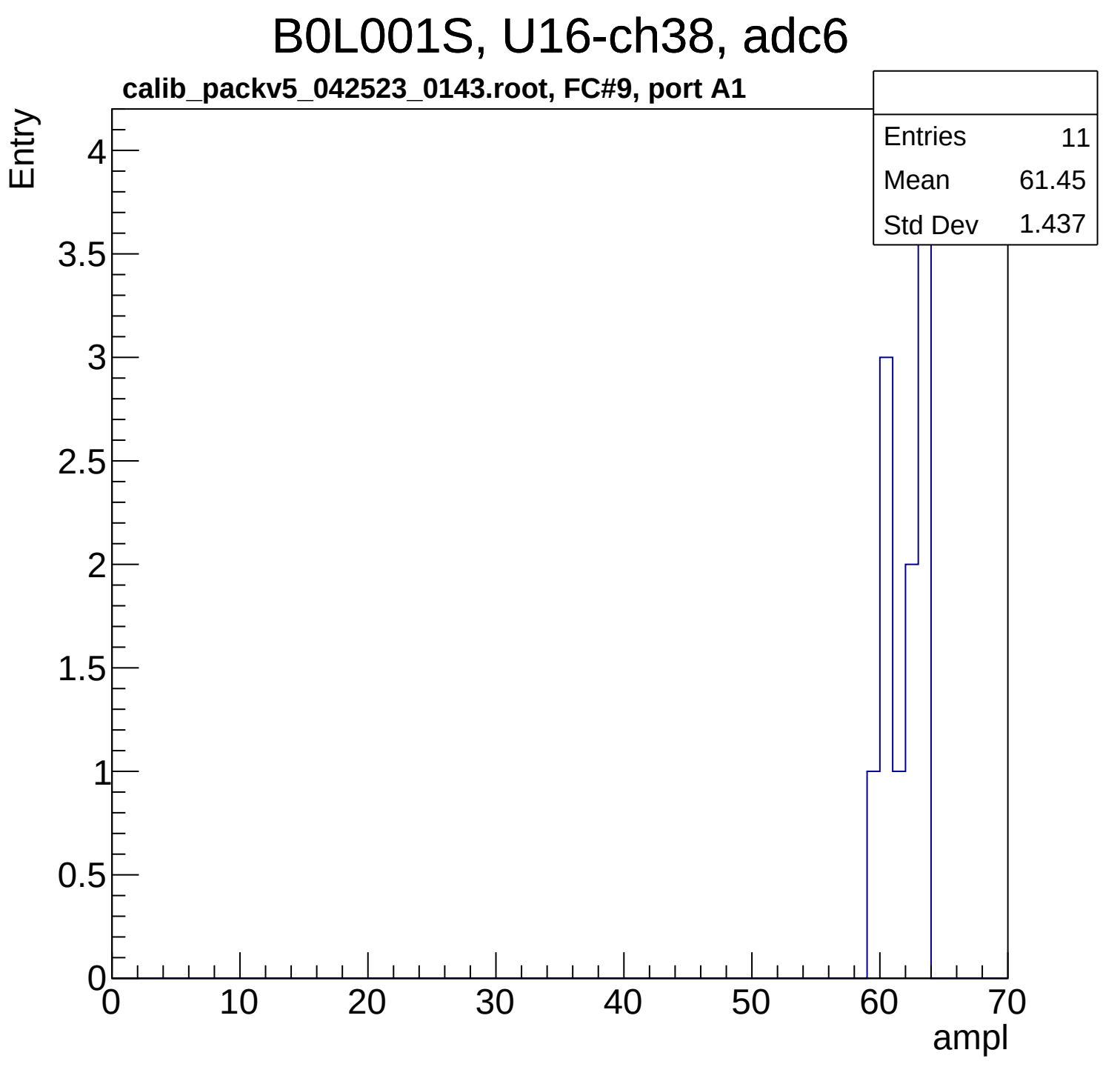
B0L001S, U16-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.45
Std Dev 1.437
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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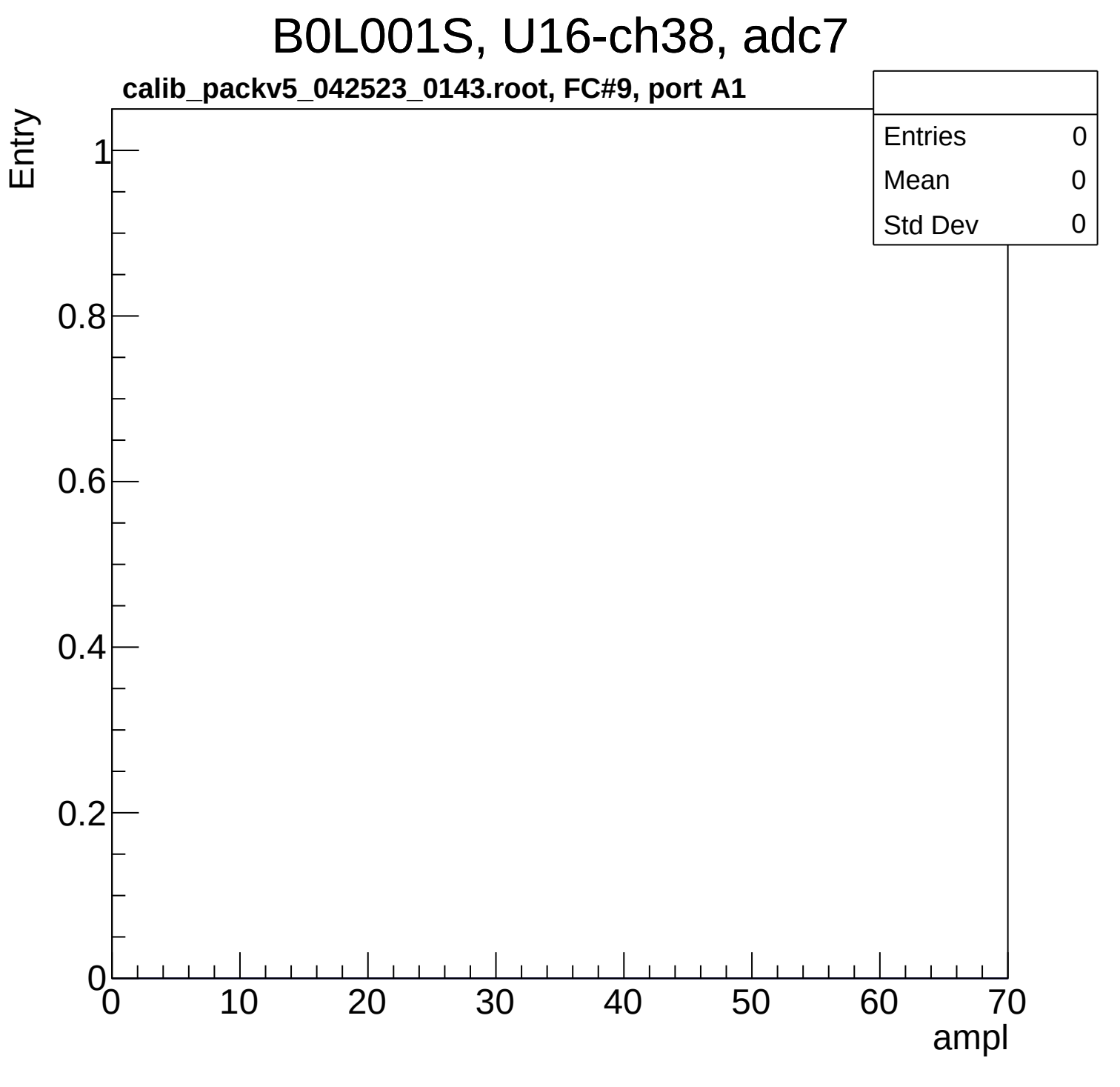
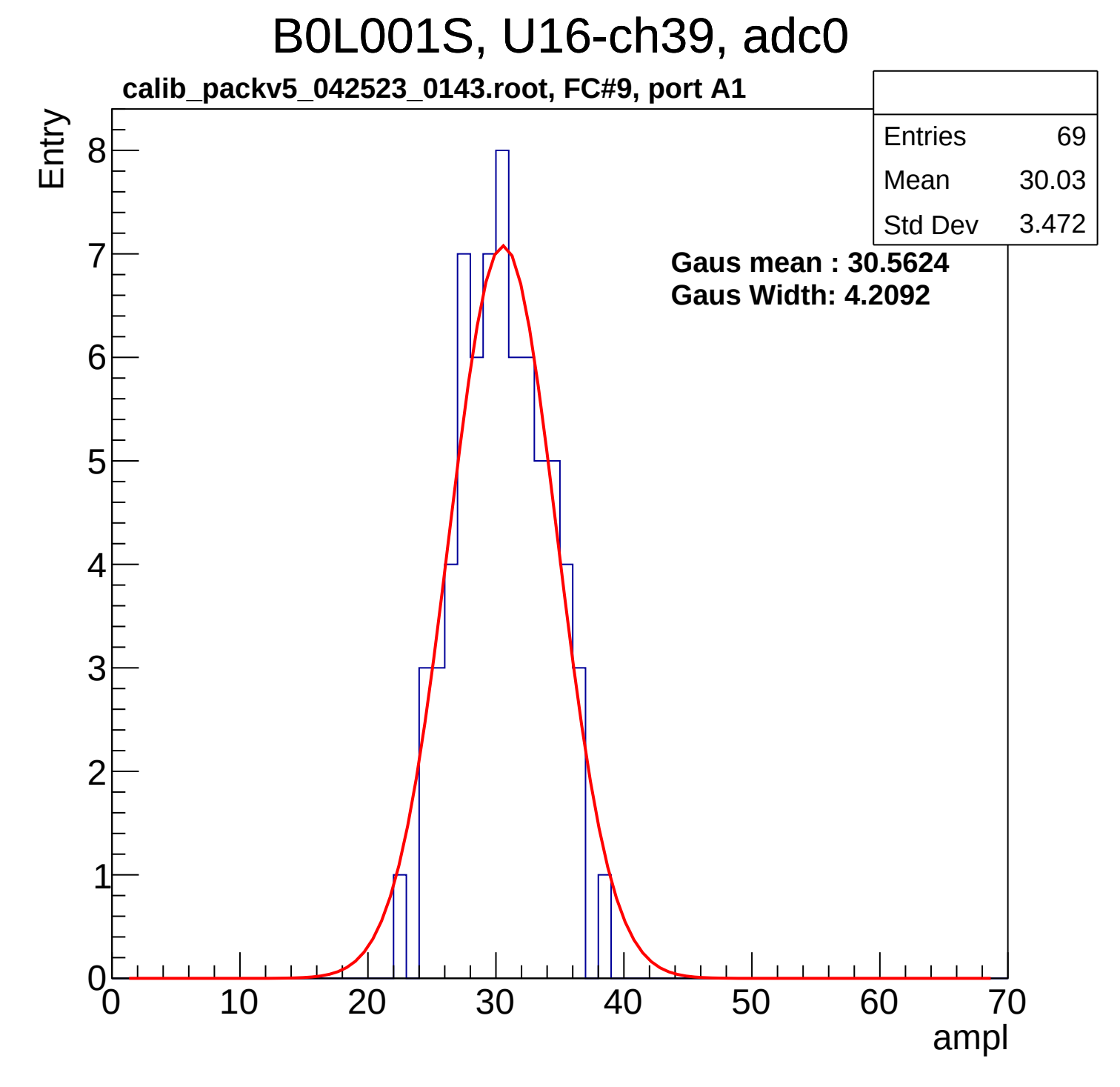

B0L001S, U16-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.03
Std Dev 3.472
Gaus mean : 30.5624
Gaus Width: 4.2092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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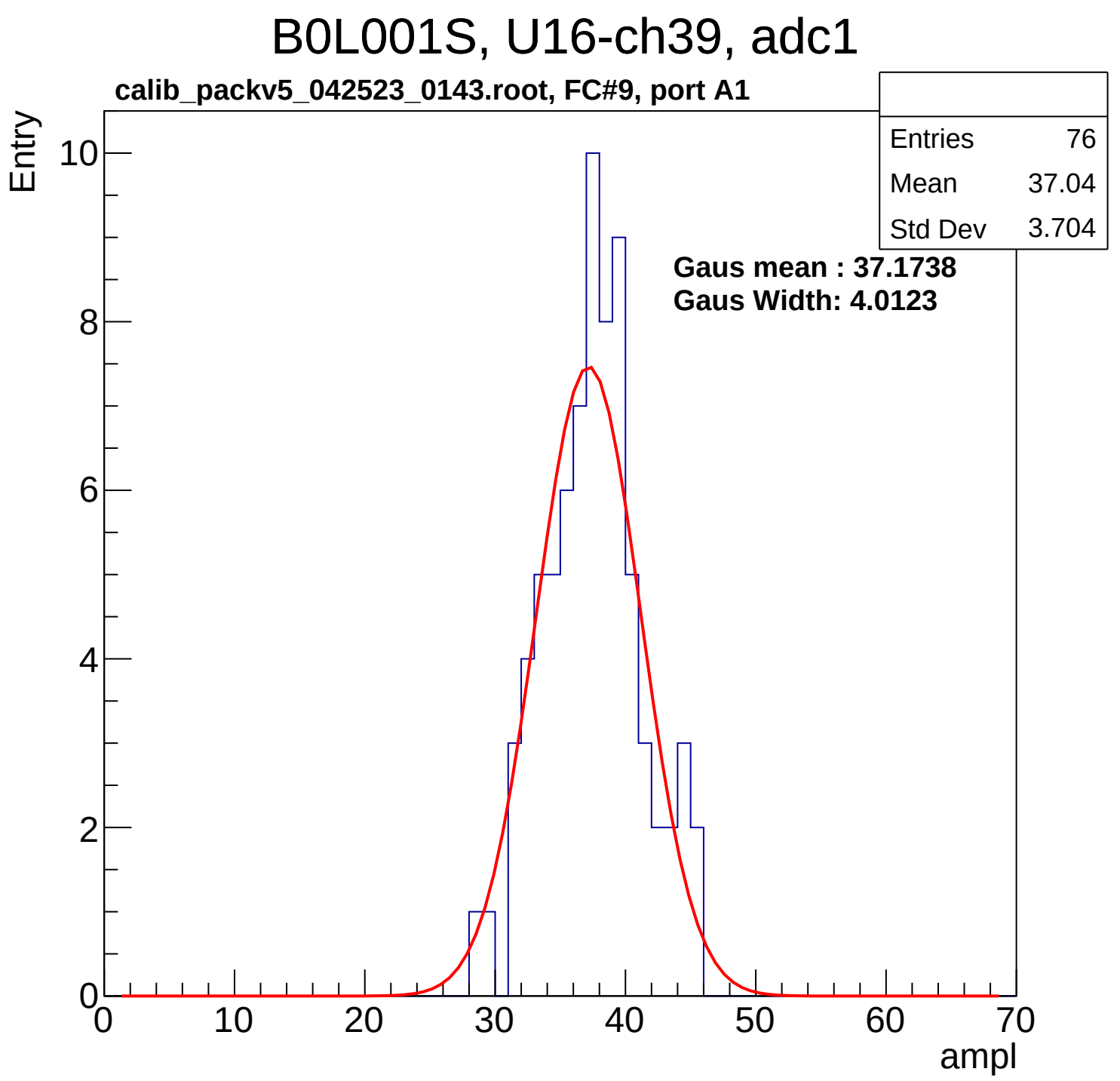
B0L001S, U16-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.04
Std Dev 3.704
Gaus mean : 37.1738
Gaus Width: 4.0123
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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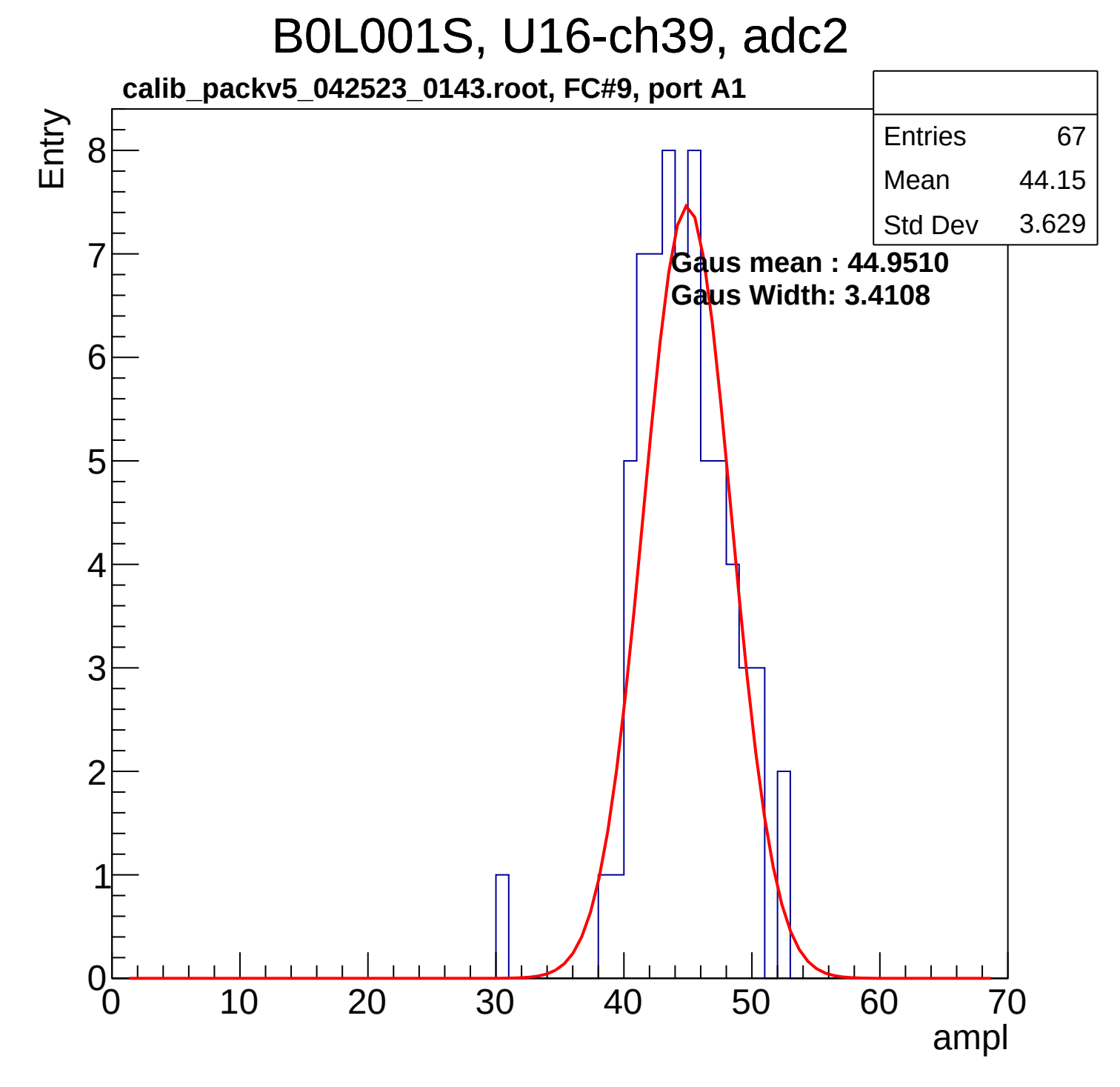
B0L001S, U16-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.15
Std Dev 3.629
Gaus mean : 44.9510
Gaus Width: 3.4108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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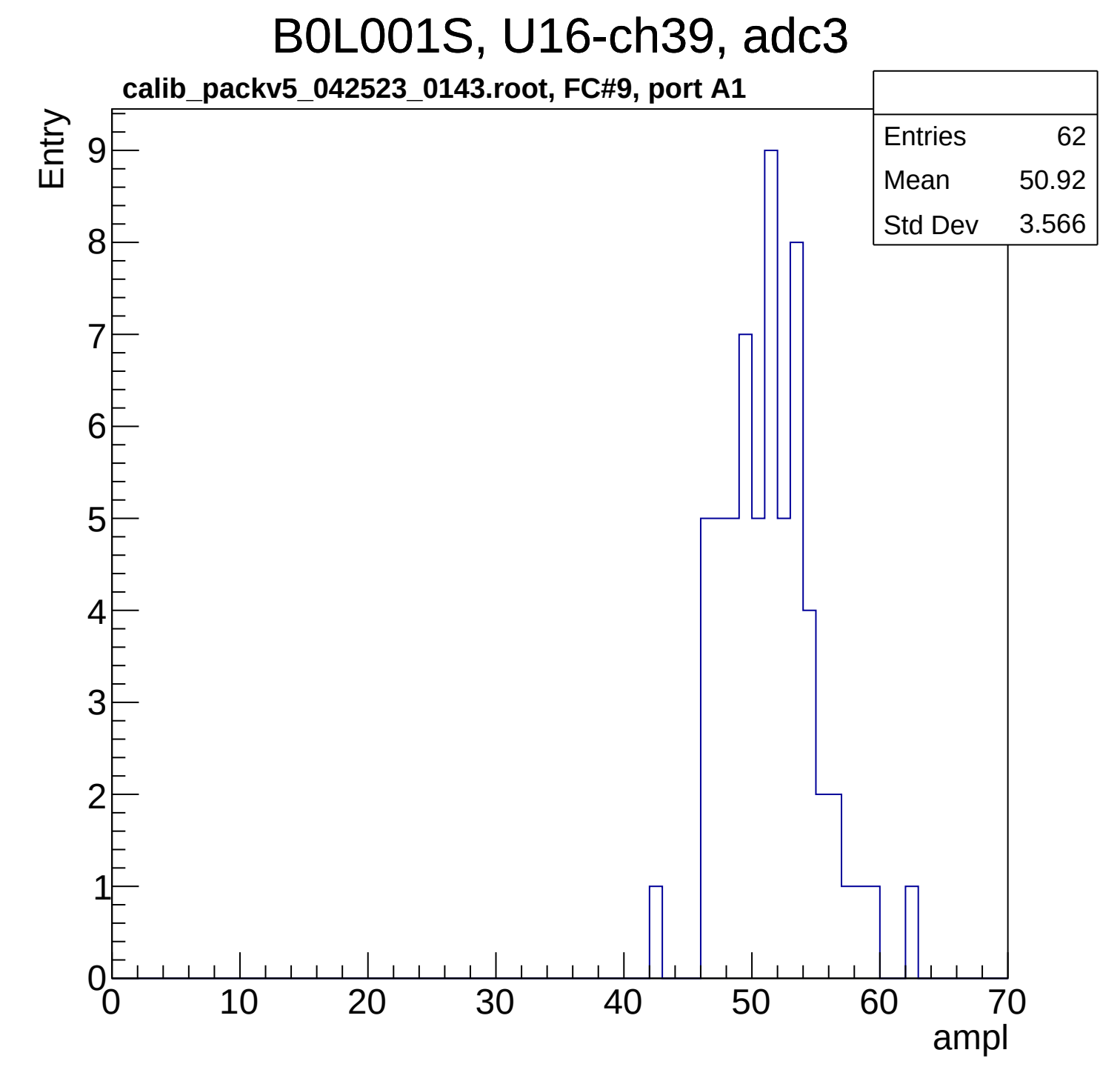
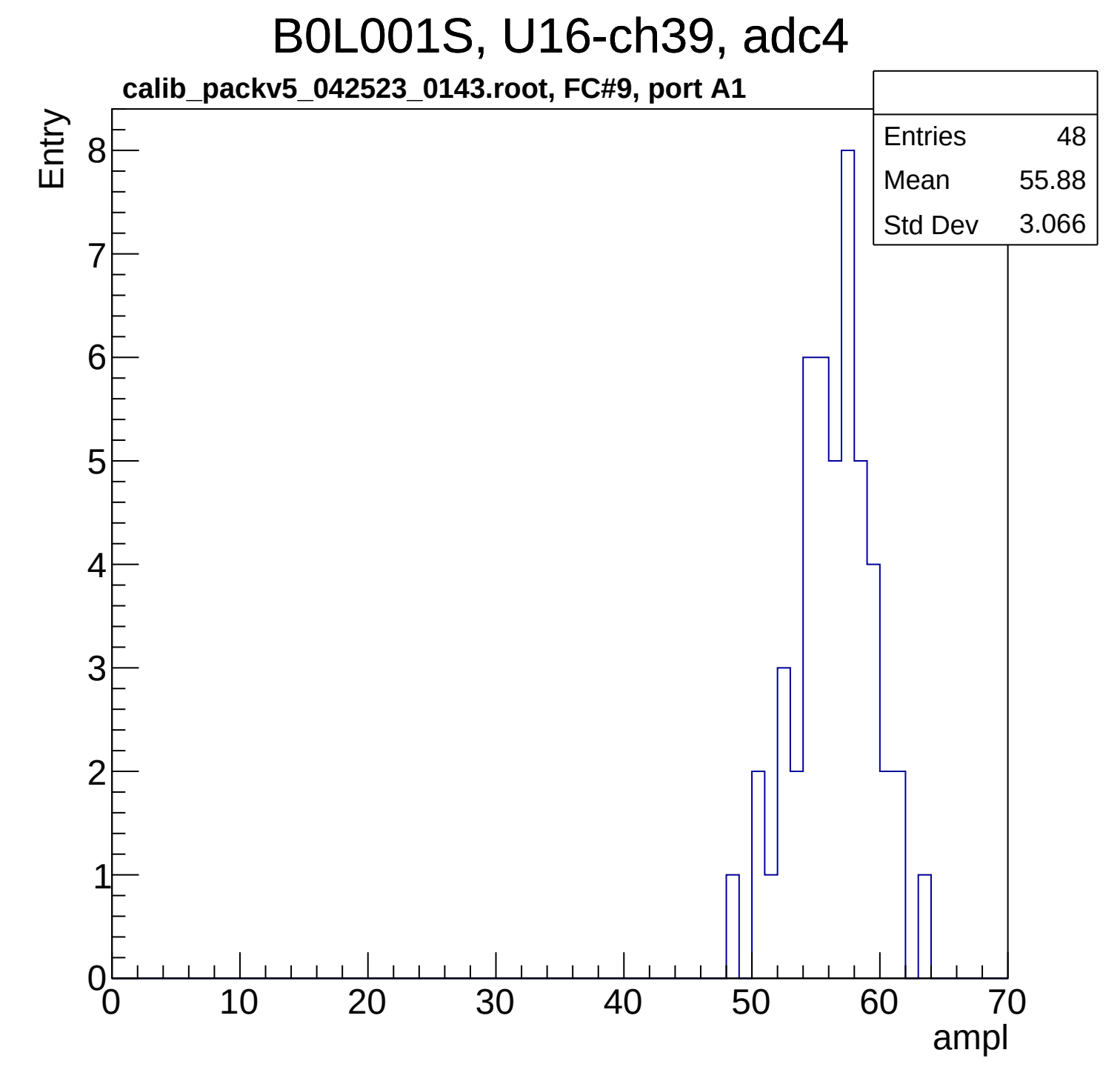
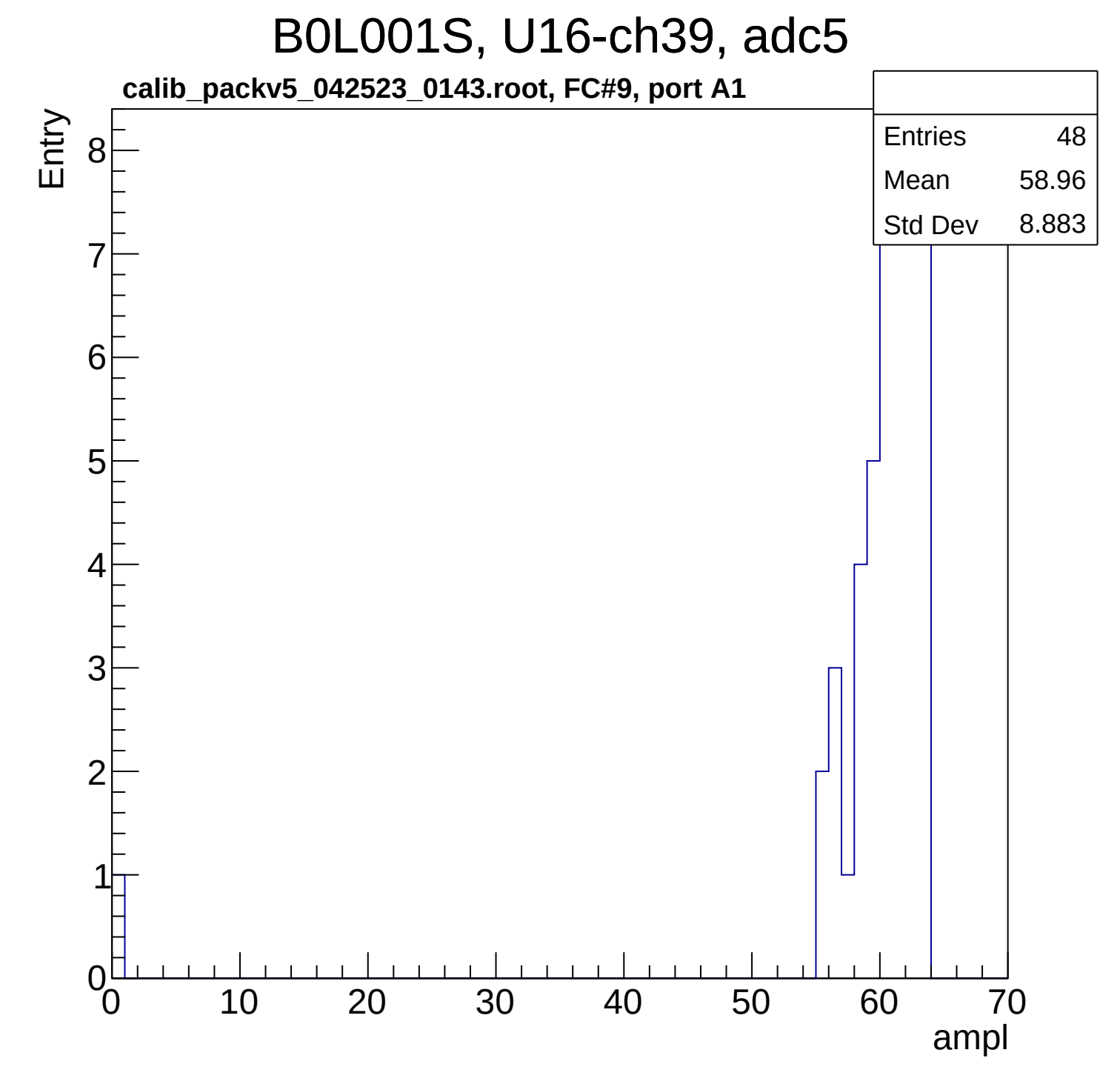

B0L001S, U16-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.92
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.88
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.96
Std Dev 8.883
Entry
ampl
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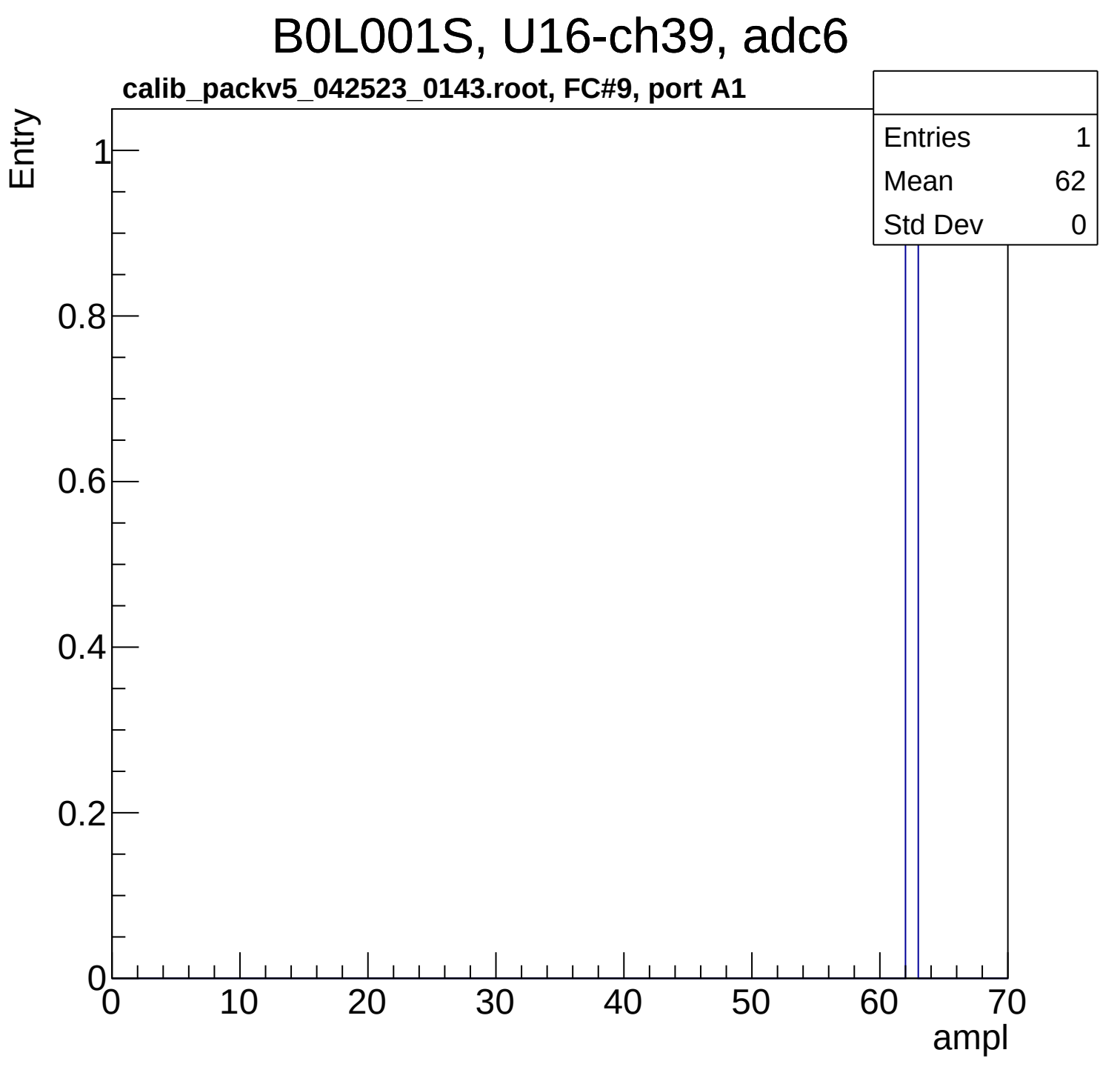
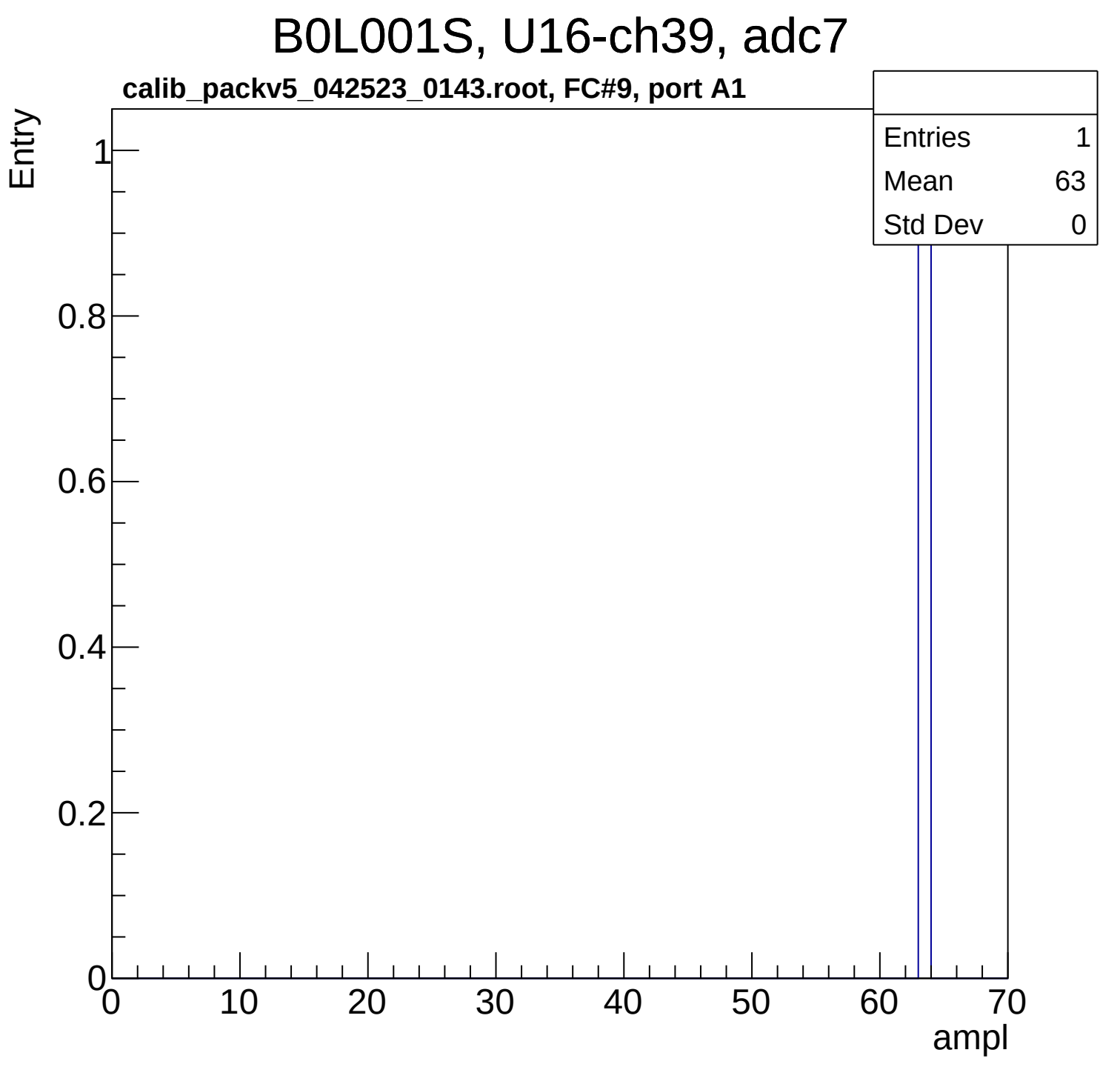
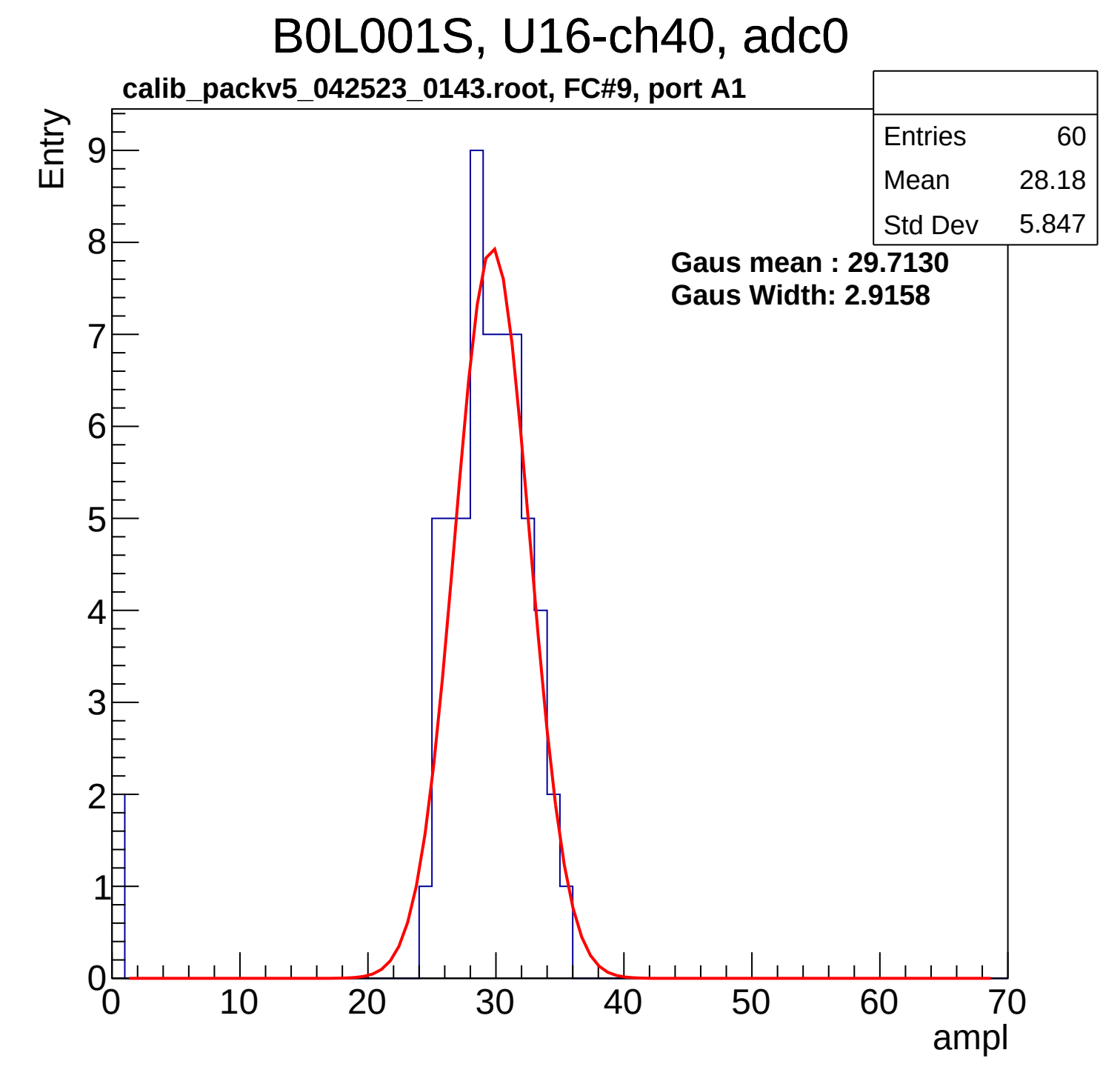
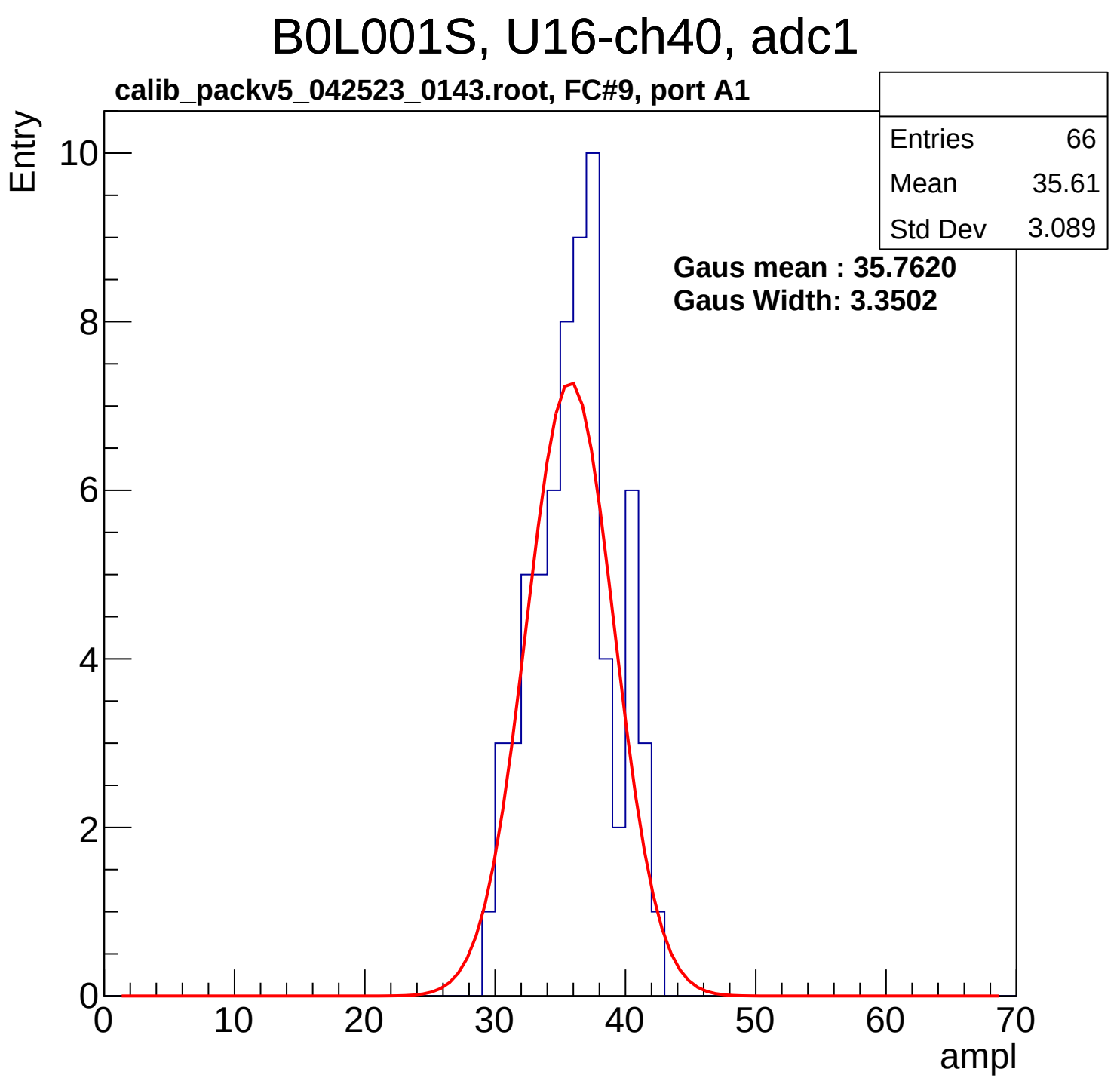
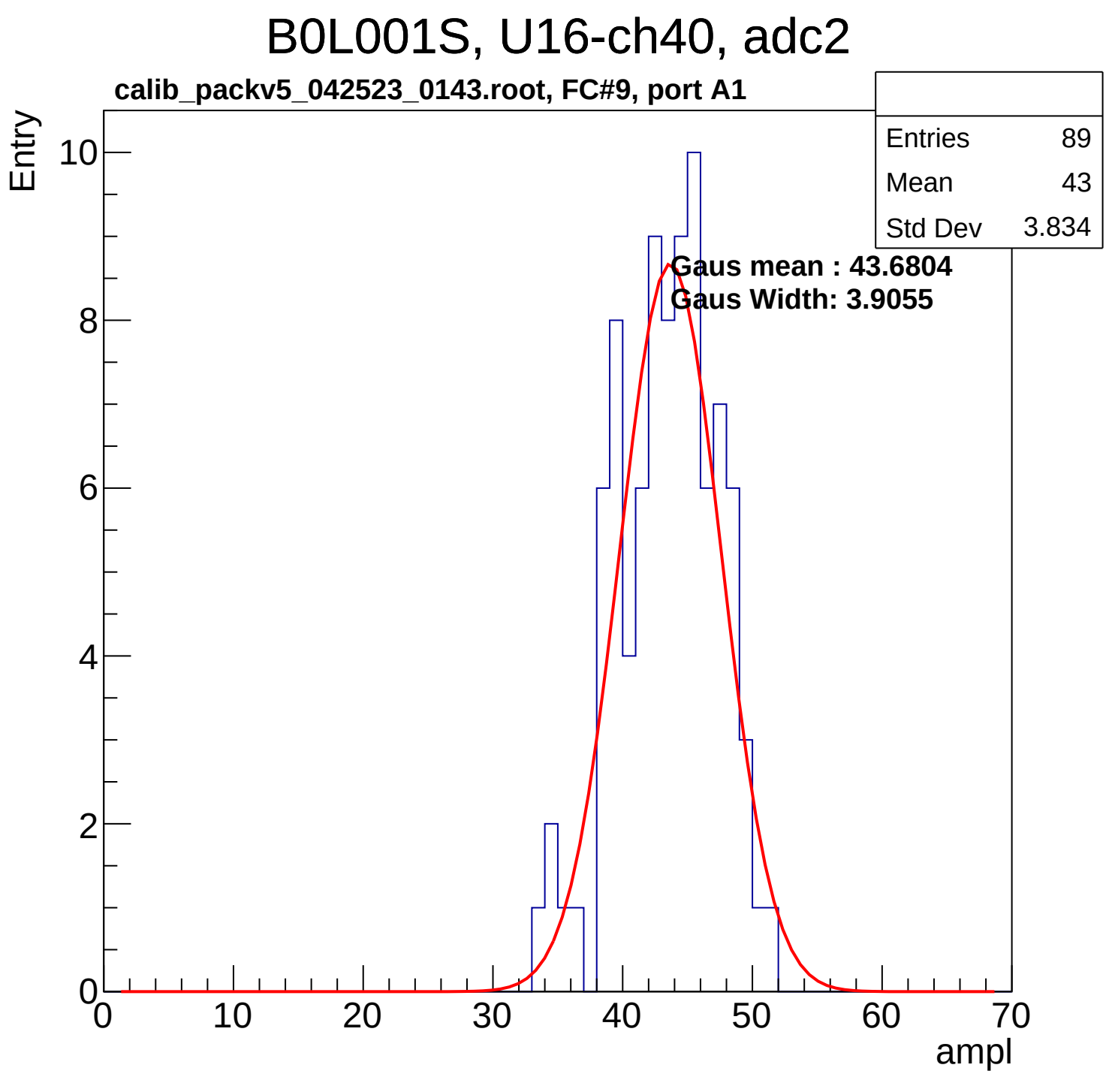
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

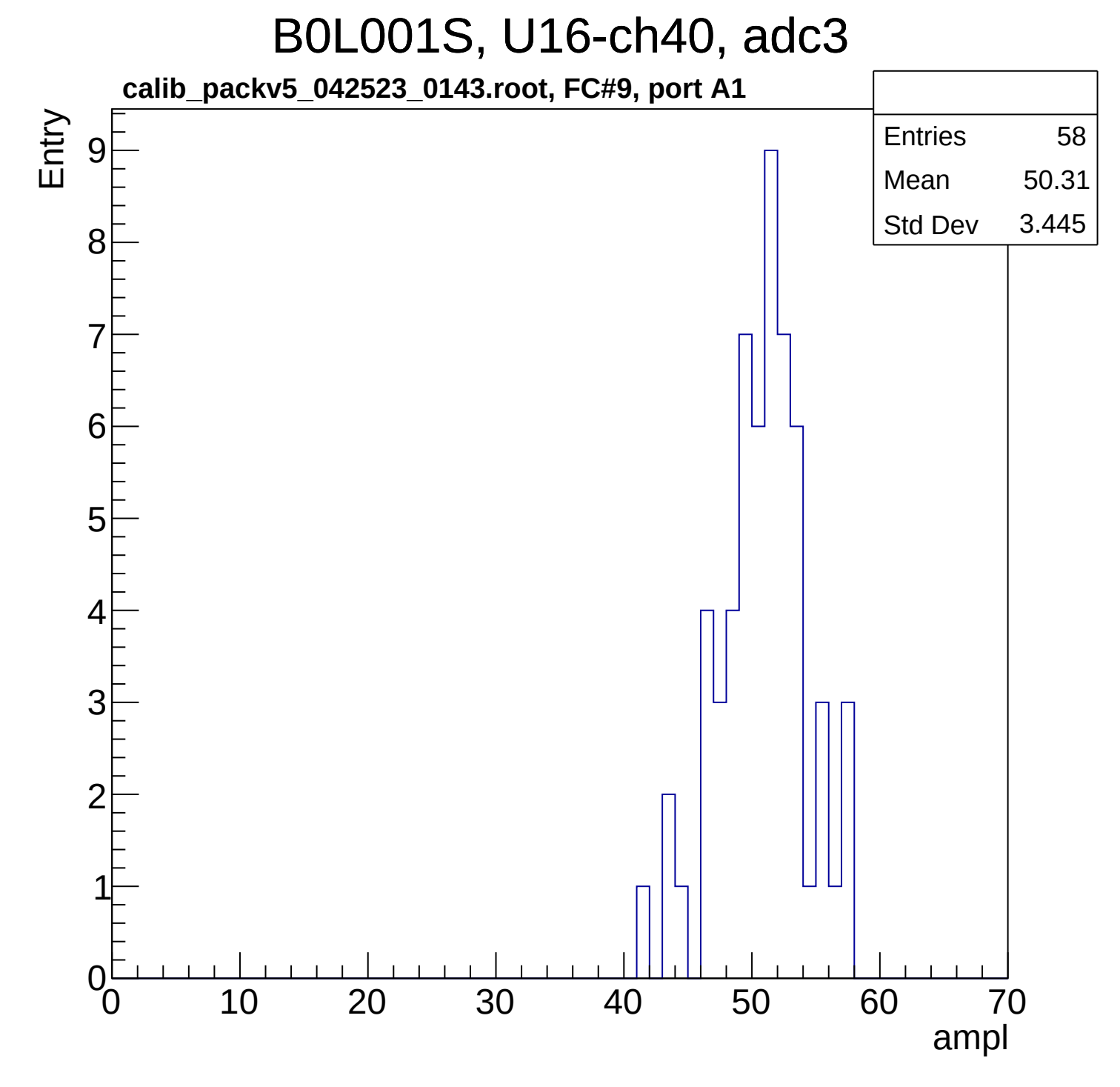

B0L001S, U16-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 28.18
Std Dev 5.847
Gaus mean : 29.7130
Gaus Width: 2.9158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 35.61
Std Dev 3.089
Gaus mean : 35.7620
Gaus Width: 3.3502
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 43
Std Dev 3.834
Gaus mean : 43.6804
Gaus Width: 3.9055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.31
Std Dev 3.445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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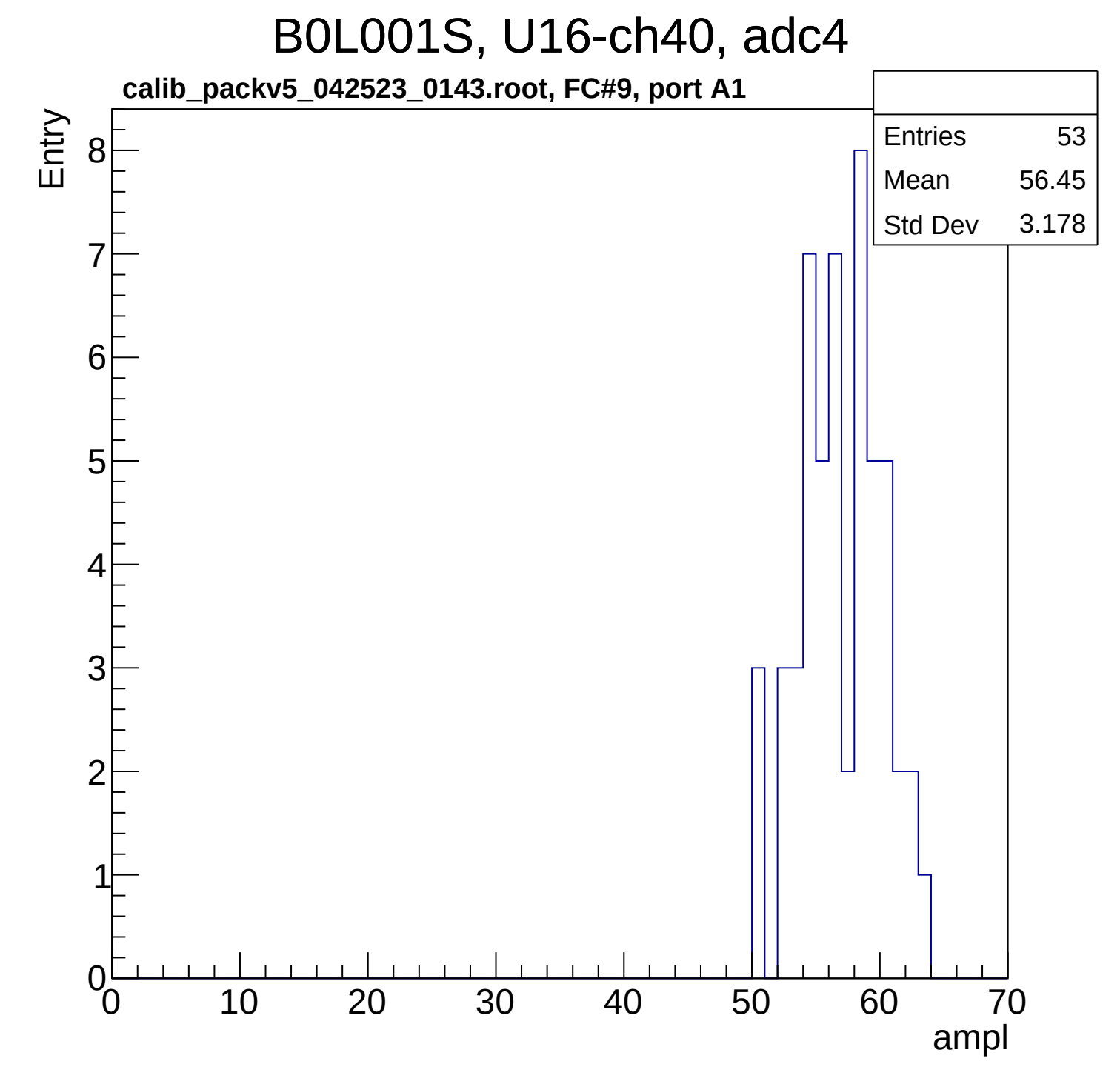
B0L001S, U16-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.45
Std Dev 3.178
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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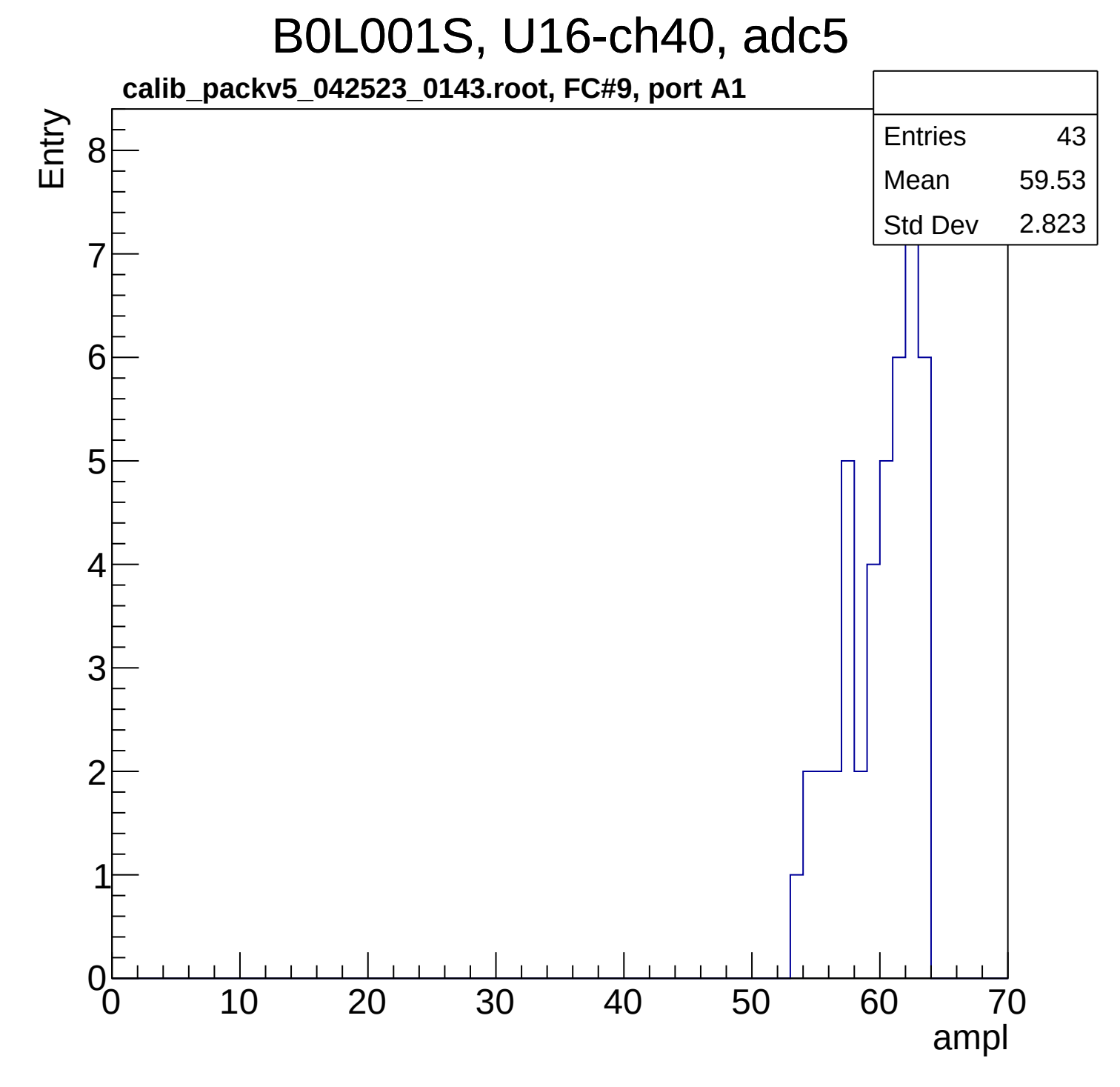
B0L001S, U16-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.53
Std Dev 2.823
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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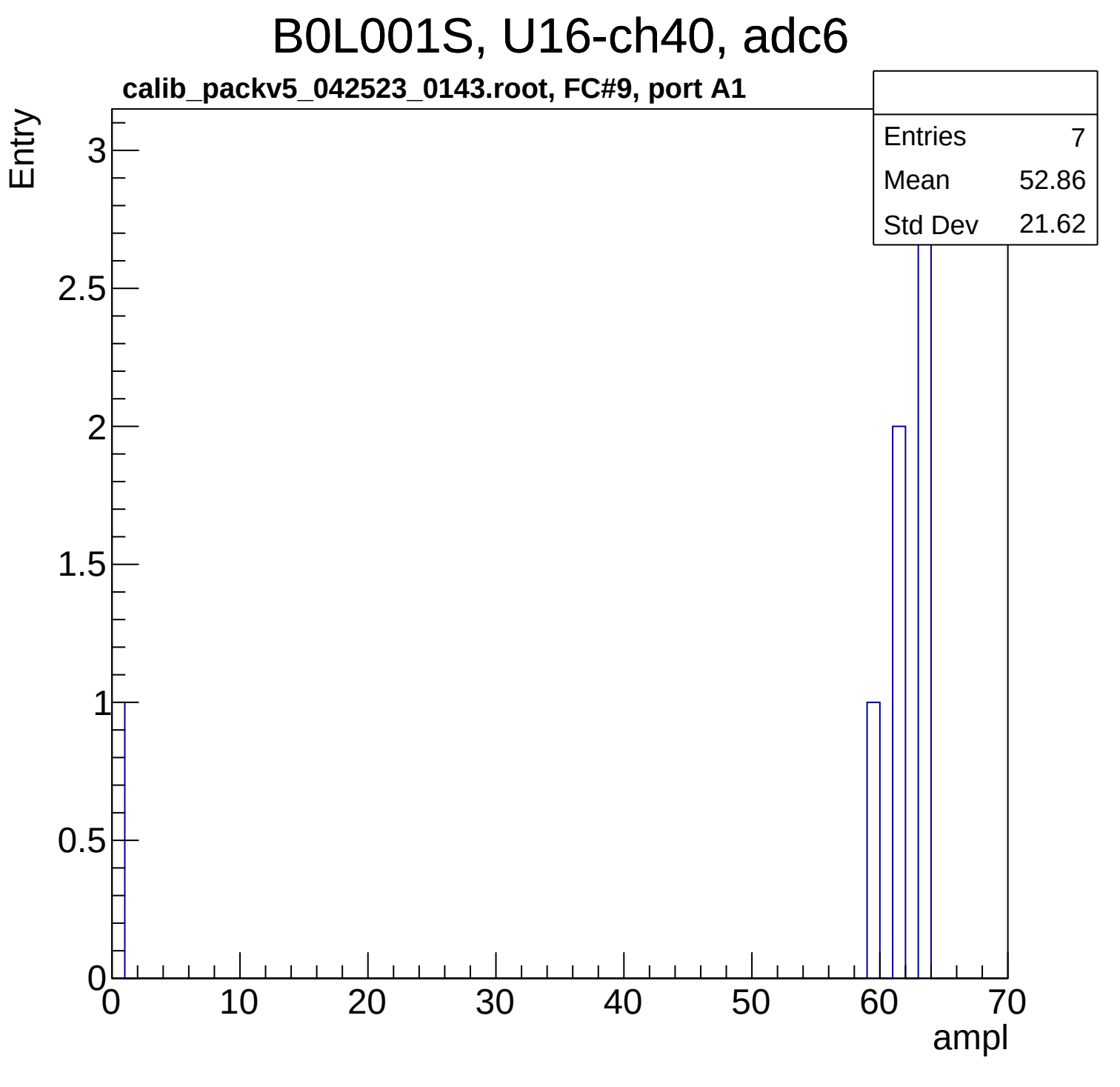
B0L001S, U16-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 52.86
Std Dev 21.62
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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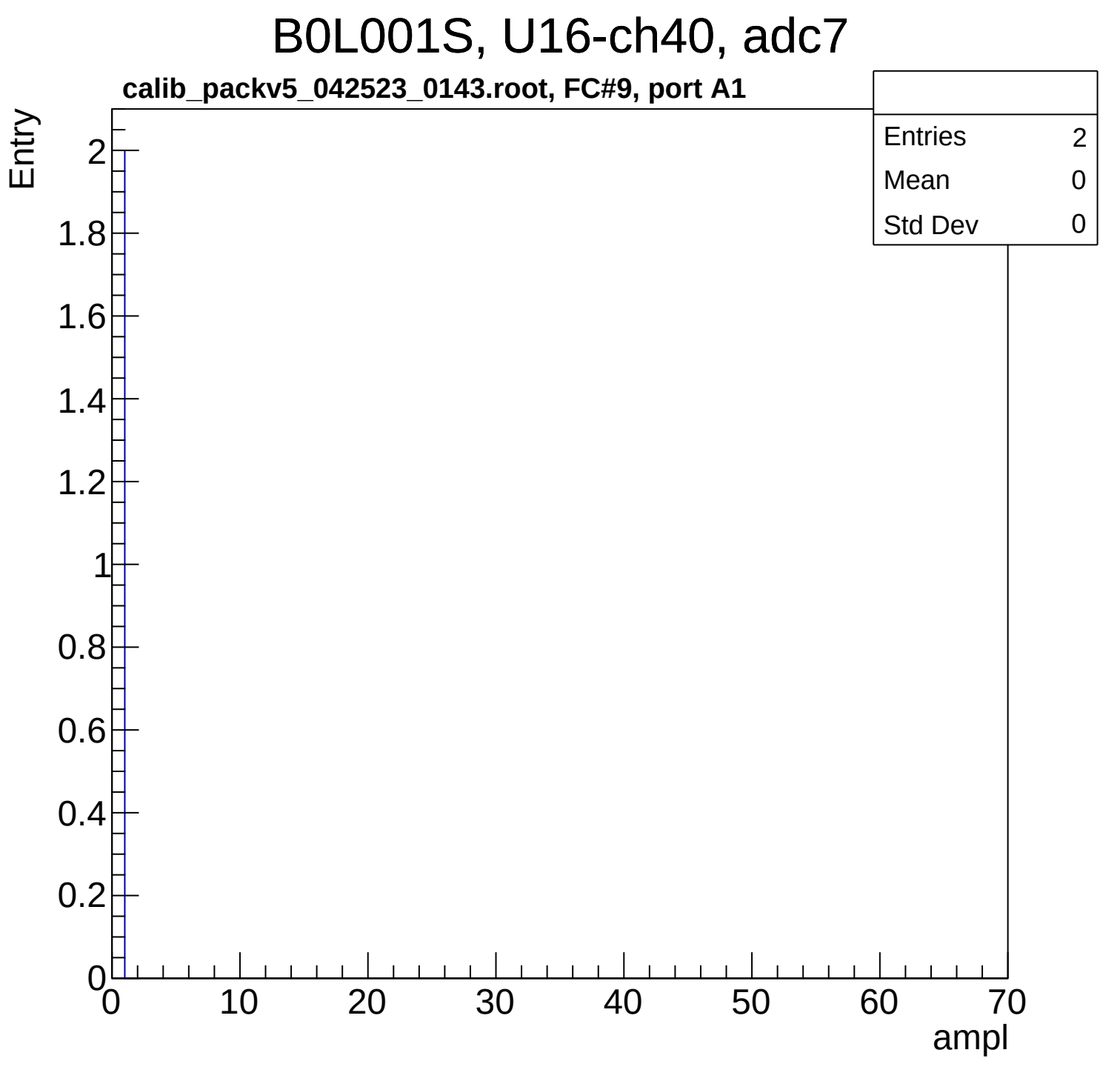
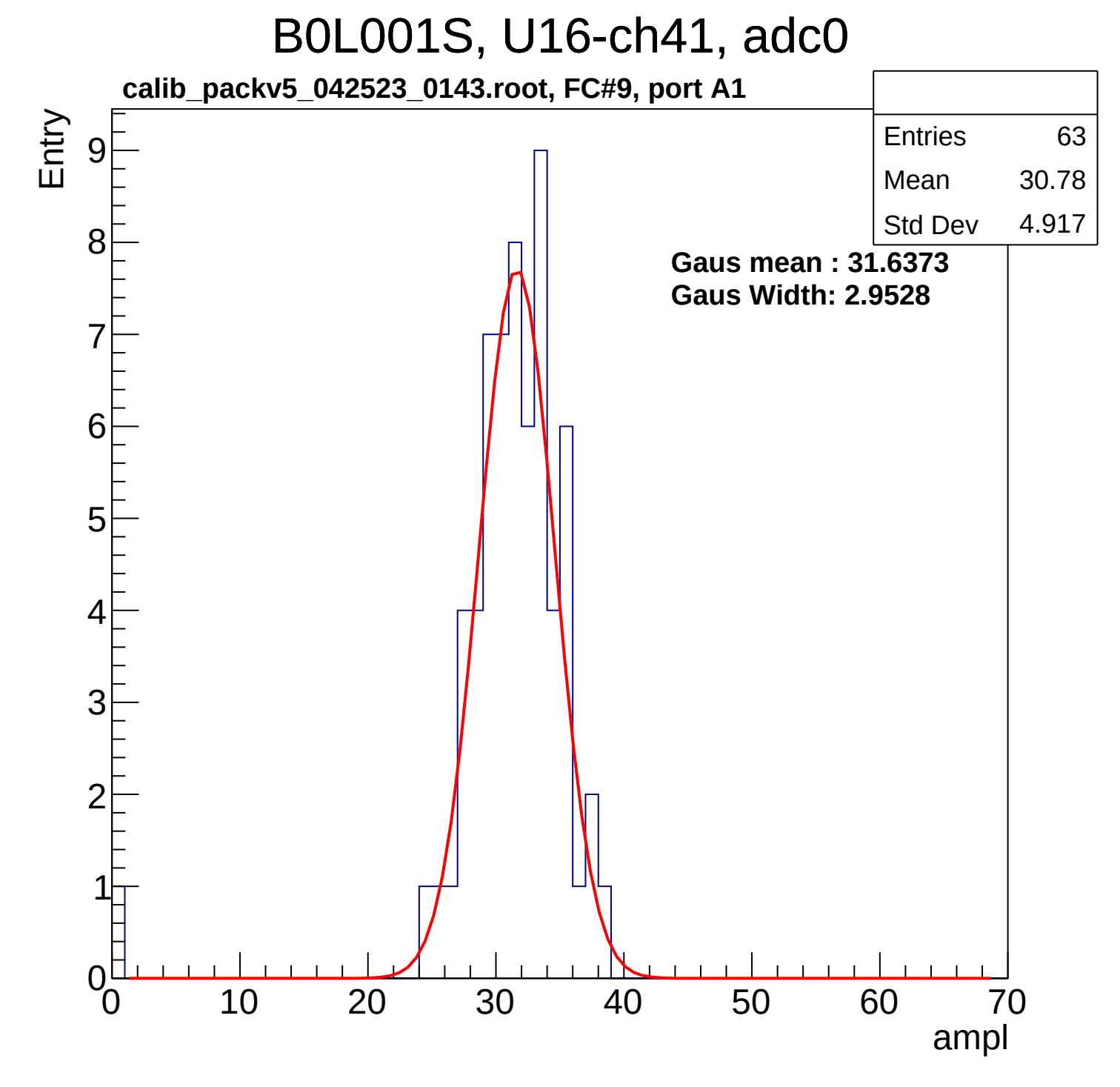
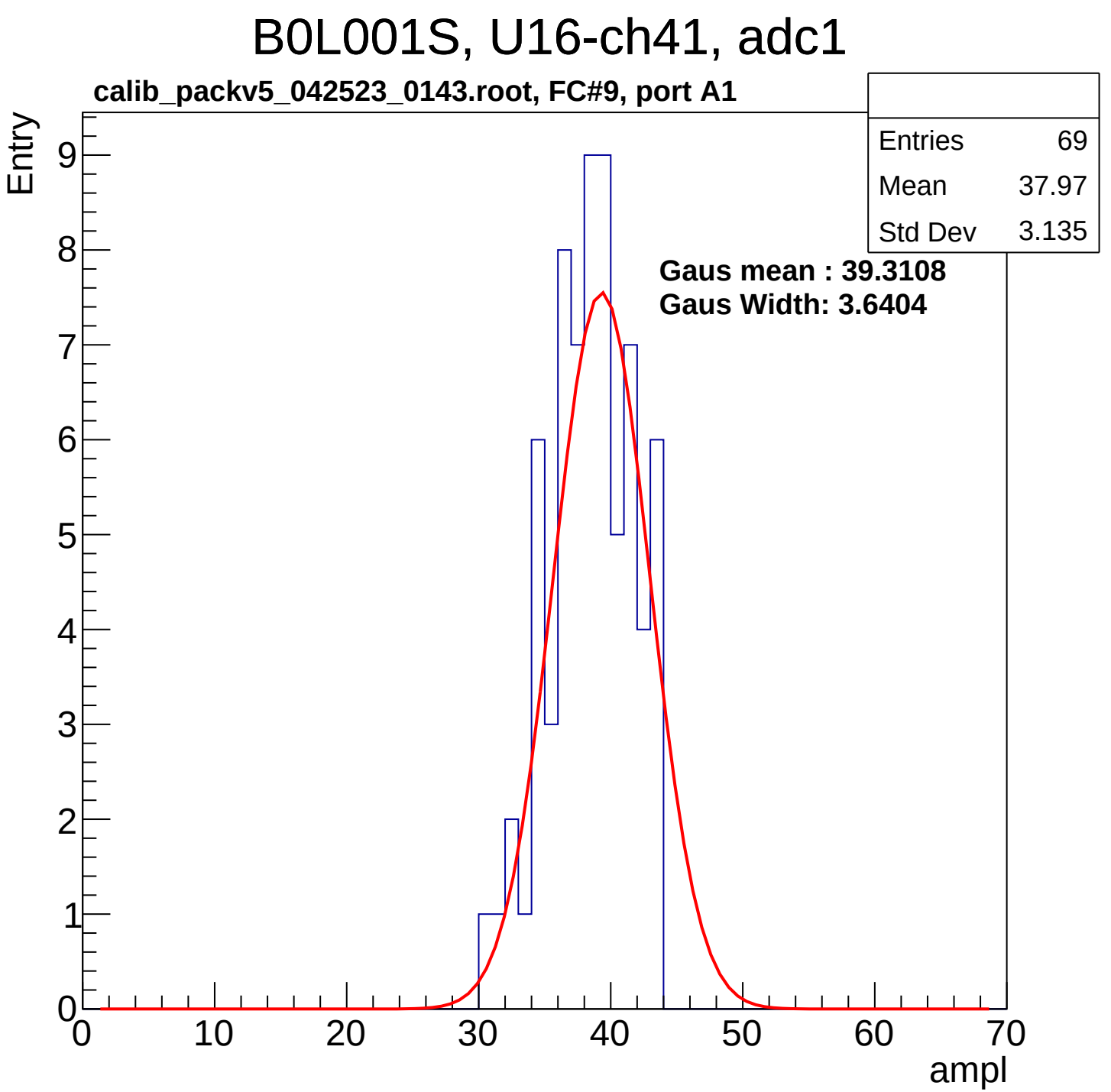
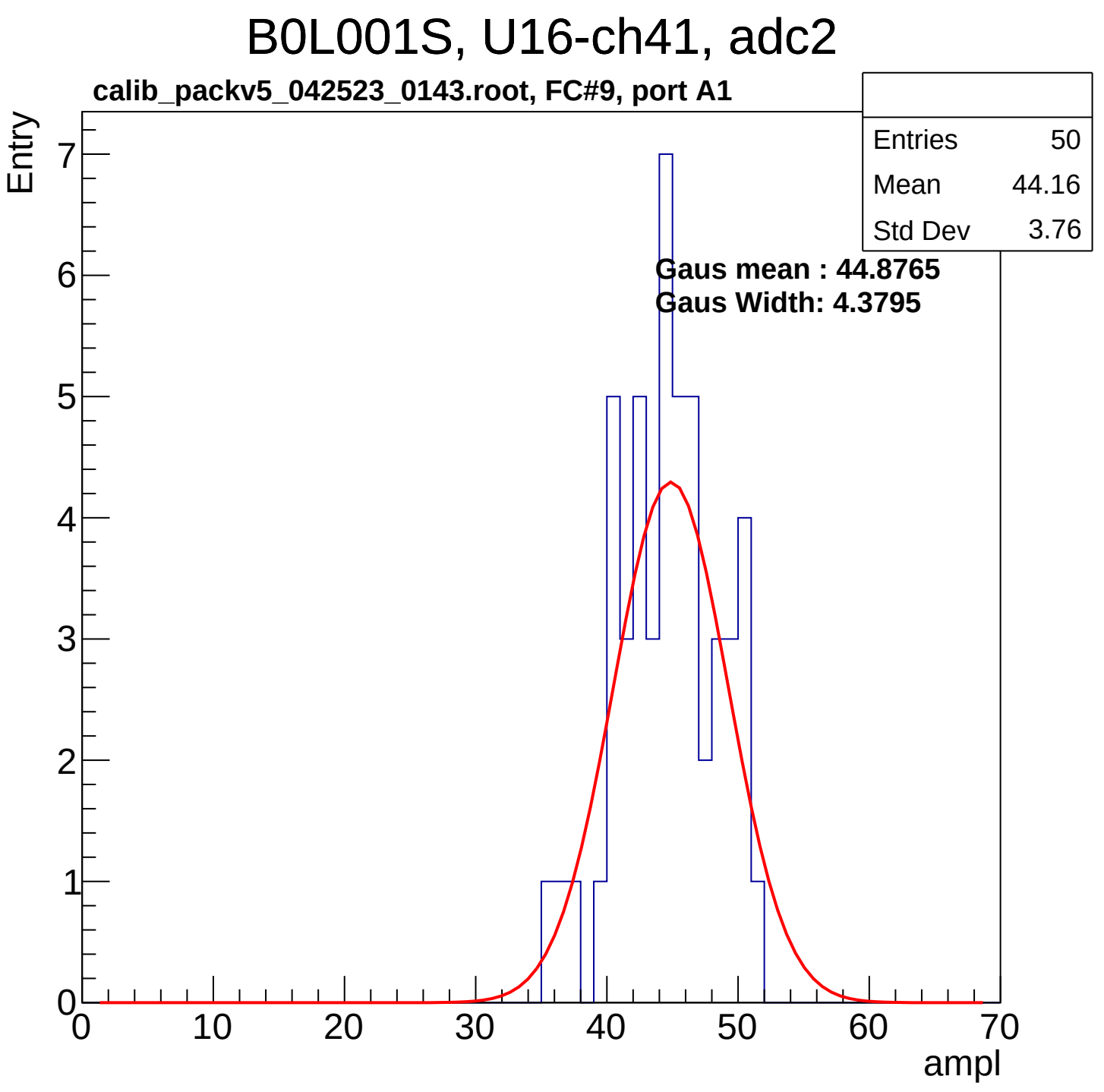
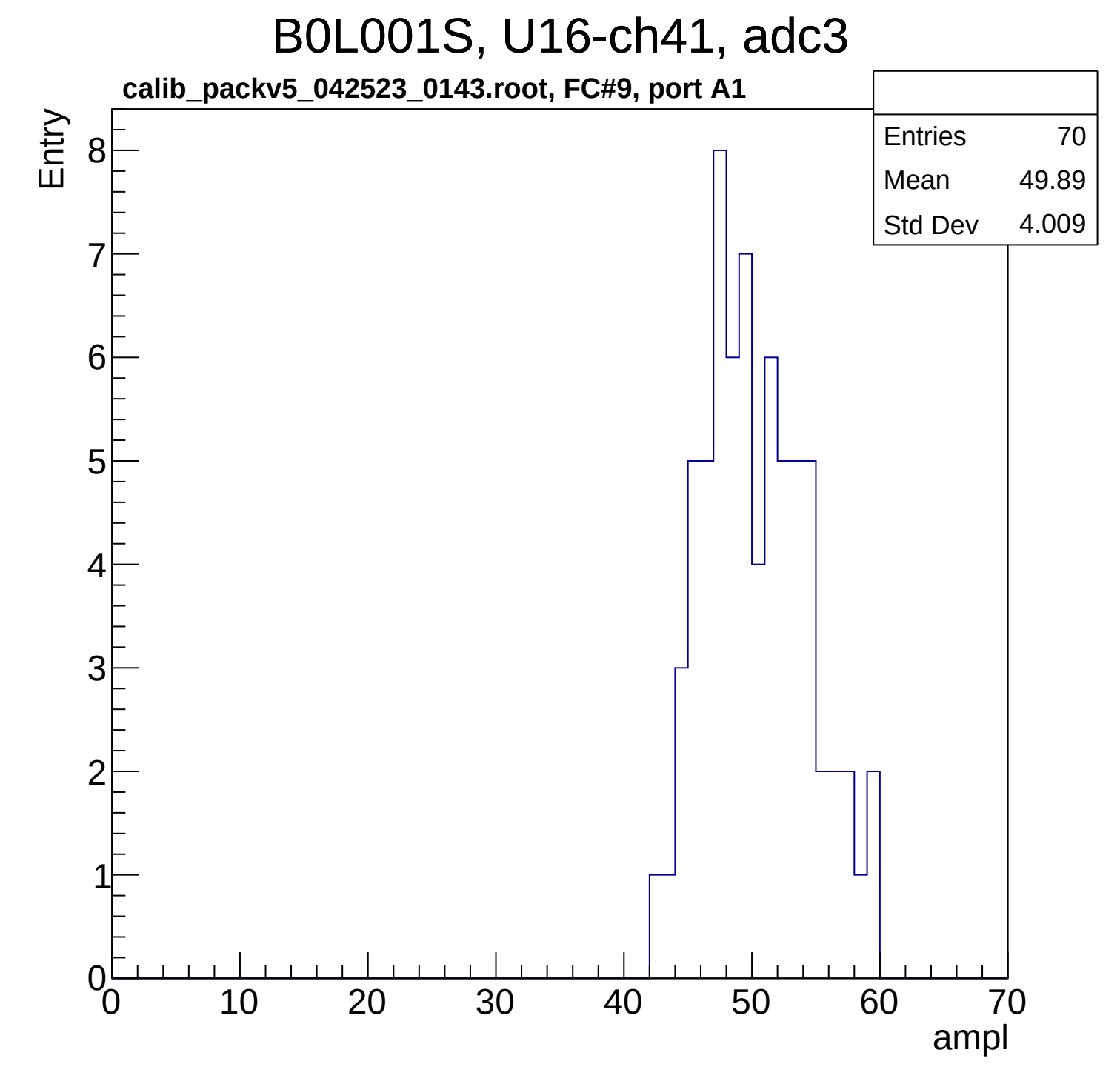

B0L001S, U16-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.78
Std Dev 4.917
Gaus mean : 31.6373
Gaus Width: 2.9528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.97
Std Dev 3.135
Gaus mean : 39.3108
Gaus Width: 3.6404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 44.16
Std Dev 3.76
Gaus mean : 44.8765
Gaus Width: 4.3795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.89
Std Dev 4.009
Entry
0
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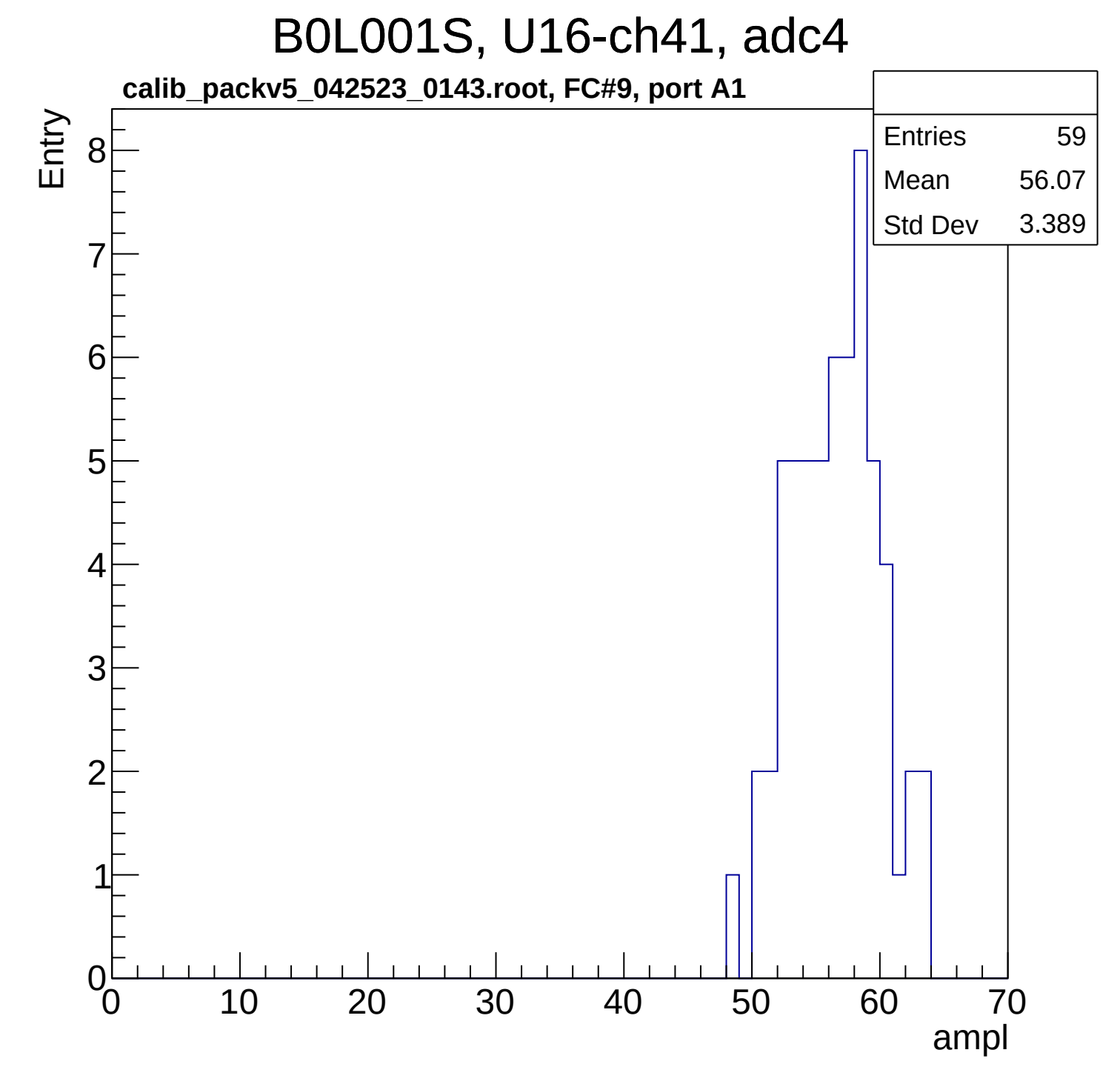
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.07
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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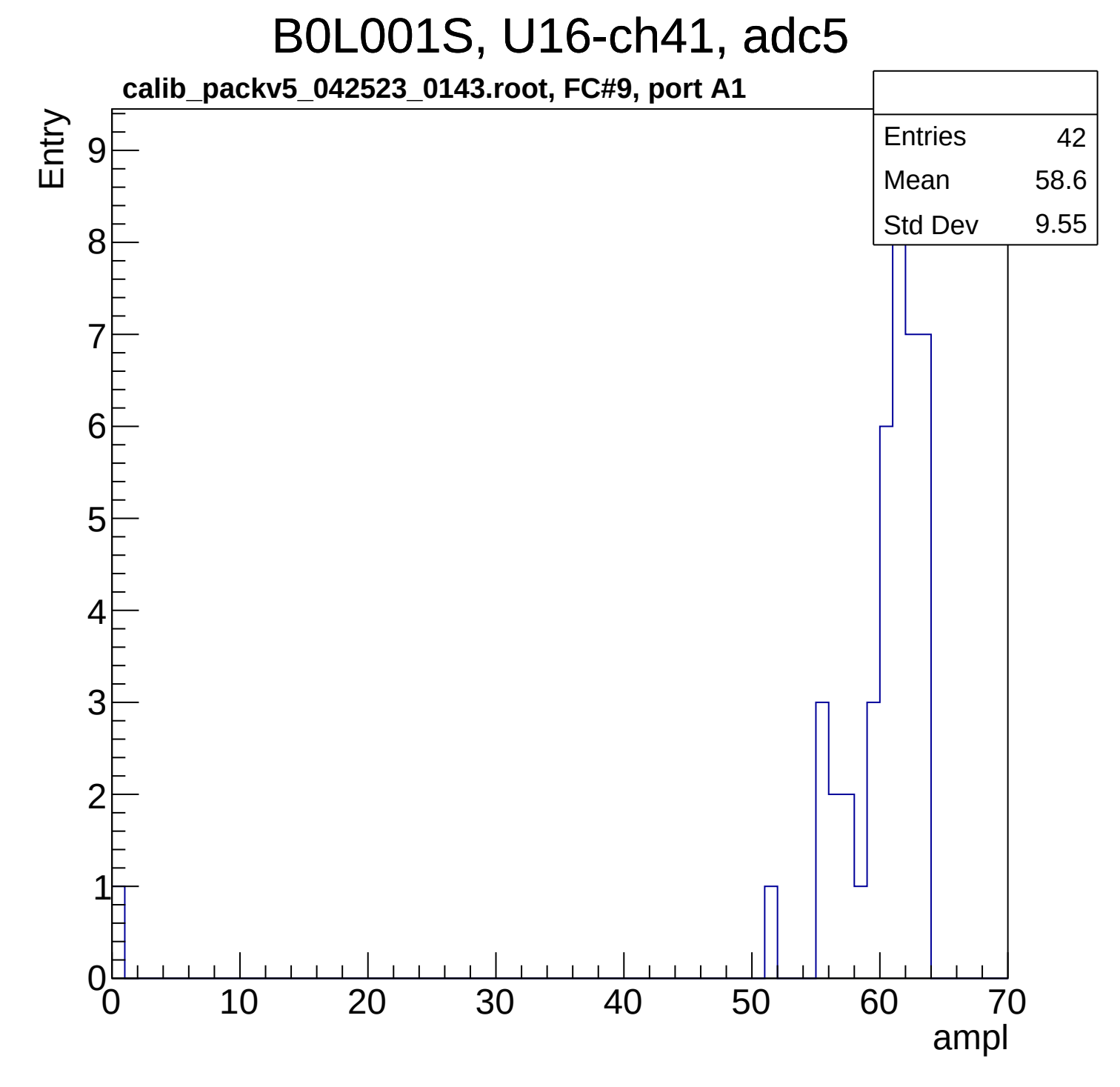
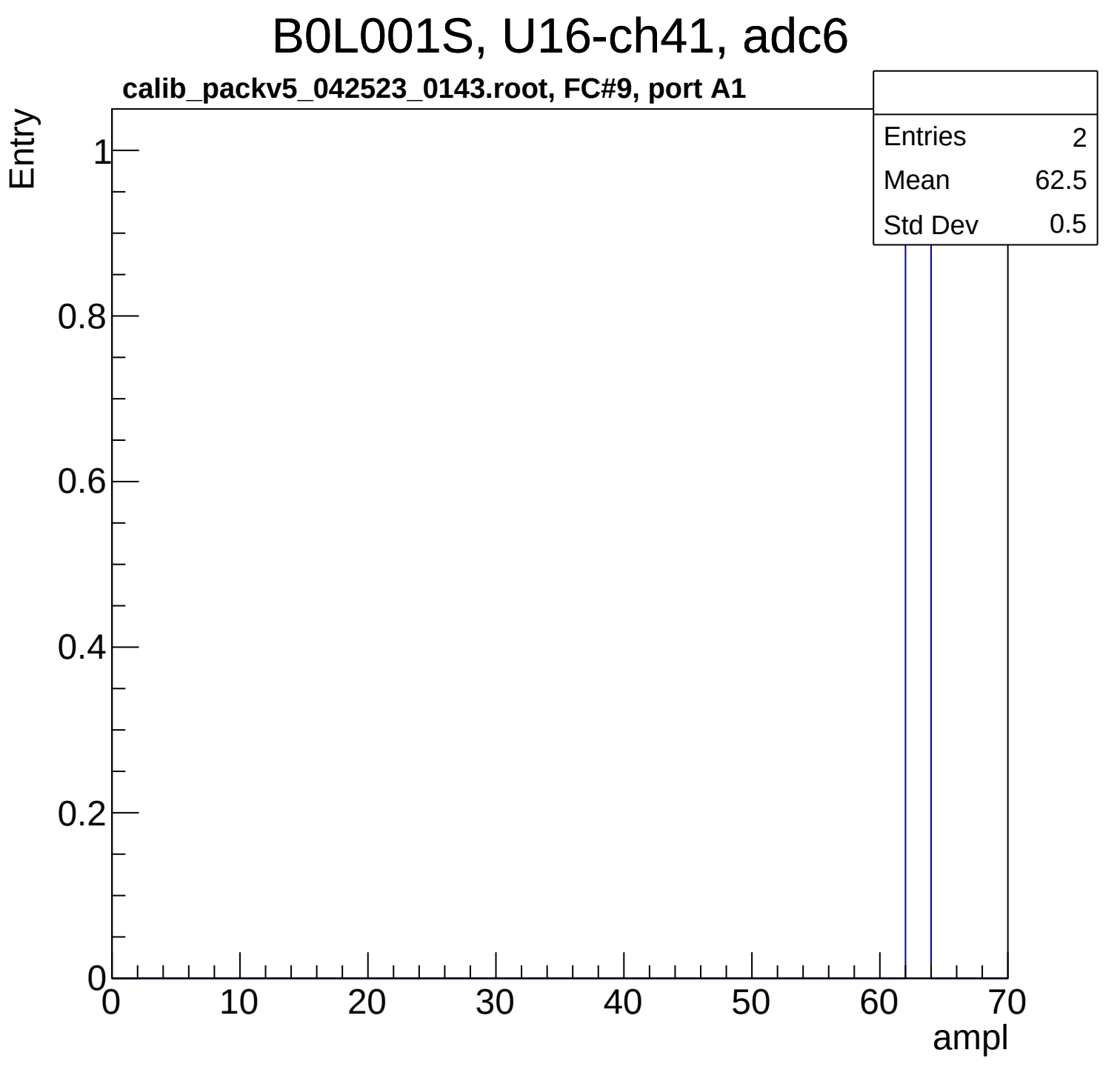
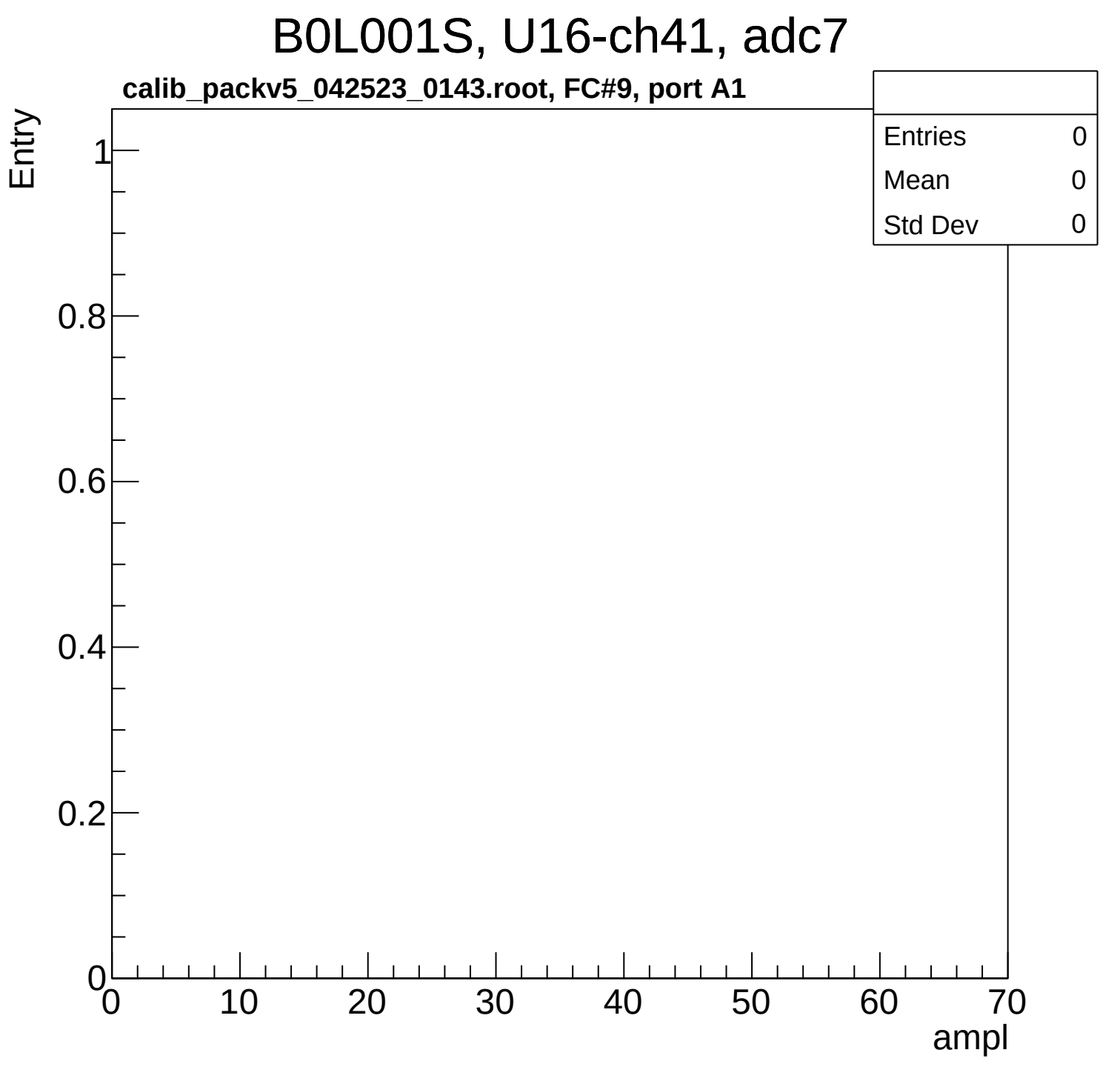

B0L001S, U16-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.6
Std Dev 9.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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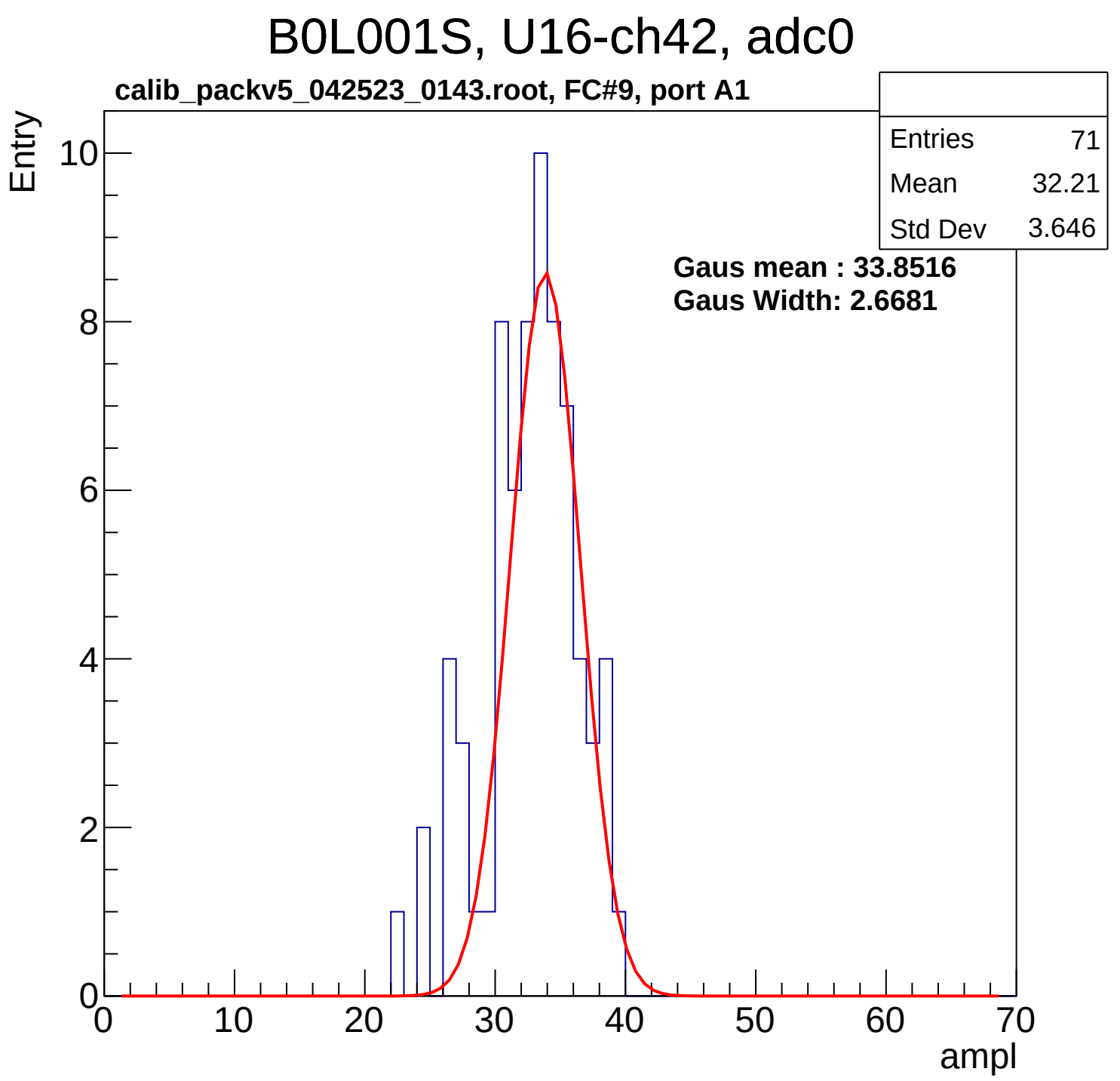
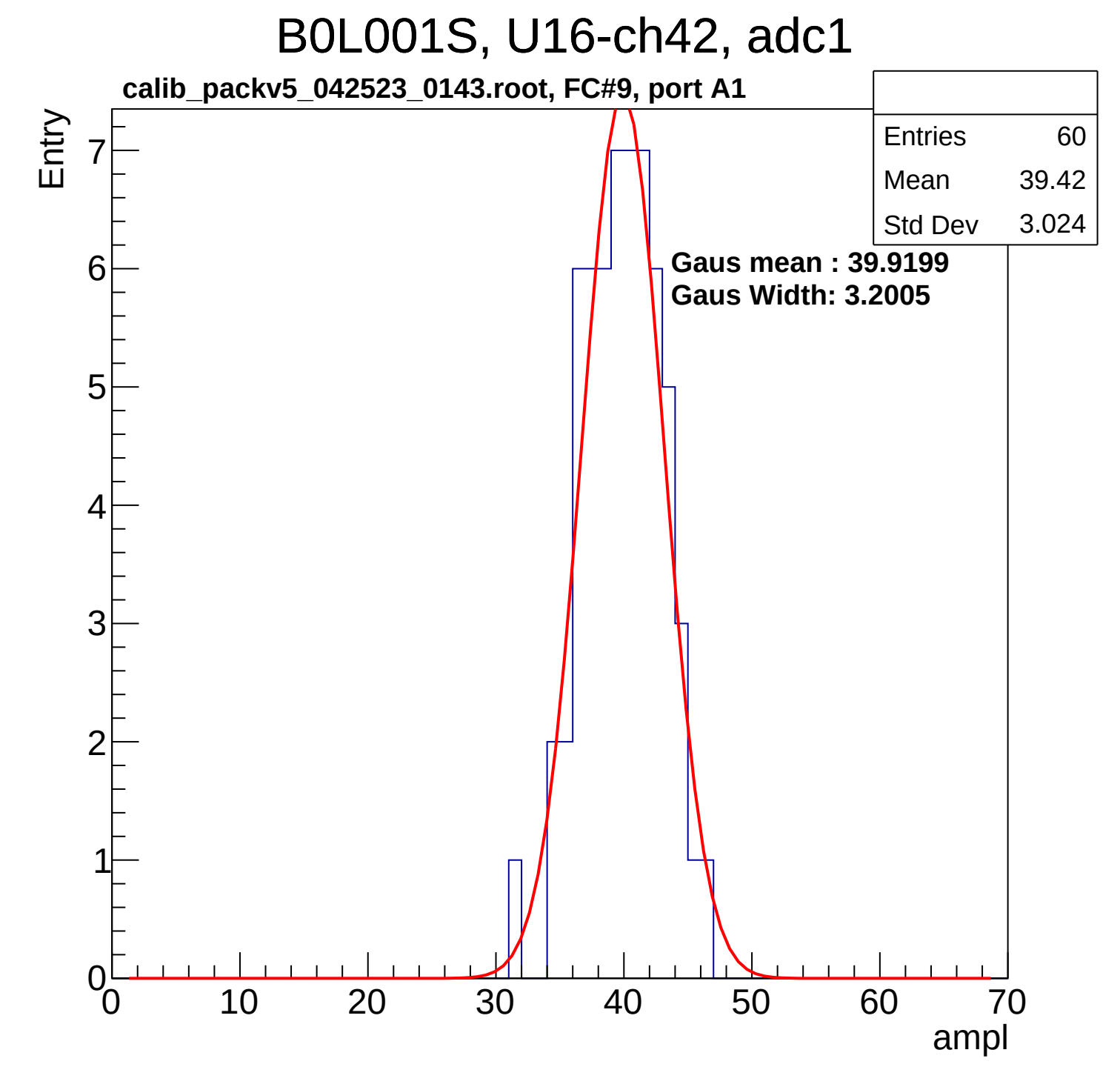
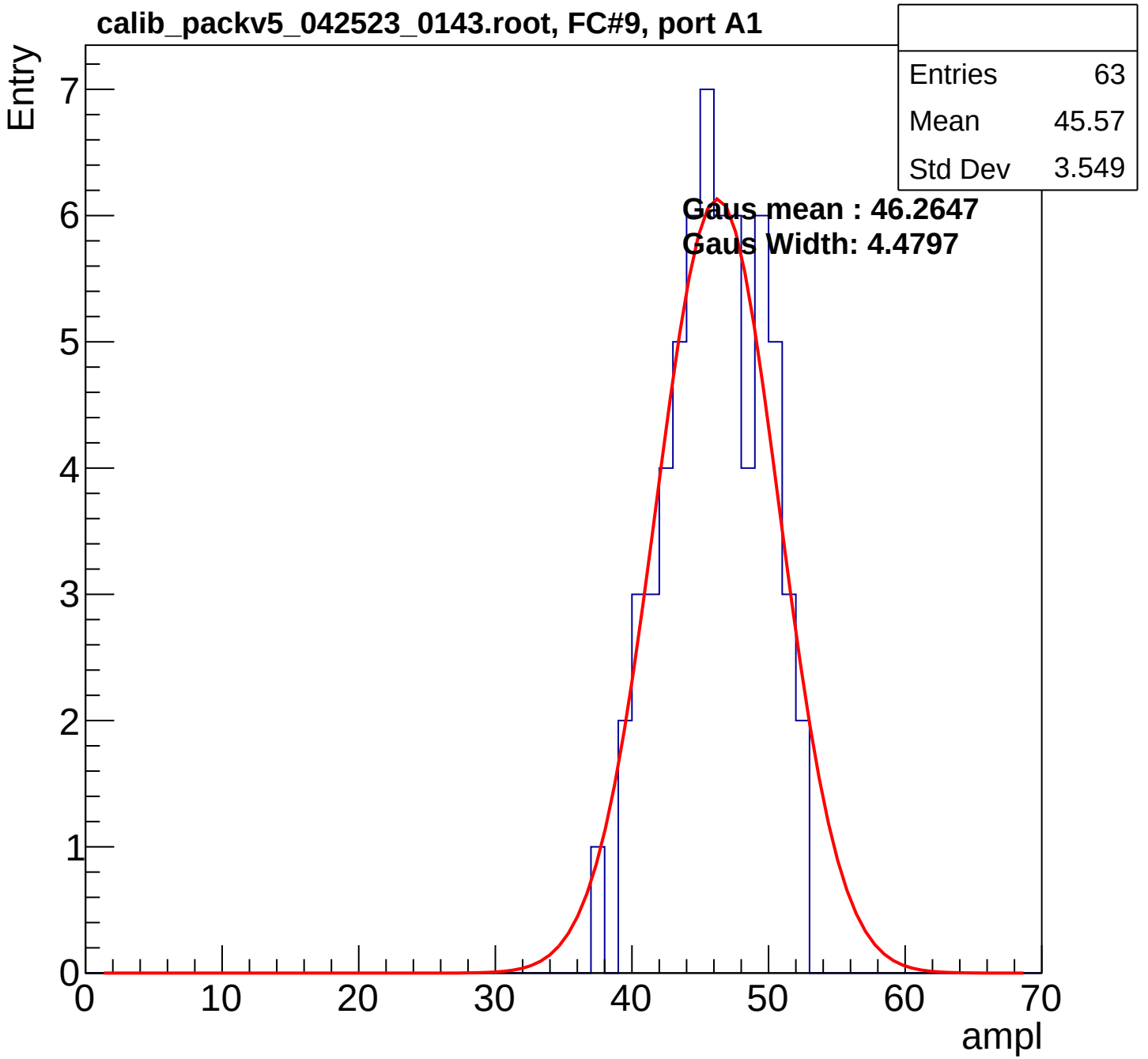
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 32.21
Std Dev 3.646
Gaus mean : 33.8516
Gaus Width: 2.6681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 39.42
Std Dev 3.024
Gaus mean : 39.9199
Gaus Width: 3.2005
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 45.57
Std Dev 3.549
Gaus mean : 46.2647
Gaus Width: 4.4797
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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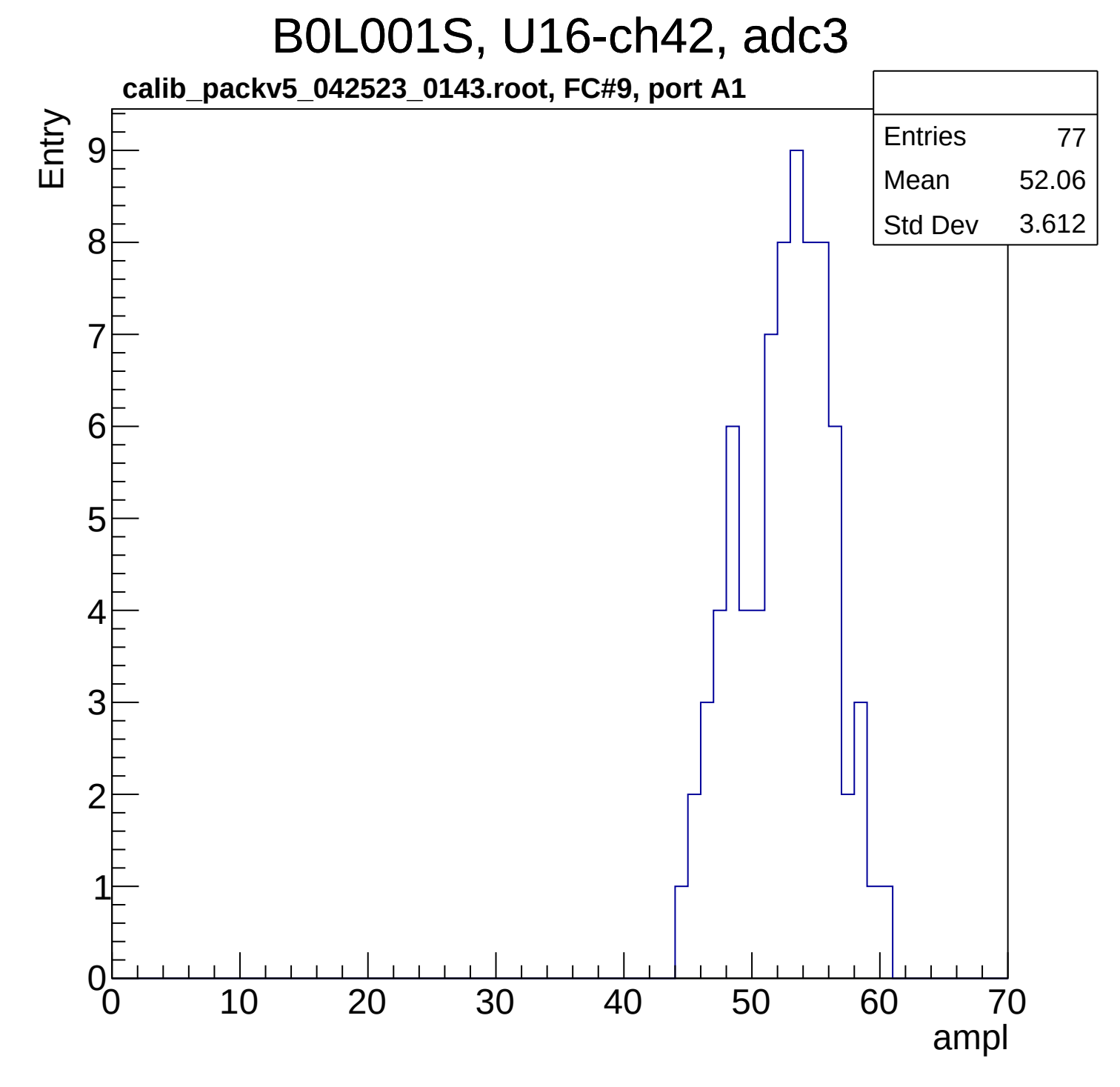
B0L001S, U16-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 52.06
Std Dev 3.612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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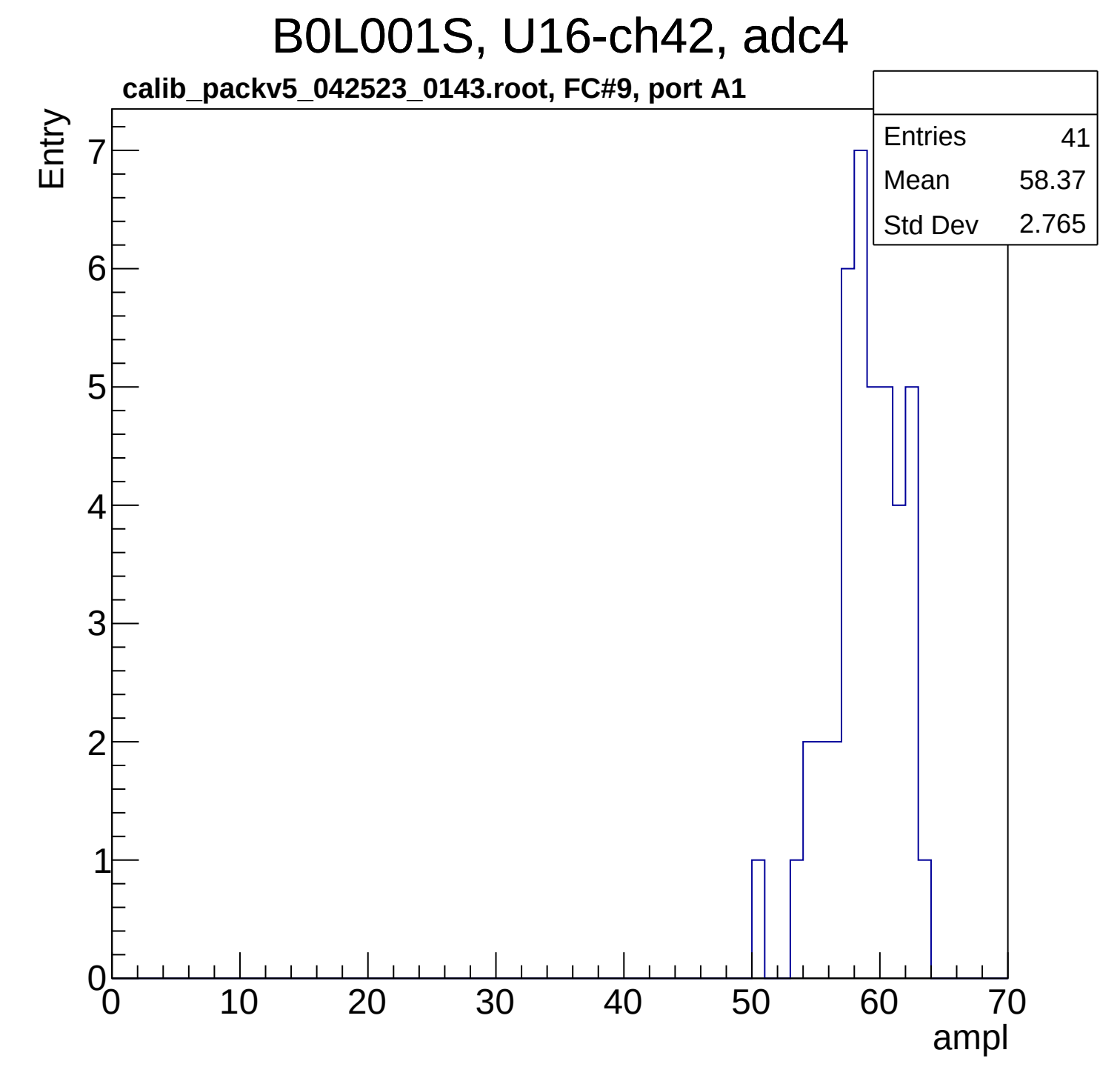
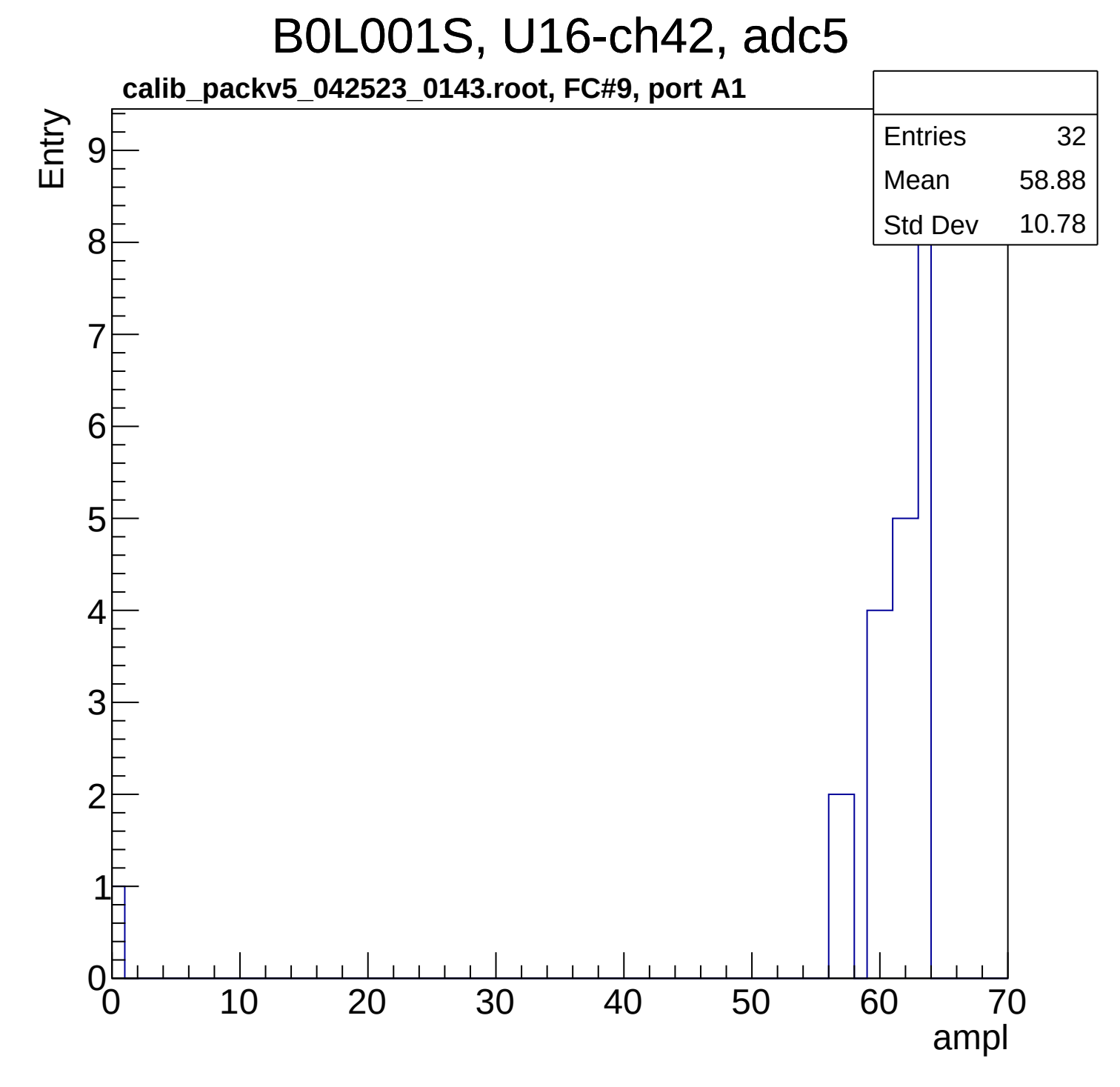
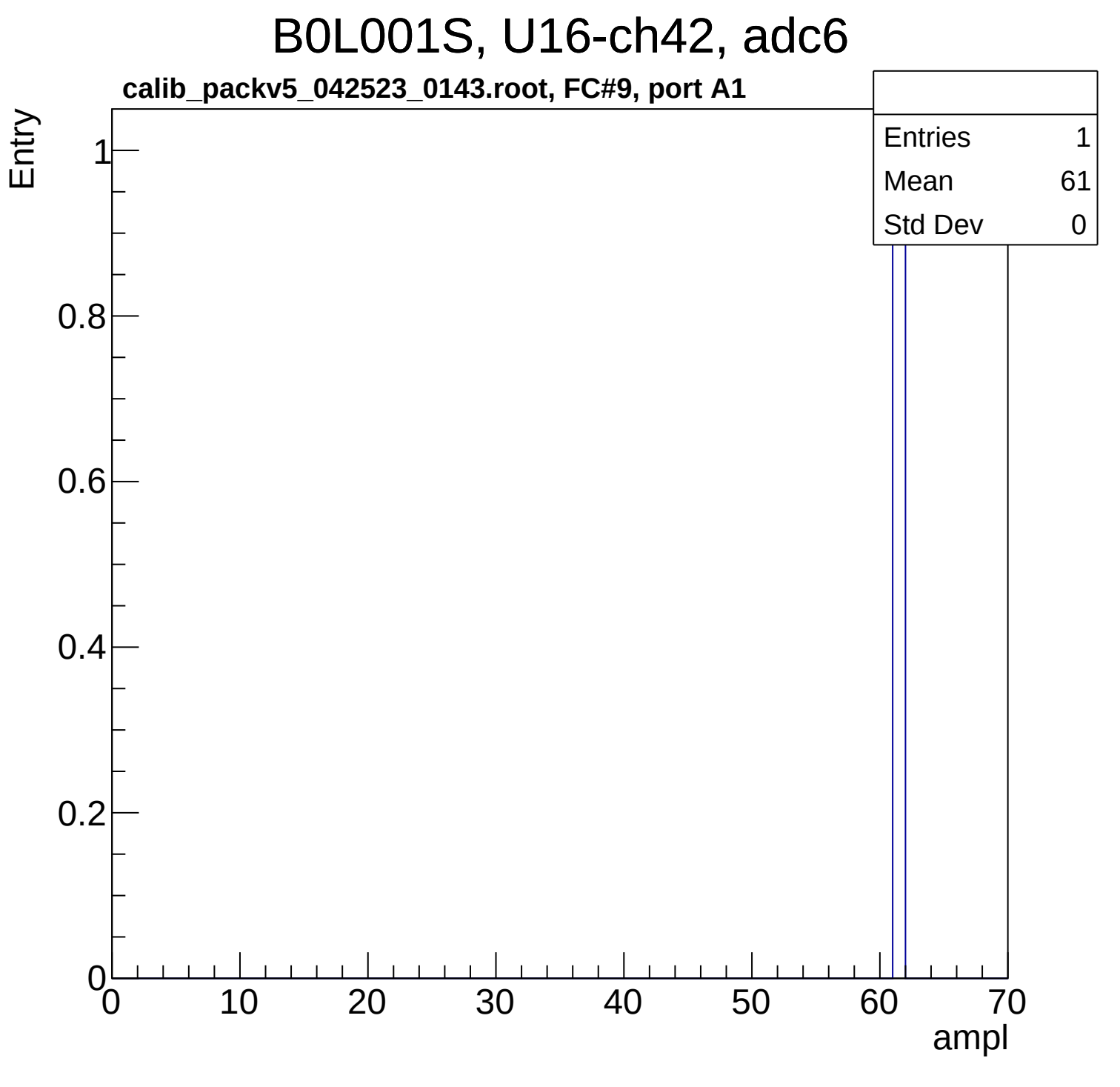
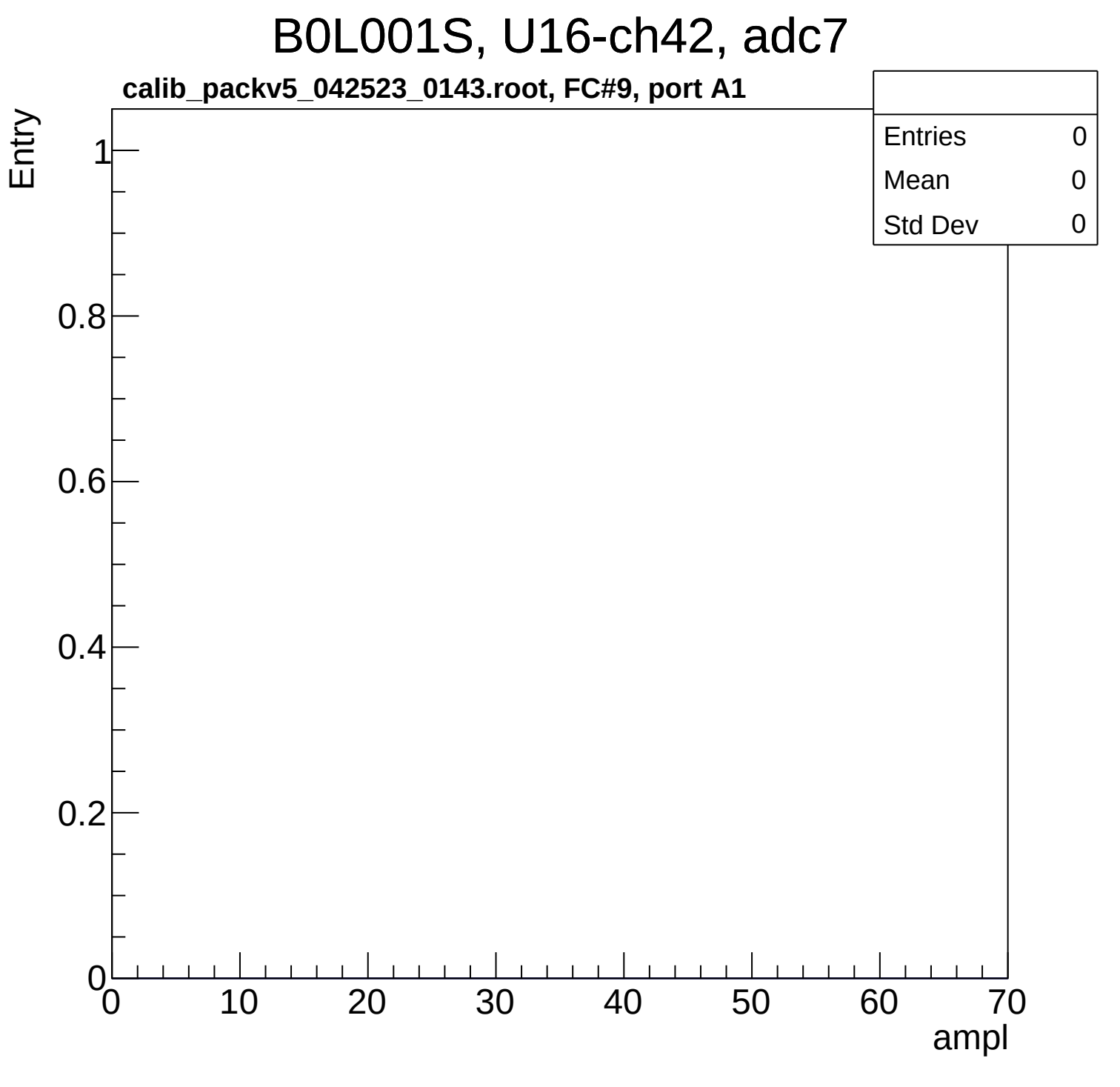
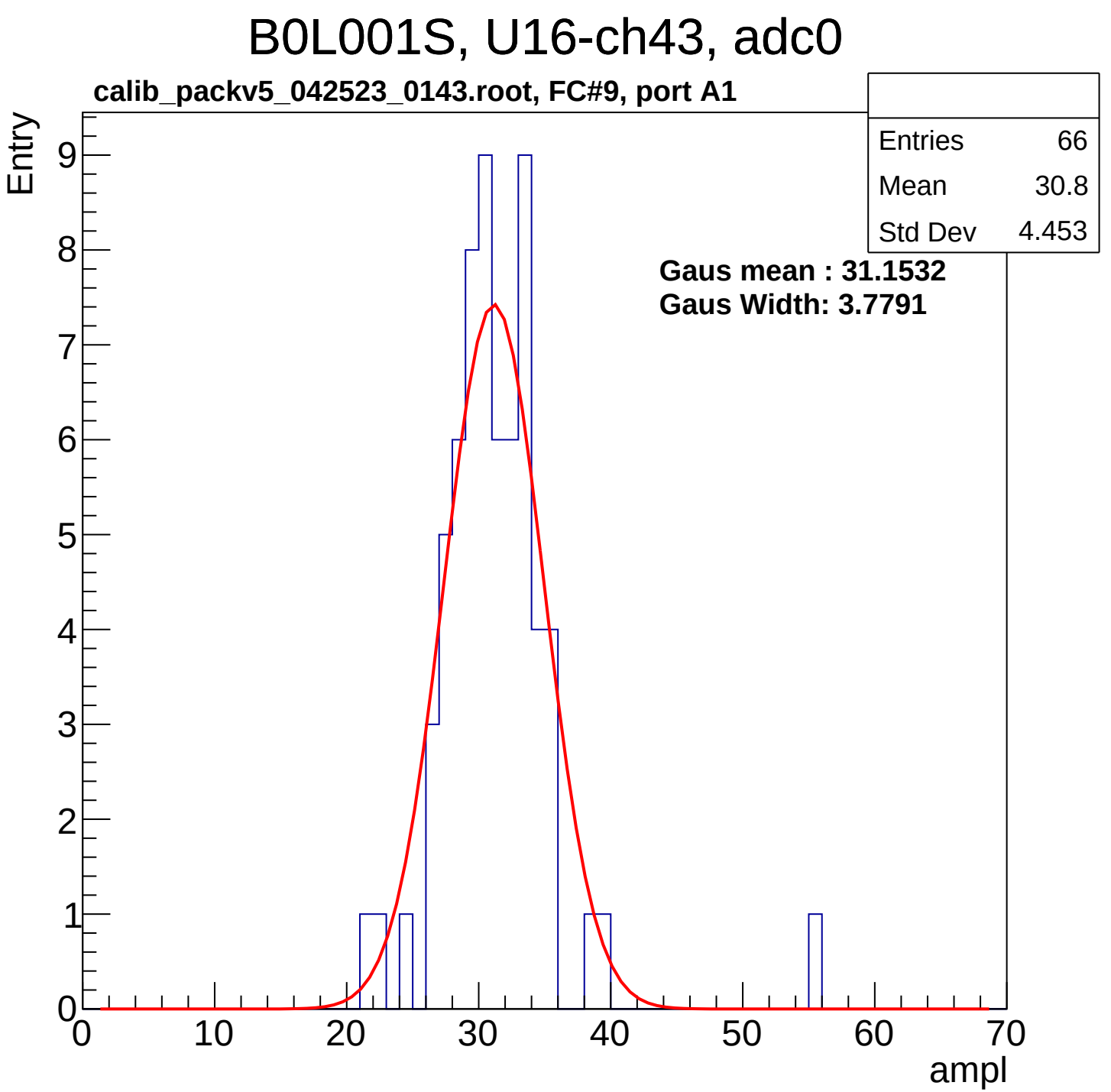
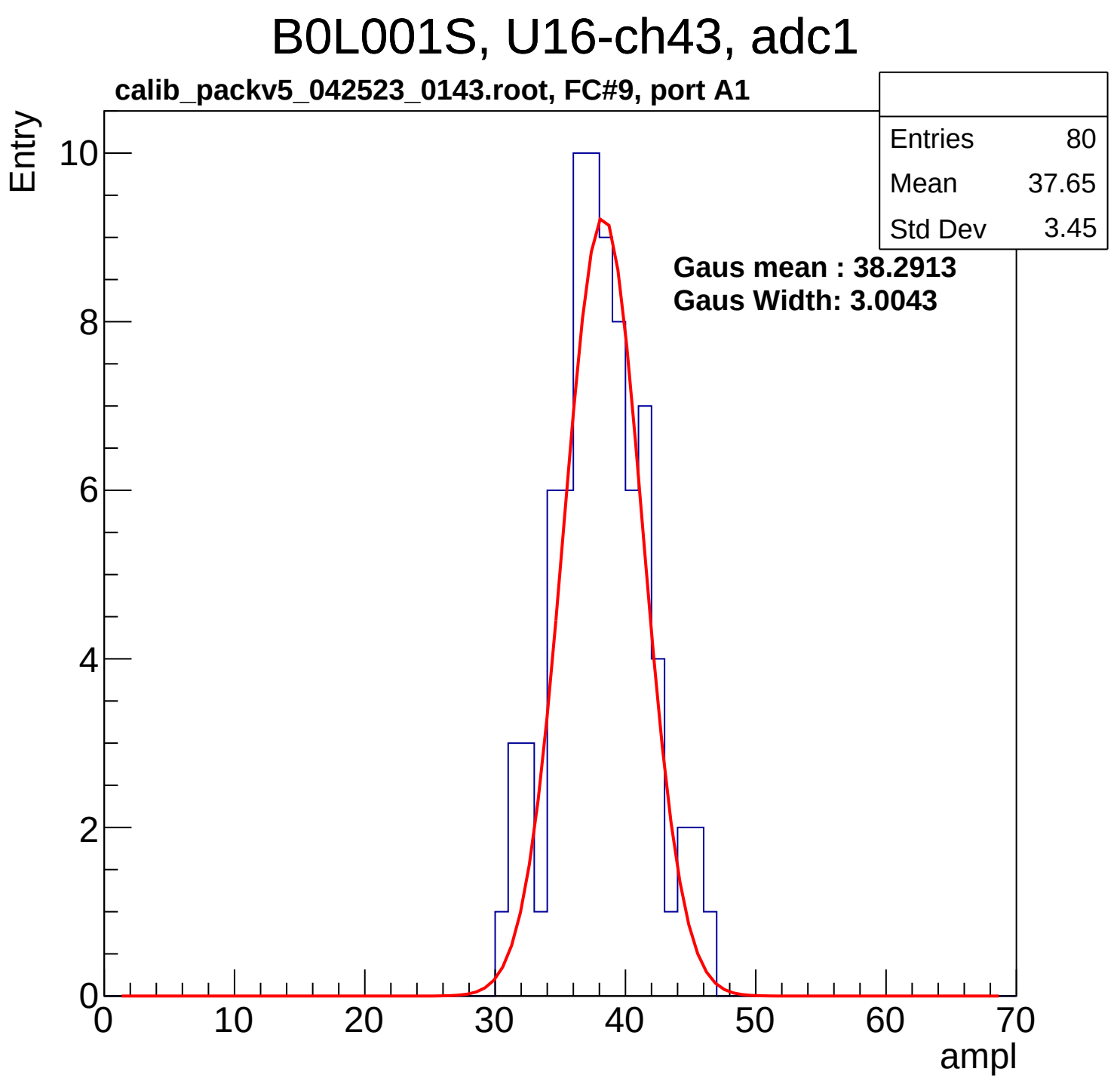

B0L001S, U16-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.37
Std Dev 2.765
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

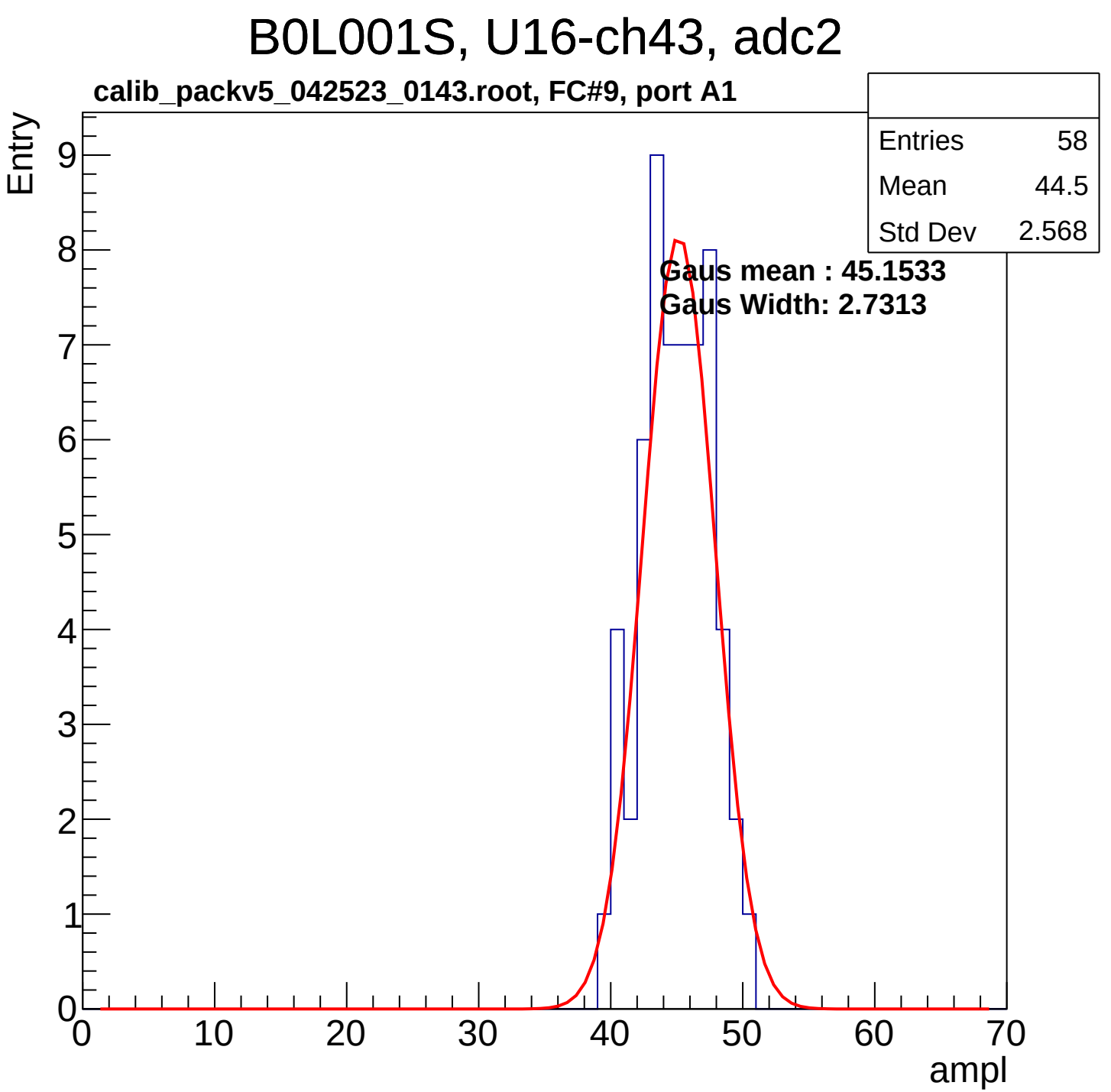

B0L001S, U16-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.88
Std Dev 10.78
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

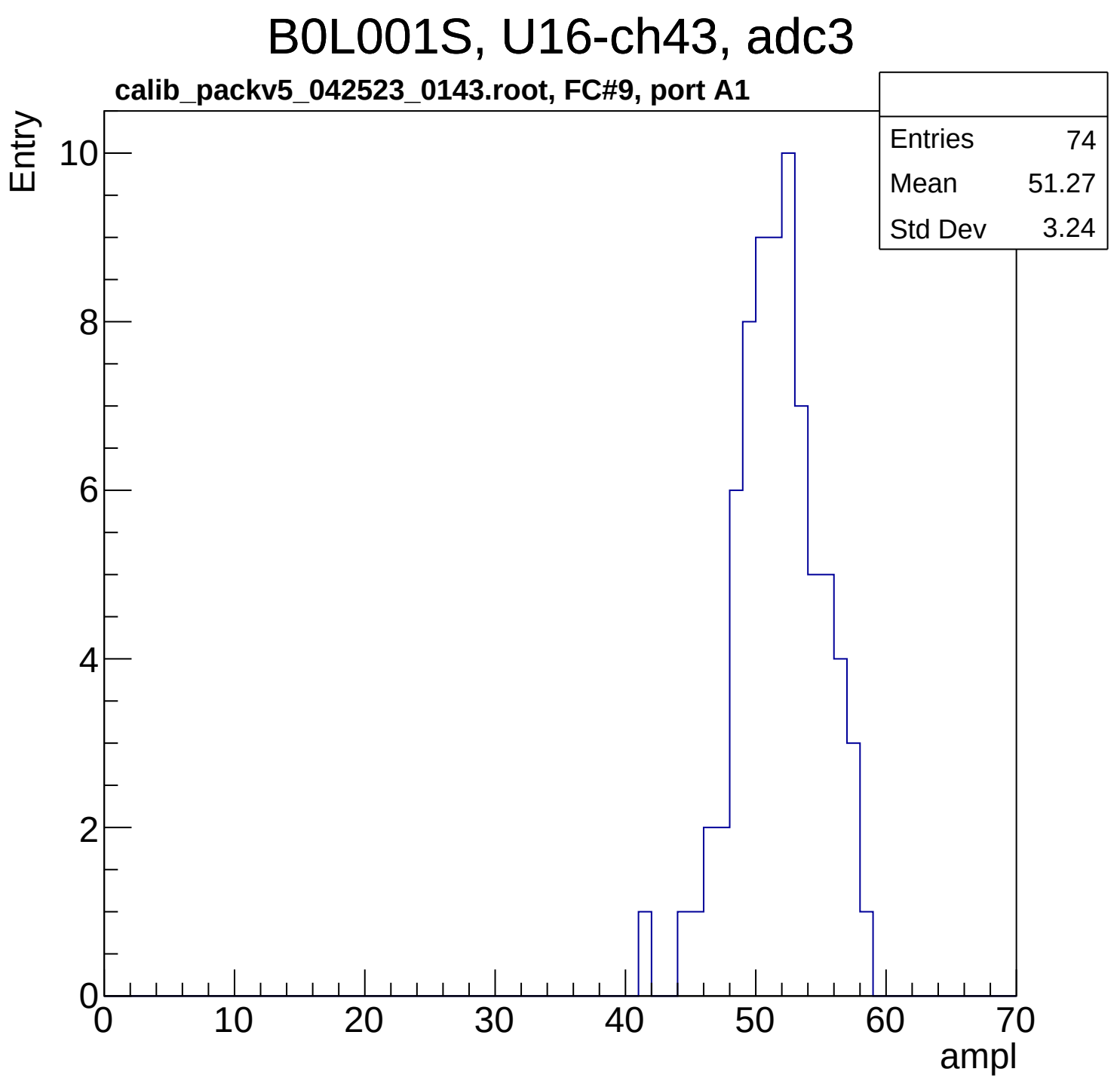

B0L001S, U16-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

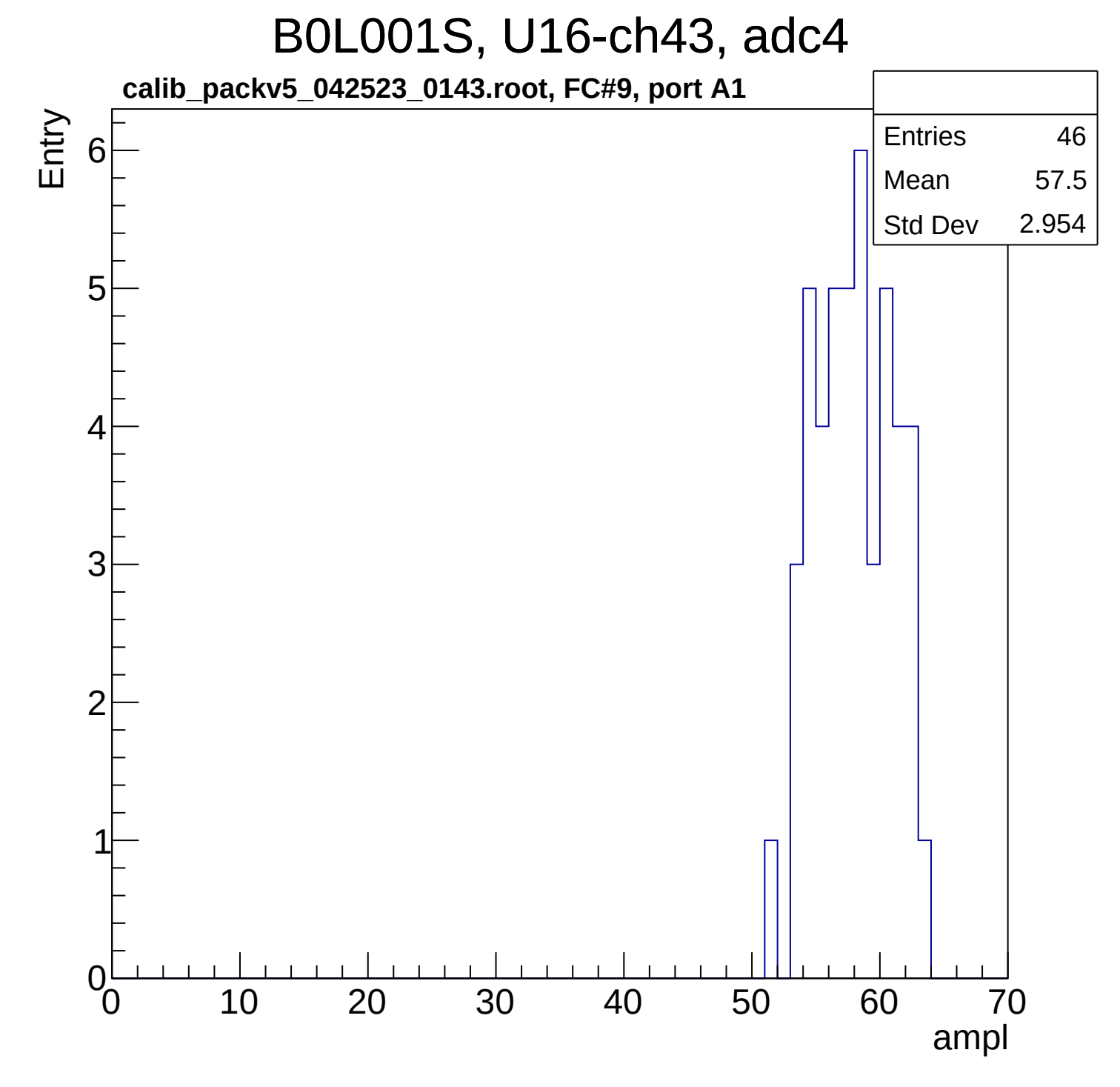

B0L001S, U16-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

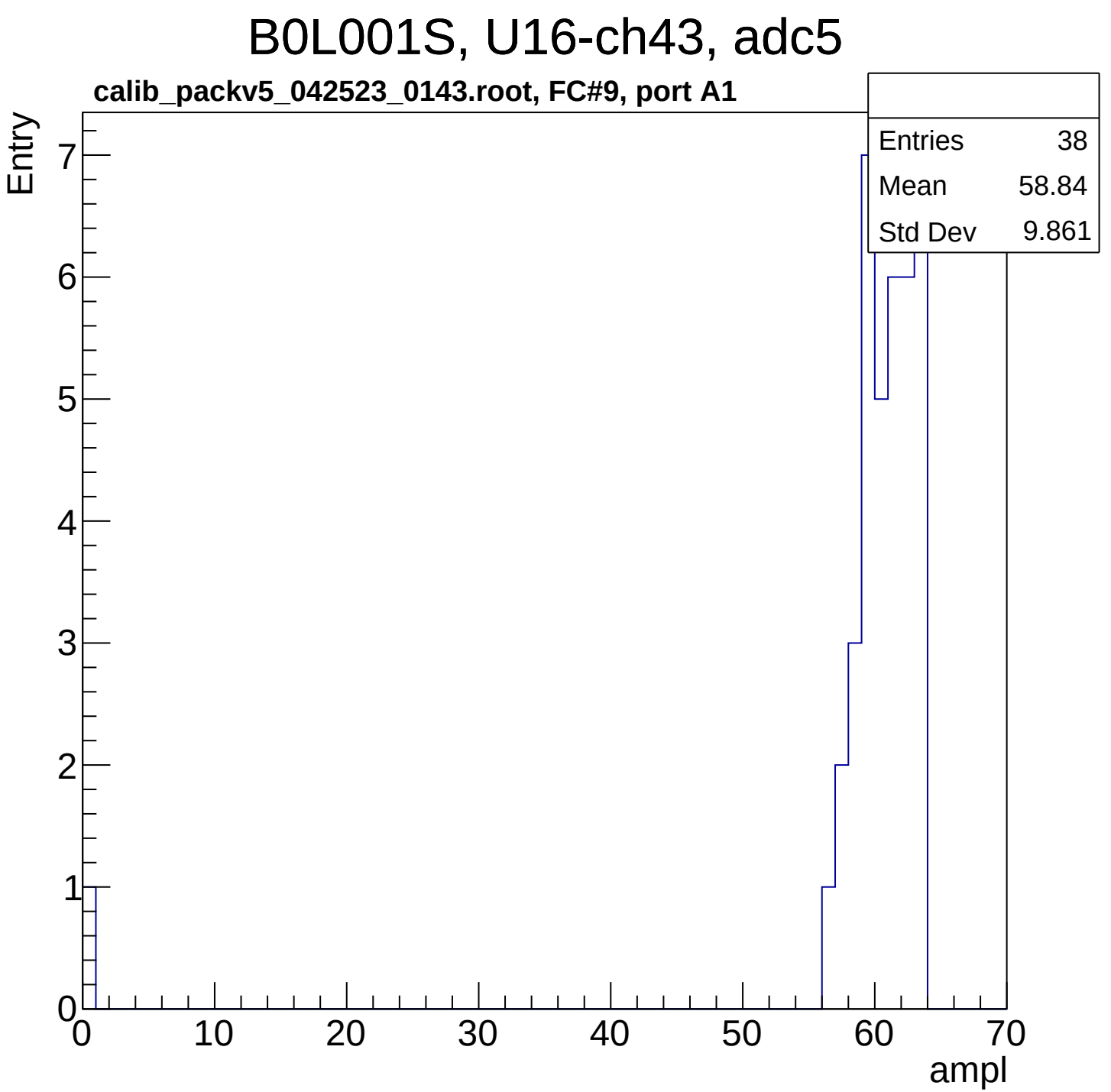

B0L001S, U16-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.8
Std Dev 4.453
Gaus mean : 31.1532
Gaus Width: 3.7791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 37.65
Std Dev 3.45
Gaus mean : 38.2913
Gaus Width: 3.0043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.5
Std Dev 2.568
Gaus mean : 45.1533
Gaus Width: 2.7313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.27
Std Dev 3.24
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.5
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.84
Std Dev 9.861
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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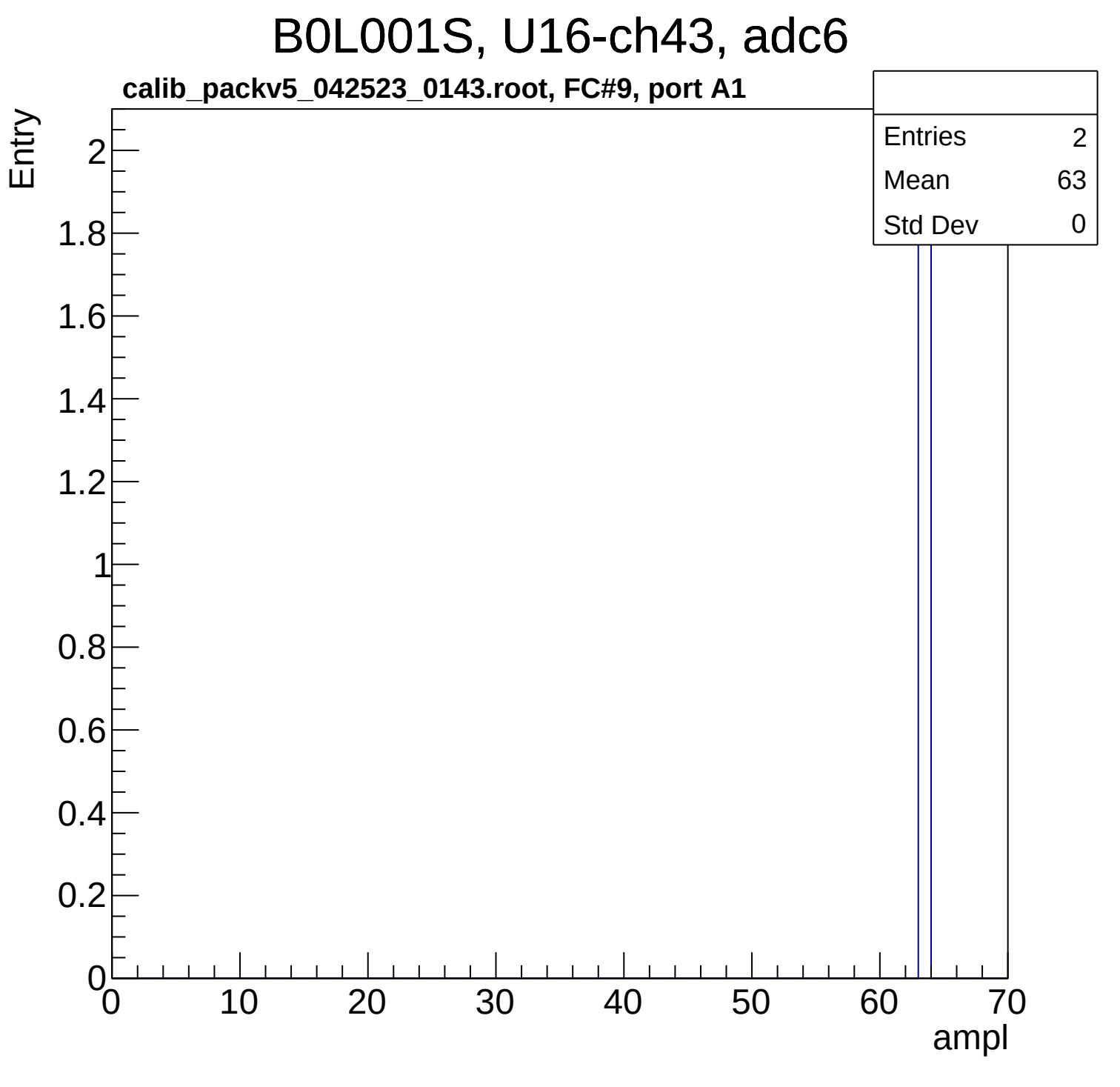
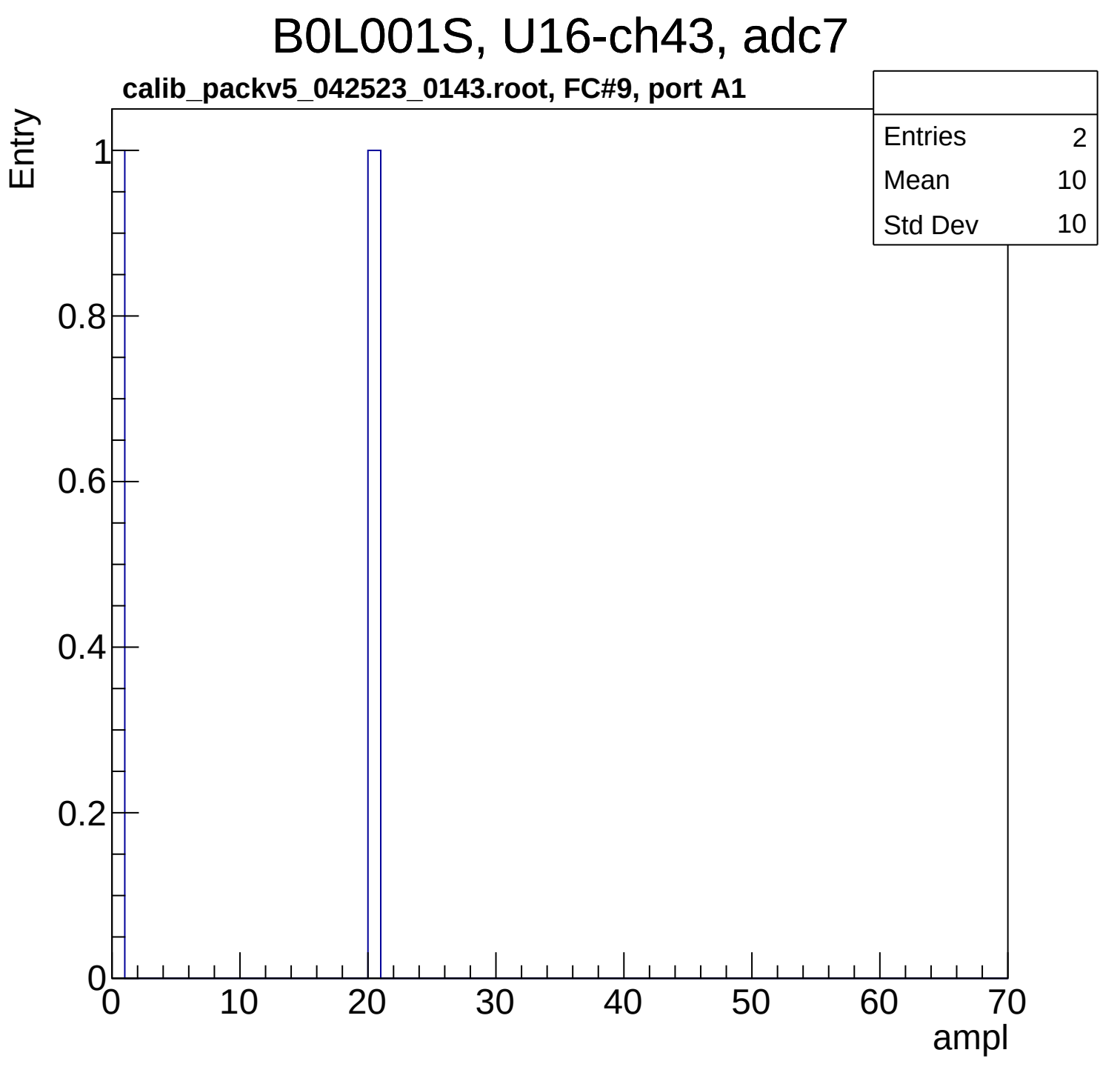
40
50
60
70


B0L001S, U16-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10
Std Dev 10
Entry
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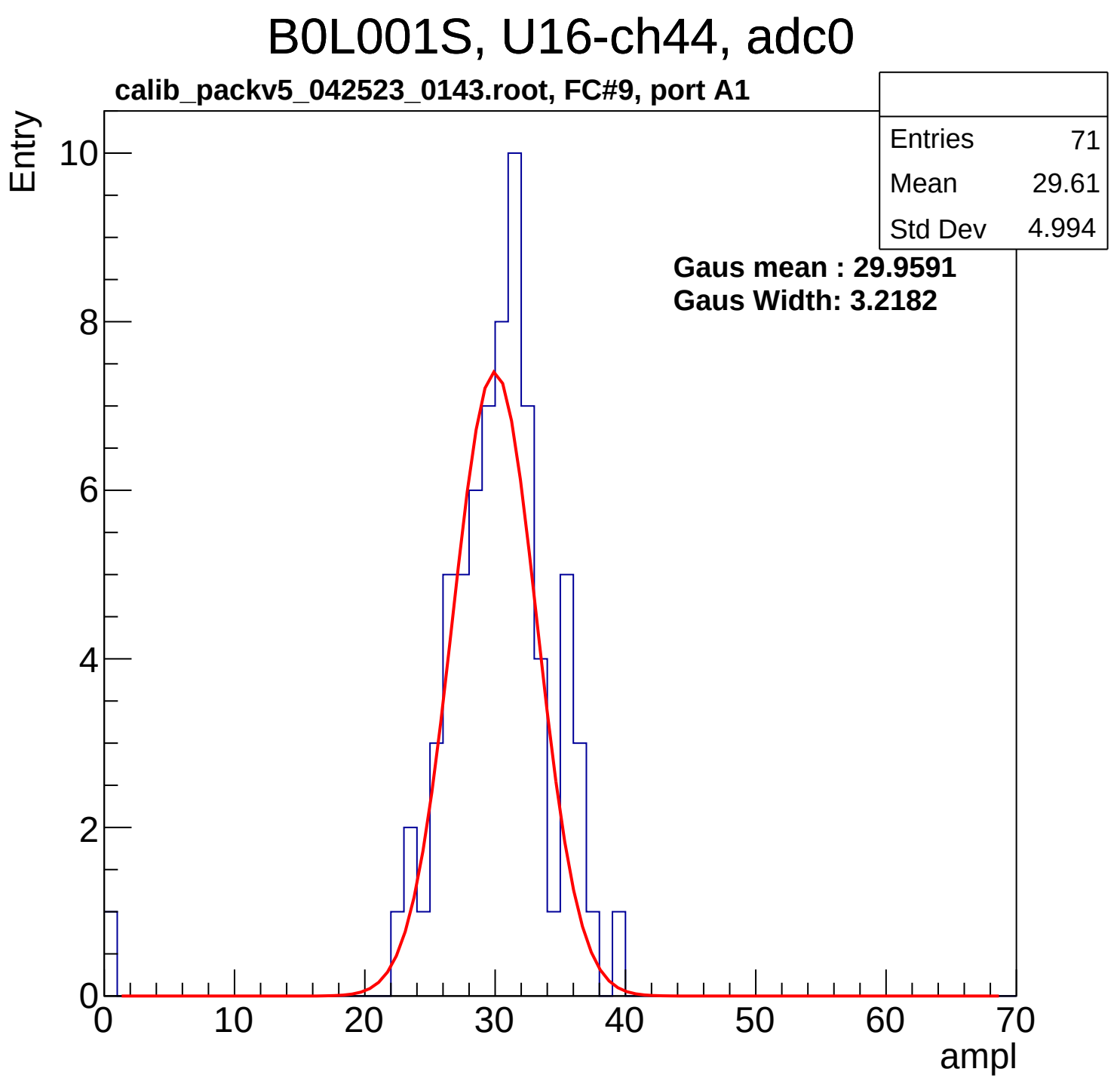
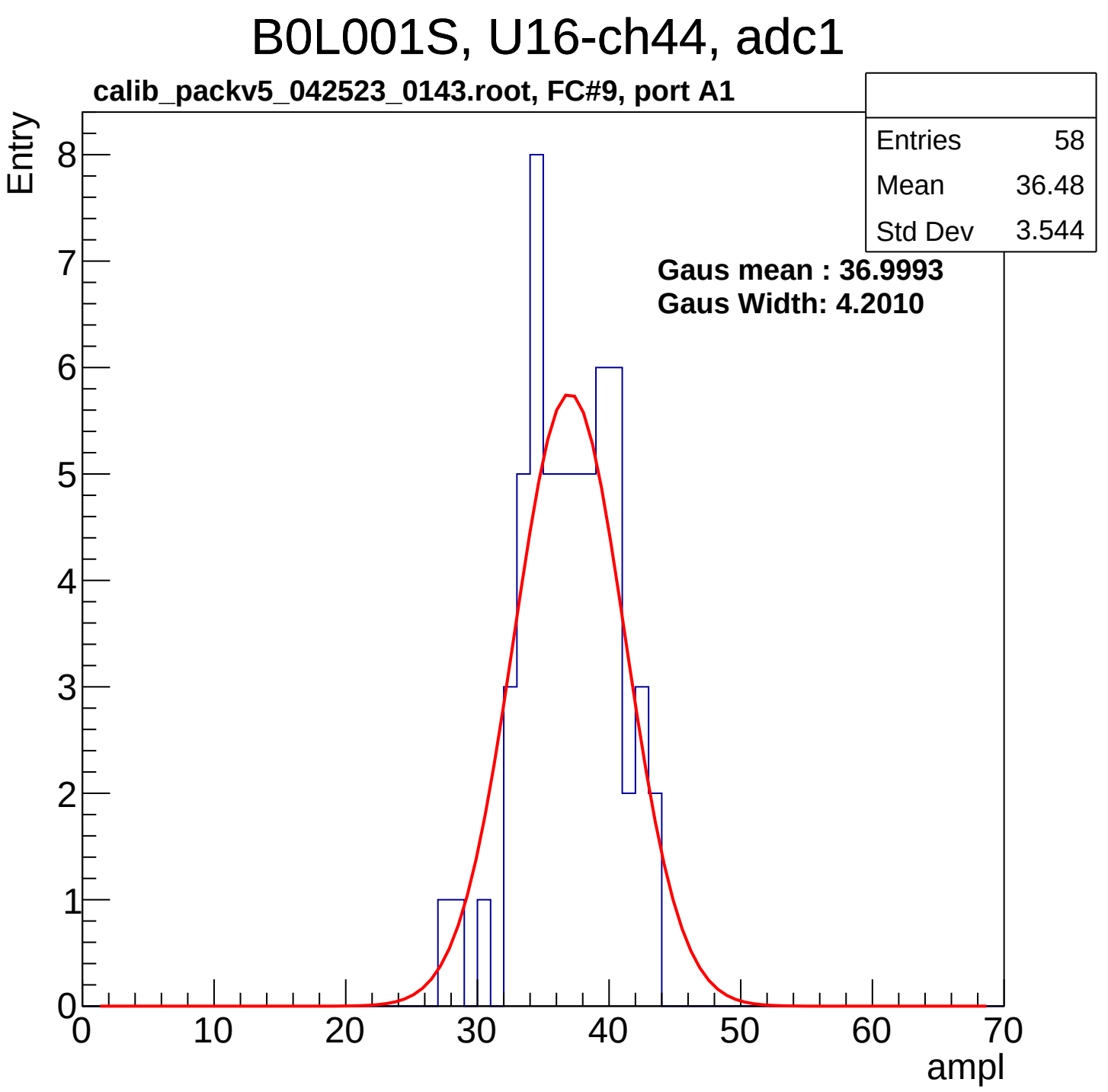
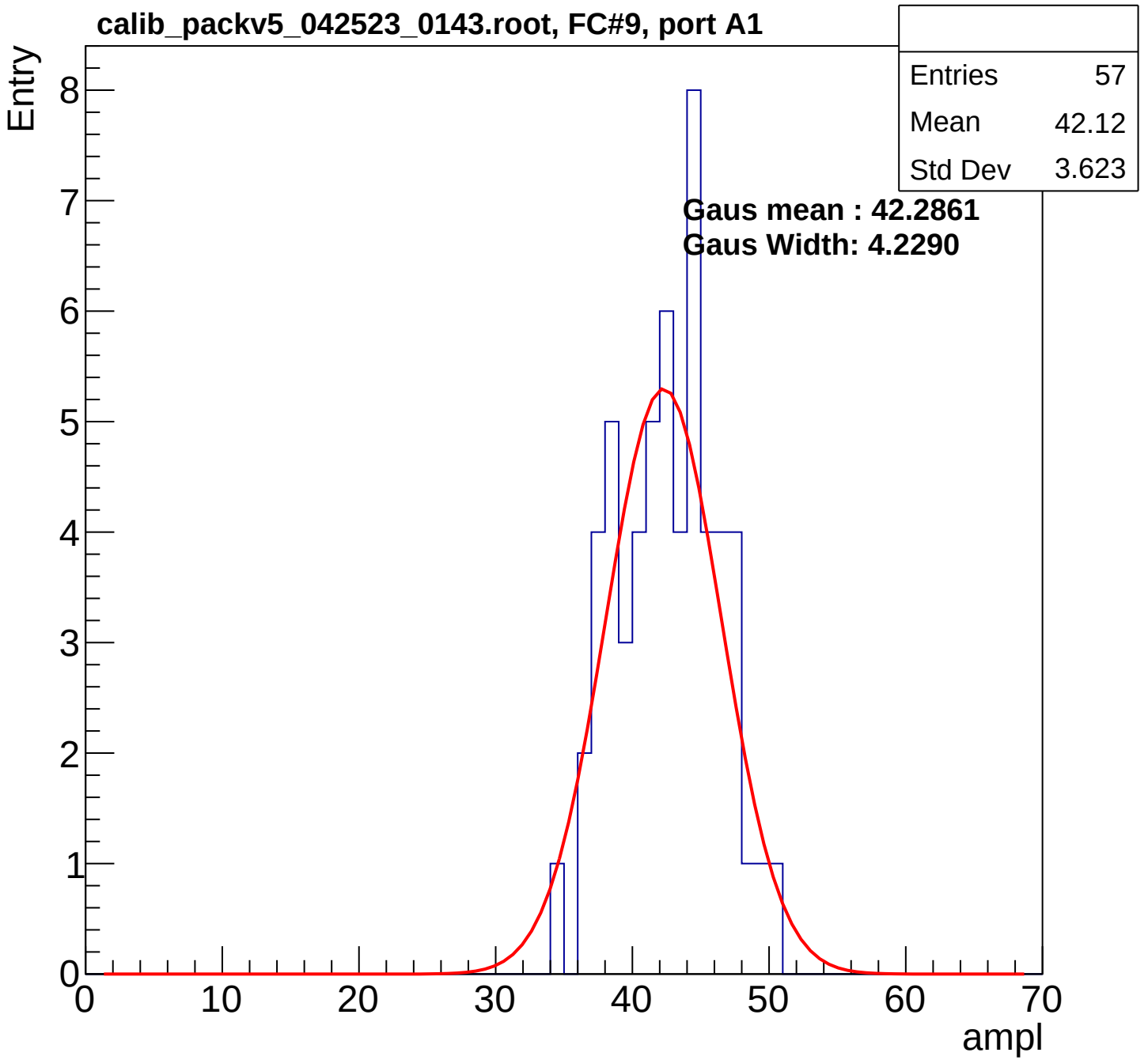
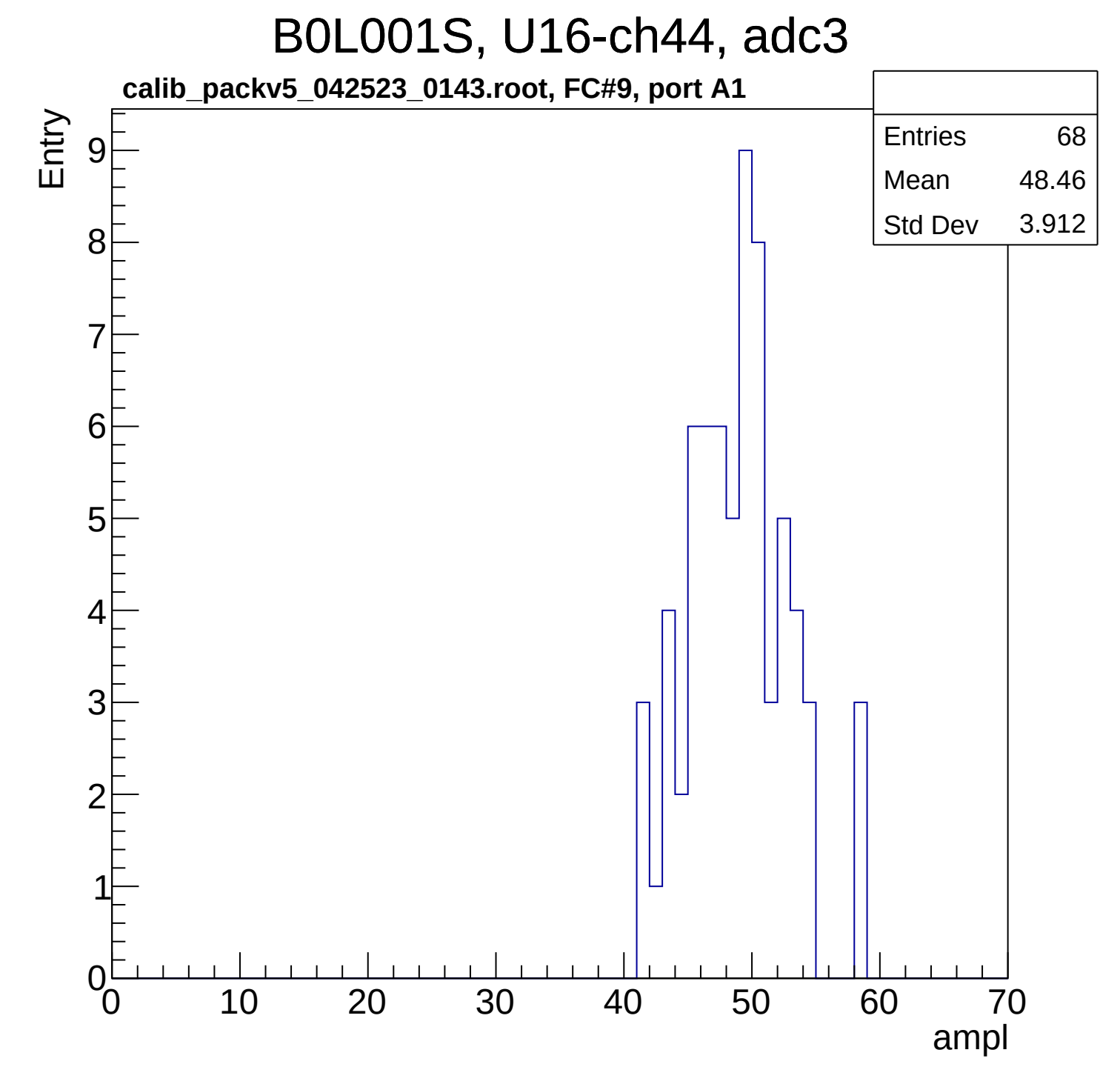
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.61
Std Dev 4.994
Gaus mean : 29.9591
Gaus Width: 3.2182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.48
Std Dev 3.544
Gaus mean : 36.9993
Gaus Width: 4.2010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.12
Std Dev 3.623
Gaus mean : 42.2861
Gaus Width: 4.2290
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.46
Std Dev 3.912
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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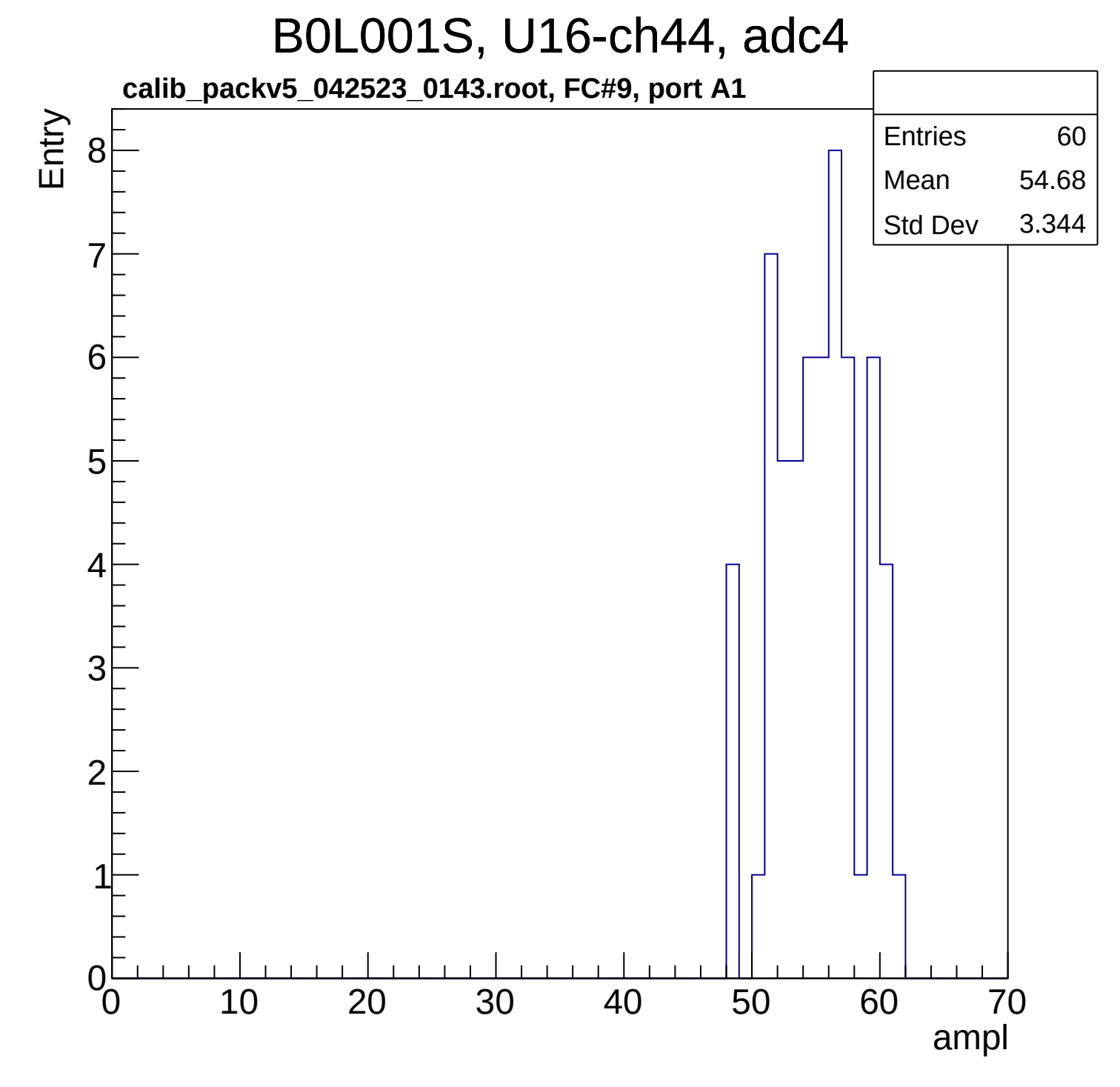
40
50
60
70


B0L001S, U16-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 54.68
Std Dev 3.344
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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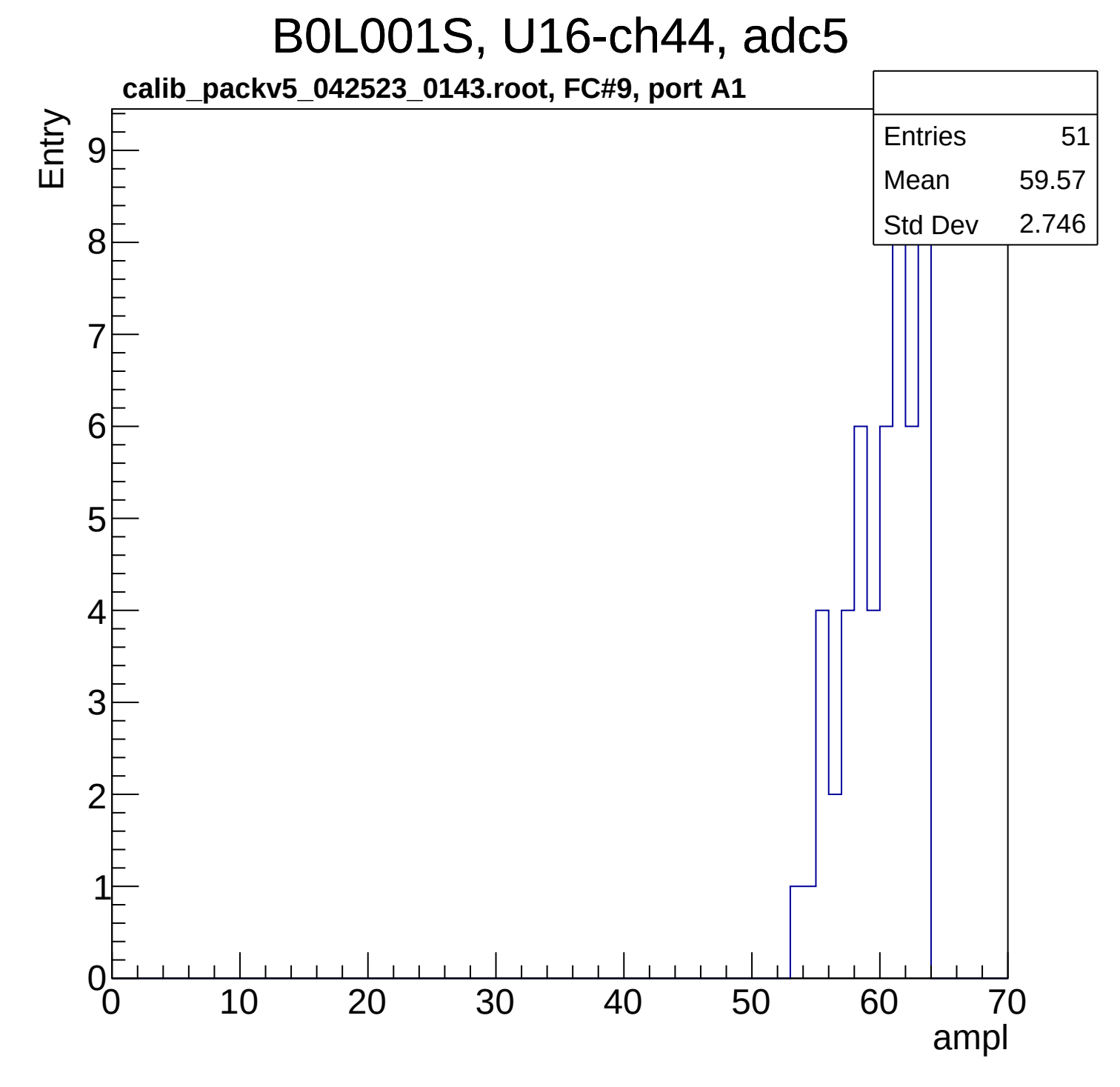
B0L001S, U16-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 59.57
Std Dev 2.746
Entry
ampl
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5
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0
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60
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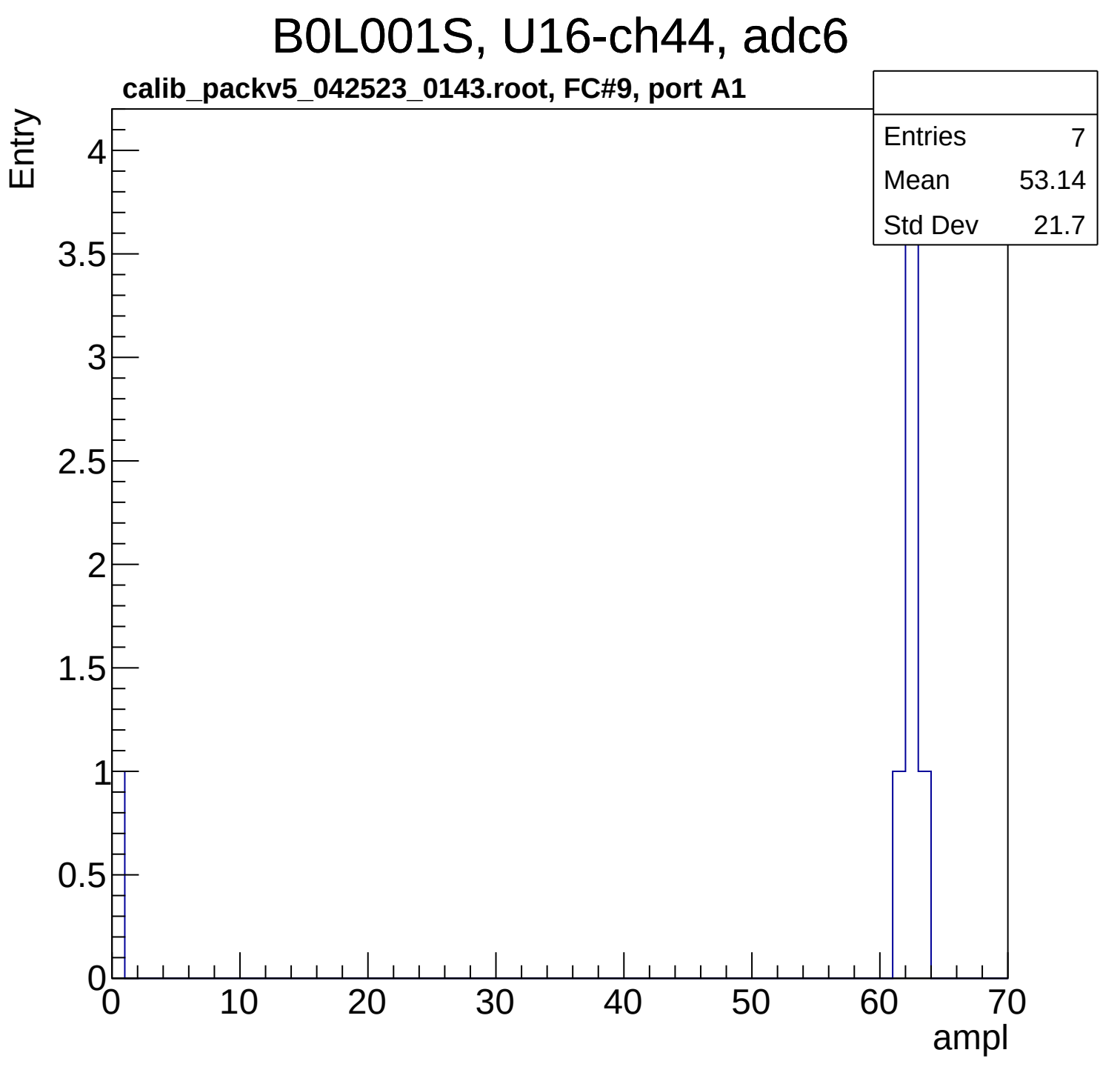
B0L001S, U16-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 53.14
Std Dev 21.7
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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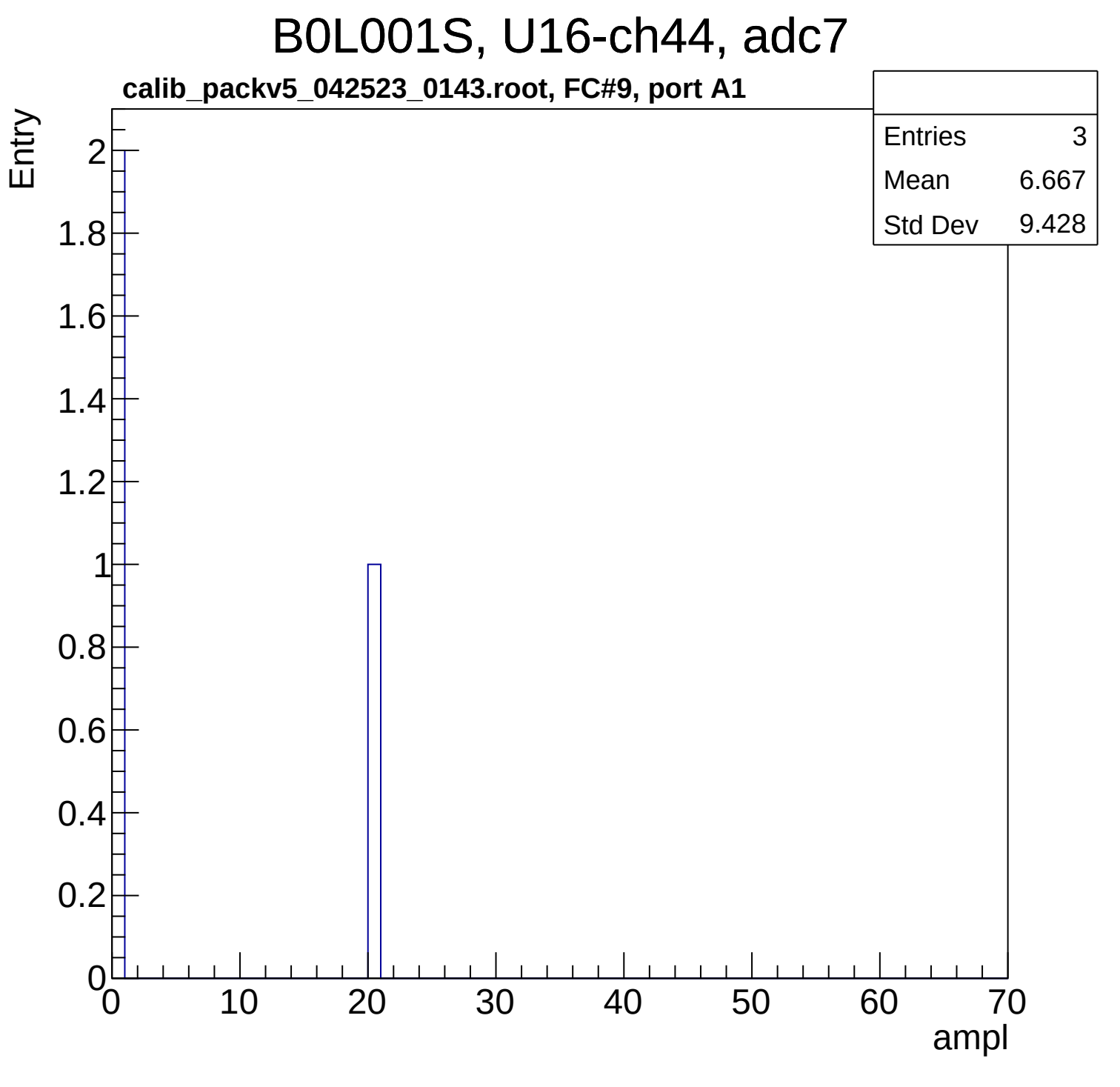
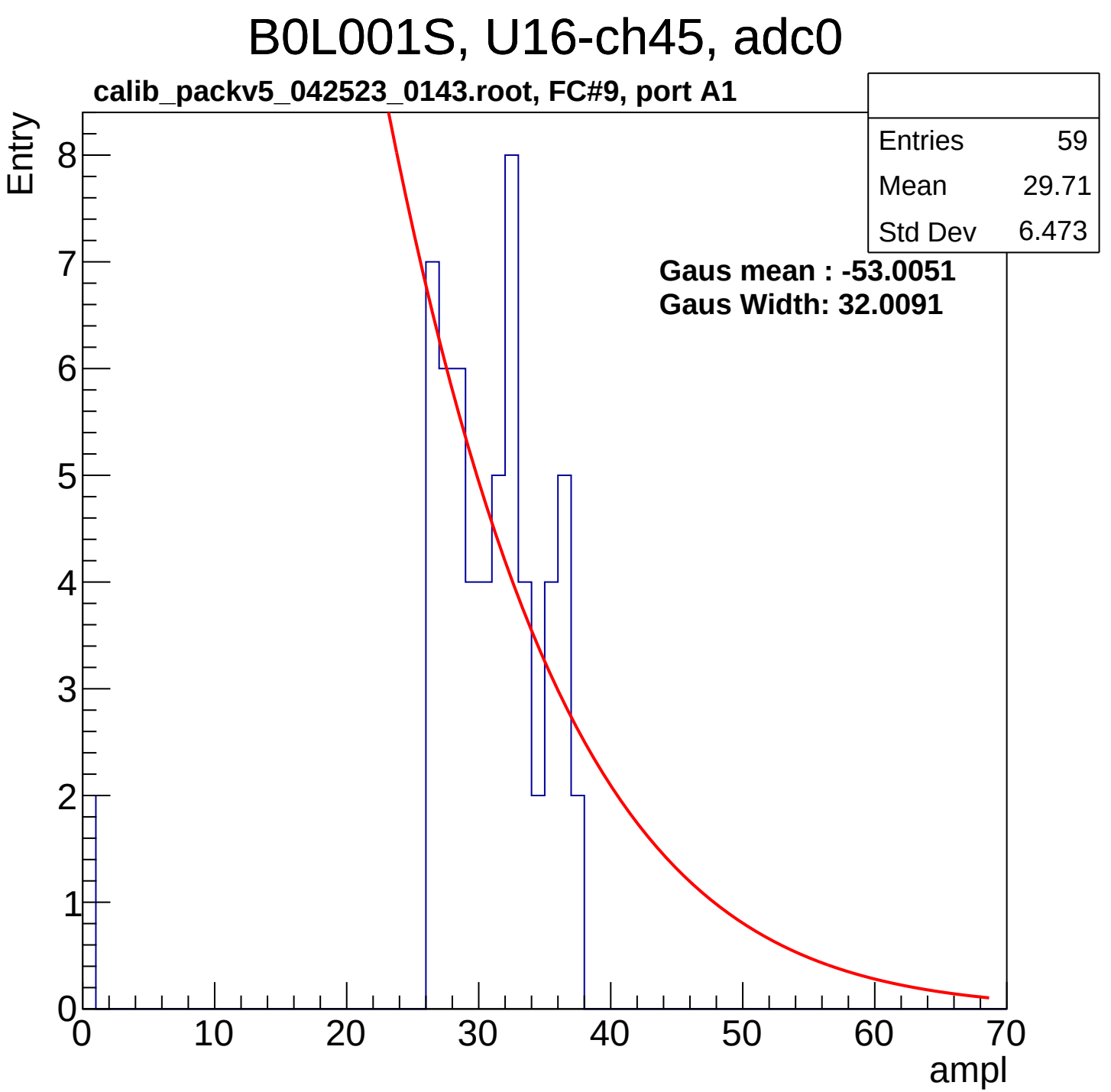
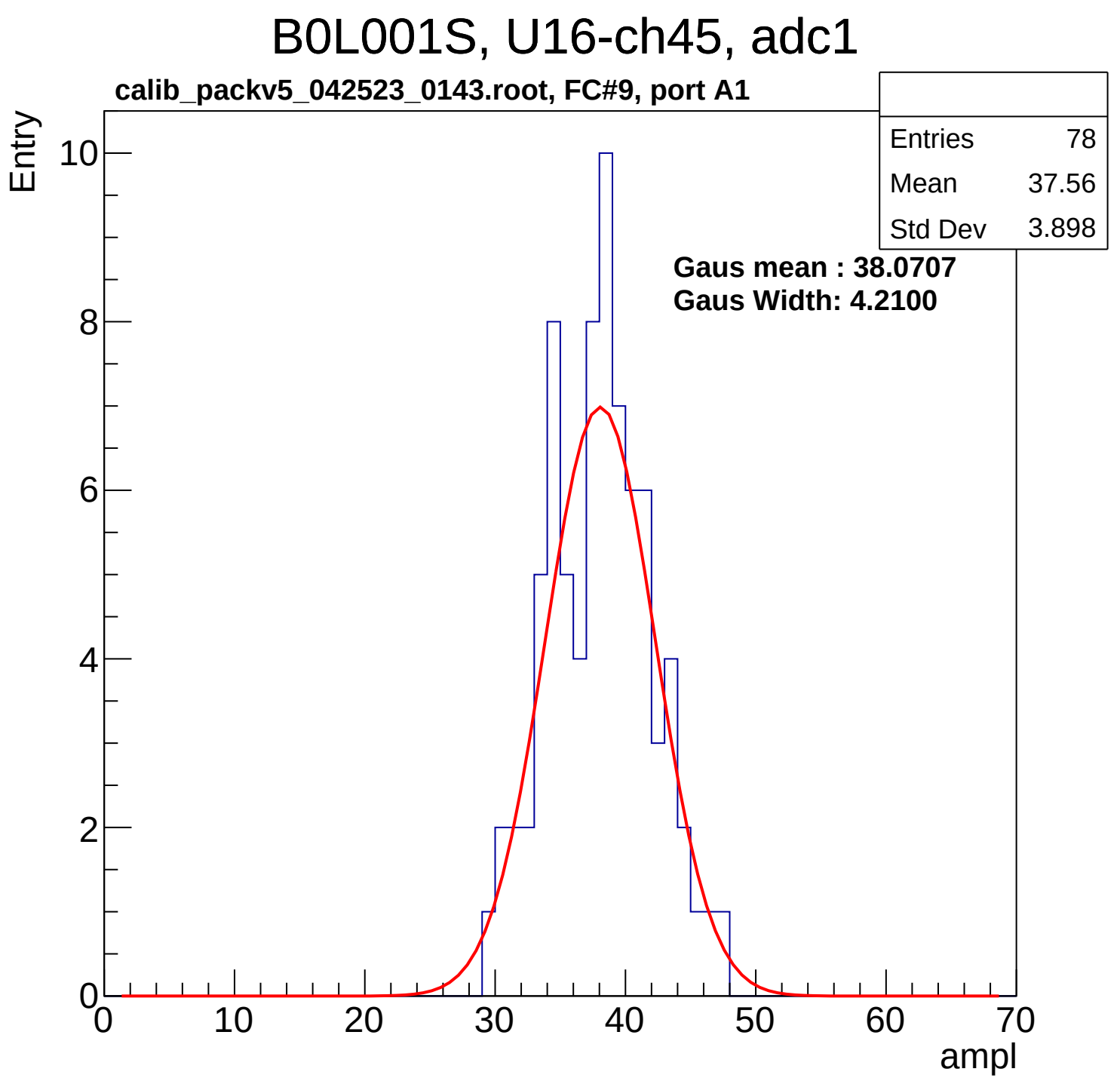
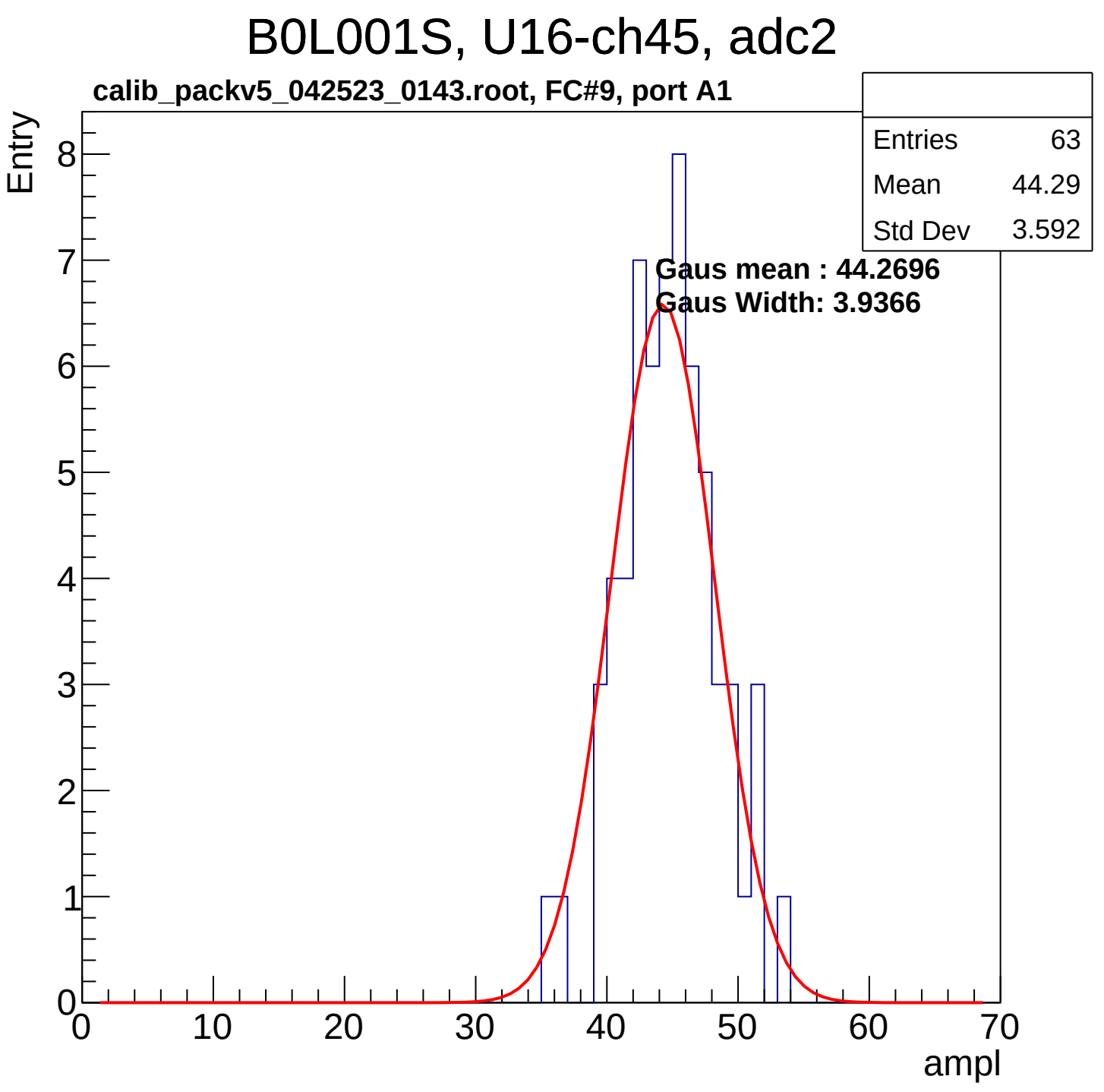
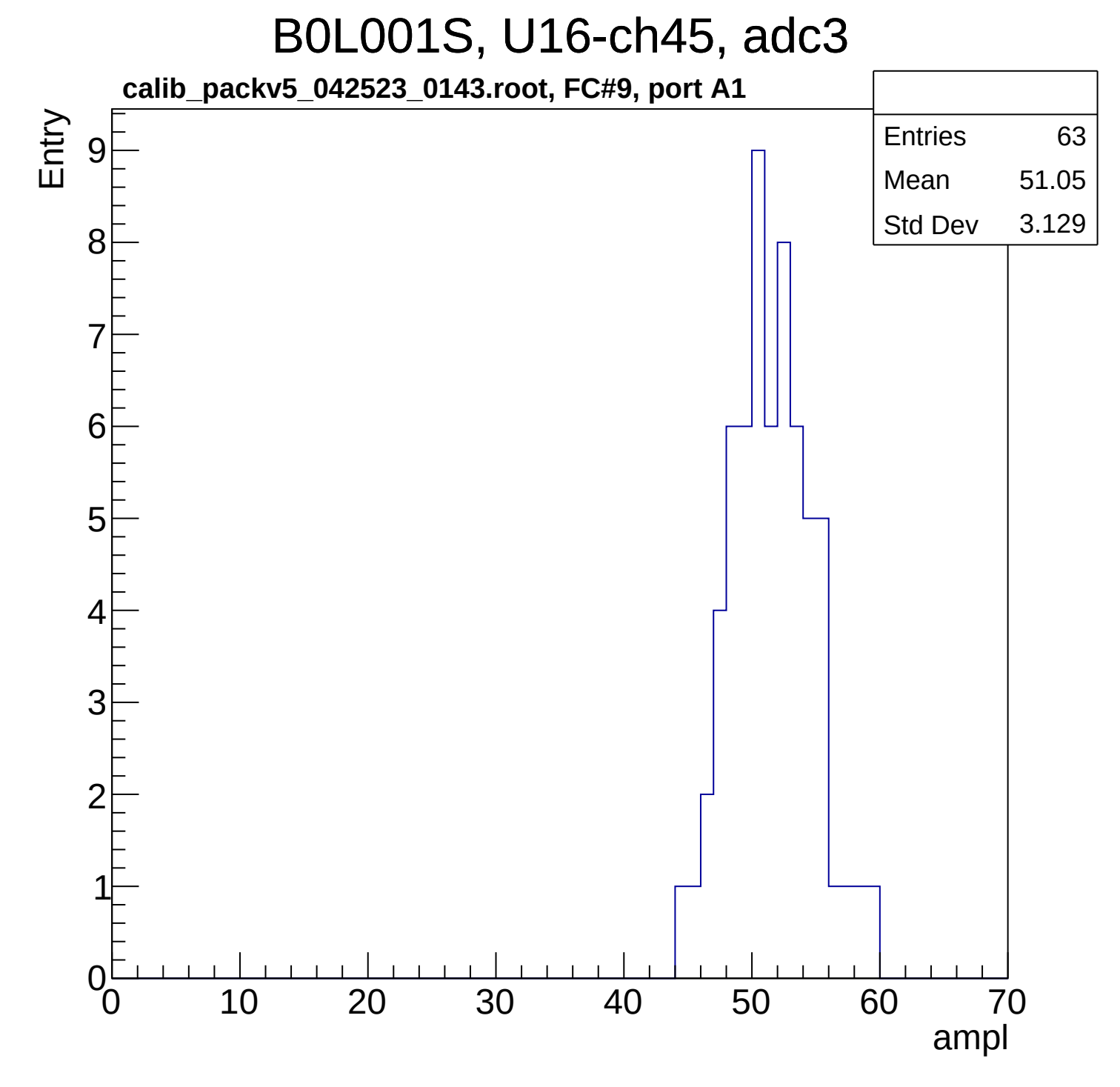
40
50
60
70


B0L001S, U16-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.71
Std Dev 6.473
Gaus mean : -53.0051
Gaus Width: 32.0091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.56
Std Dev 3.898
Gaus mean : 38.0707
Gaus Width: 4.2100
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.29
Std Dev 3.592
Gaus mean : 44.2696
Gaus Width: 3.9366
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.05
Std Dev 3.129
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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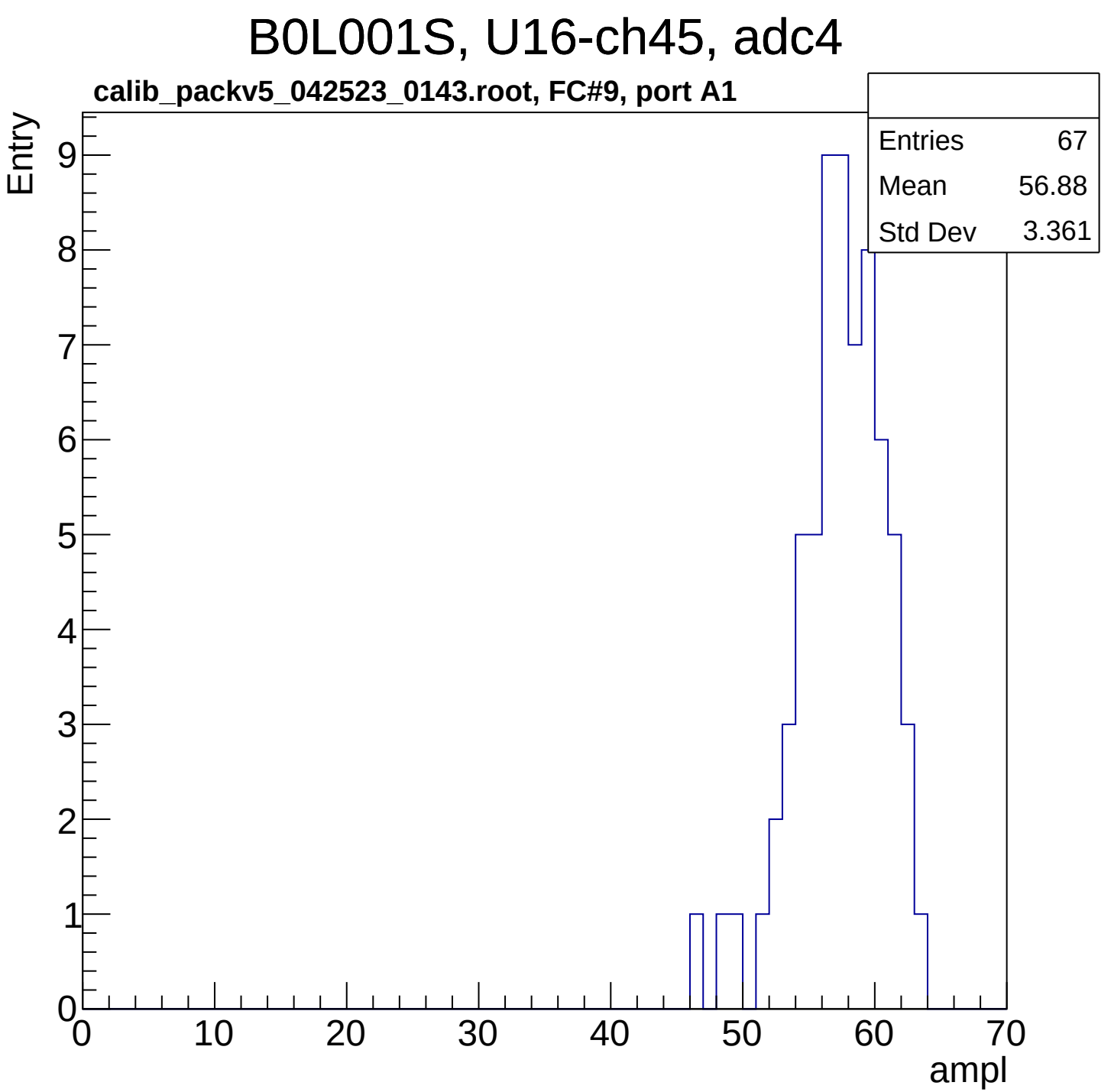
B0L001S, U16-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.88
Std Dev 3.361
Entry
ampl
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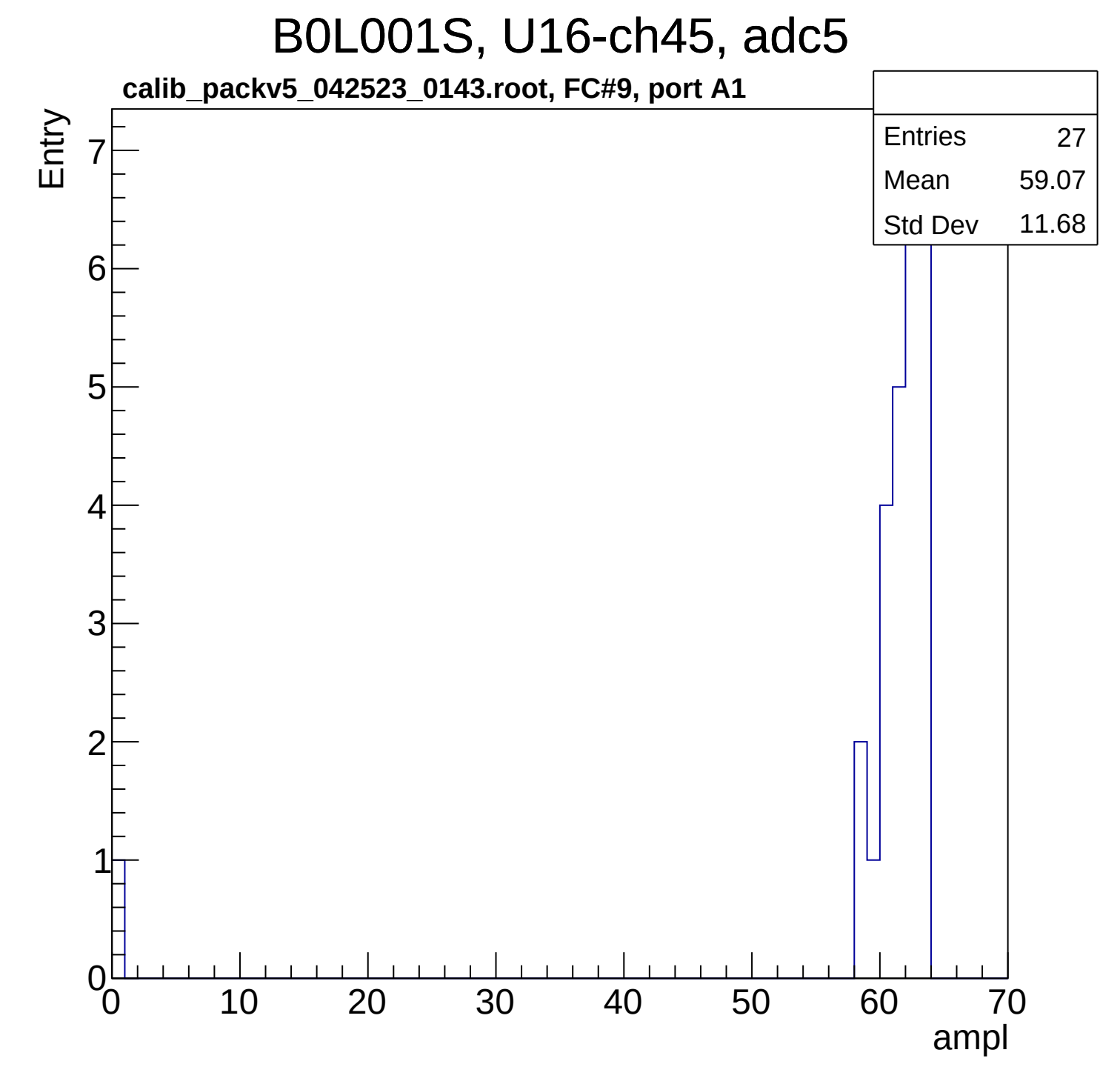
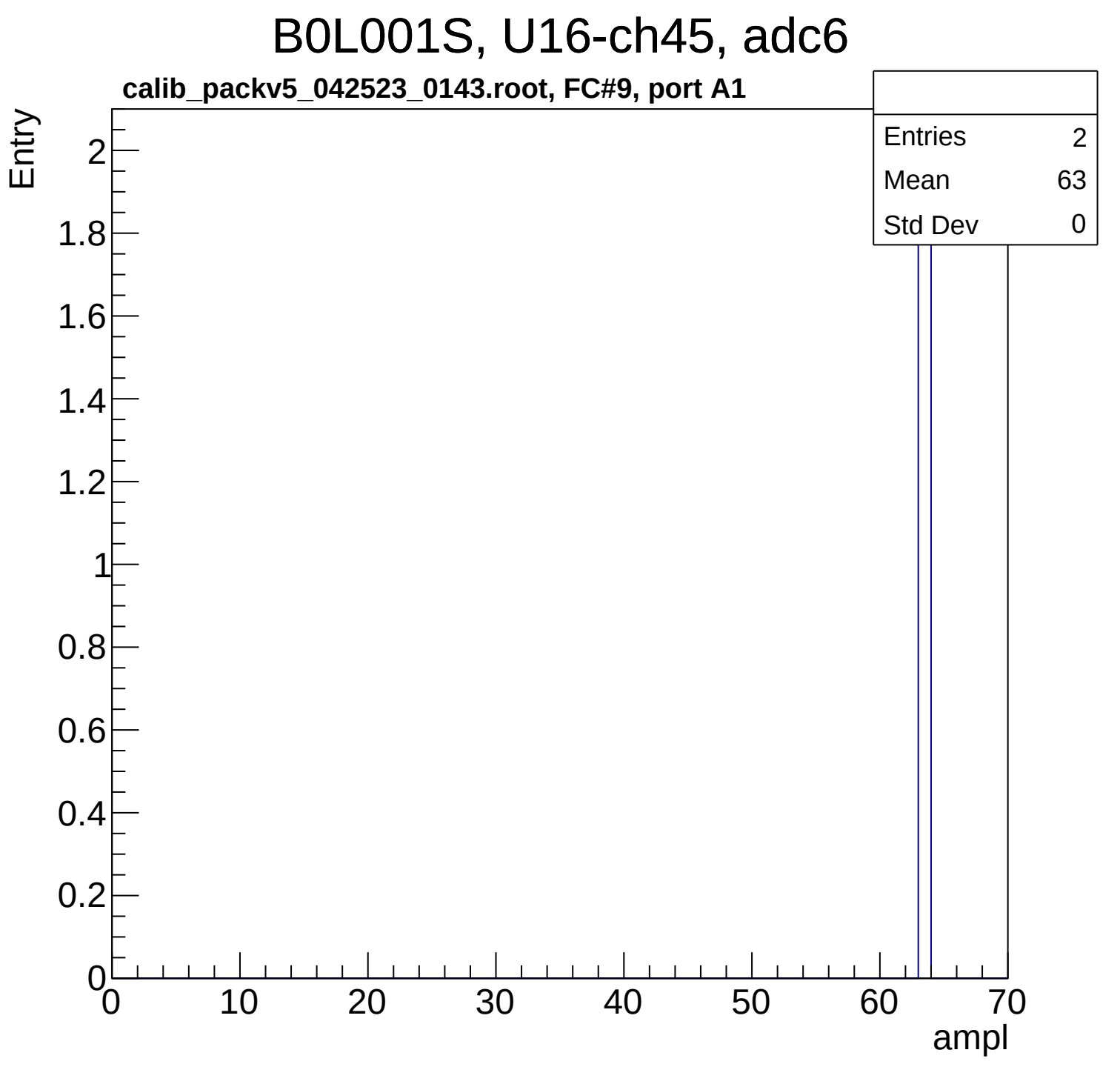
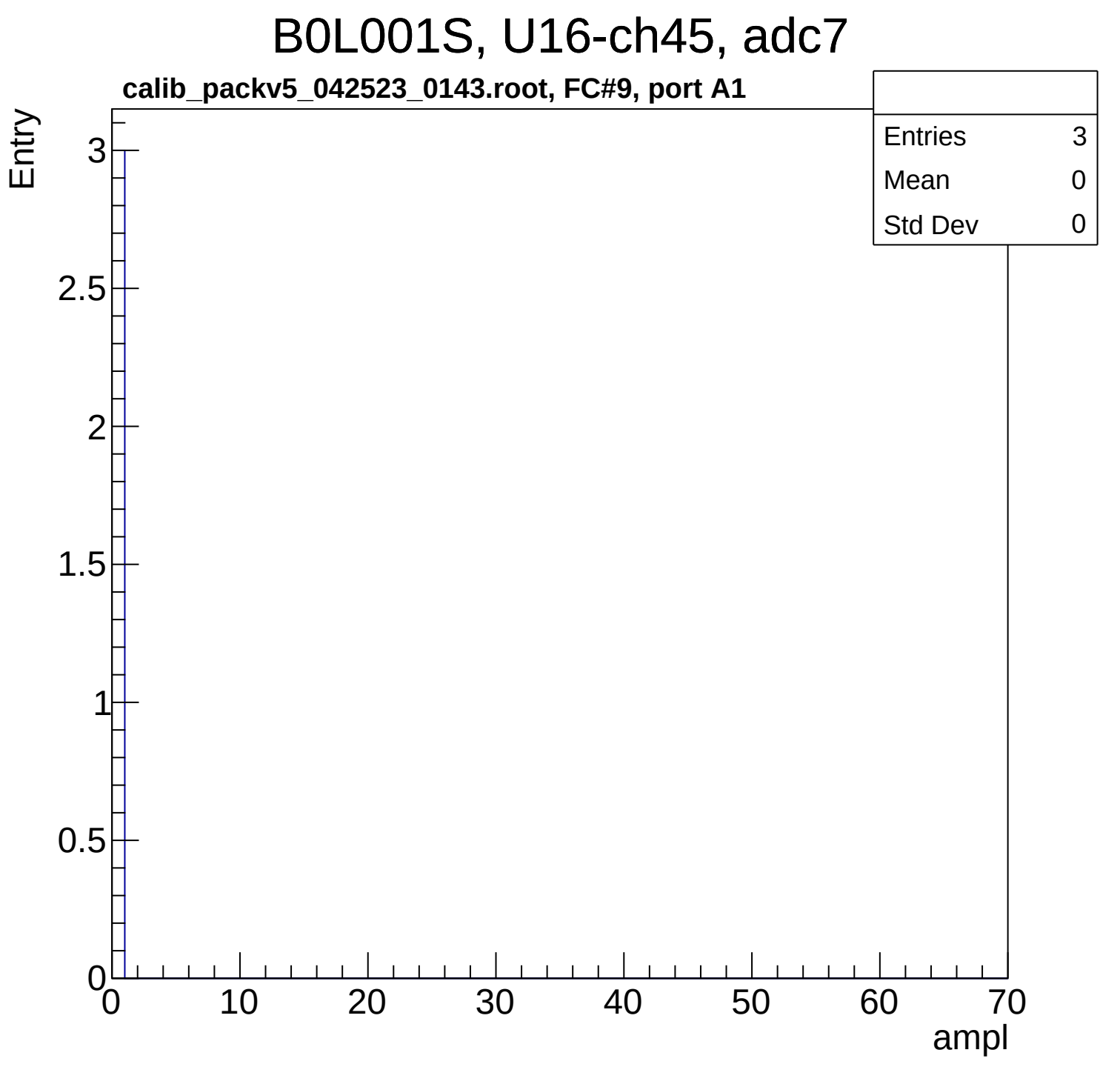
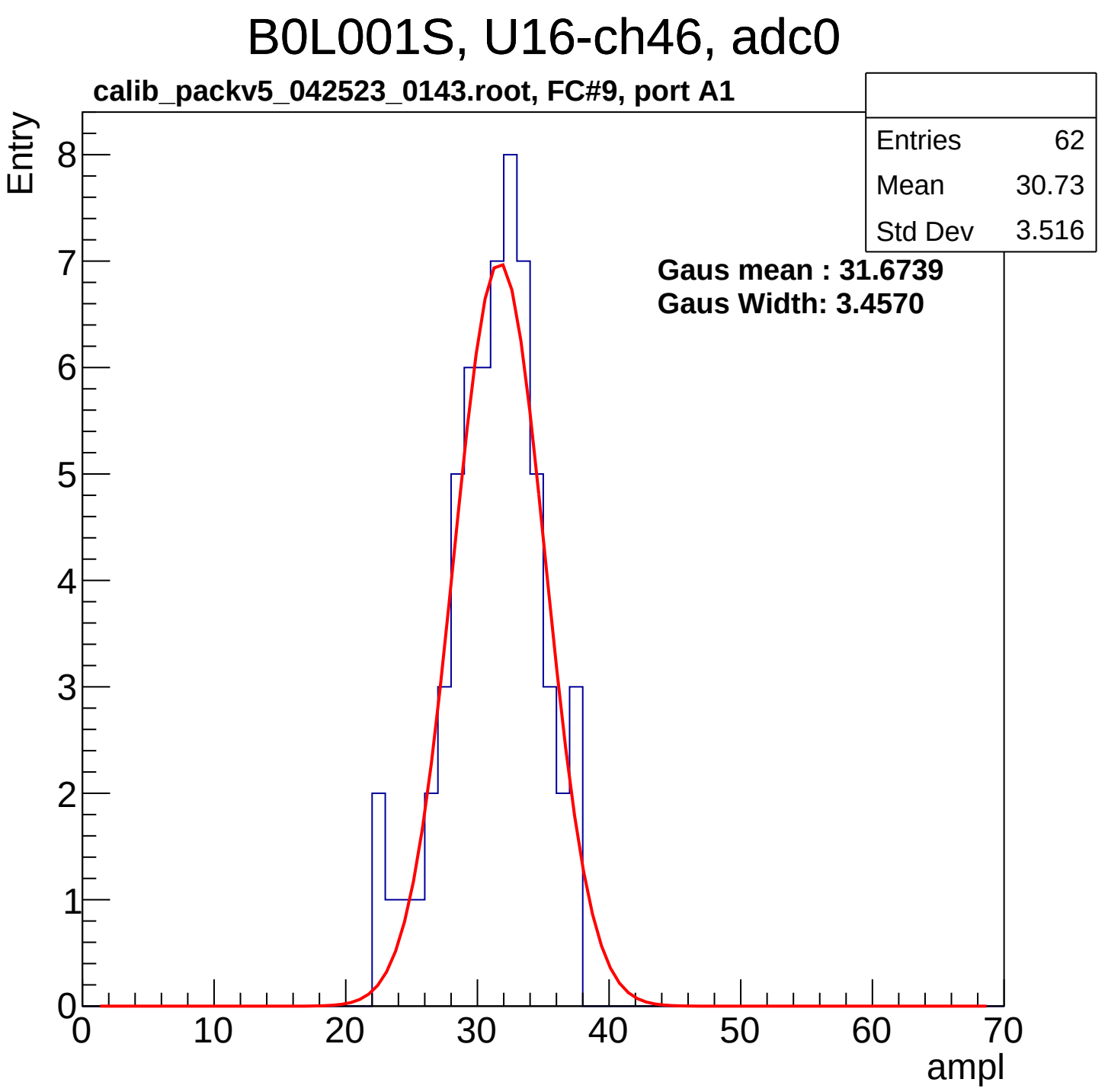
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 59.07
Std Dev 11.68
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.73
Std Dev 3.516
Gaus mean : 31.6739
Gaus Width: 3.4570
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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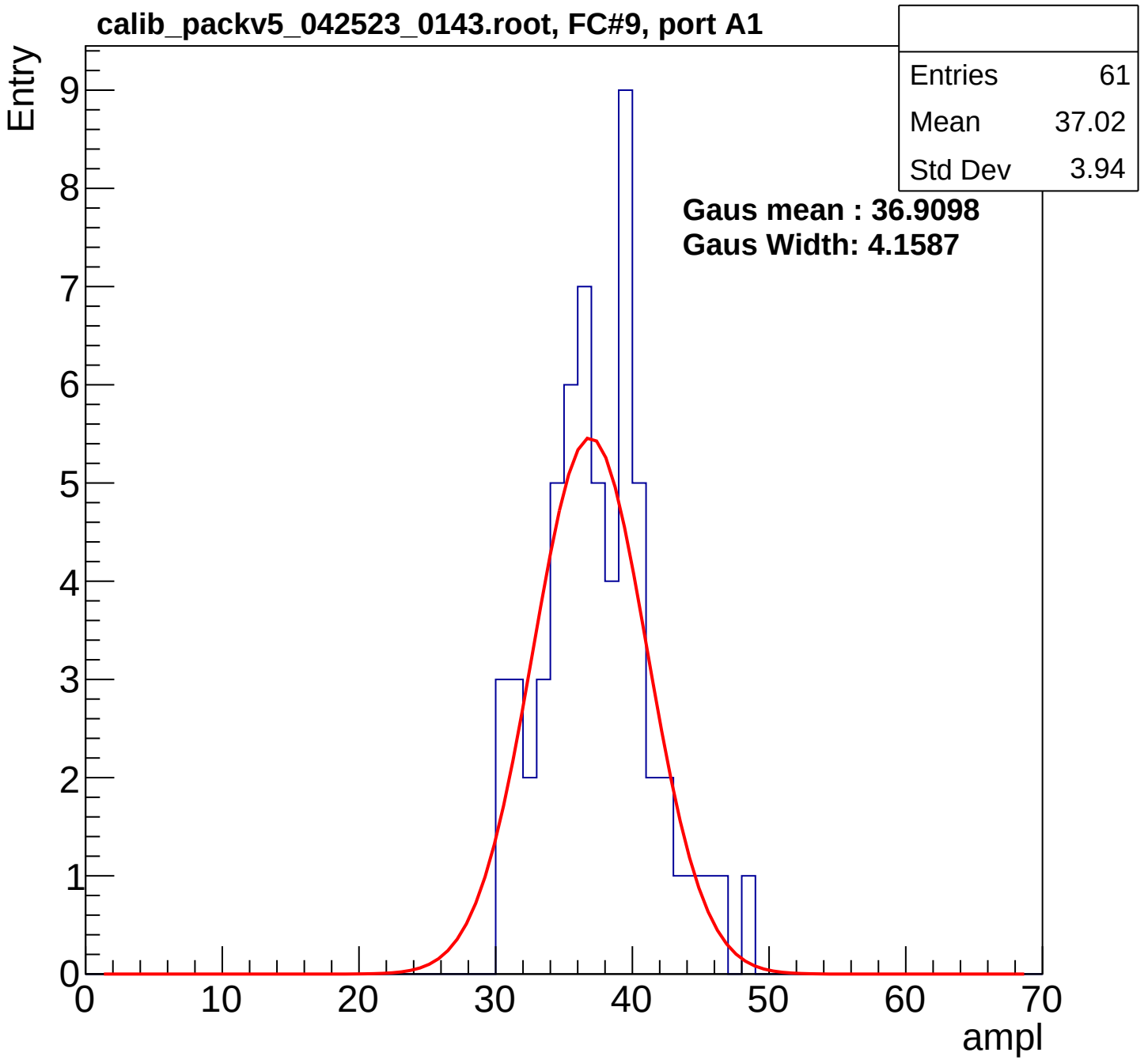
B0L001S, U16-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.02
Std Dev 3.94
Gaus mean : 36.9098
Gaus Width: 4.1587
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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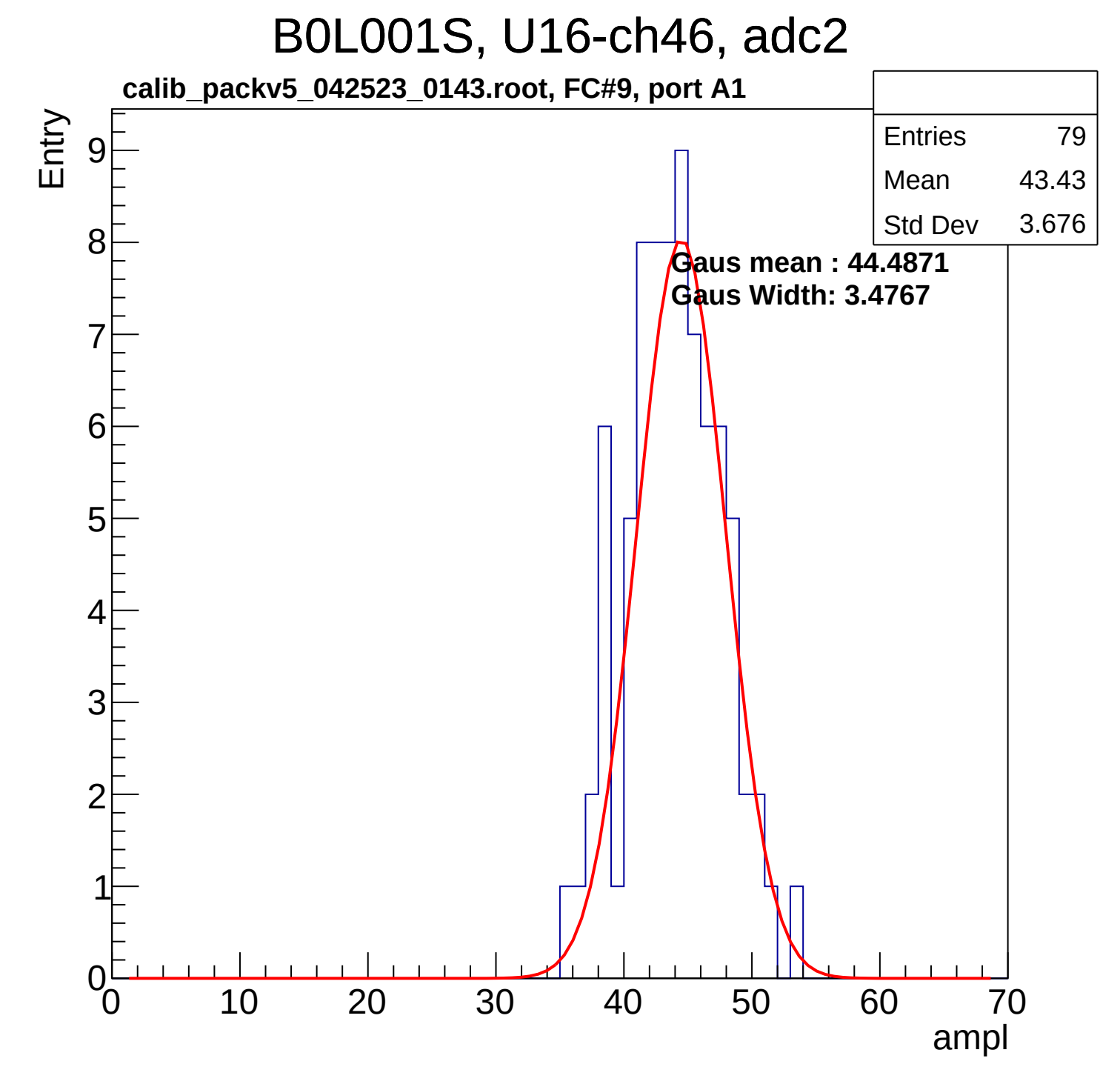
B0L001S, U16-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 43.43
Std Dev 3.676
Gaus mean : 44.4871
Gaus Width: 3.4767
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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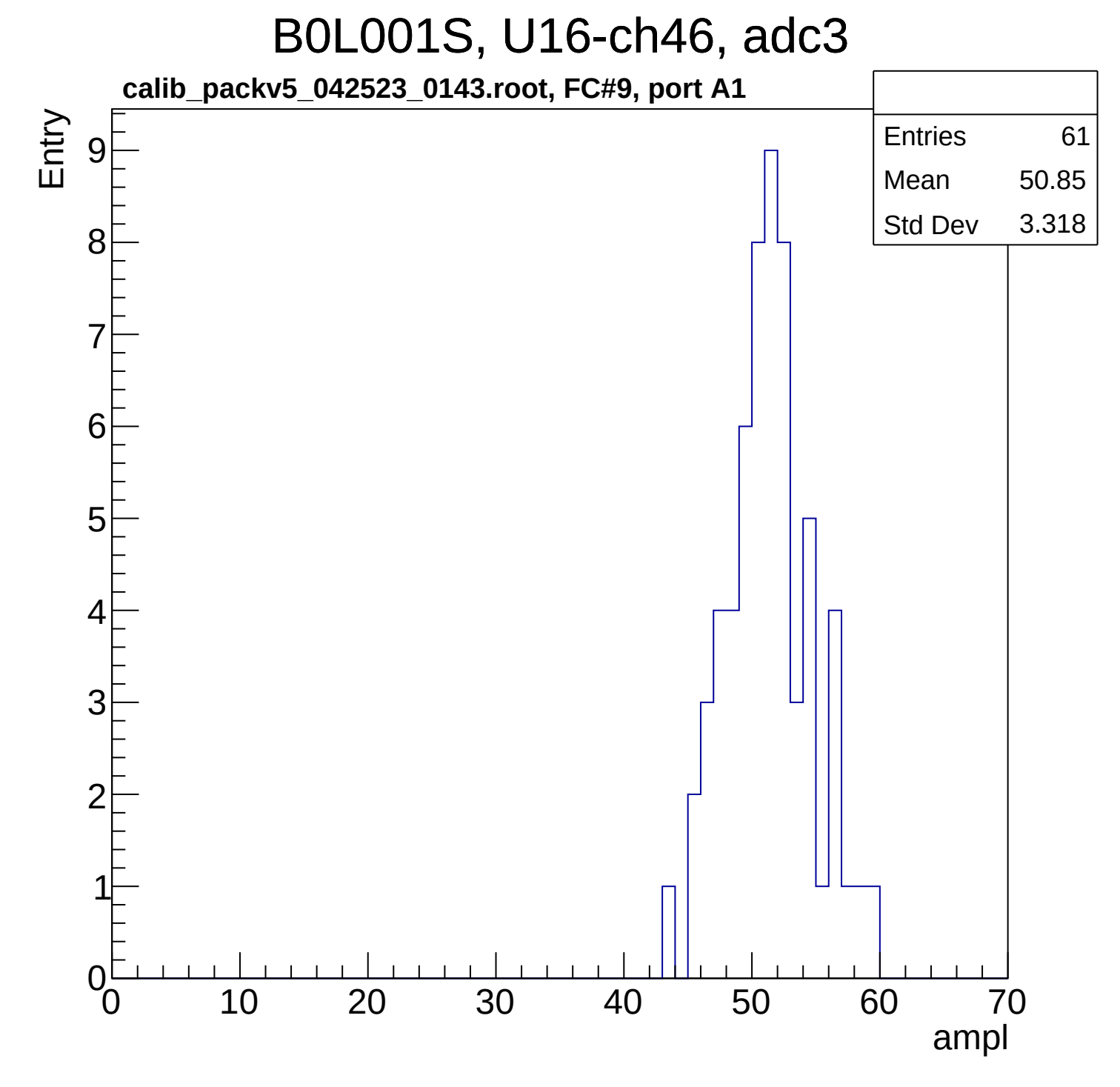
B0L001S, U16-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 50.85
Std Dev 3.318
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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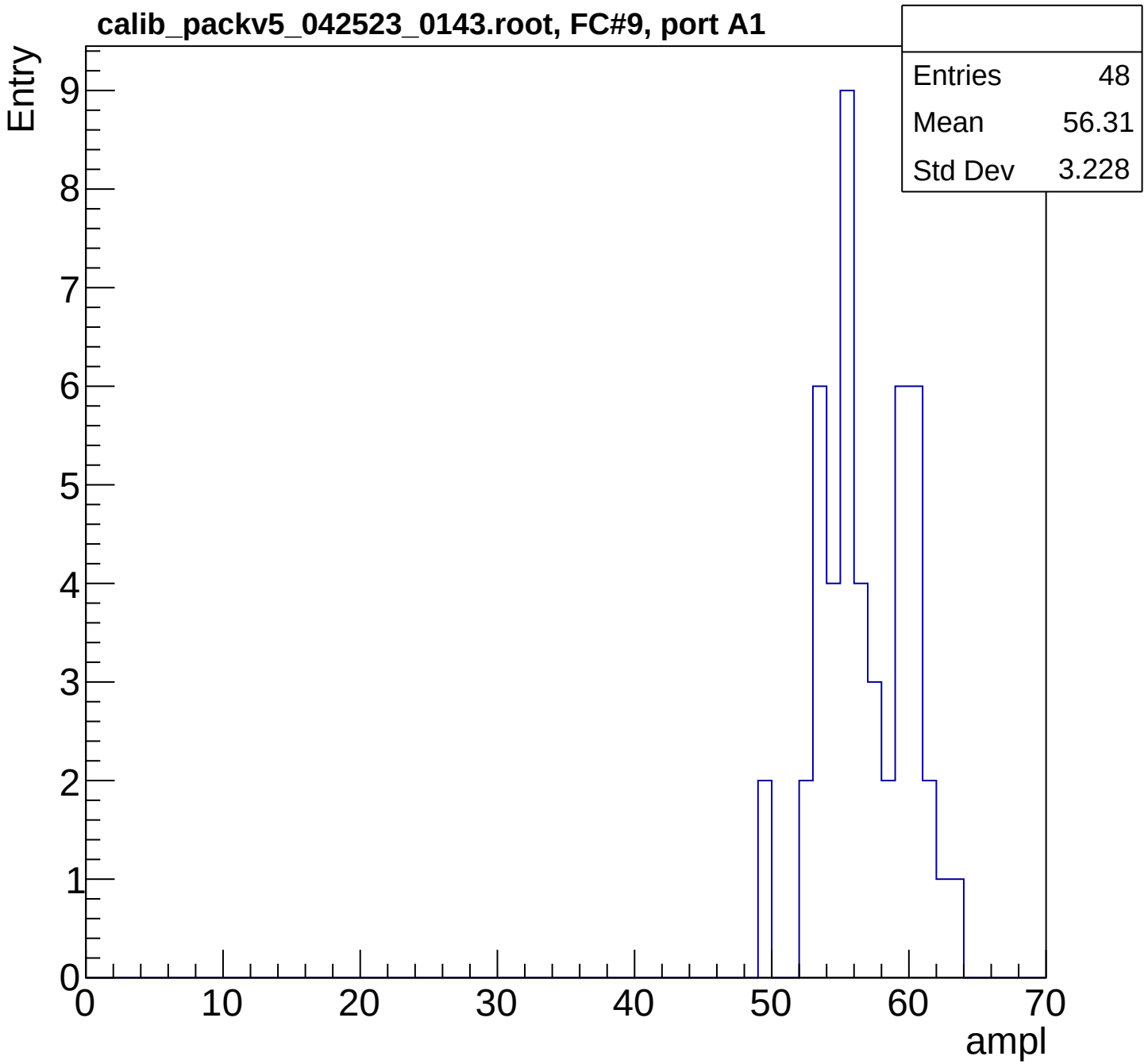
B0L001S, U16-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.31
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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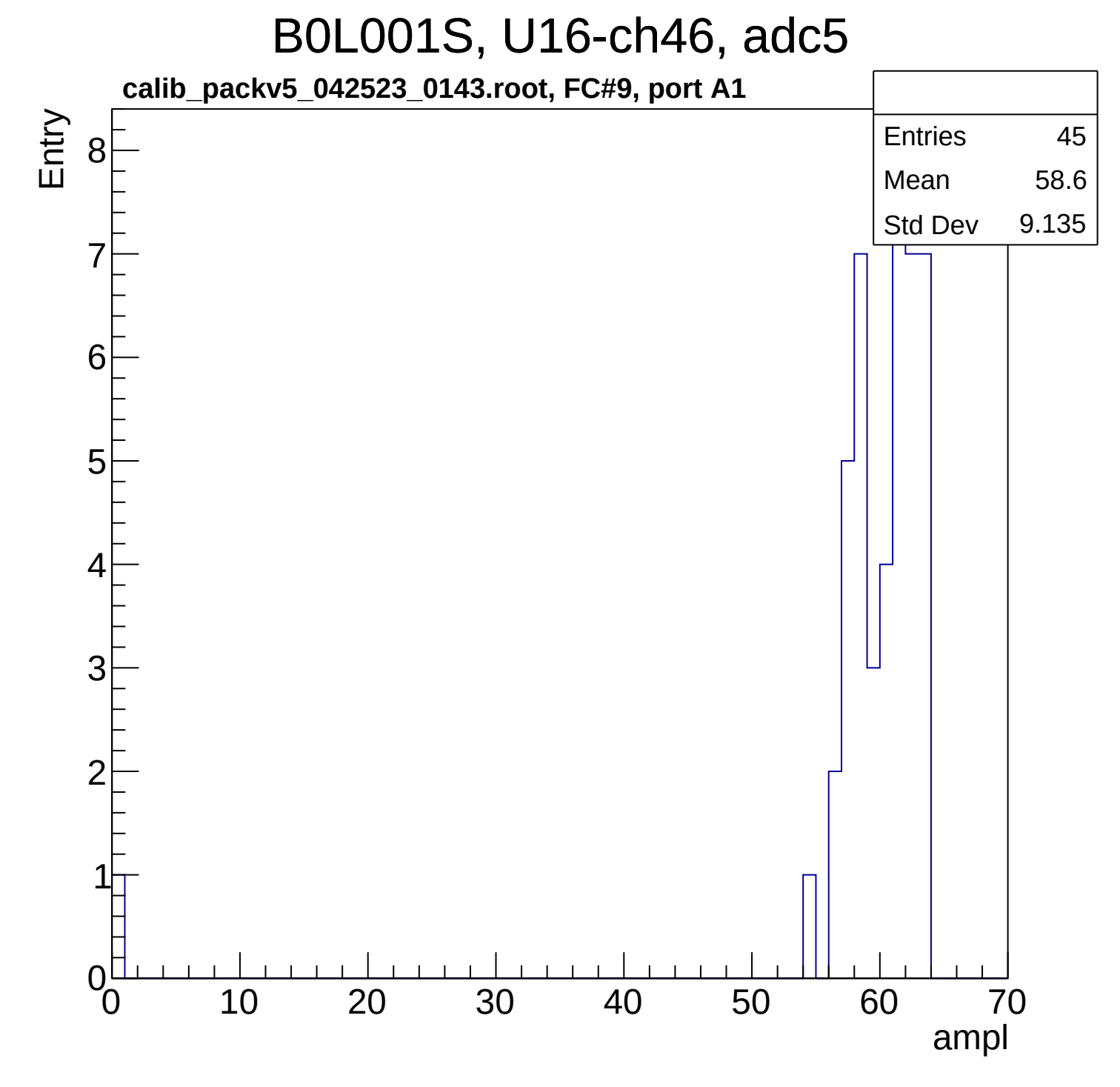
60
70


B0L001S, U16-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.6
Std Dev 9.135
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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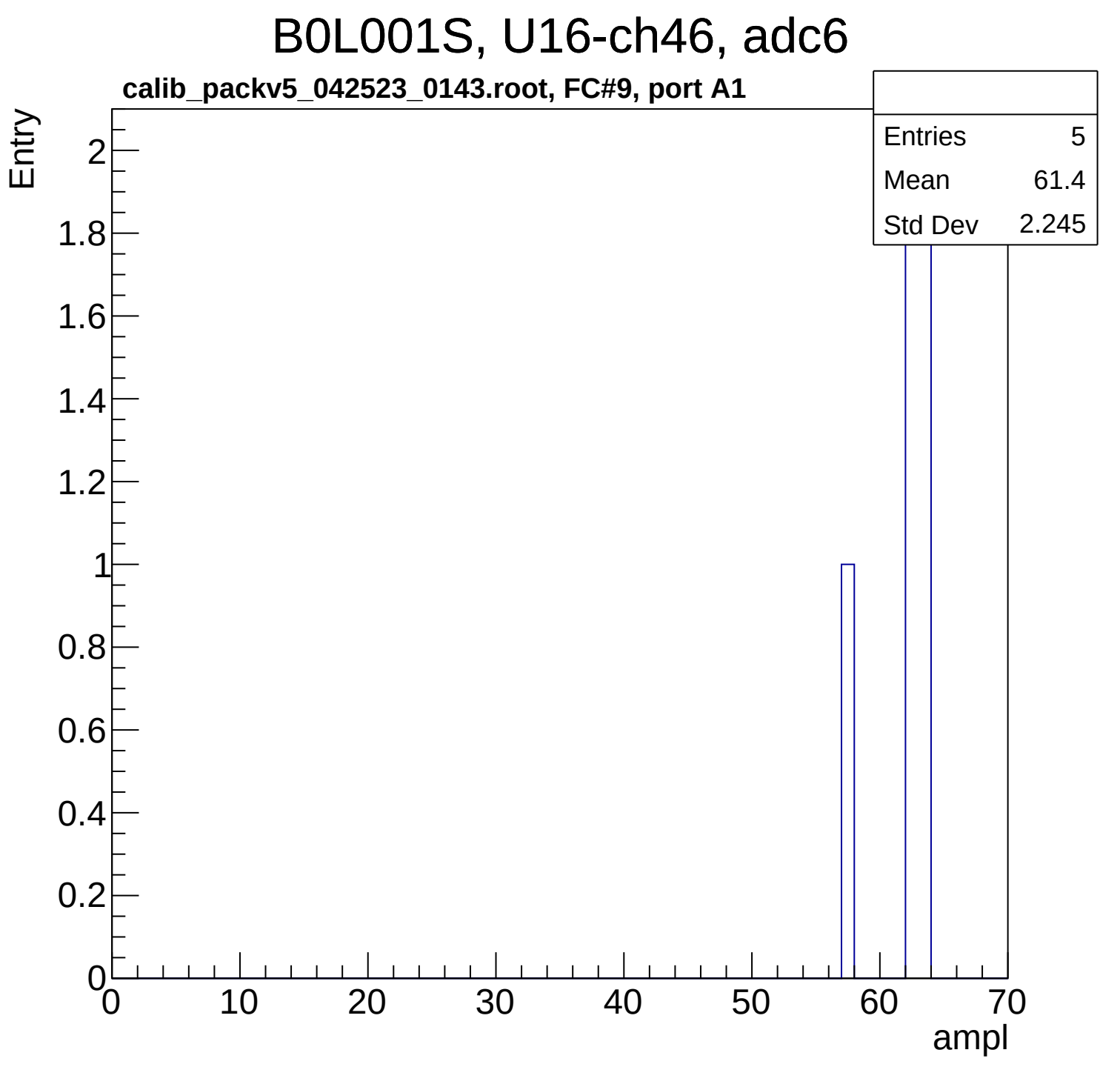
B0L001S, U16-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 2.245
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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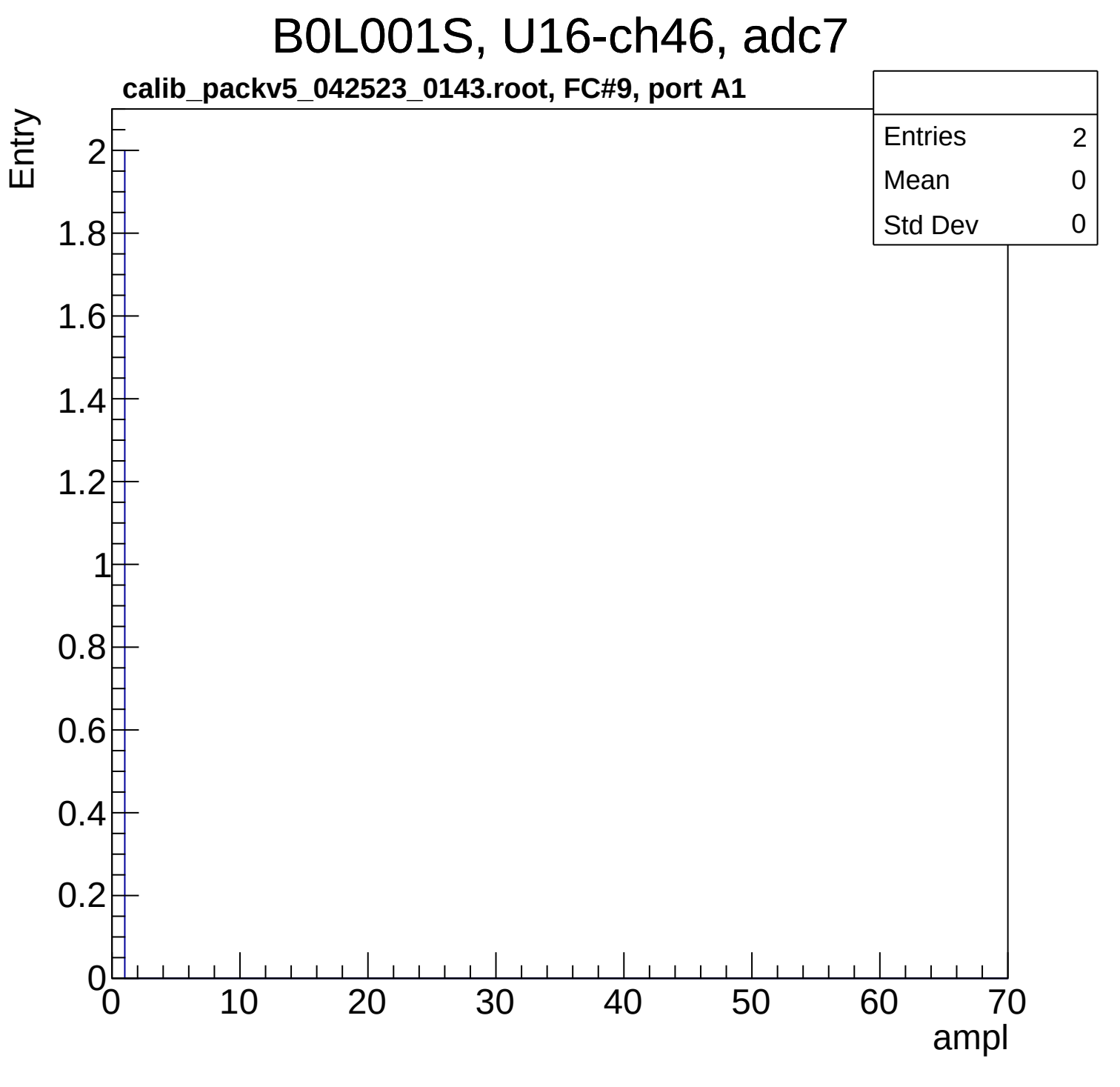
B0L001S, U16-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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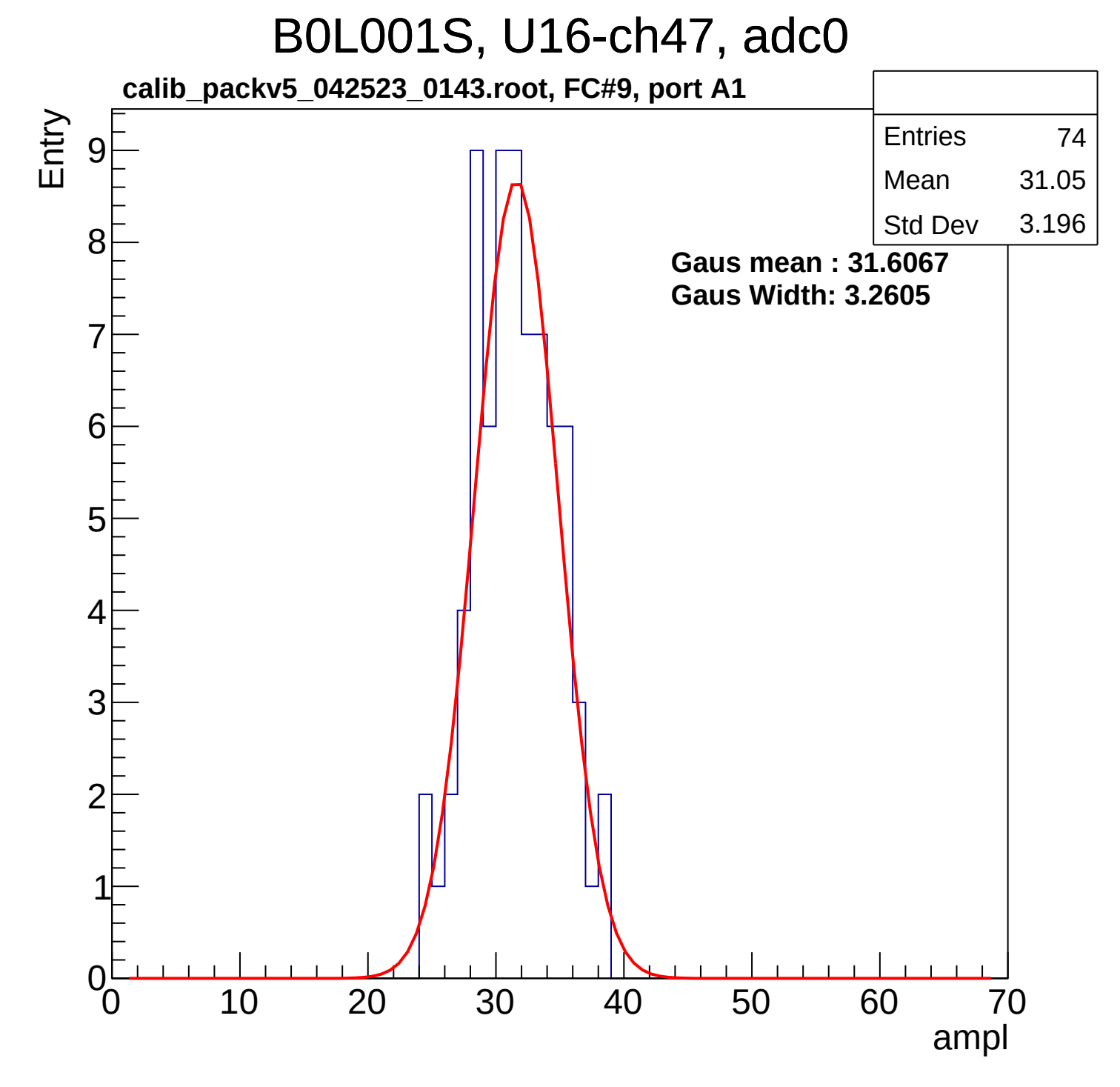
B0L001S, U16-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 31.05
Std Dev 3.196
Gaus mean : 31.6067
Gaus Width: 3.2605
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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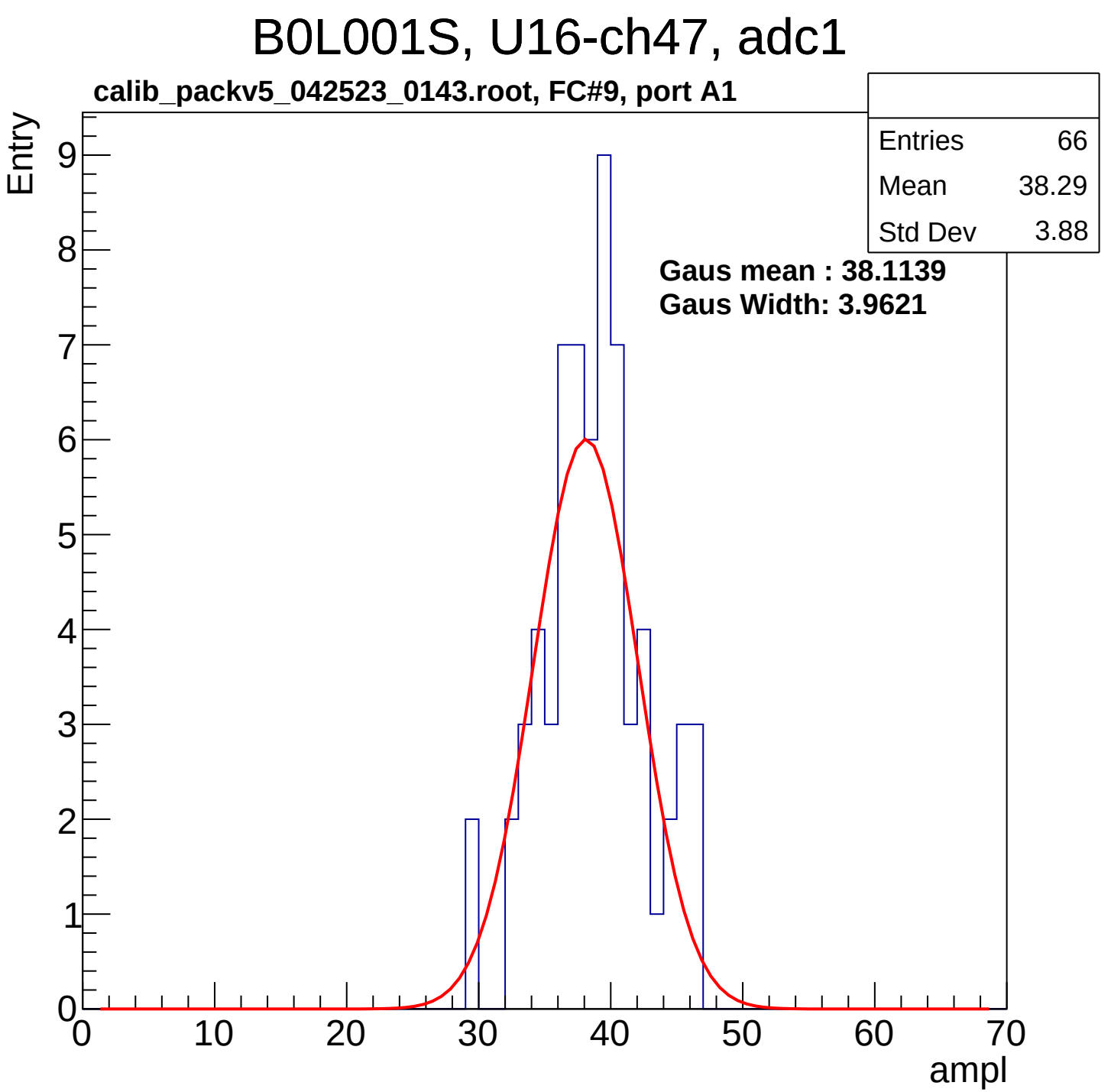
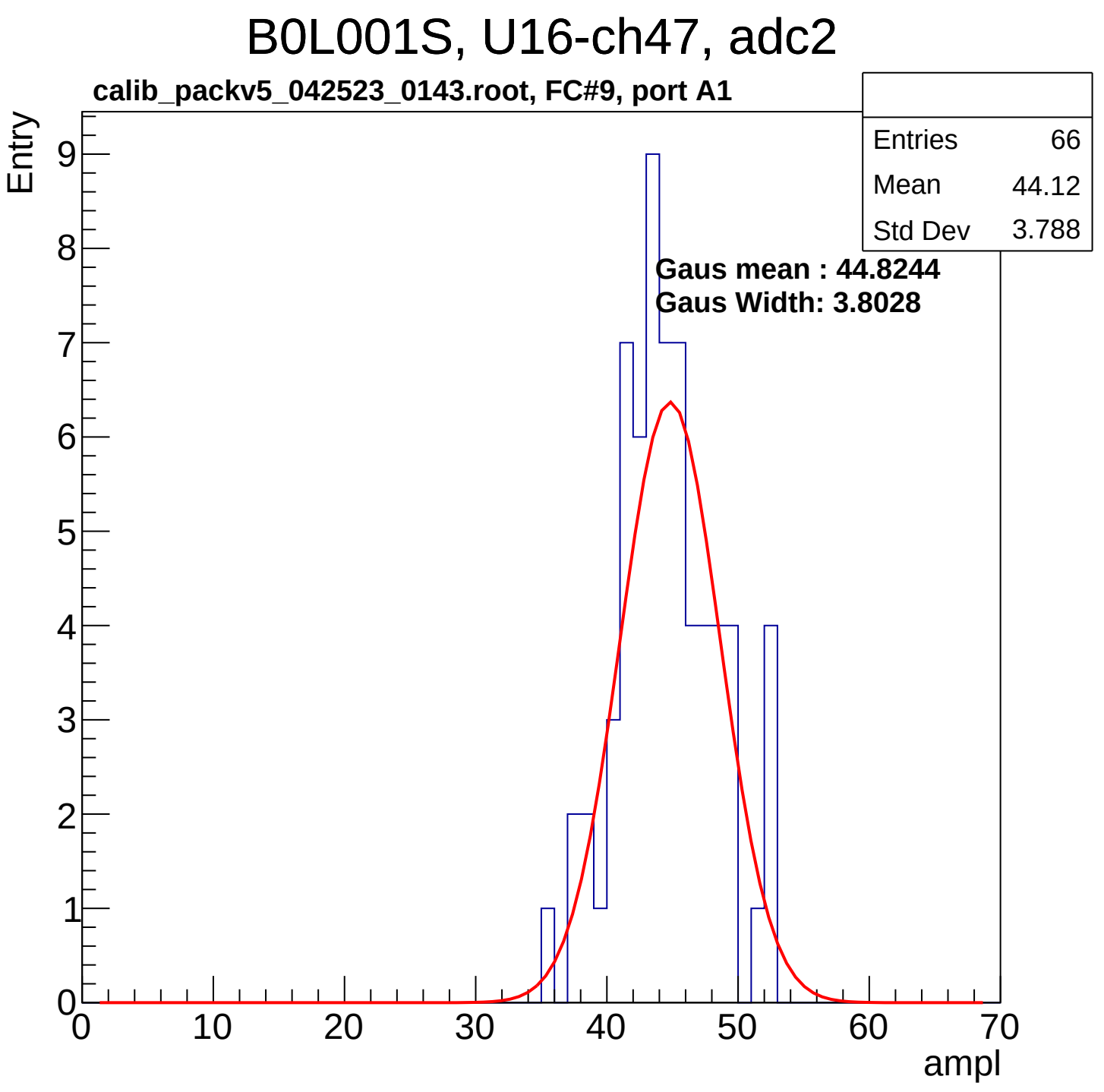

B0L001S, U16-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.29
Std Dev 3.88
Gaus mean : 38.1139
Gaus Width: 3.9621
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.12
Std Dev 3.788
Gaus mean : 44.8244
Gaus Width: 3.8028
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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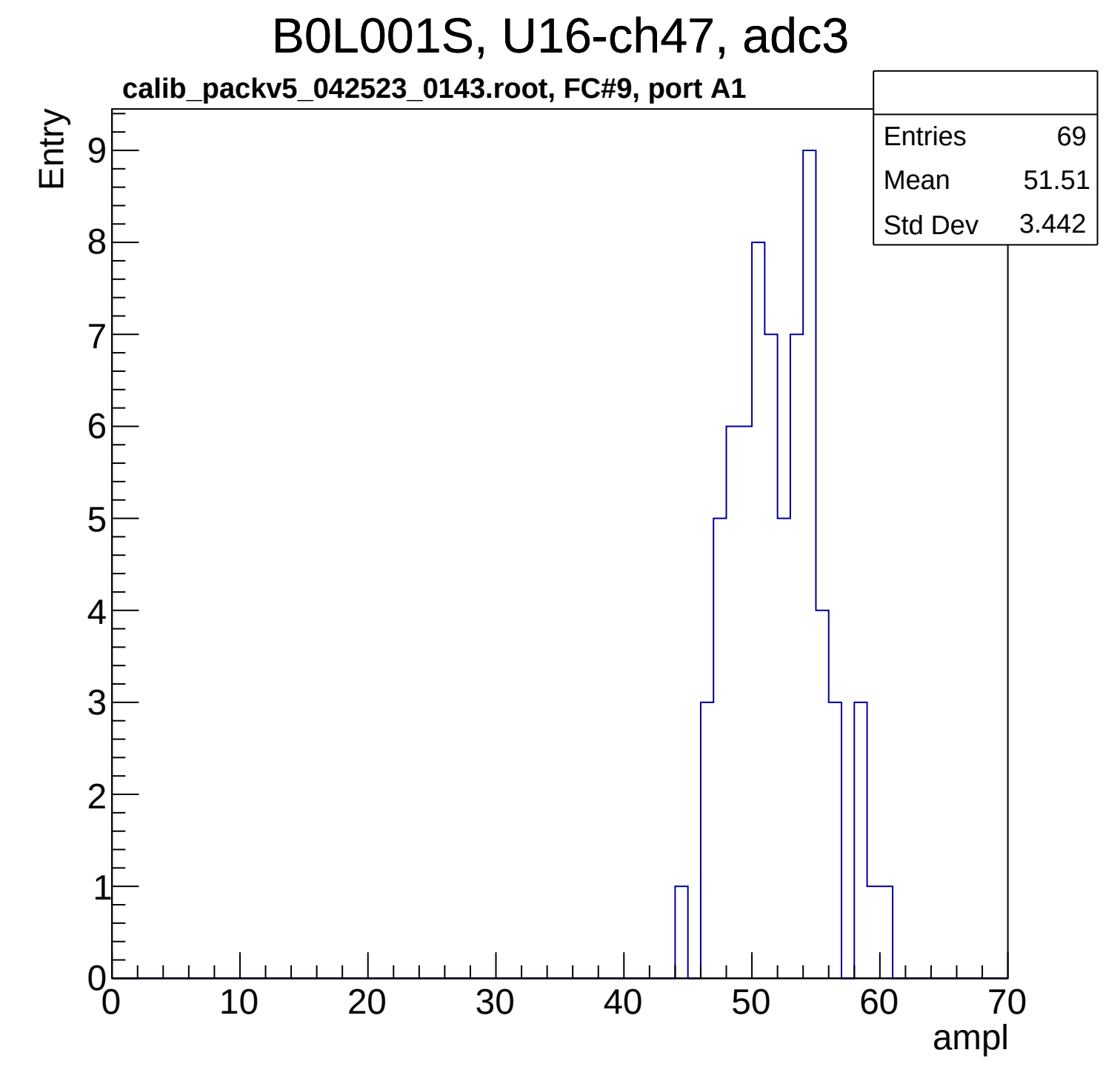
B0L001S, U16-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.51
Std Dev 3.442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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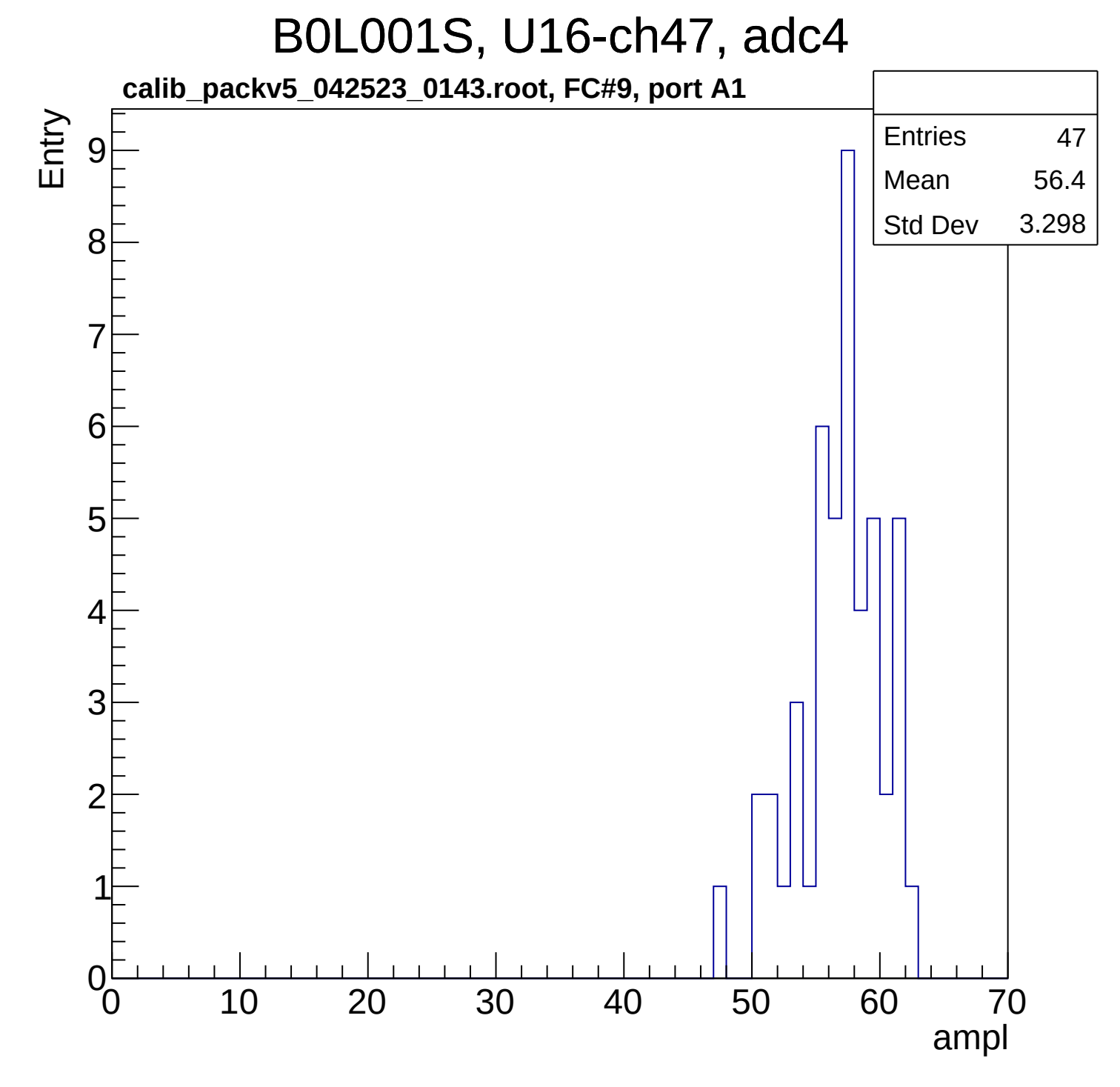
B0L001S, U16-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.4
Std Dev 3.298
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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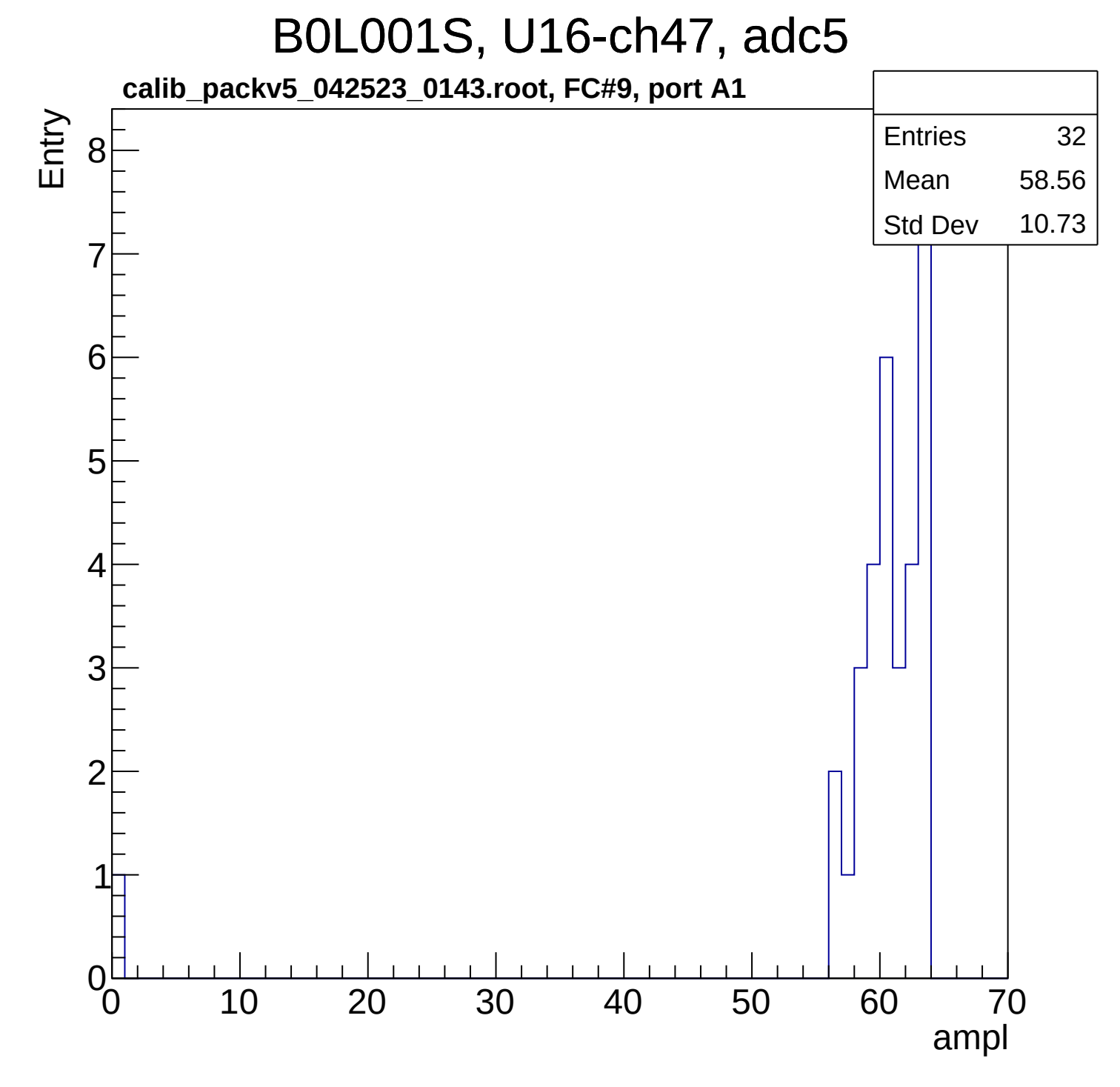
B0L001S, U16-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.56
Std Dev 10.73
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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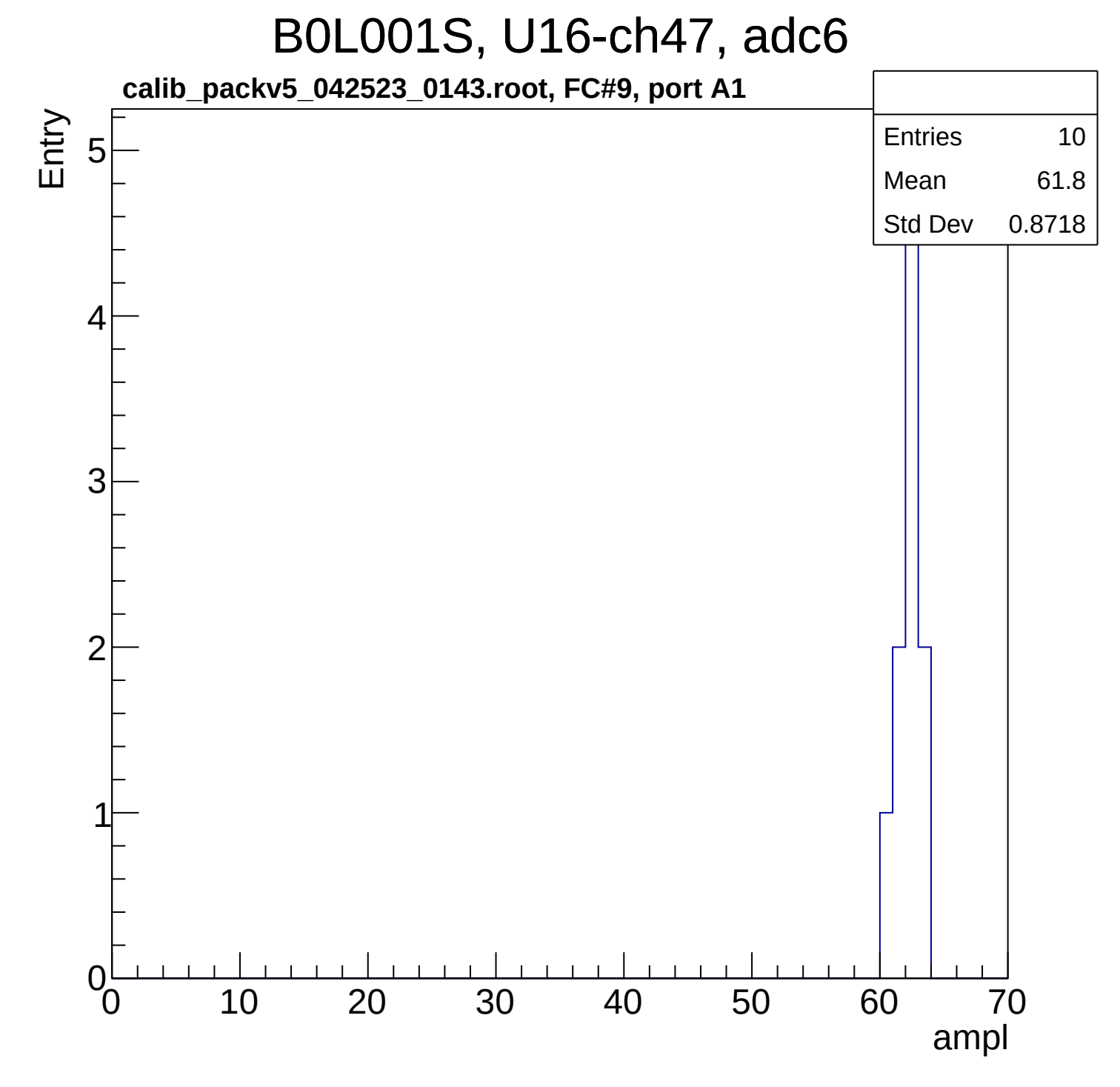
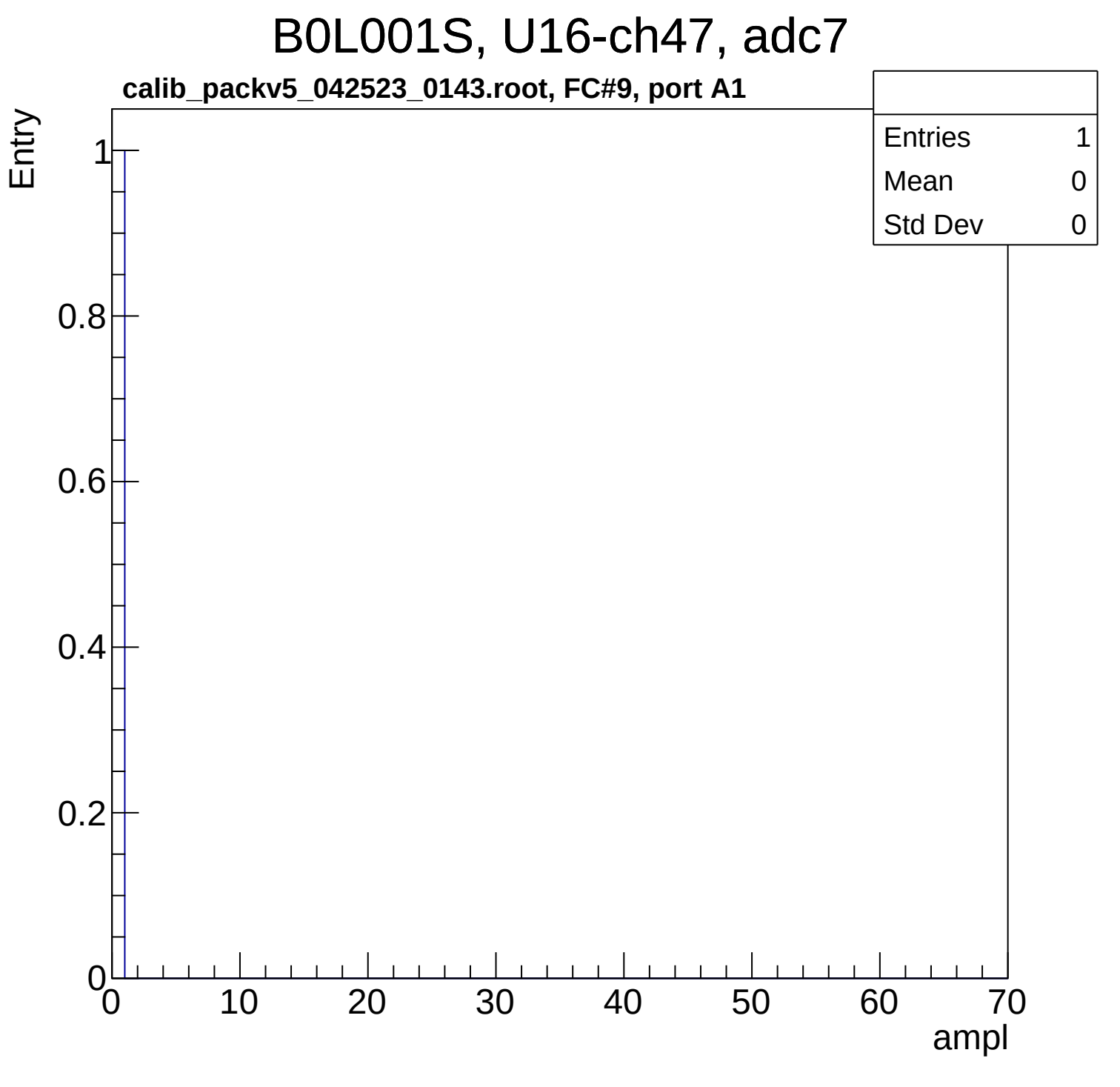
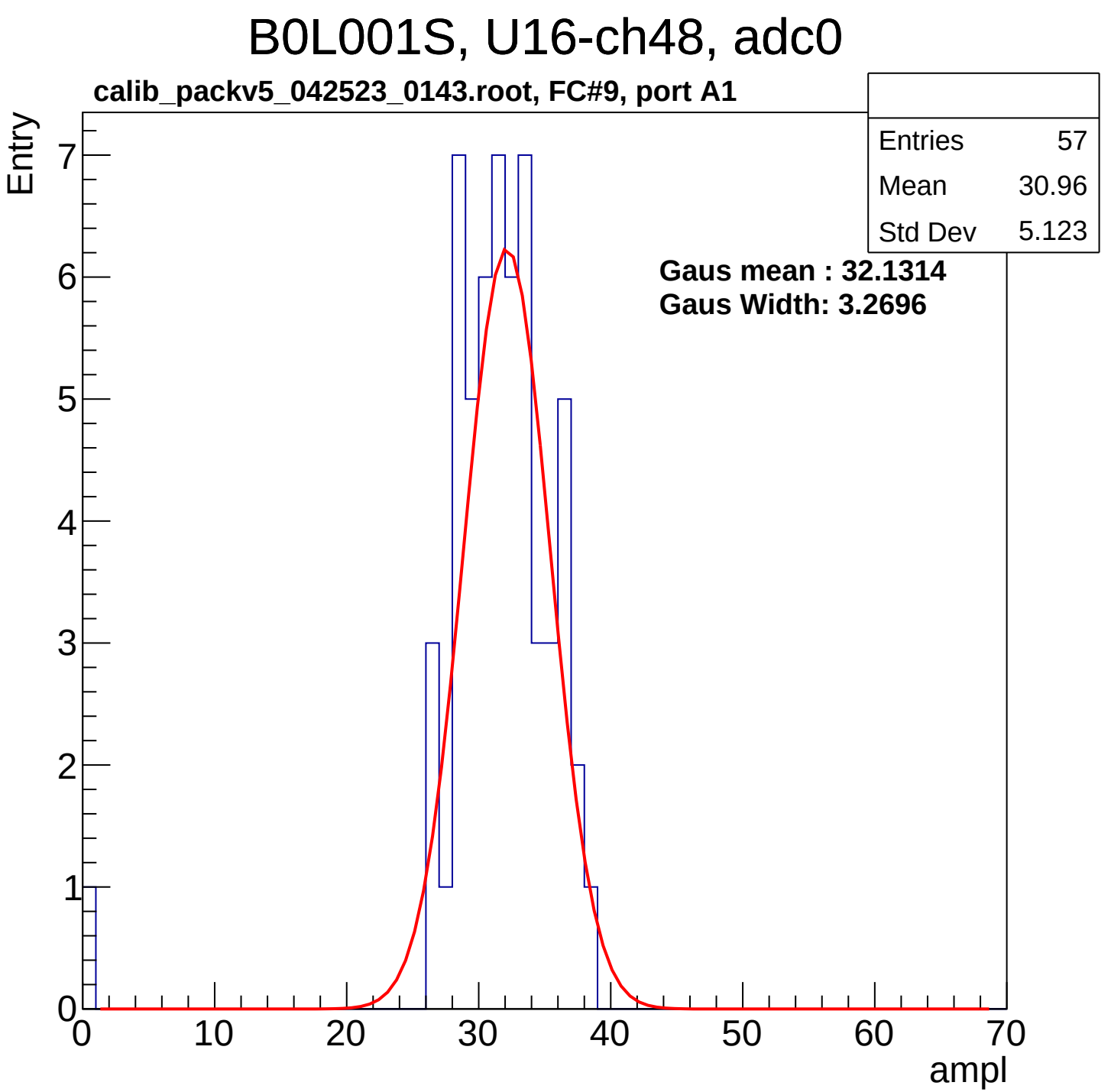
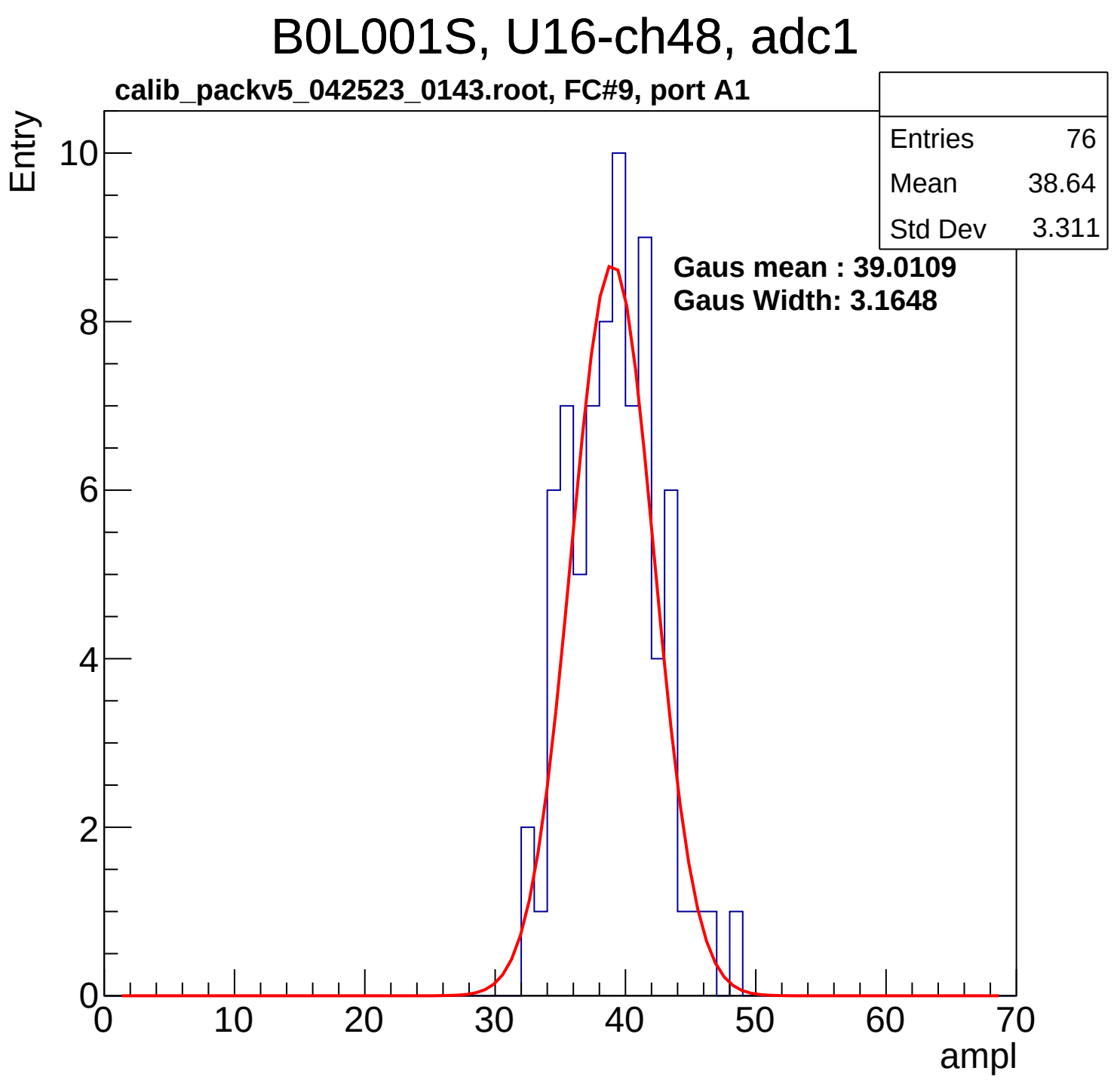
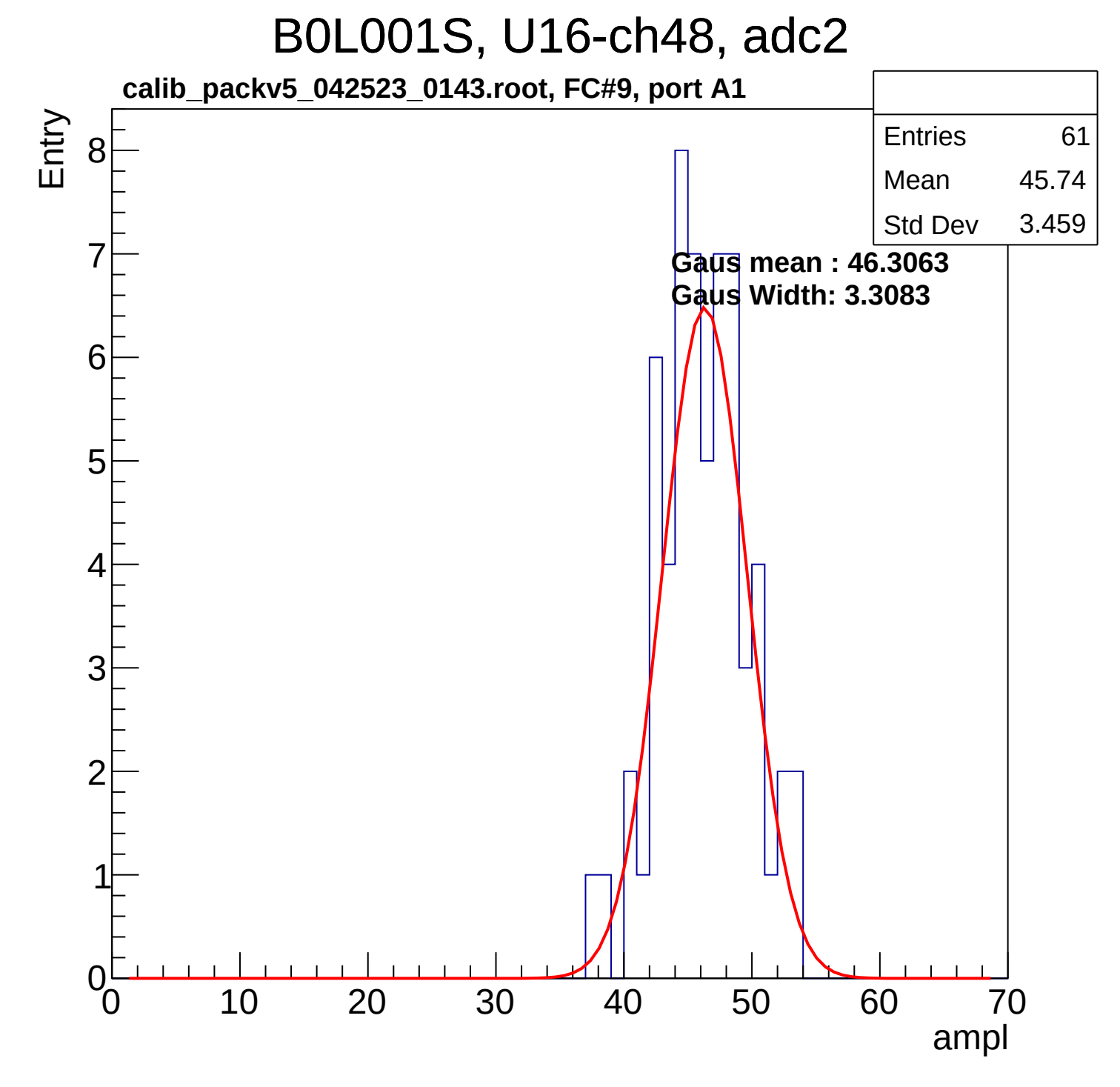

B0L001S, U16-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.8
Std Dev 0.8718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 30.96
Std Dev 5.123
Gaus mean : 32.1314
Gaus Width: 3.2696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.64
Std Dev 3.311
Gaus mean : 39.0109
Gaus Width: 3.1648
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.74
Std Dev 3.459
Gaus mean : 46.3063
Gaus Width: 3.3083
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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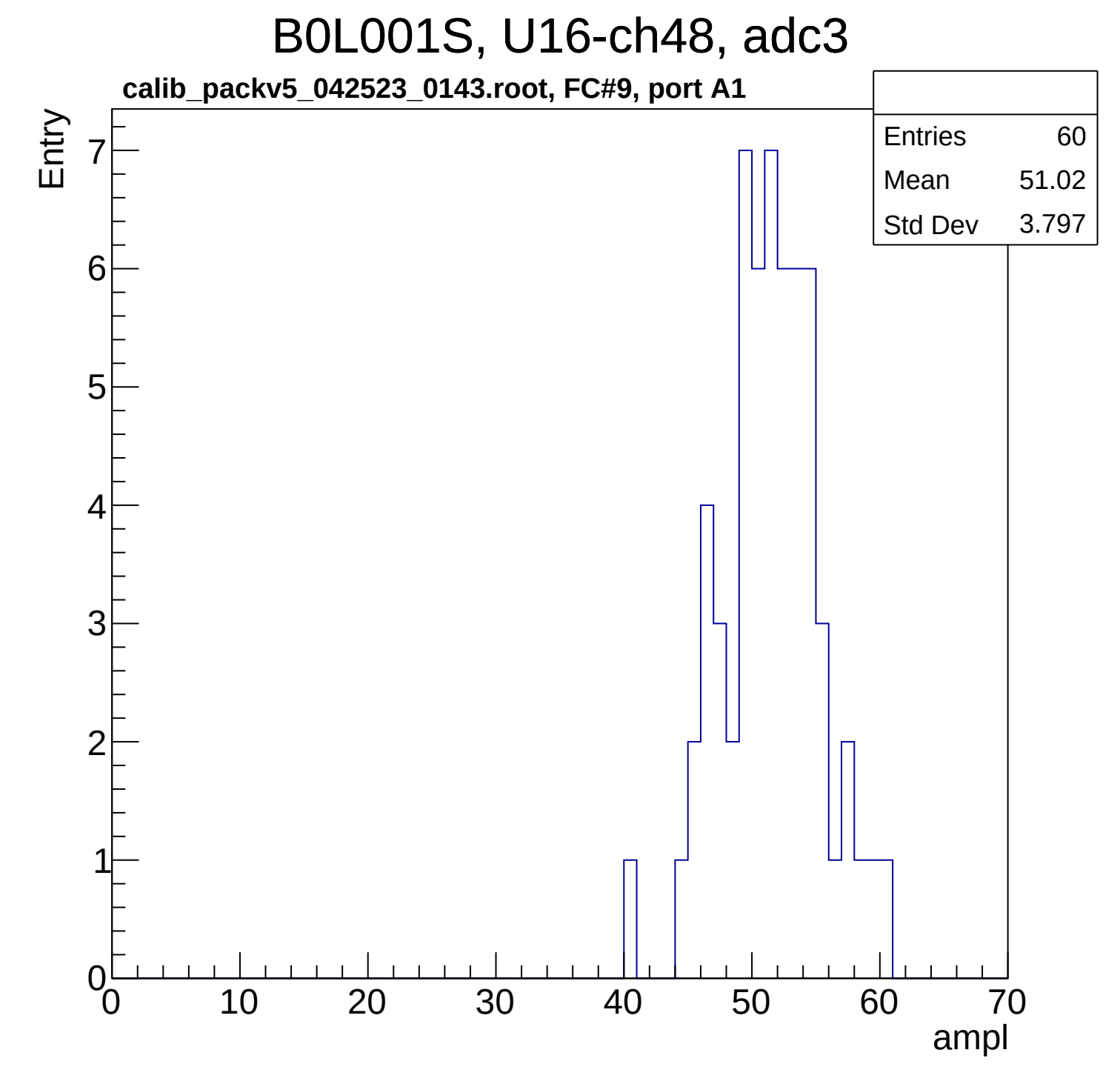
B0L001S, U16-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.02
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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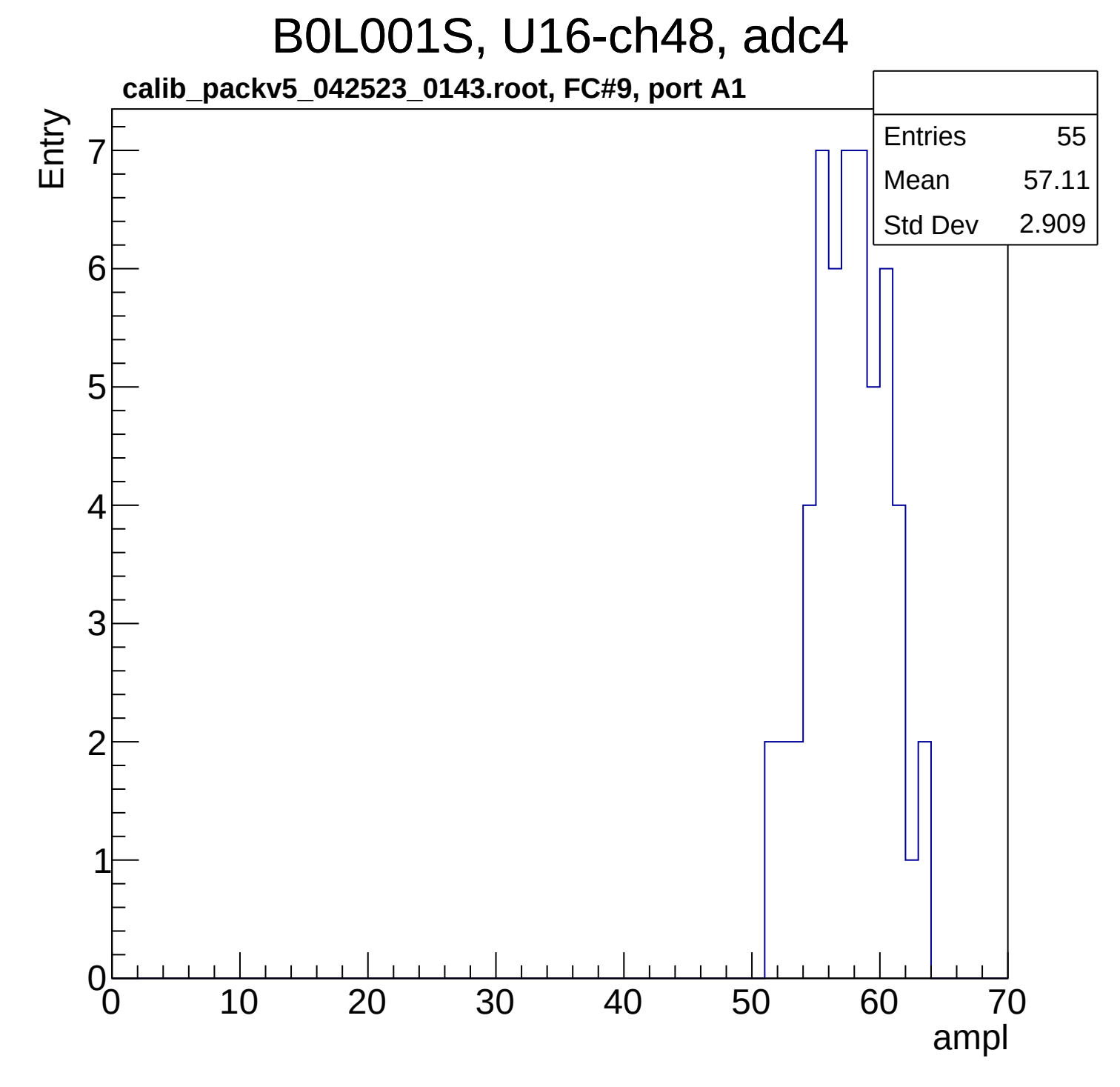
40
50
60
70


B0L001S, U16-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.11
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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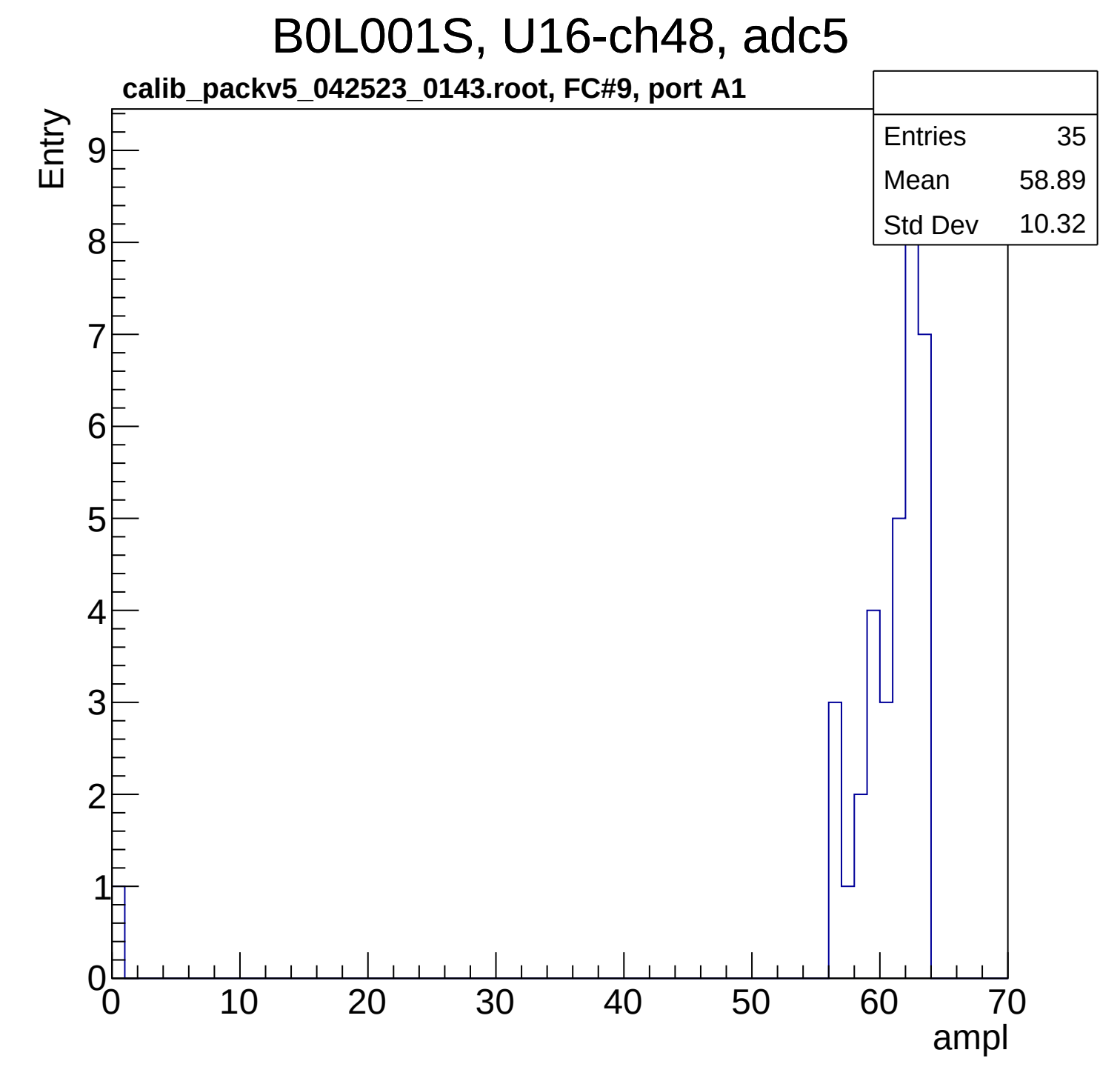
40
50
60
70


B0L001S, U16-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.89
Std Dev 10.32
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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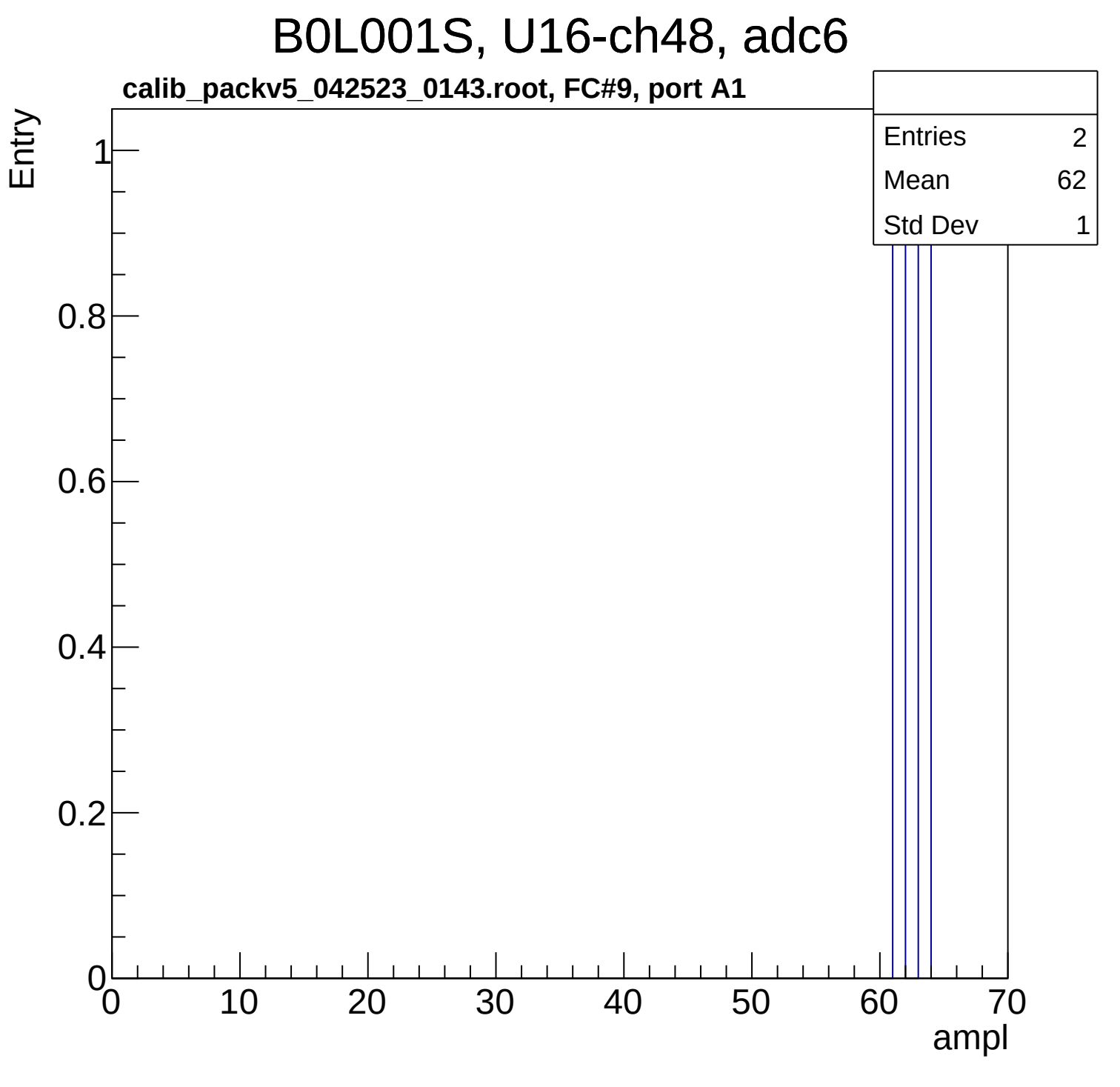
B0L001S, U16-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
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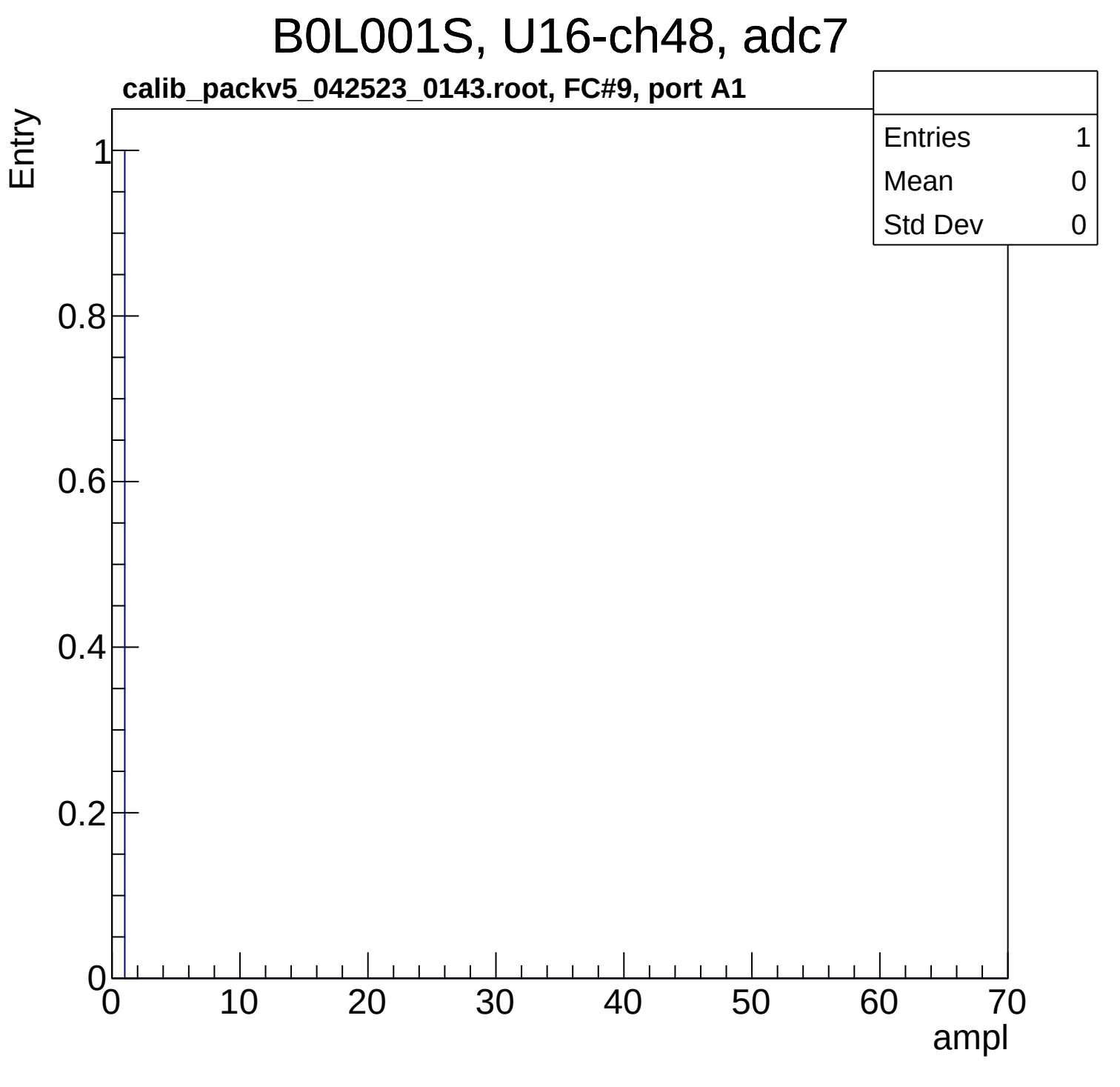
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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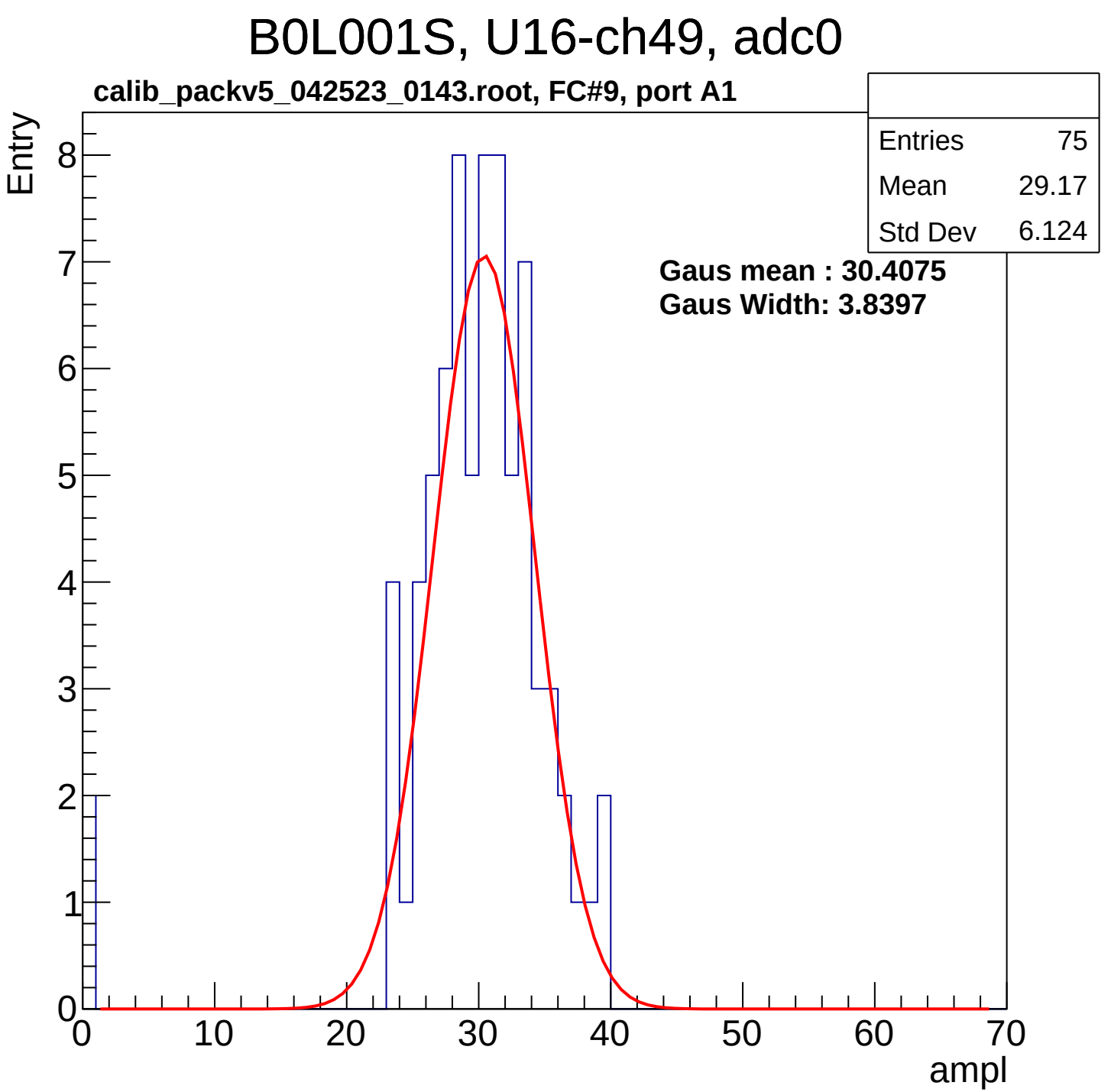
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.17
Std Dev 6.124
Gaus mean : 30.4075
Gaus Width: 3.8397
Entry
ampl
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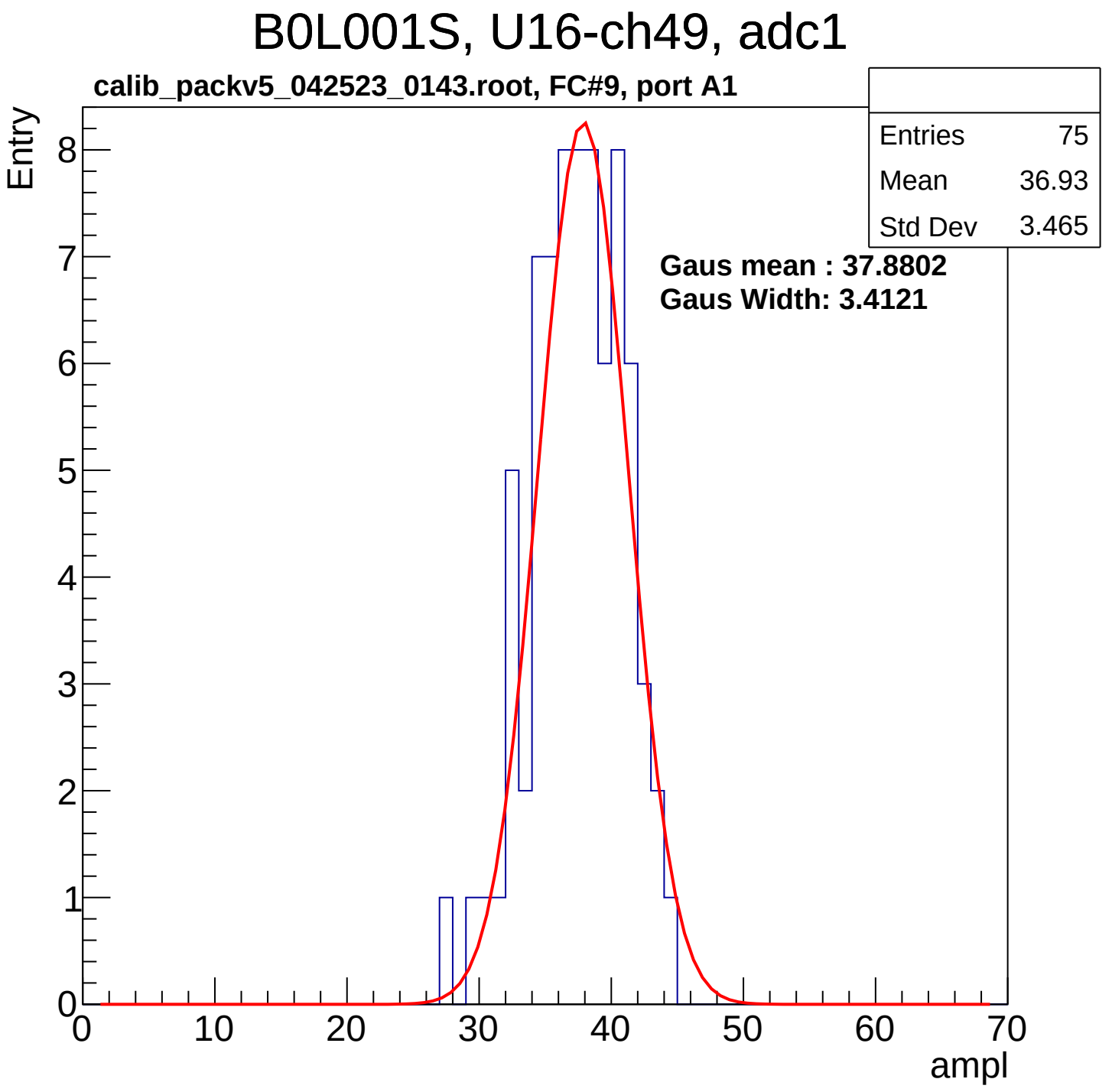
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 36.93
Std Dev 3.465
Gaus mean : 37.8802
Gaus Width: 3.4121
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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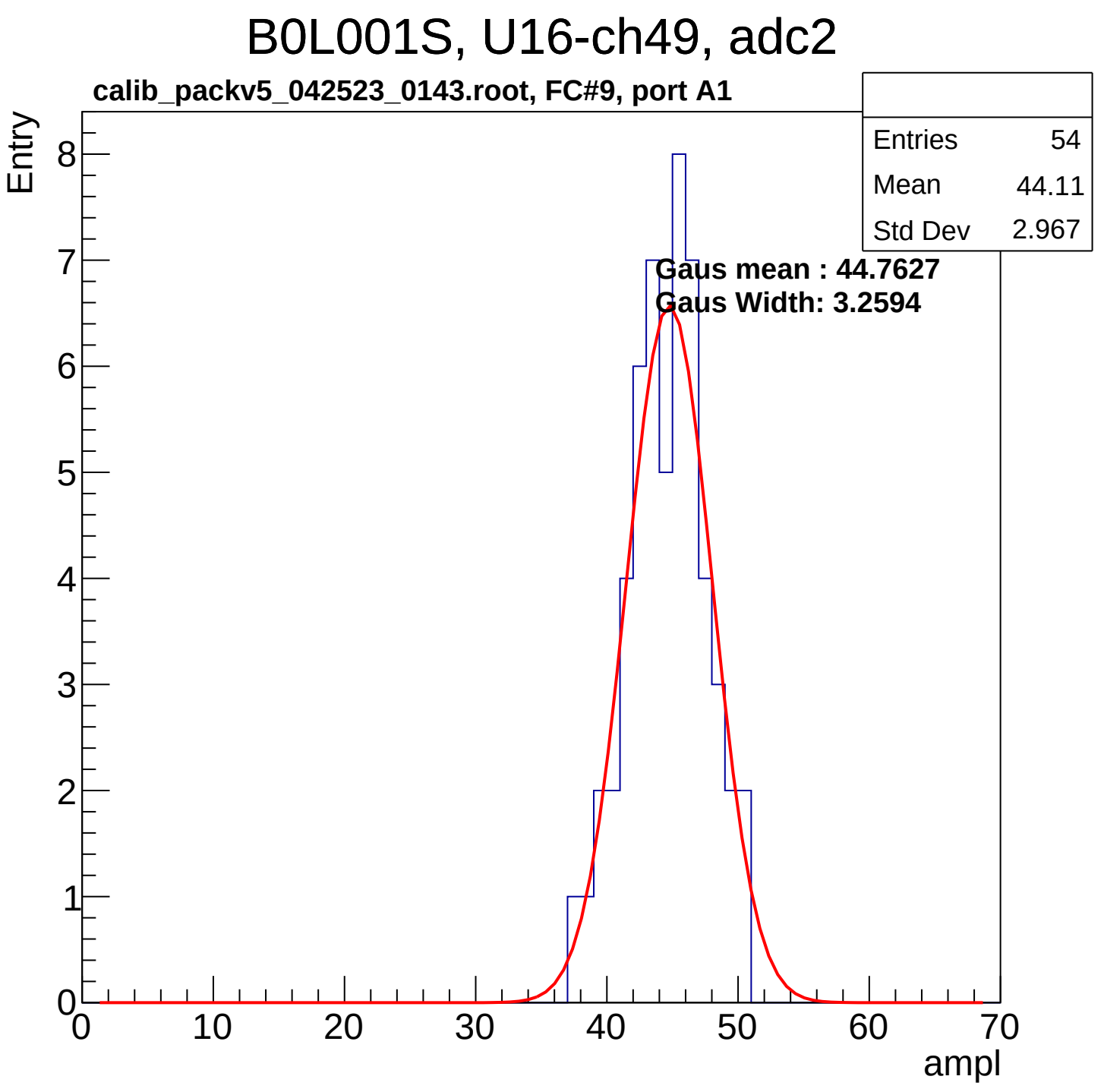
B0L001S, U16-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.11
Std Dev 2.967
Gaus mean : 44.7627
Gaus Width: 3.2594
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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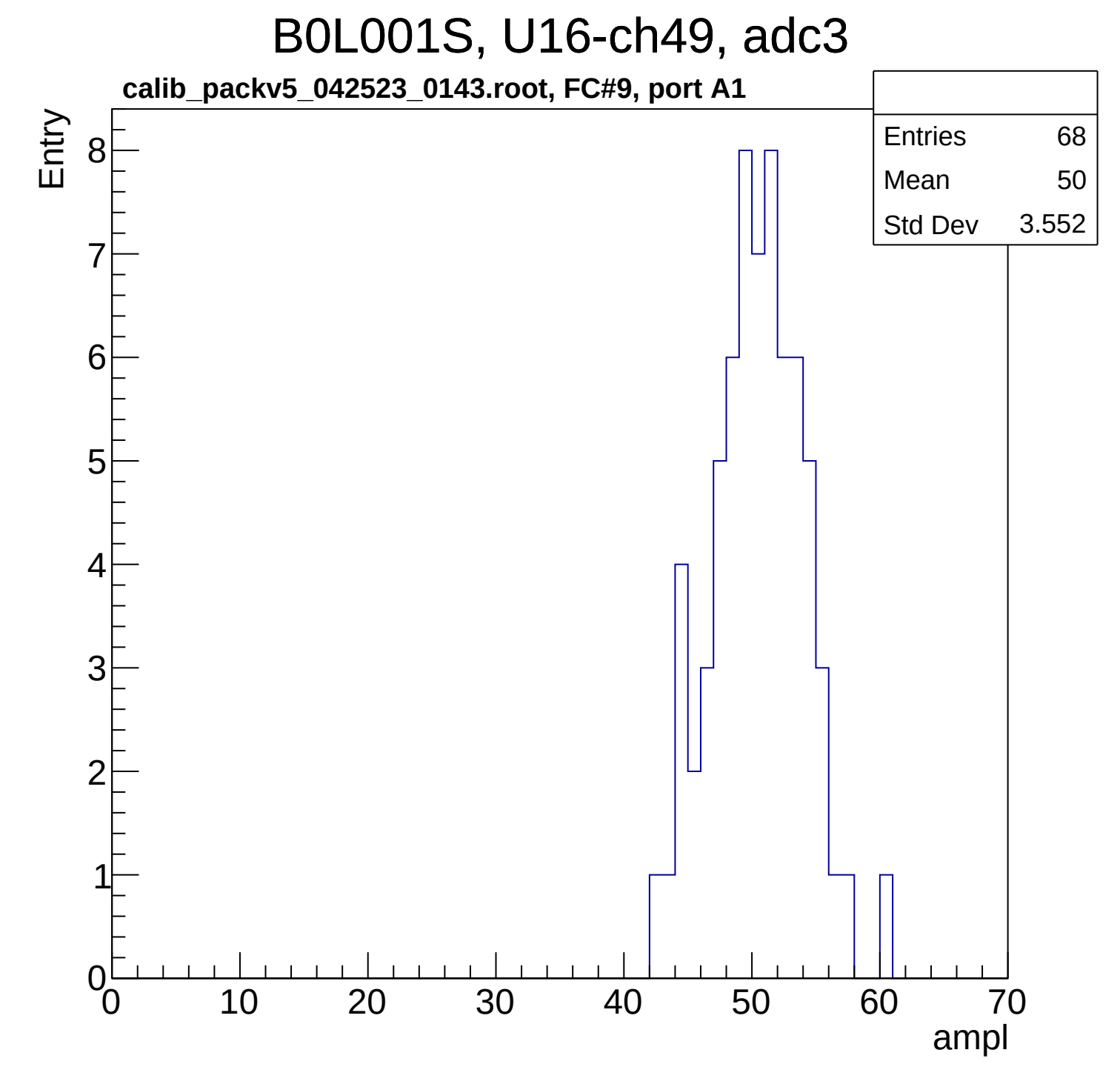
B0L001S, U16-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50
Std Dev 3.552
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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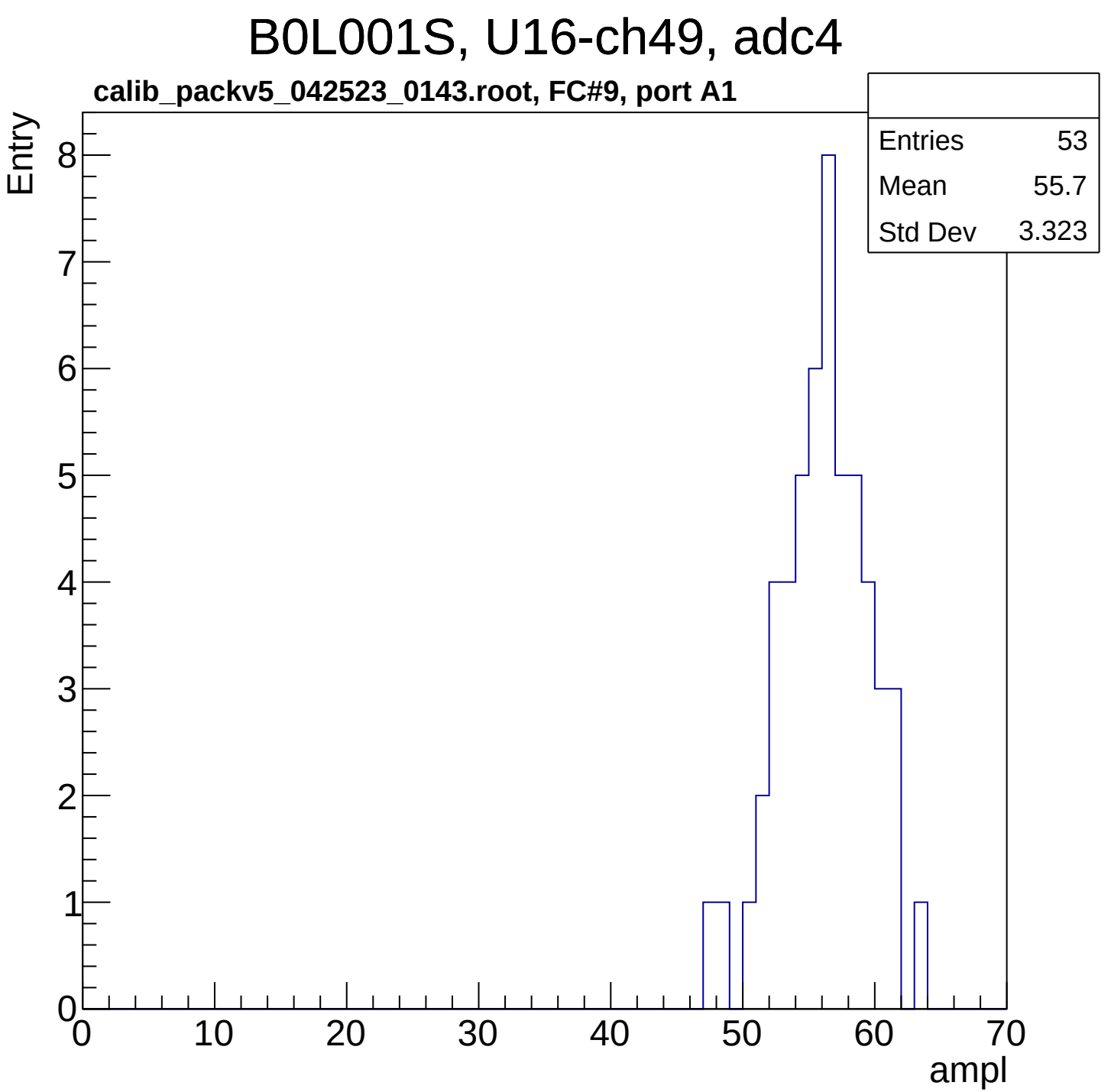
B0L001S, U16-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.7
Std Dev 3.323
Entry
ampl
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1
2
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4
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60
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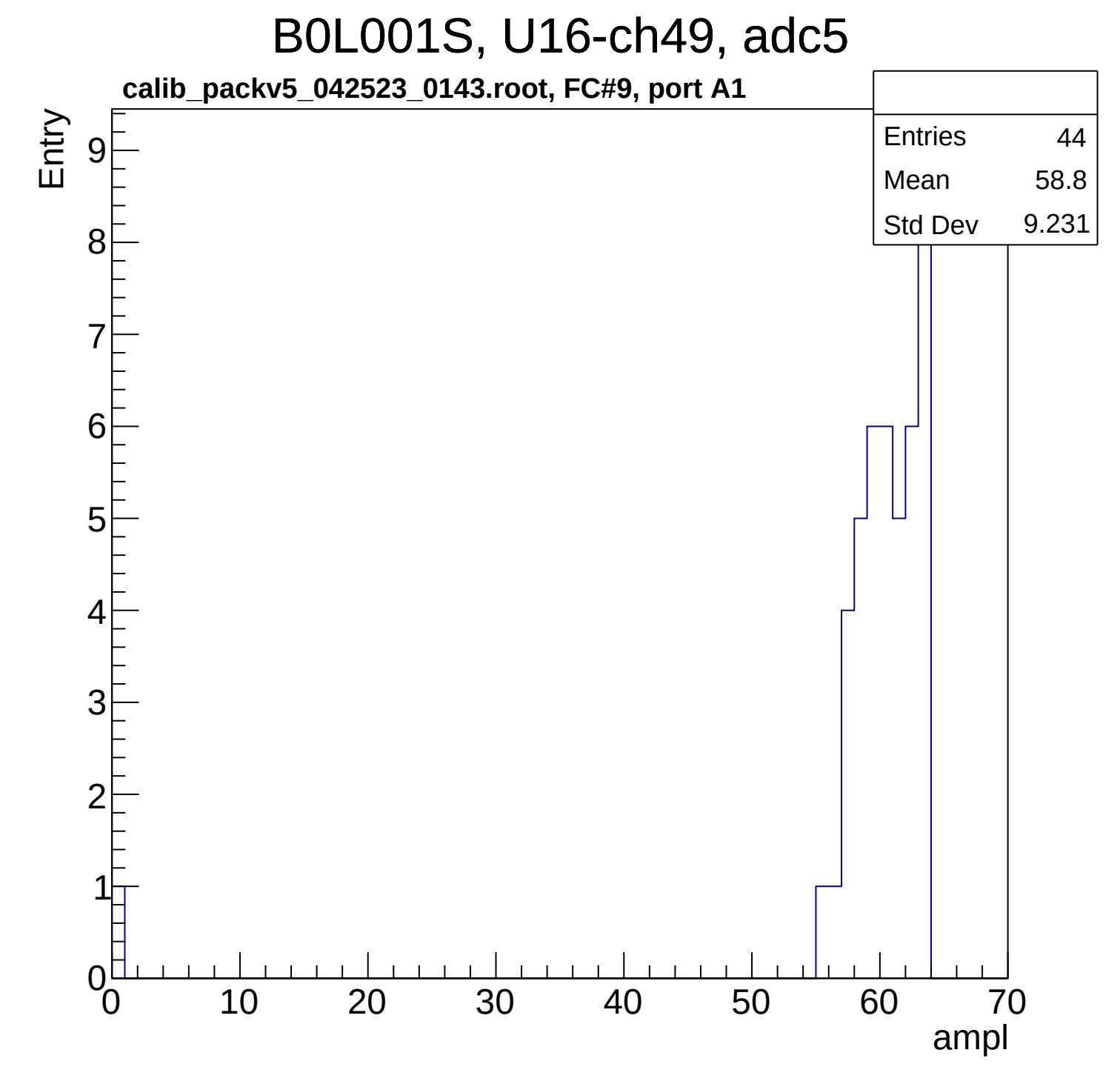
B0L001S, U16-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.8
Std Dev 9.231
Entry
ampl
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3
4
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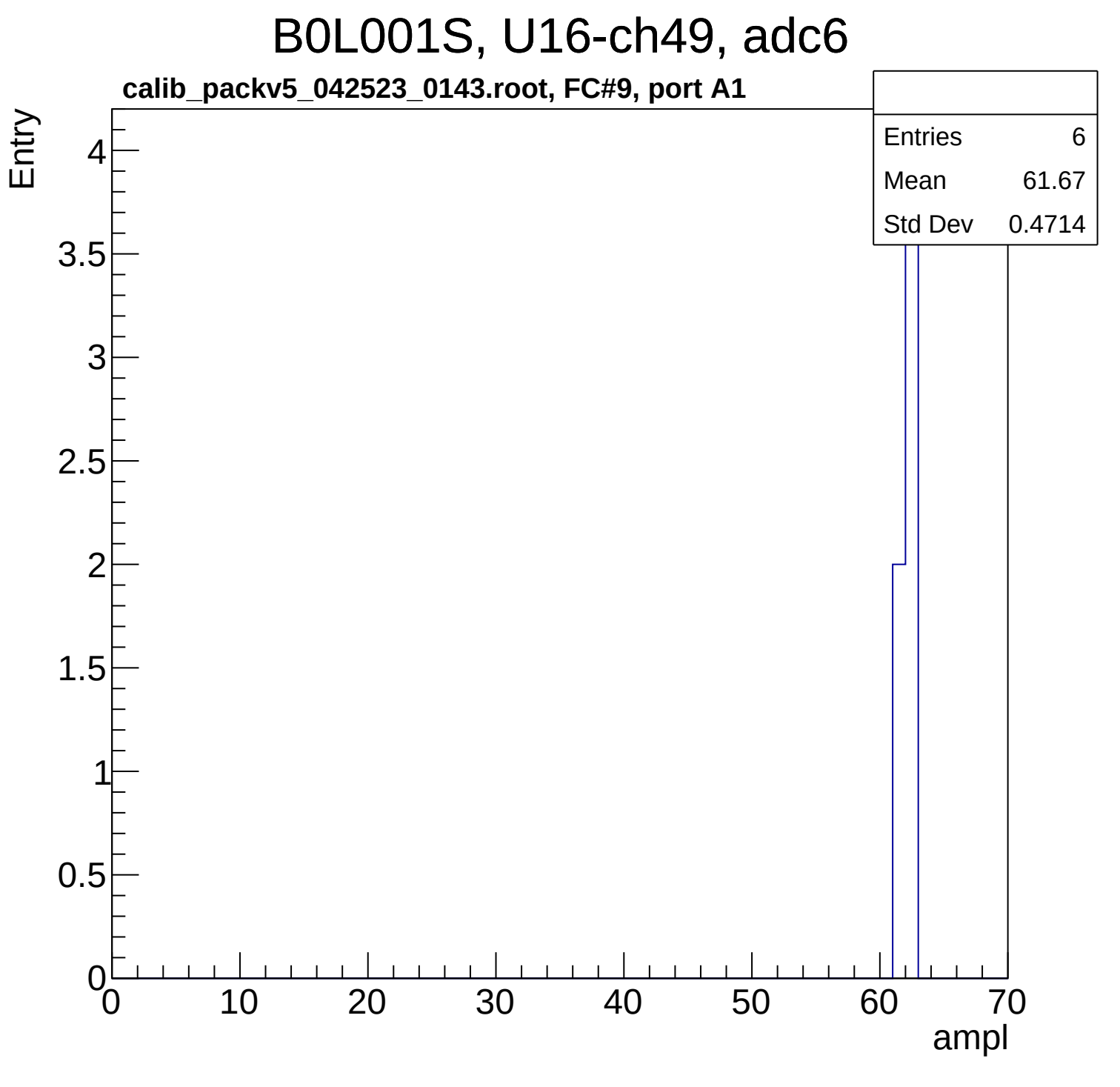
B0L001S, U16-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.67
Std Dev 0.4714
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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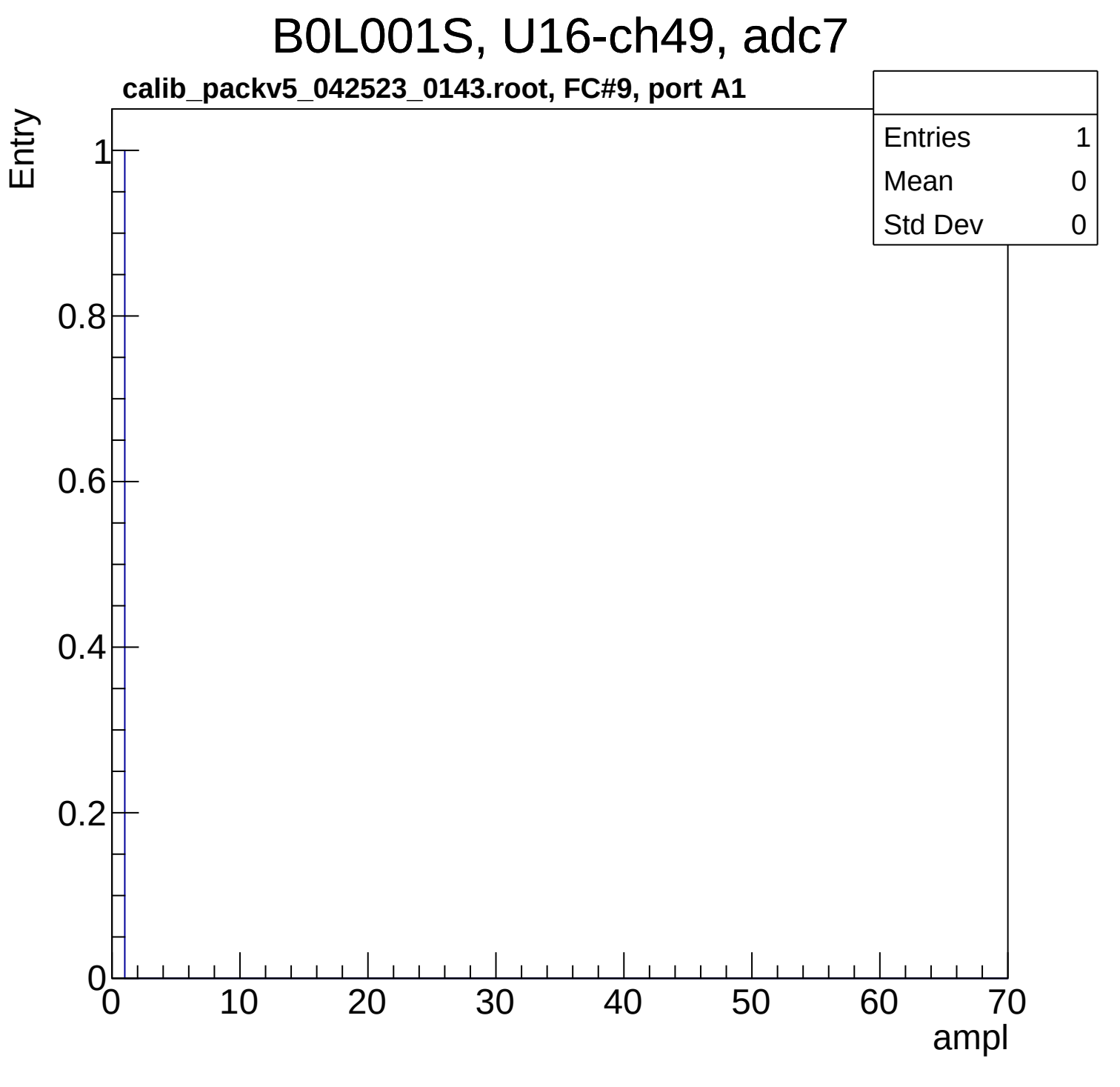
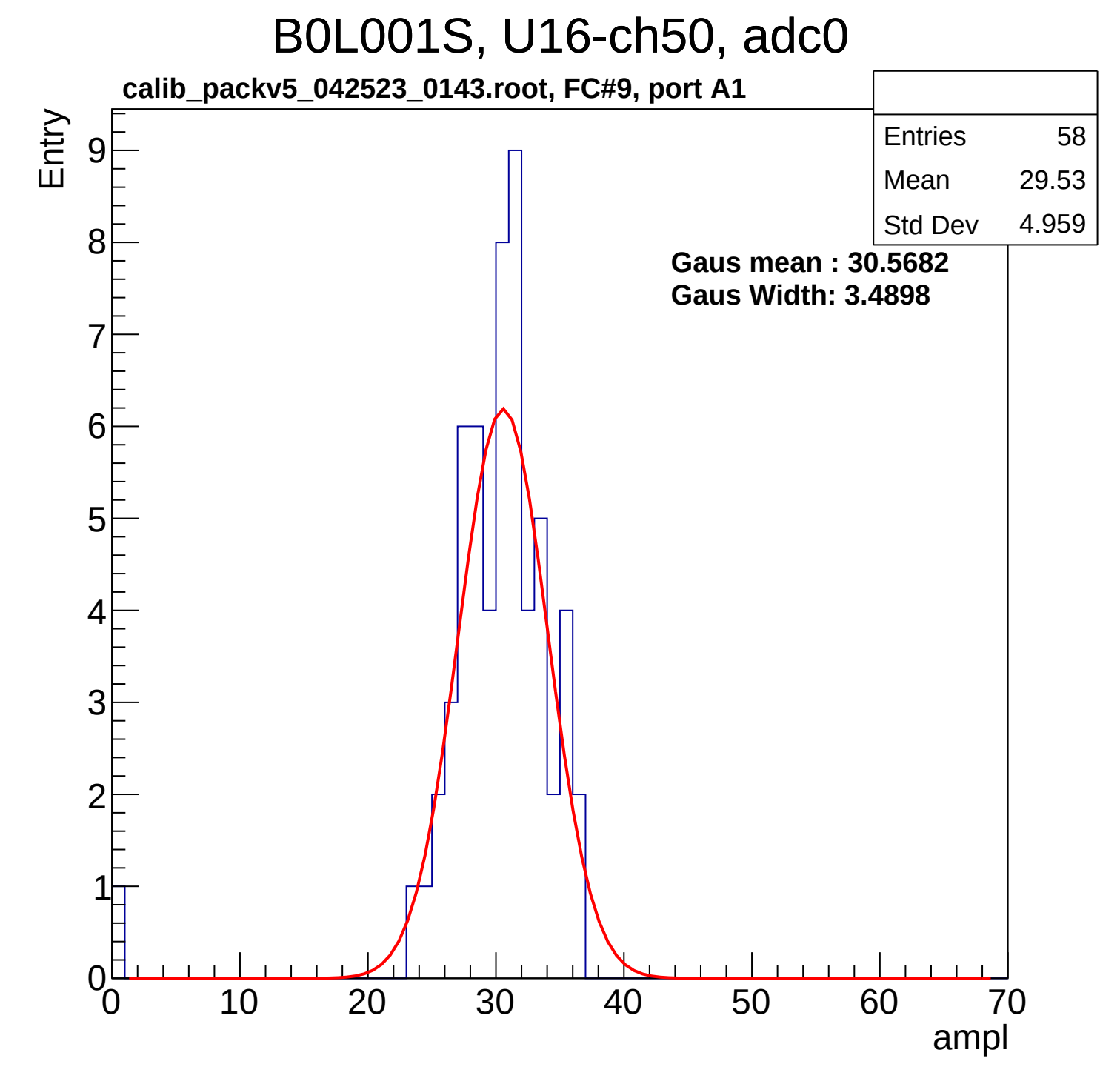

B0L001S, U16-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 29.53
Std Dev 4.959
Gaus mean : 30.5682
Gaus Width: 3.4898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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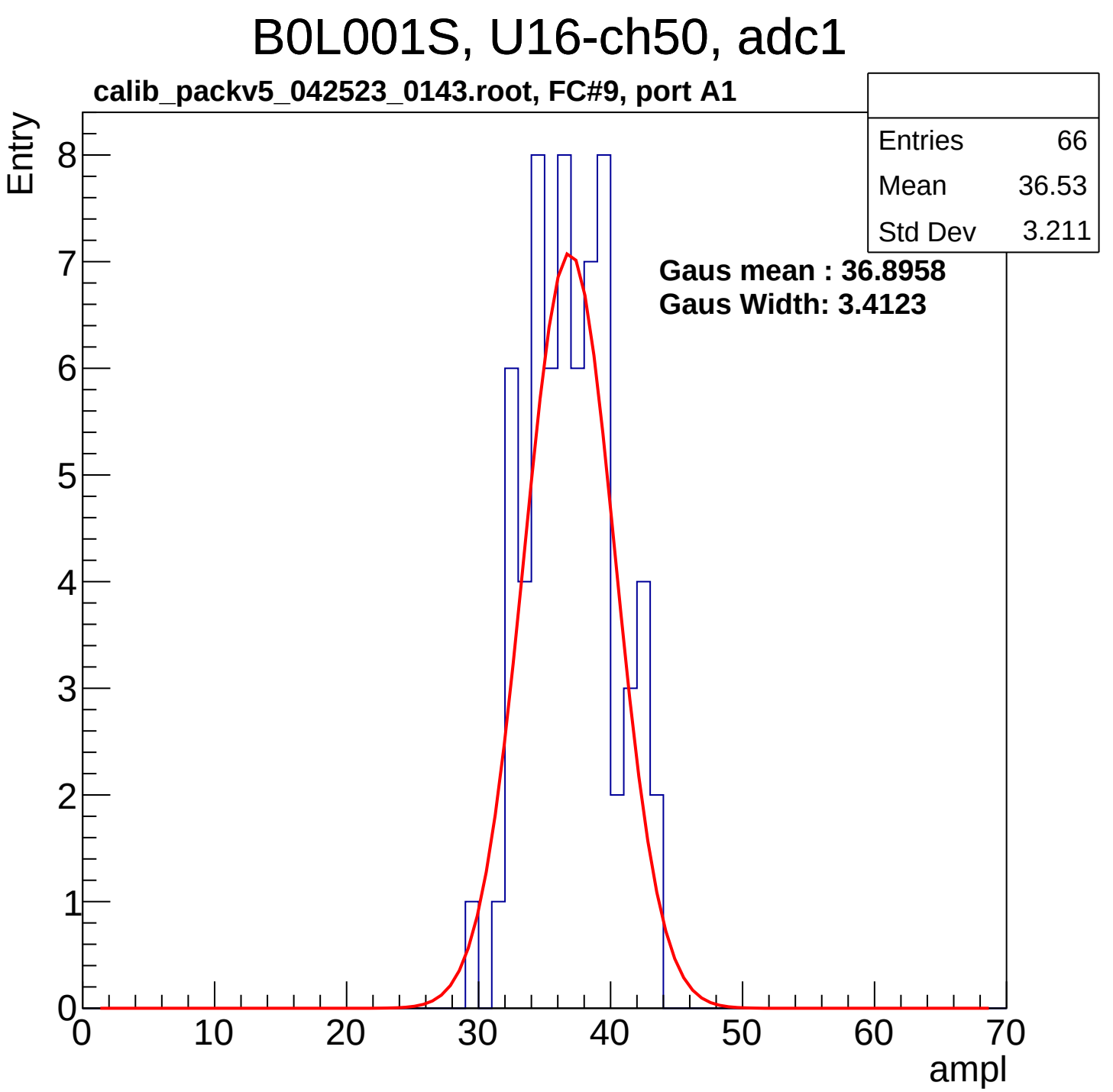
B0L001S, U16-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.53
Std Dev 3.211
Gaus mean : 36.8958
Gaus Width: 3.4123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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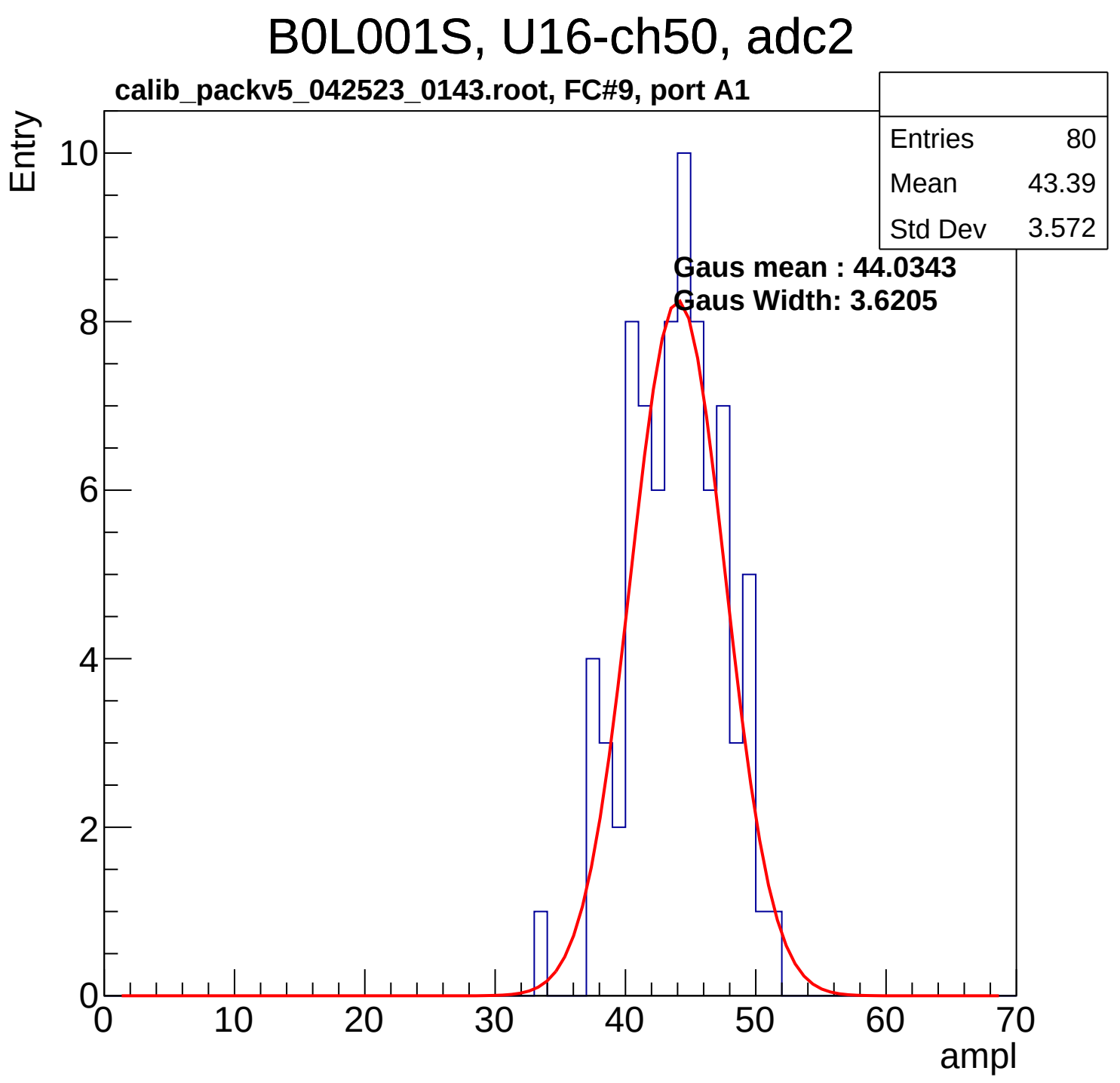
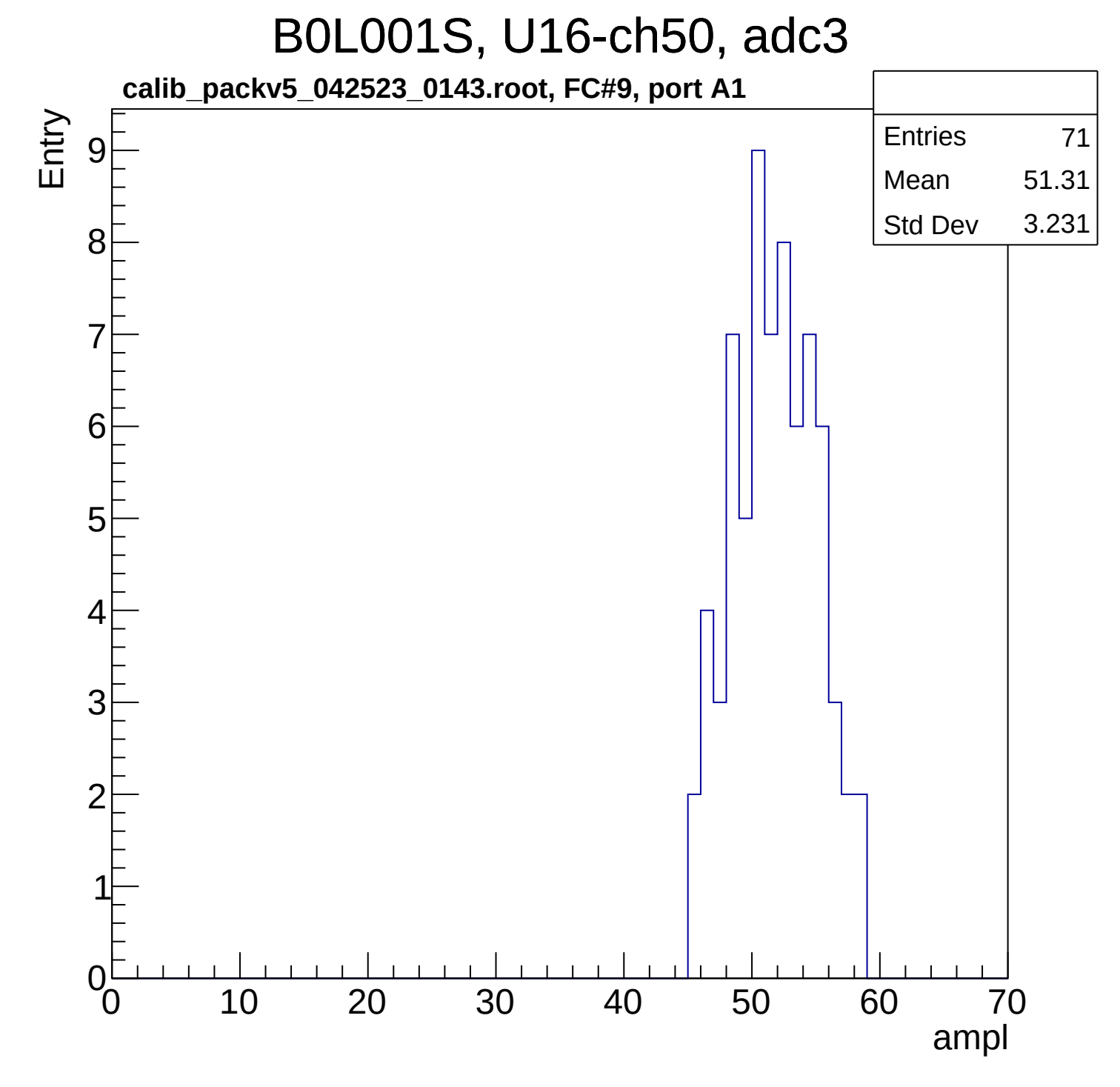
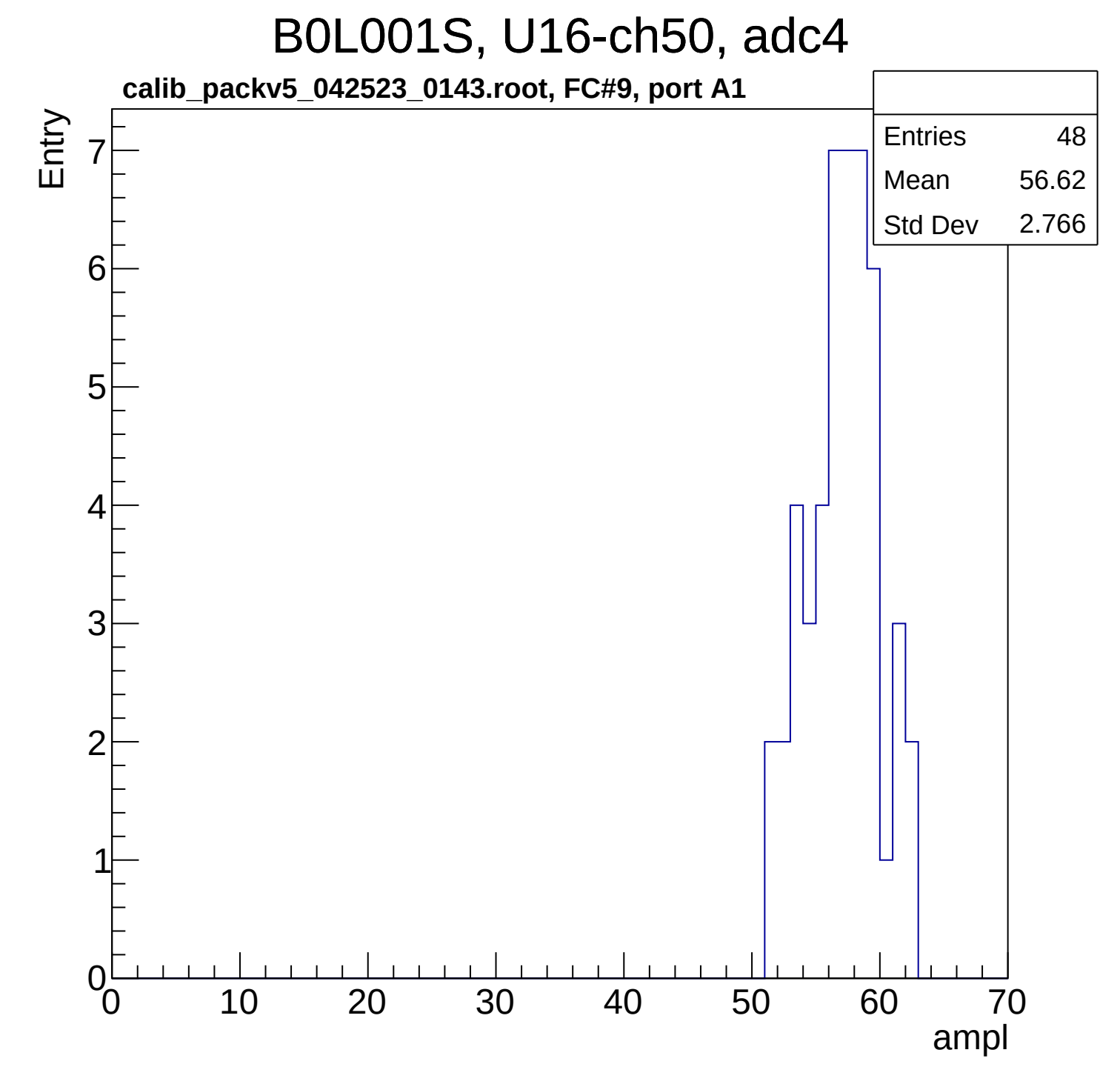
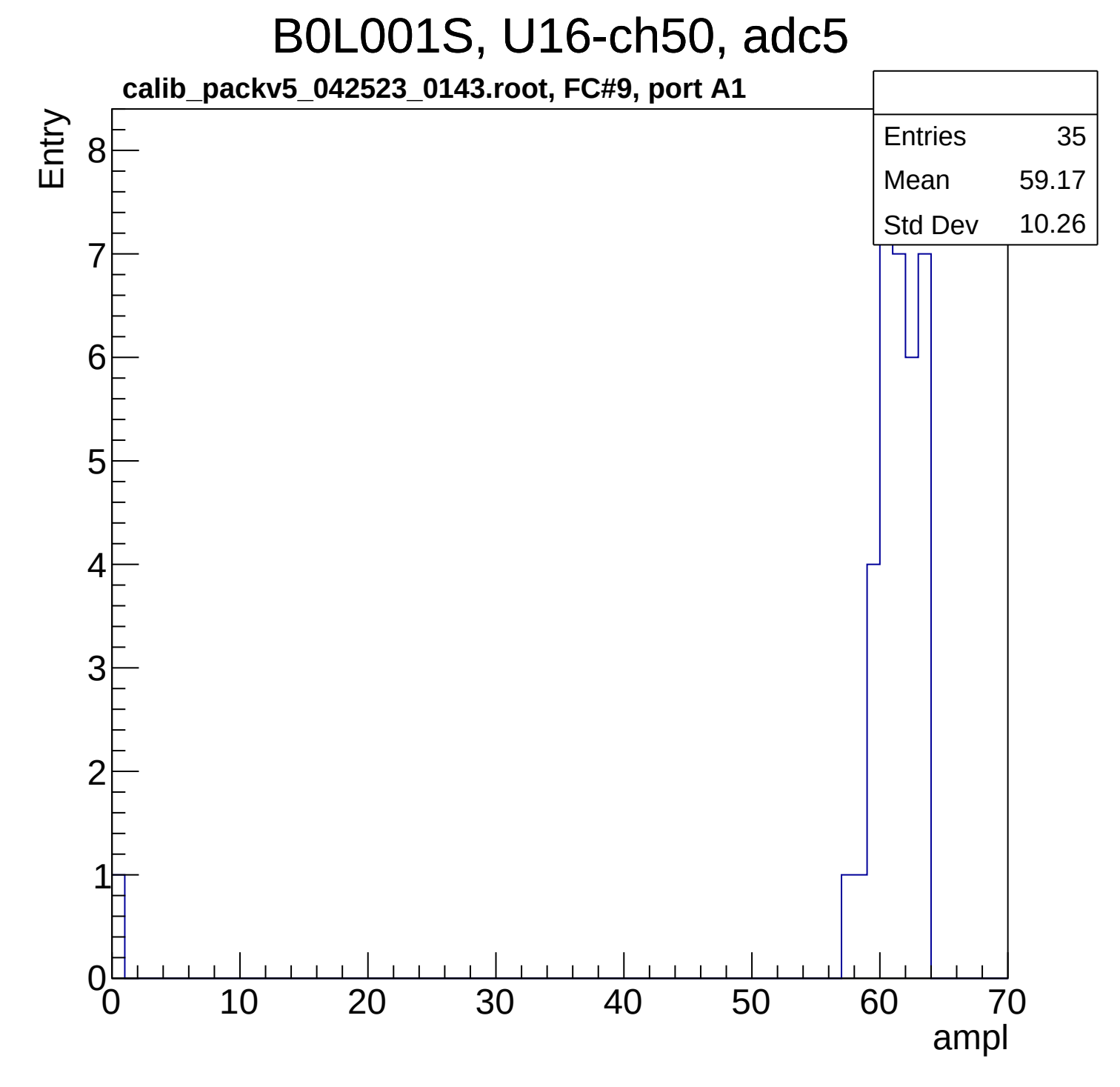

B0L001S, U16-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.39
Std Dev 3.572
Gaus mean : 44.0343
Gaus Width: 3.6205
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.31
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.62
Std Dev 2.766
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.17
Std Dev 10.26
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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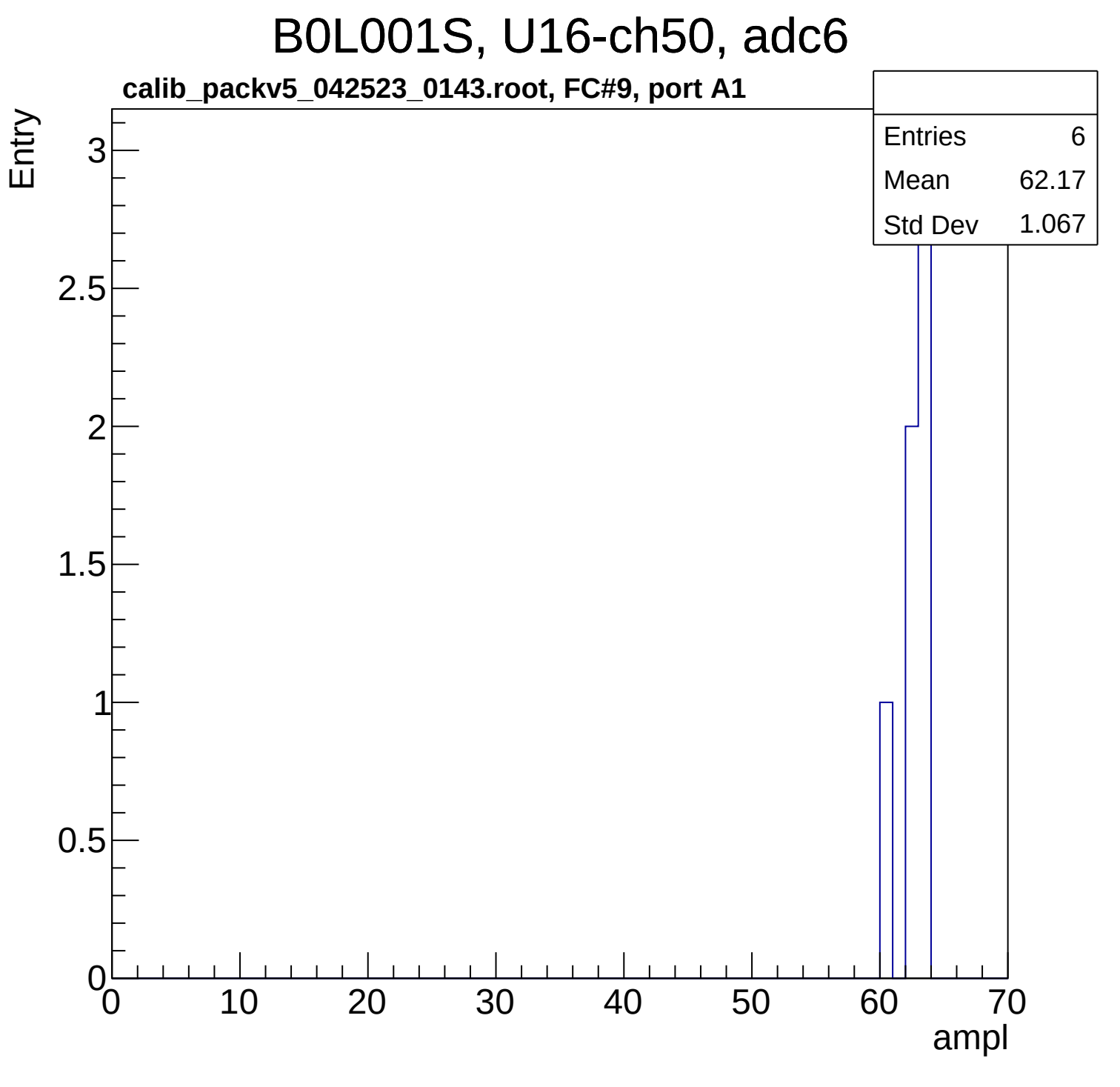
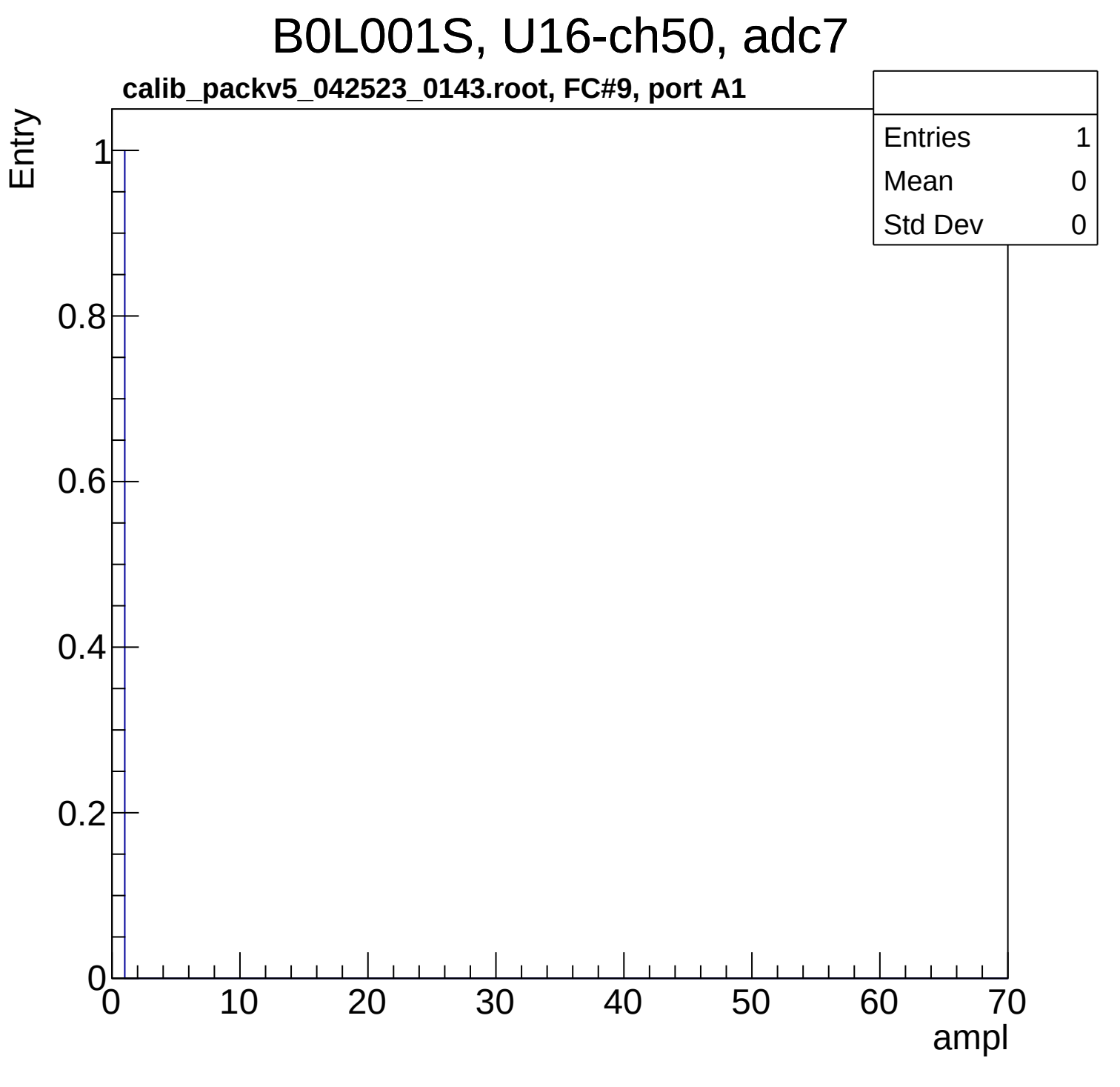
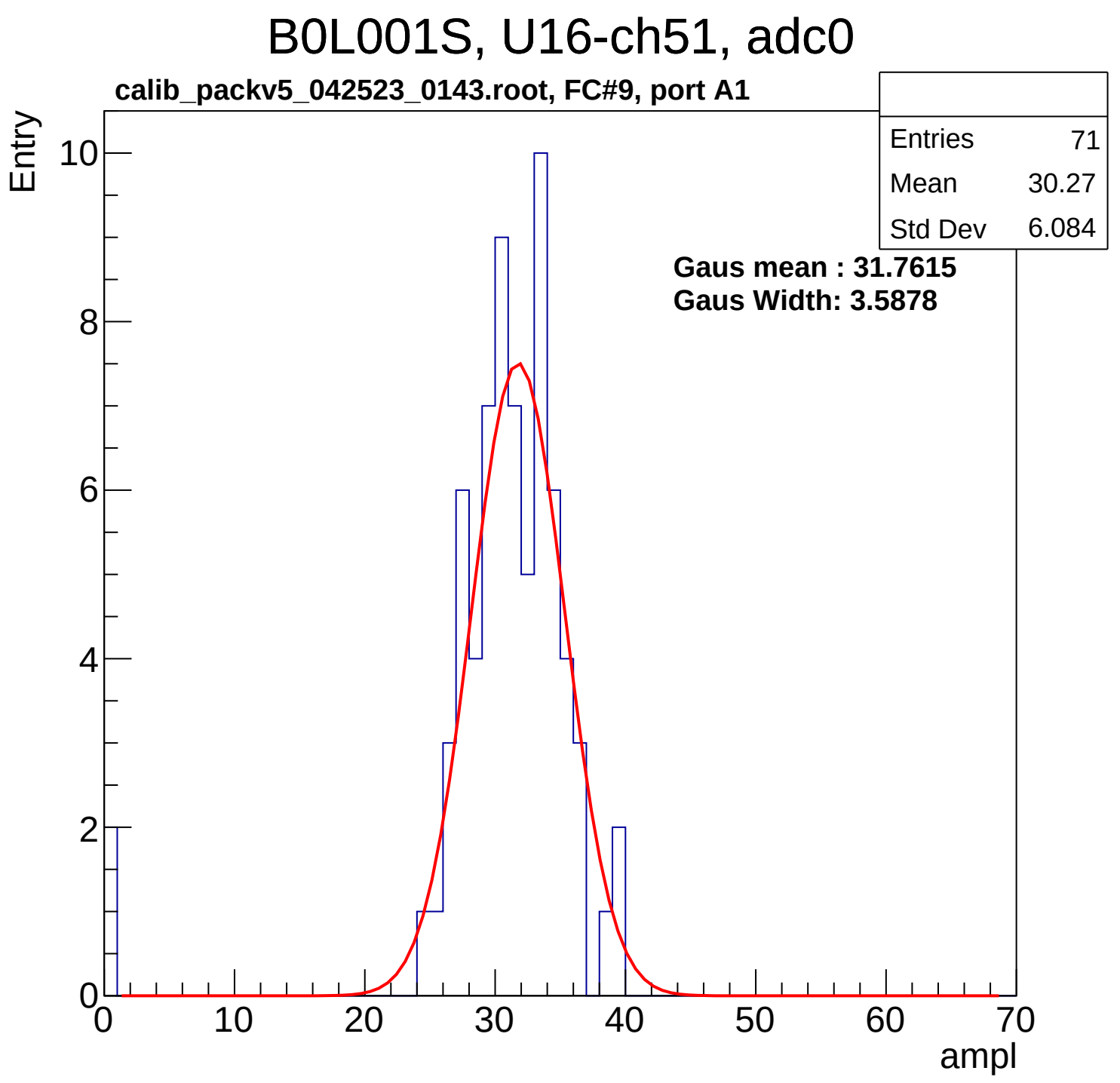
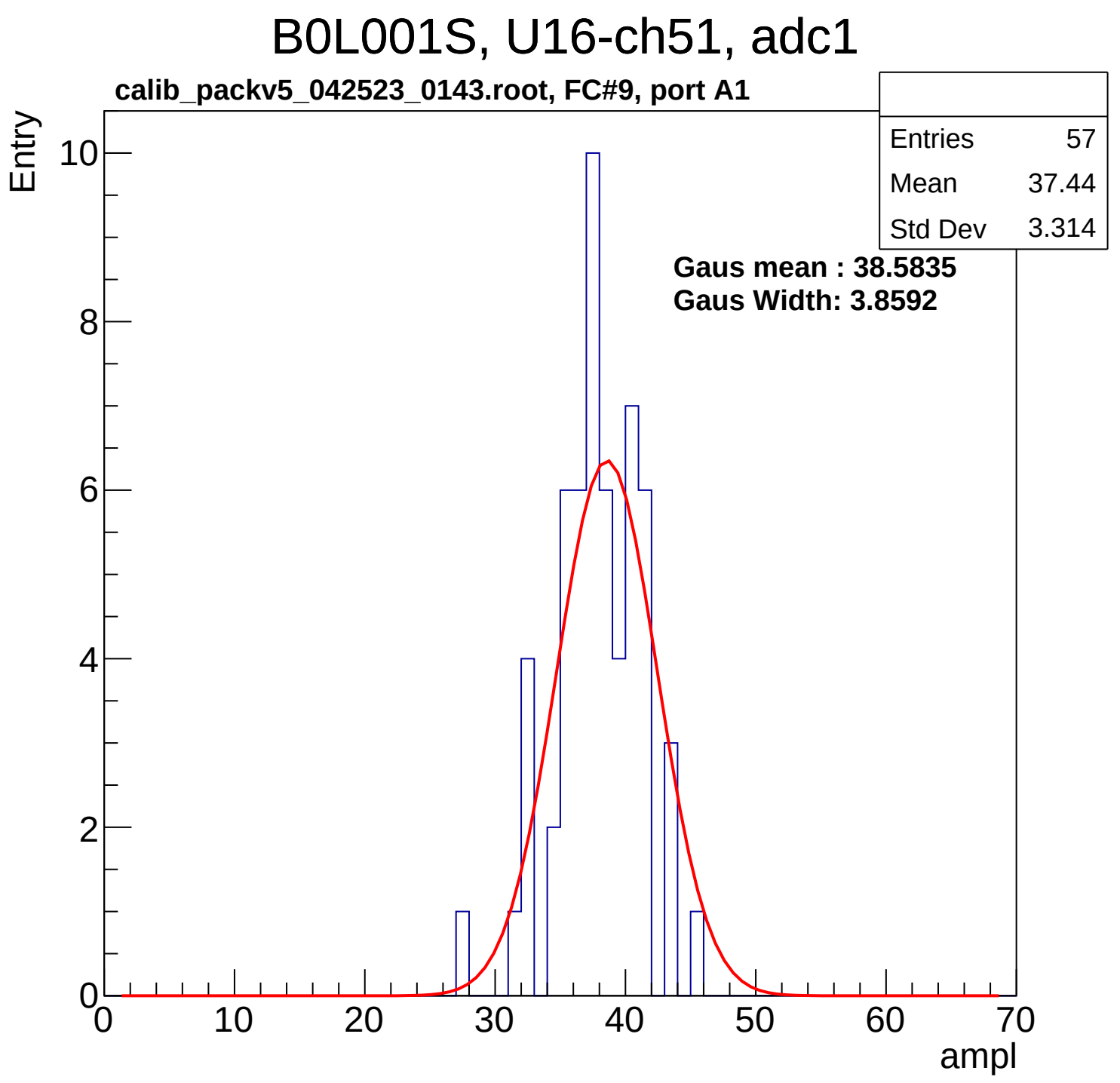
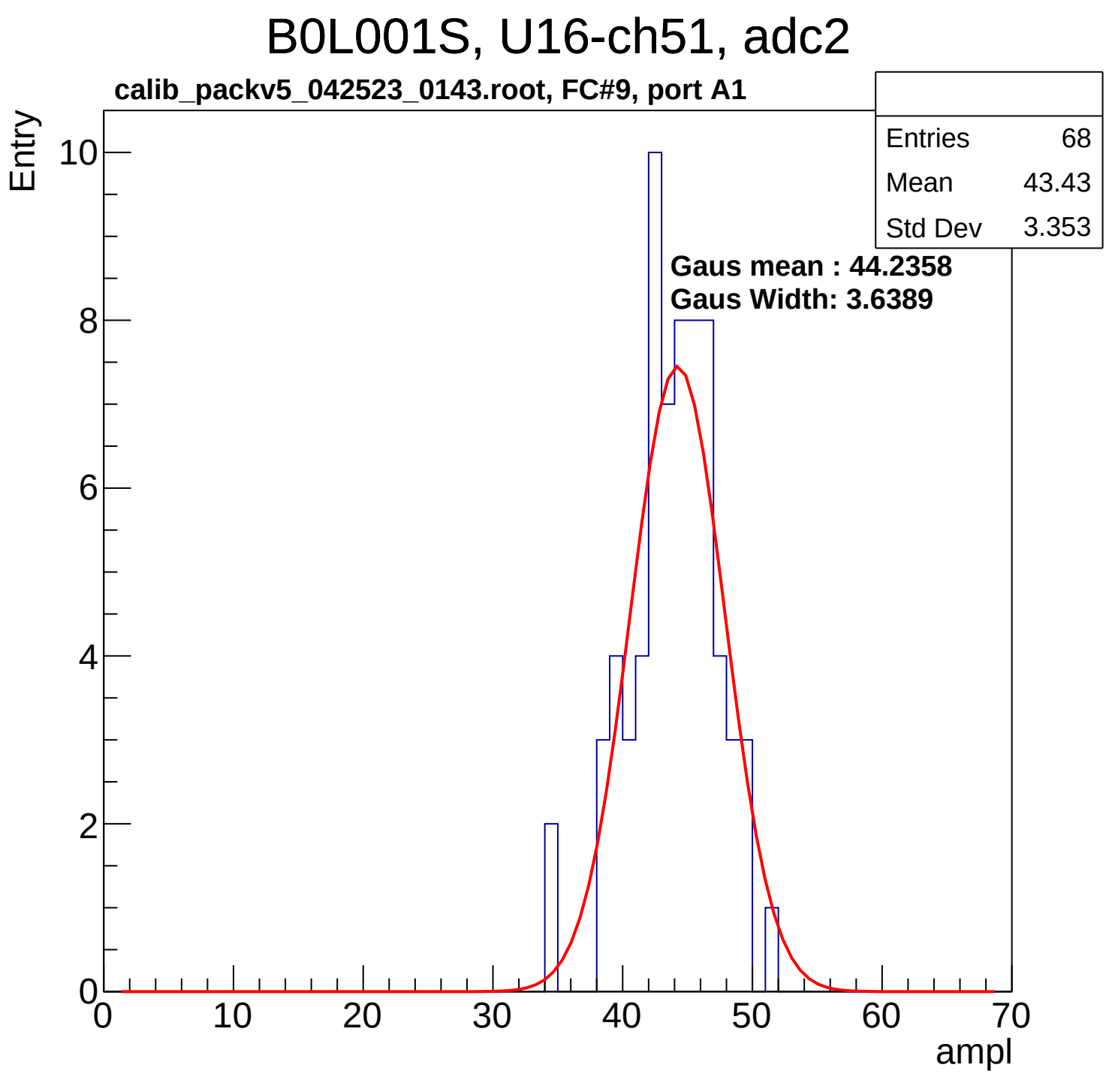
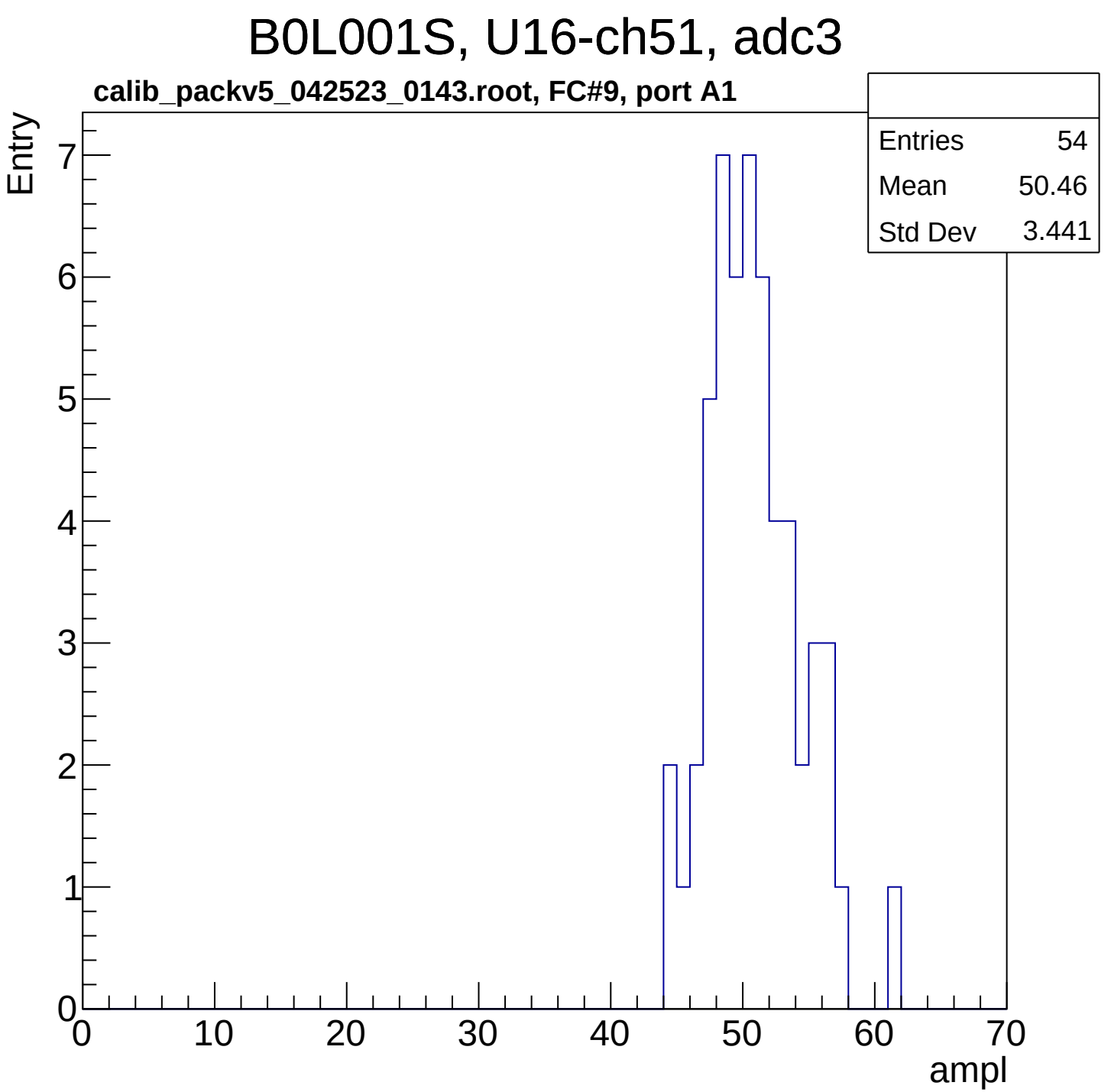

B0L001S, U16-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

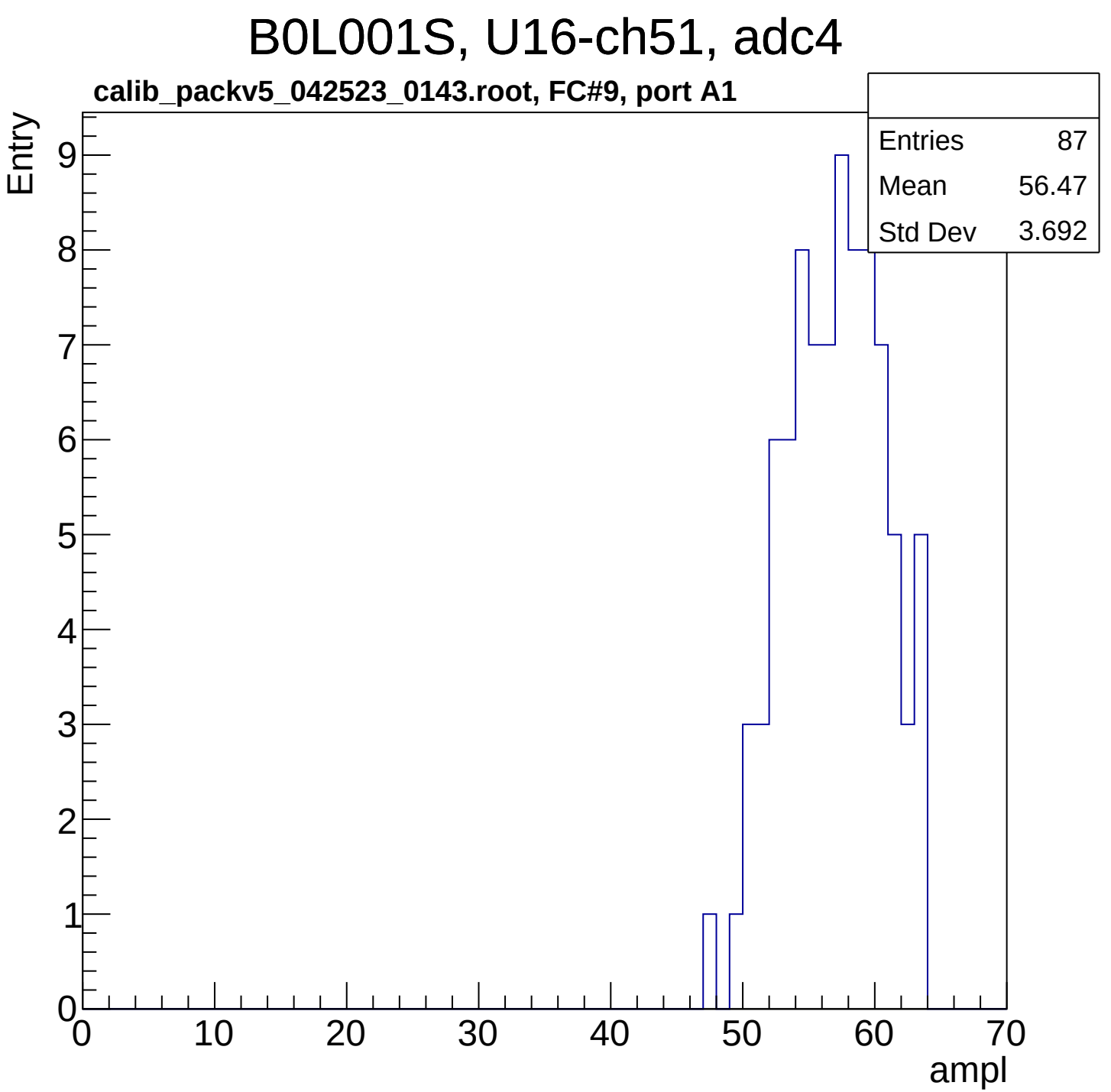

B0L001S, U16-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

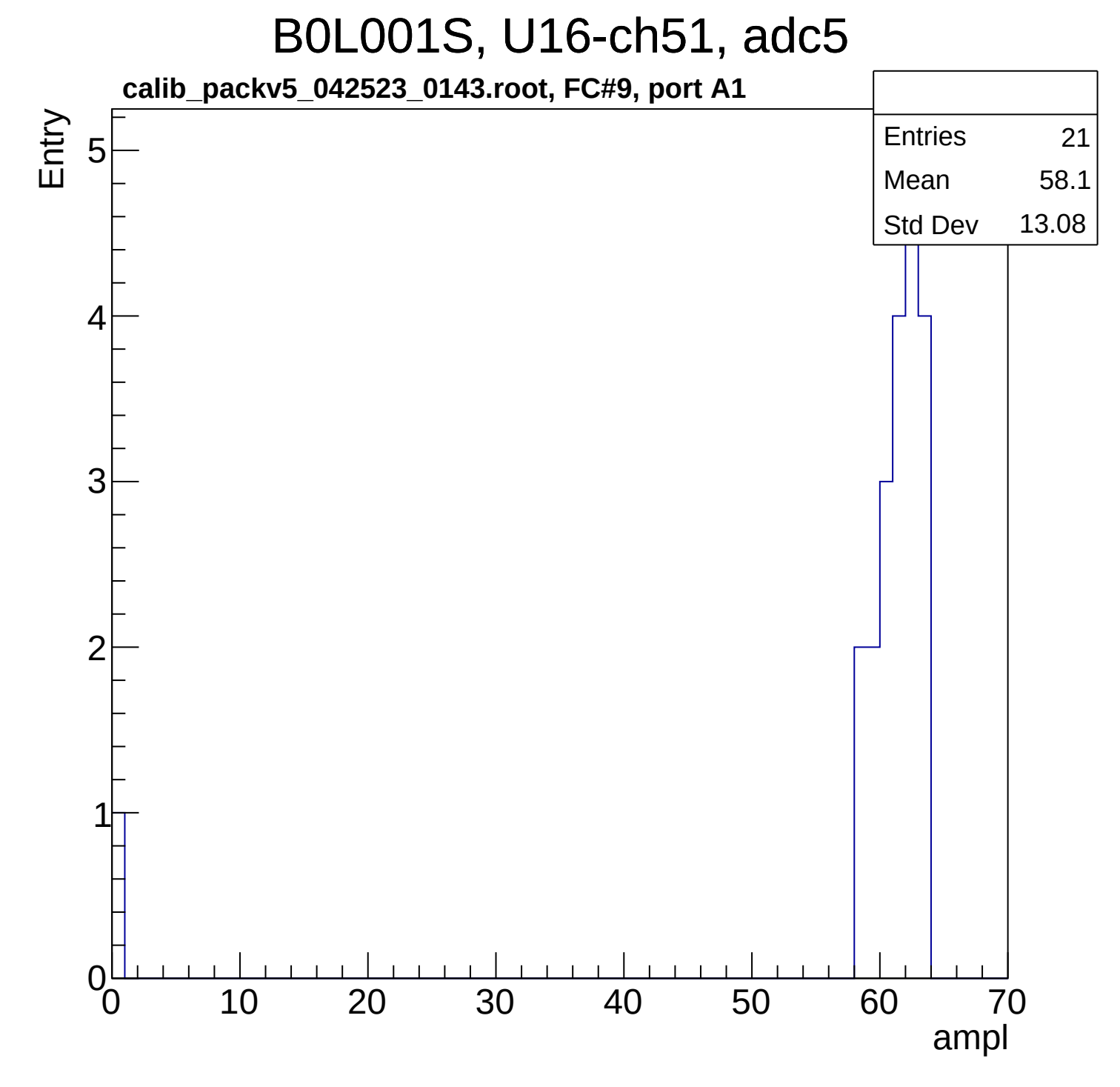

B0L001S, U16-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.27
Std Dev 6.084
Gaus mean : 31.7615
Gaus Width: 3.5878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

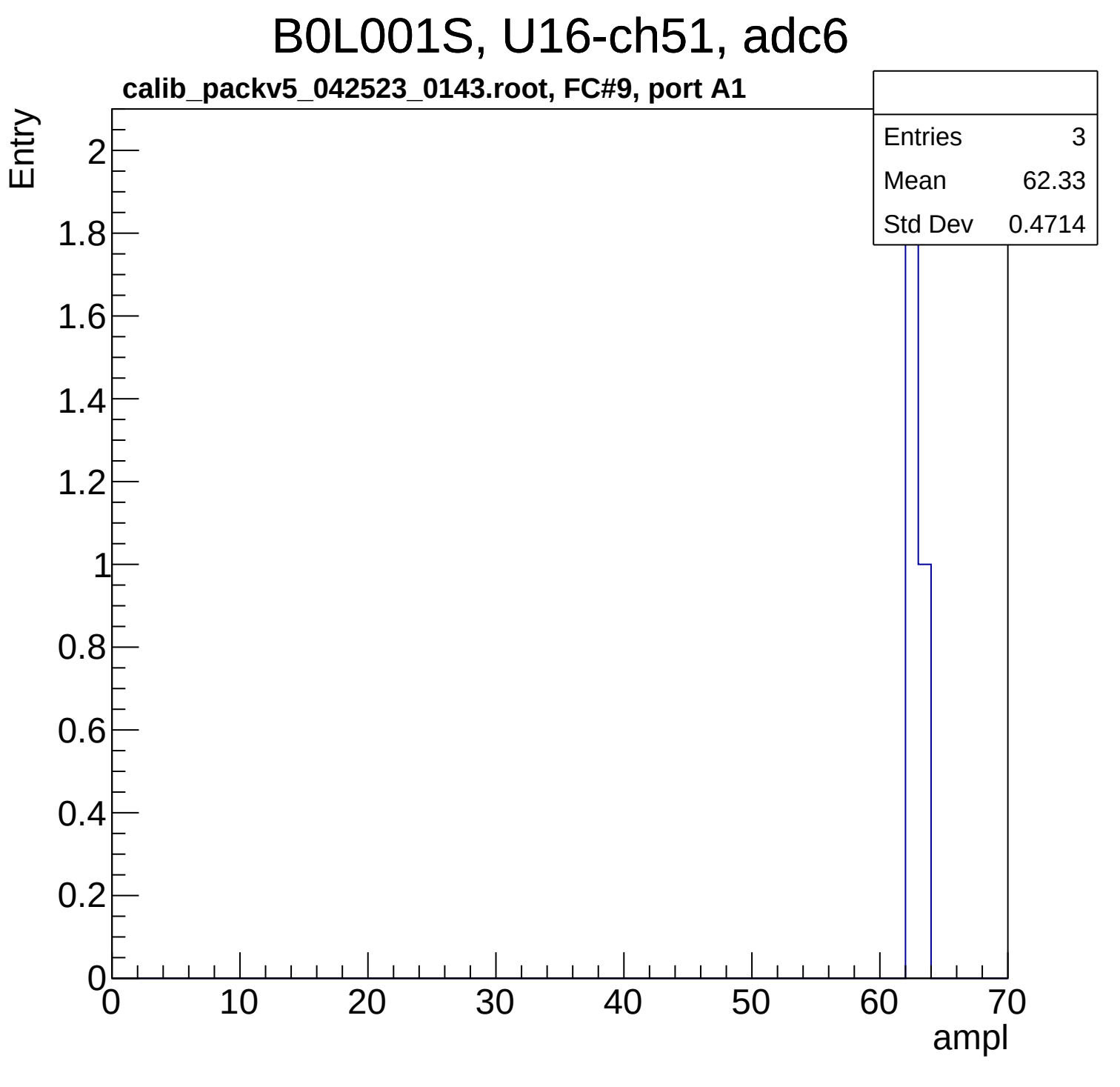

B0L001S, U16-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.44
Std Dev 3.314
Gaus mean : 38.5835
Gaus Width: 3.8592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

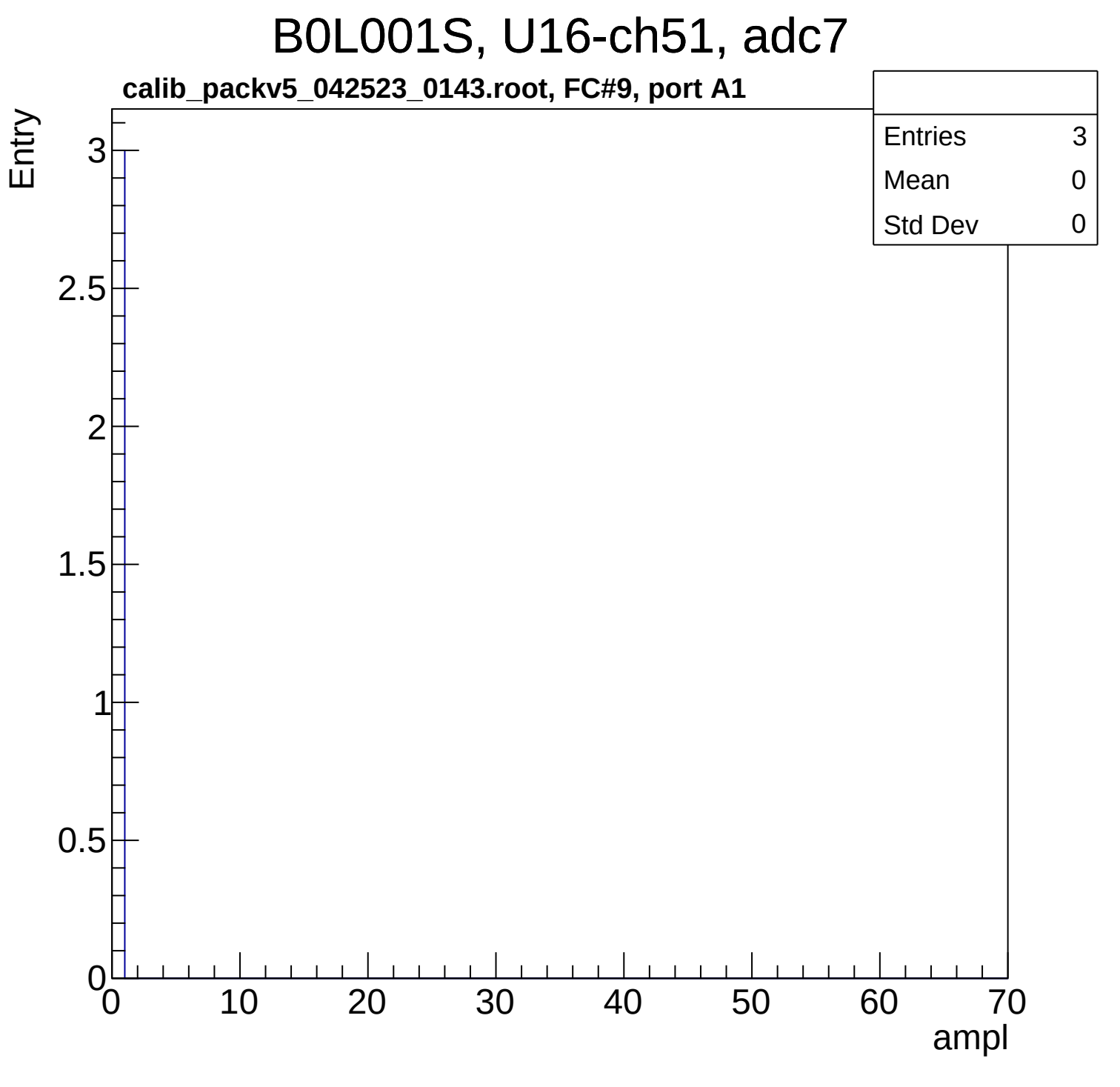

B0L001S, U16-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.43
Std Dev 3.353
Gaus mean : 44.2358
Gaus Width: 3.6389
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

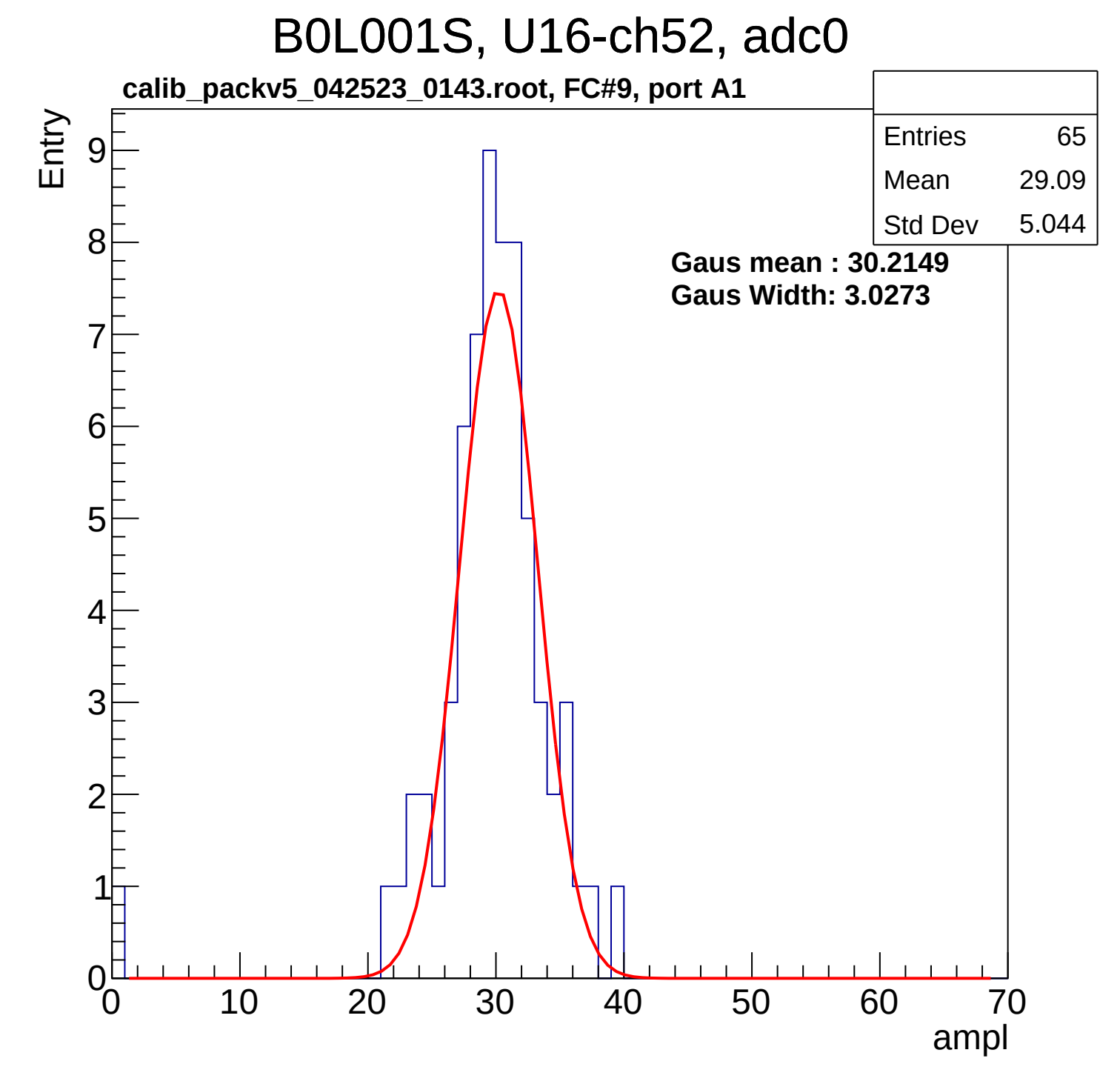

B0L001S, U16-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 50.46
Std Dev 3.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

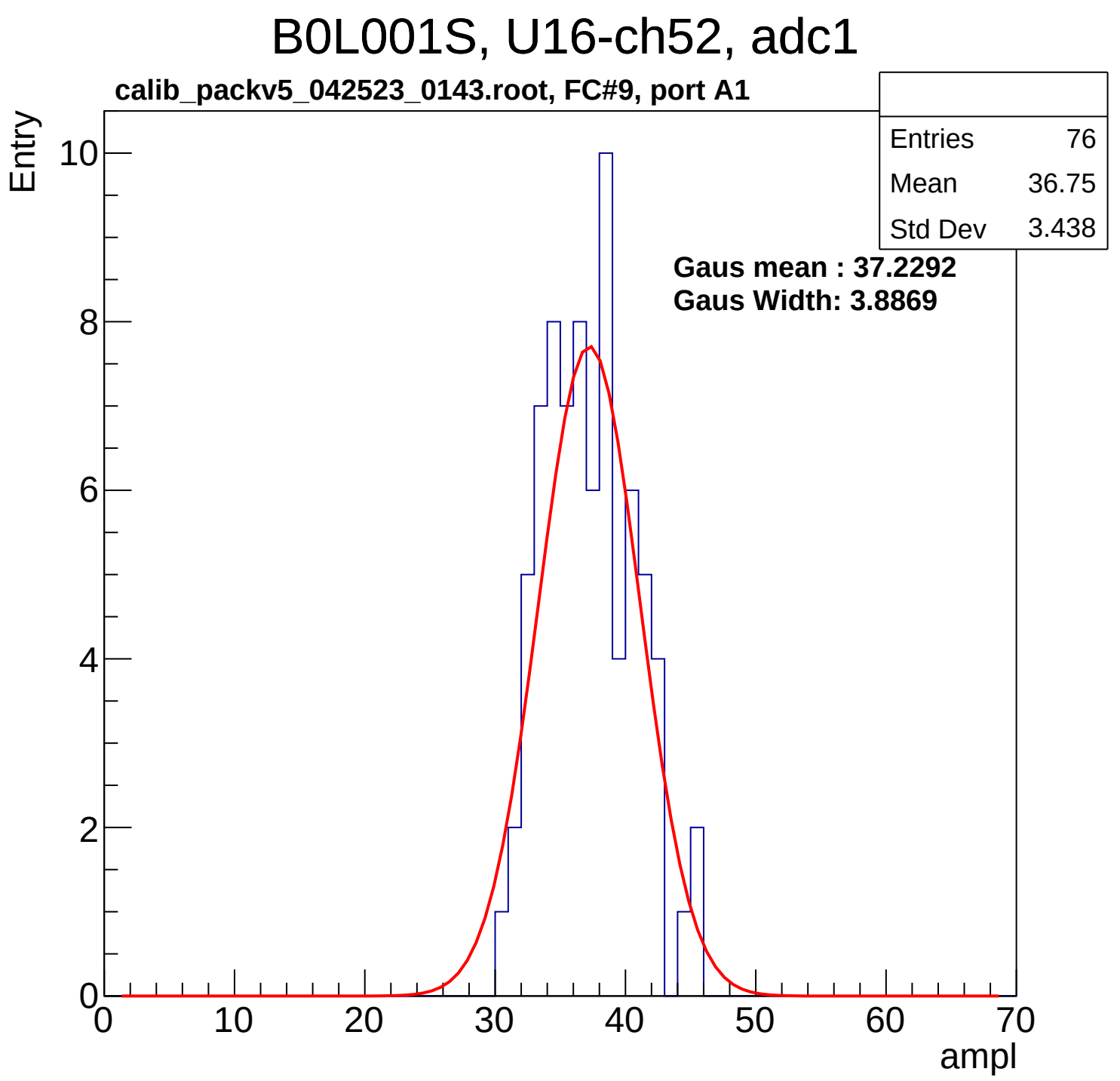

B0L001S, U16-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 56.47
Std Dev 3.692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

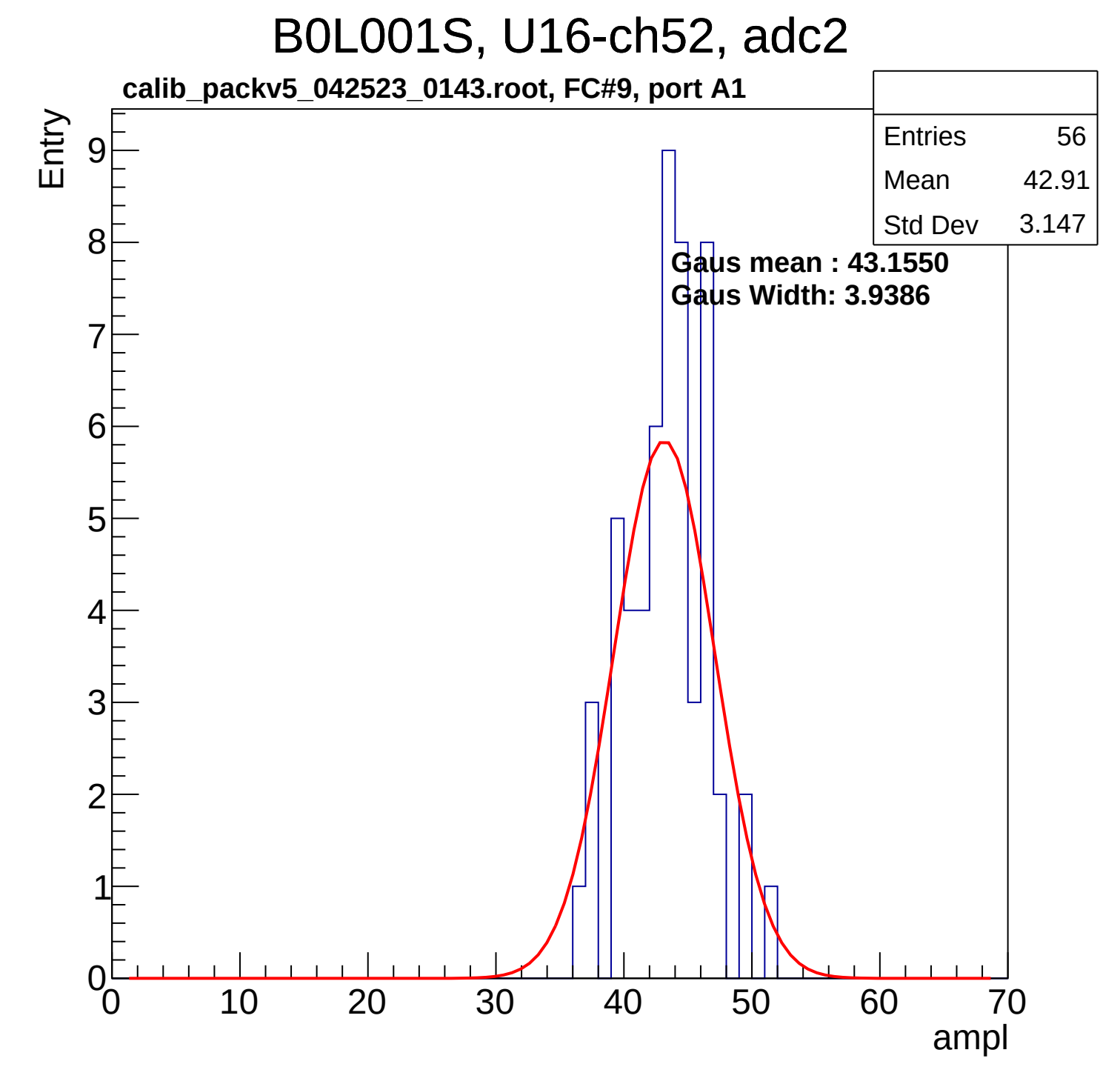

B0L001S, U16-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.1
Std Dev 13.08
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

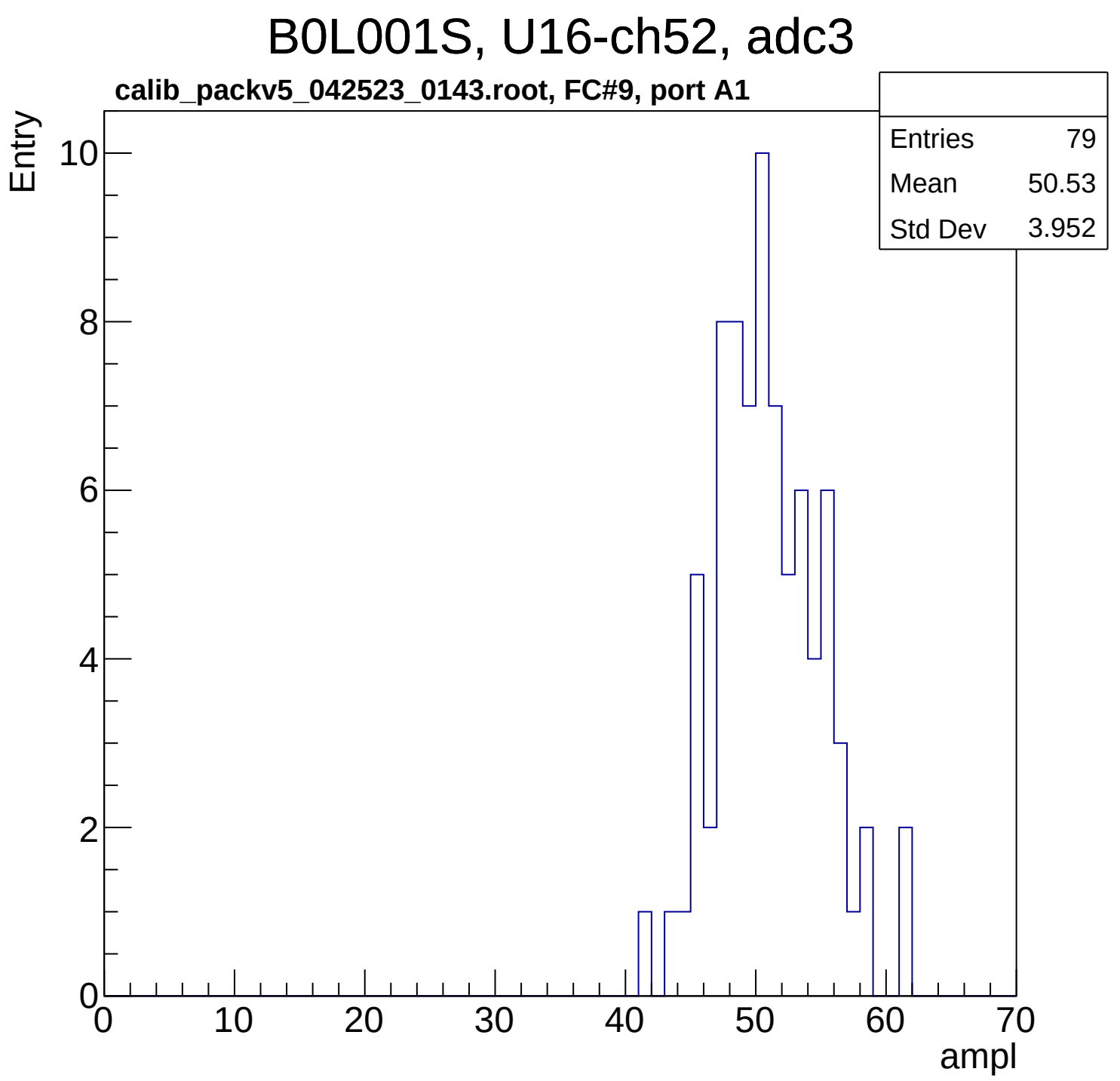

B0L001S, U16-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

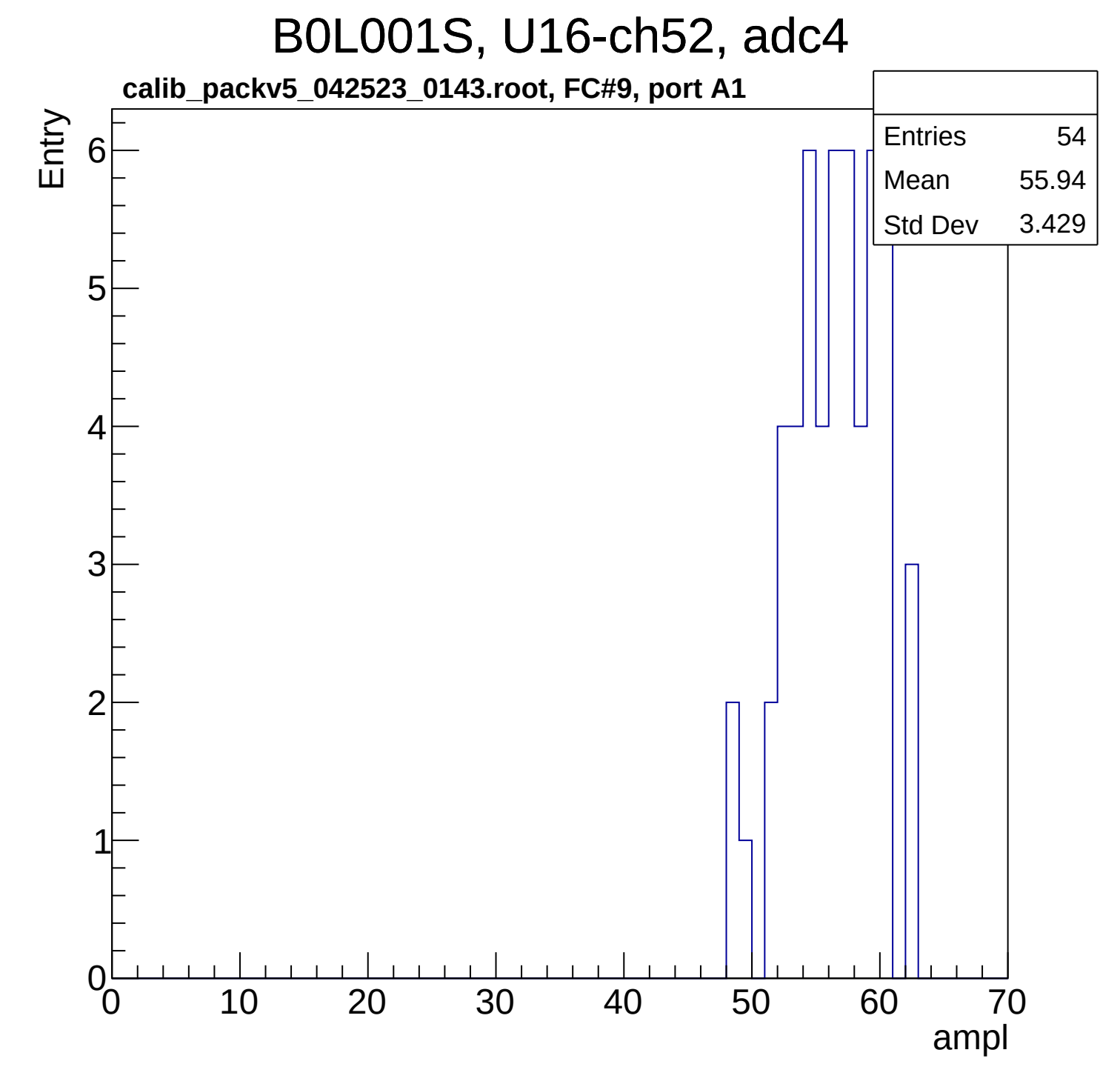

B0L001S, U16-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

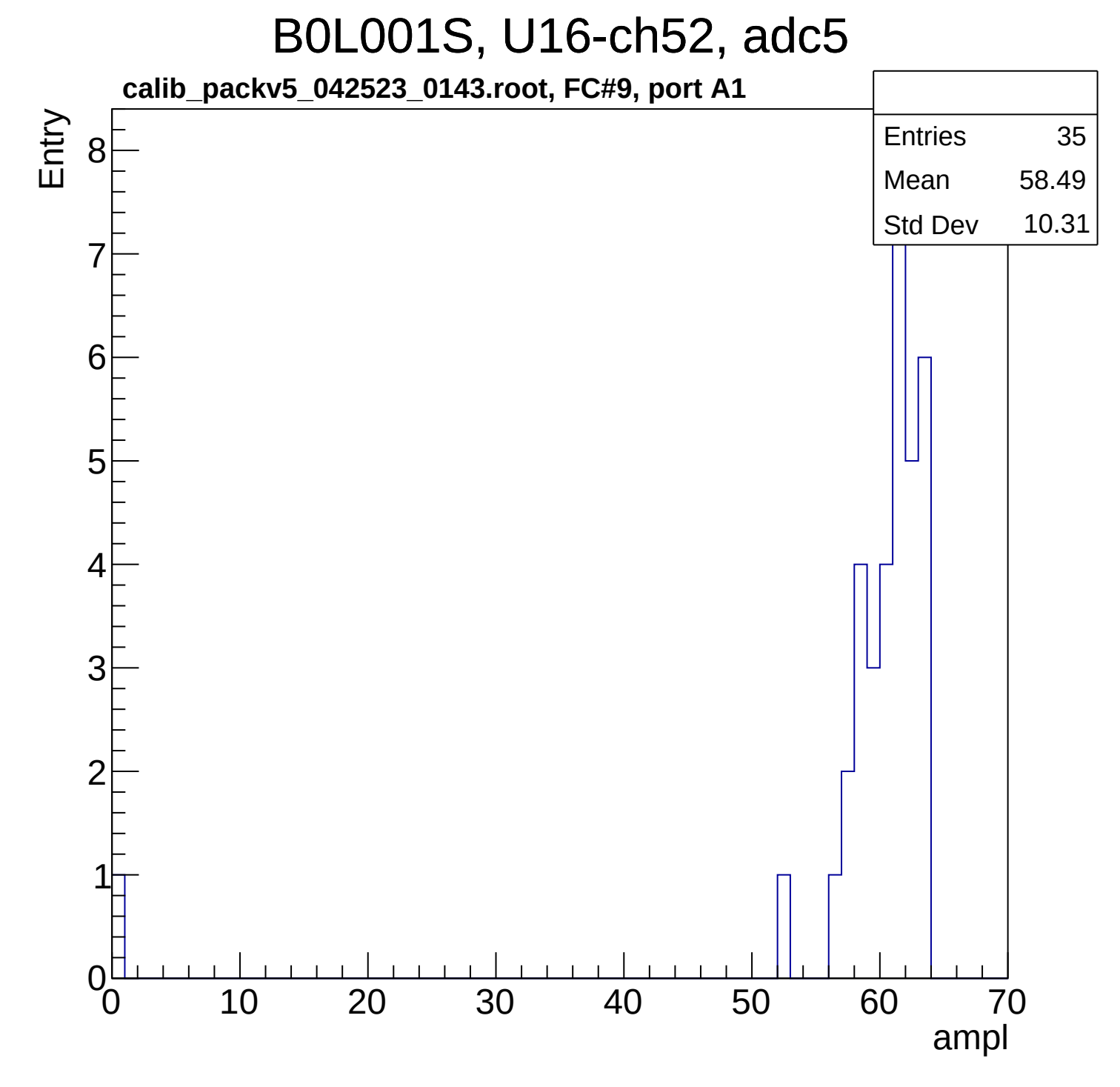

B0L001S, U16-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.09
Std Dev 5.044
Gaus mean : 30.2149
Gaus Width: 3.0273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.75
Std Dev 3.438
Gaus mean : 37.2292
Gaus Width: 3.8869
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.91
Std Dev 3.147
Gaus mean : 43.1550
Gaus Width: 3.9386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 50.53
Std Dev 3.952
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.94
Std Dev 3.429
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.49
Std Dev 10.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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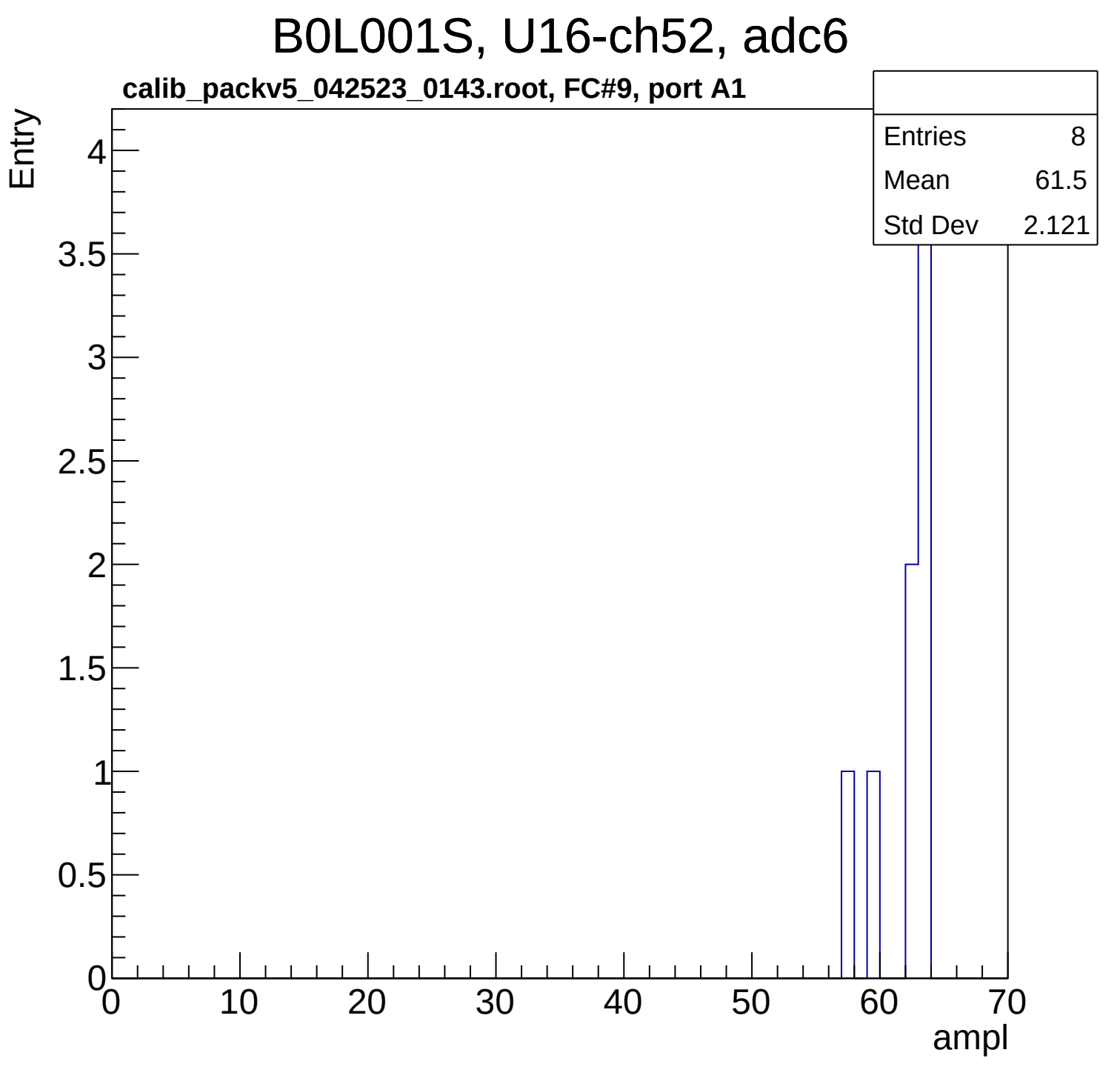
B0L001S, U16-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.5
Std Dev 2.121
Entry
ampl
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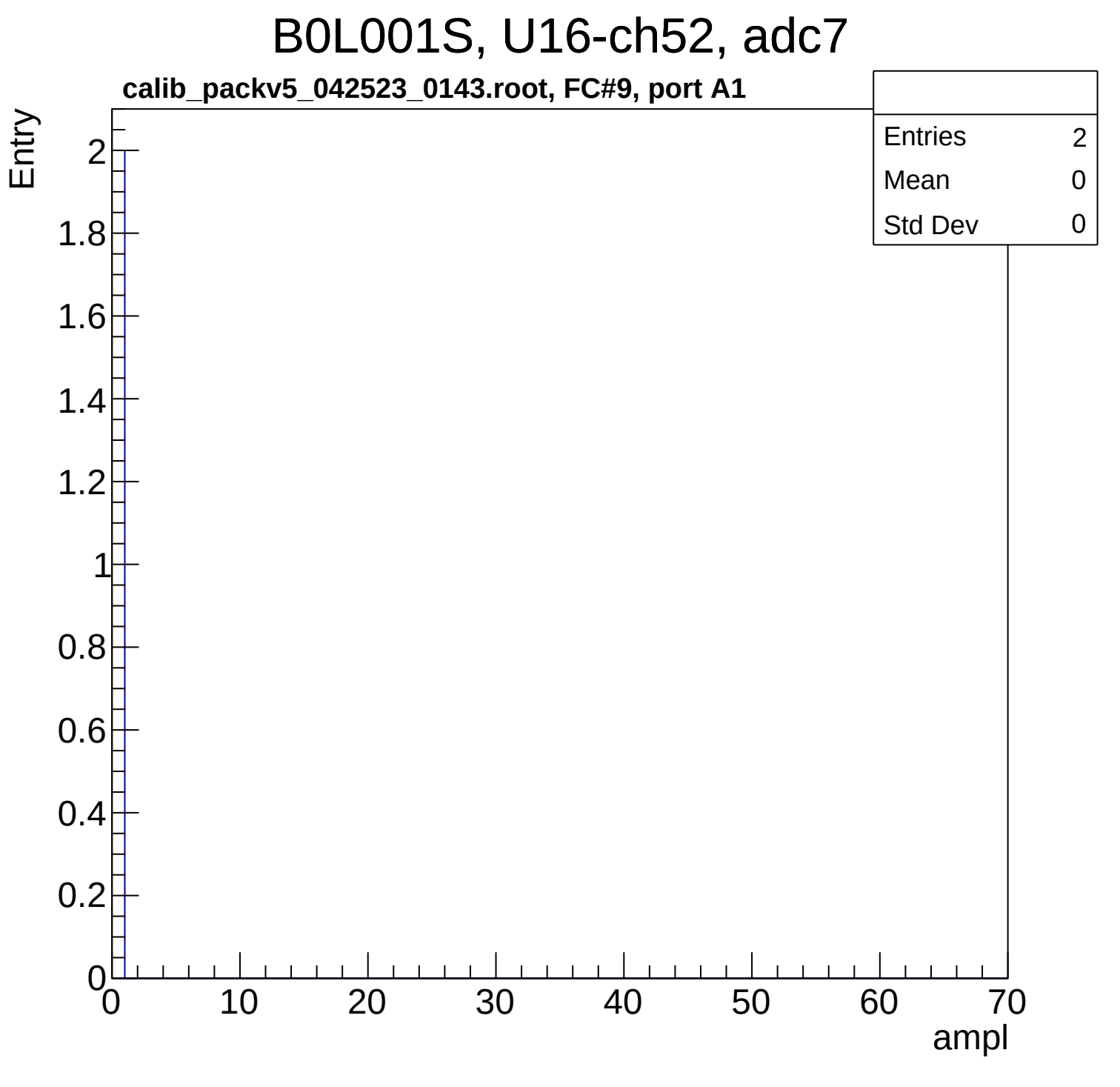
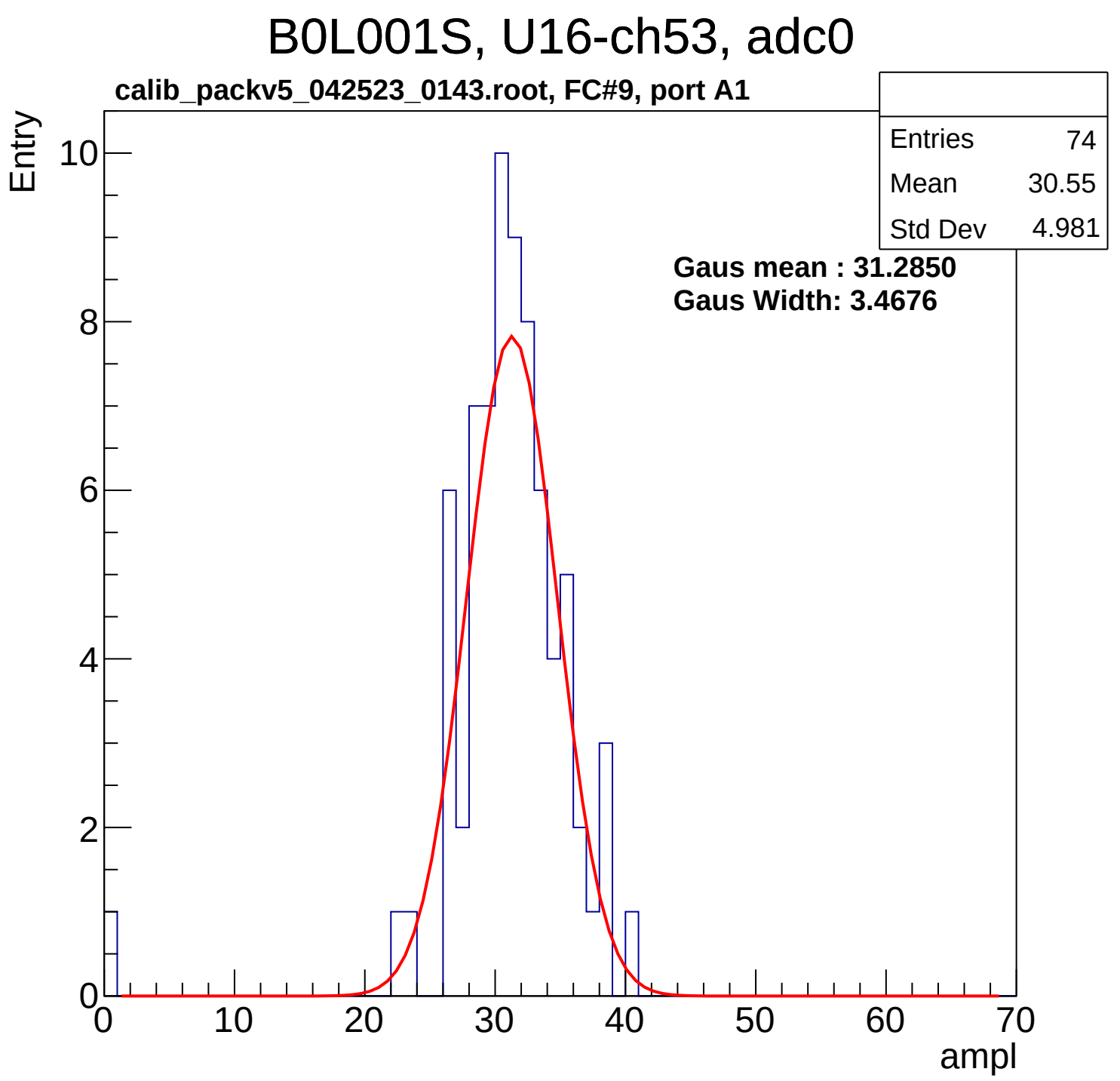
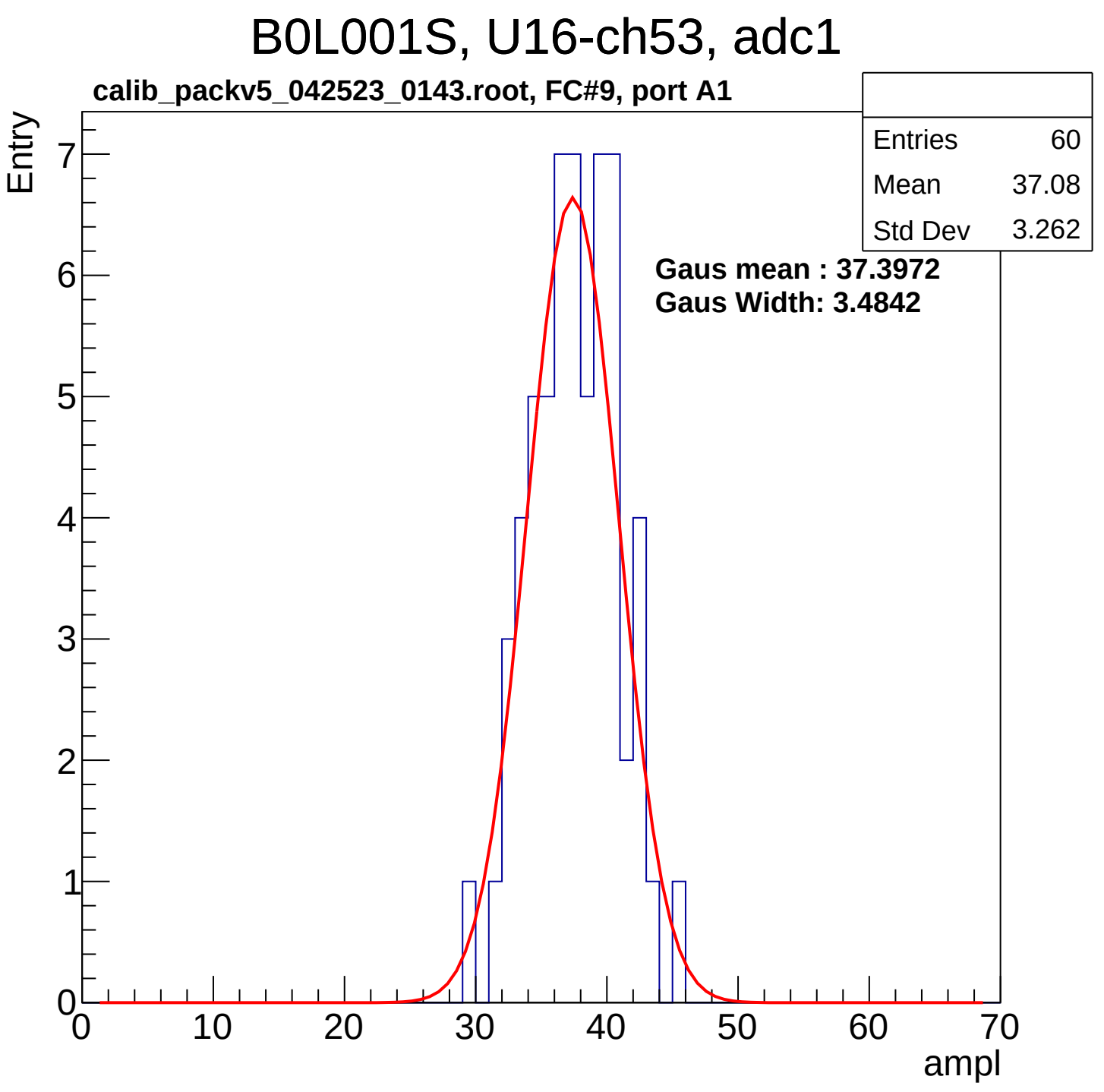
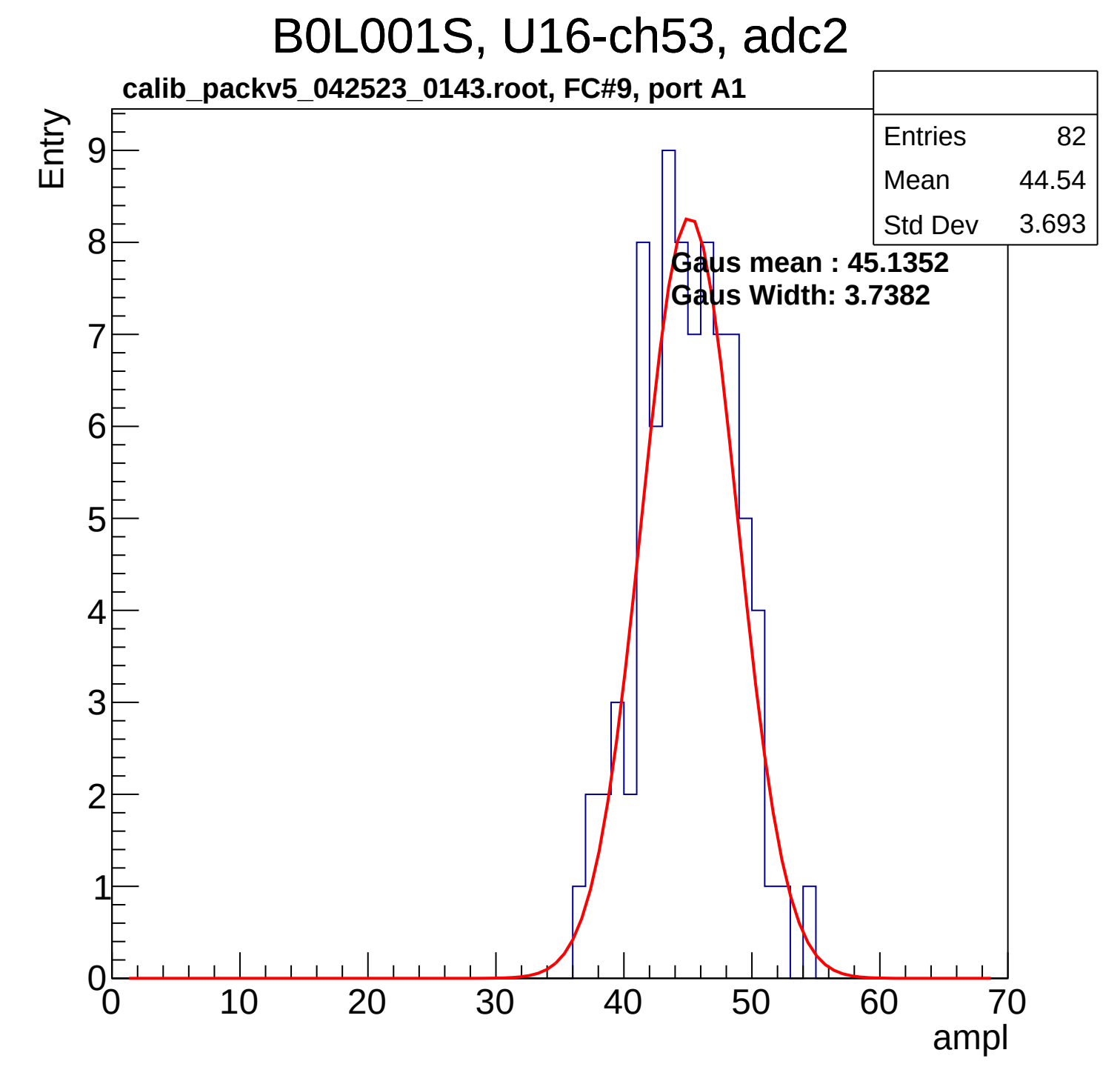
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.55
Std Dev 4.981
Gaus mean : 31.2850
Gaus Width: 3.4676
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.08
Std Dev 3.262
Gaus mean : 37.3972
Gaus Width: 3.4842
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 44.54
Std Dev 3.693
Gaus mean : 45.1352
Gaus Width: 3.7382
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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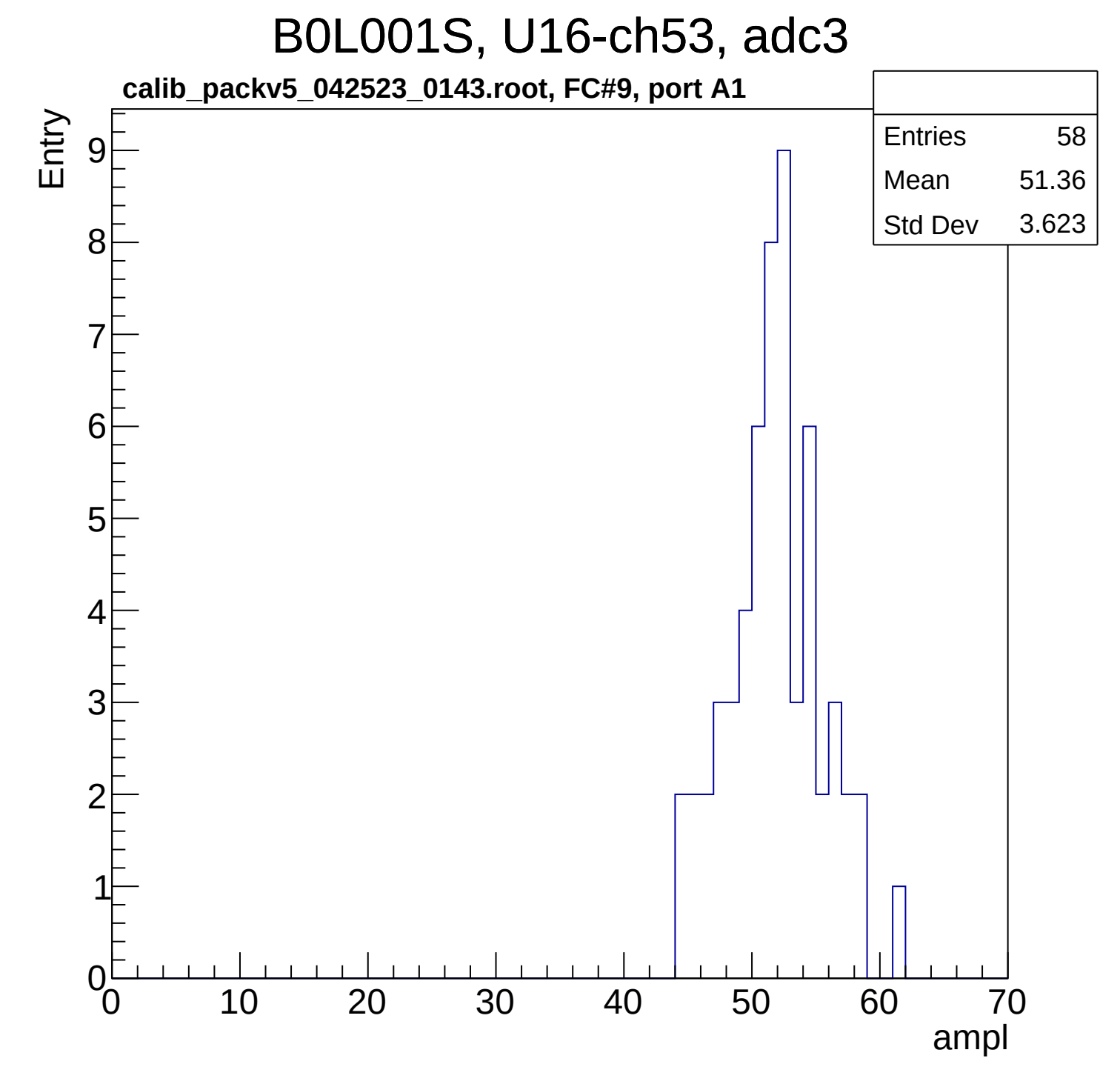
B0L001S, U16-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 51.36
Std Dev 3.623
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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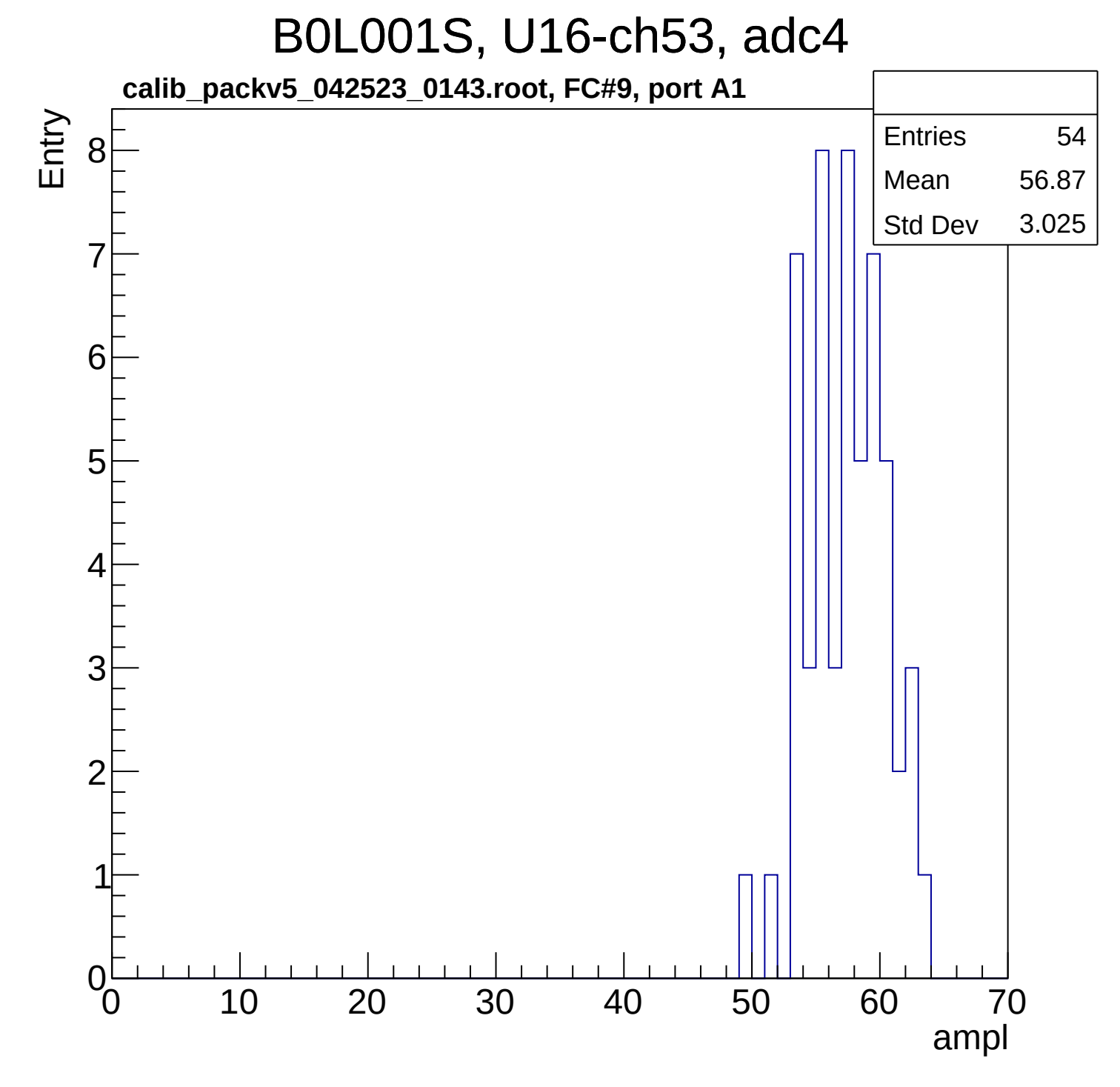
B0L001S, U16-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.87
Std Dev 3.025
Entry
ampl
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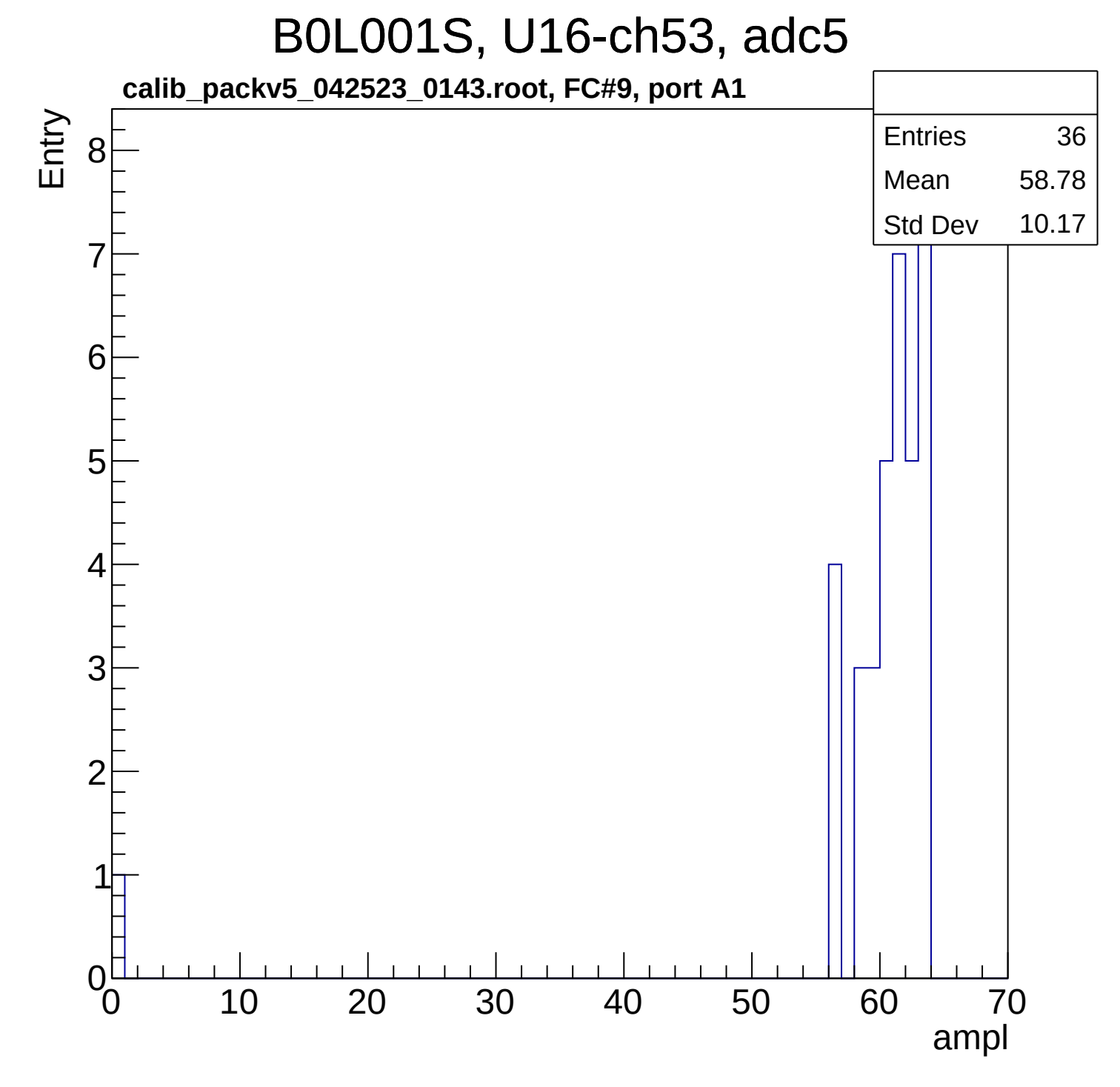
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.78
Std Dev 10.17
Entry
ampl
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2
3
4
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0
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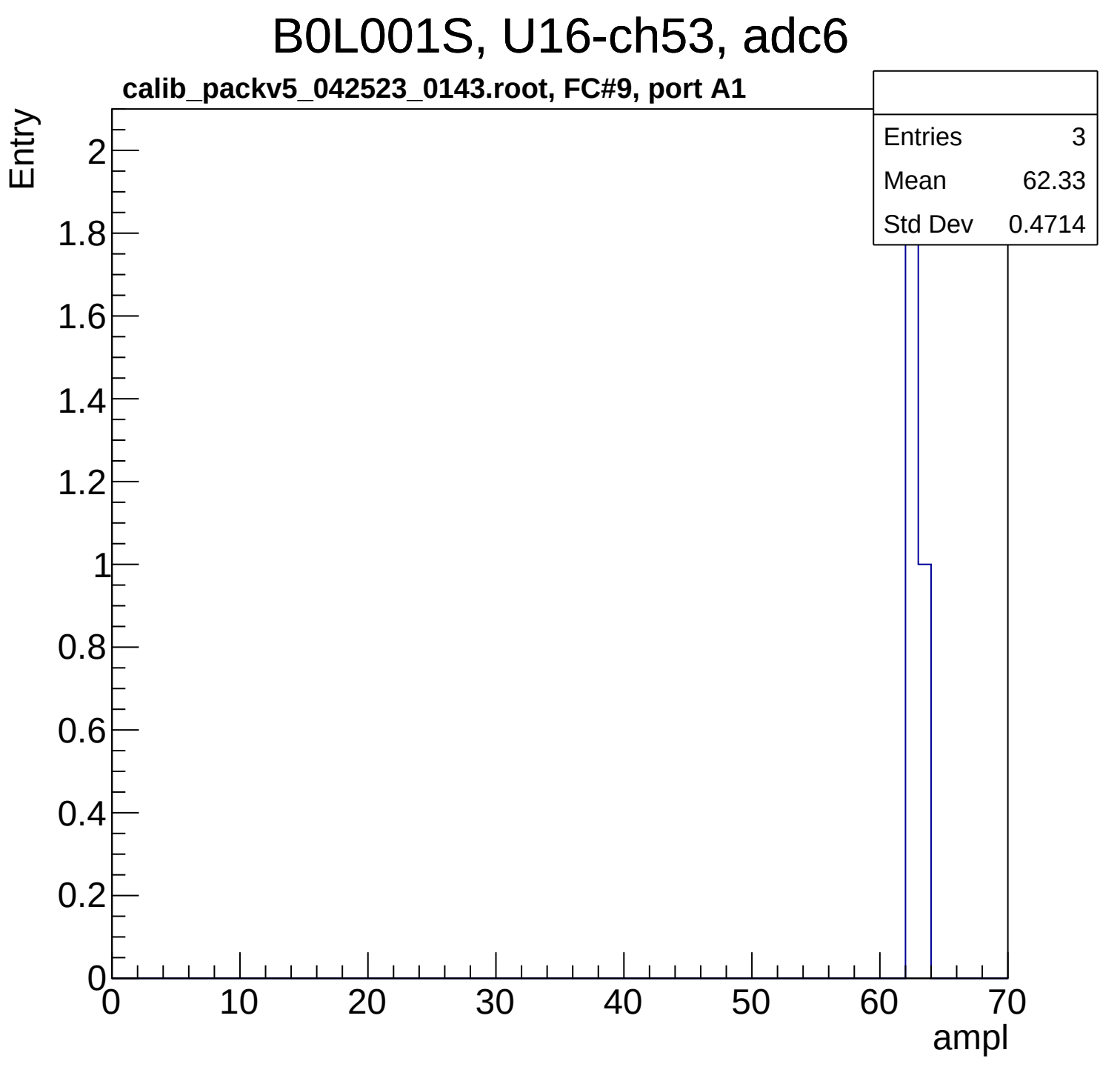
20
30
40
50
60
70


B0L001S, U16-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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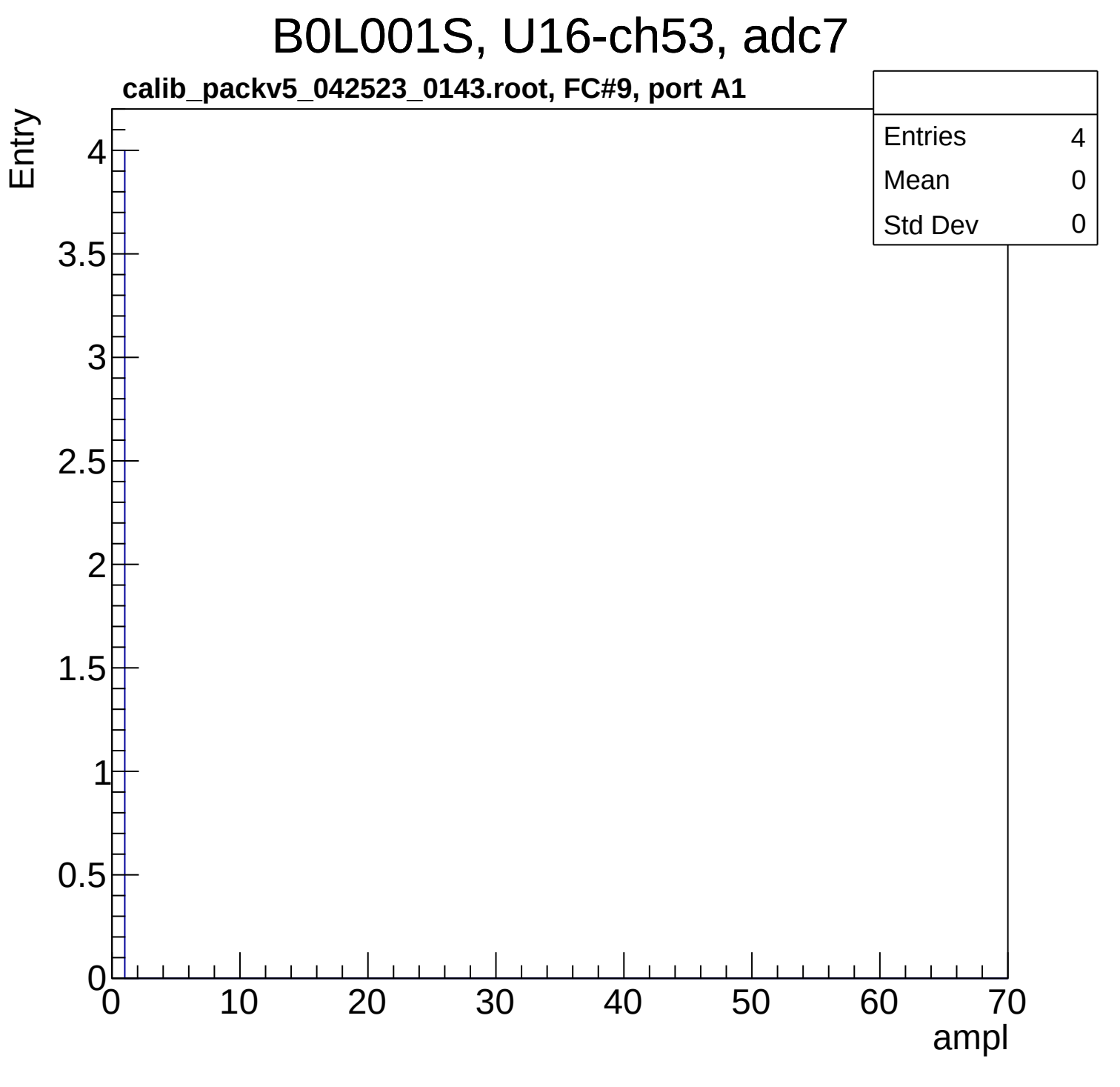
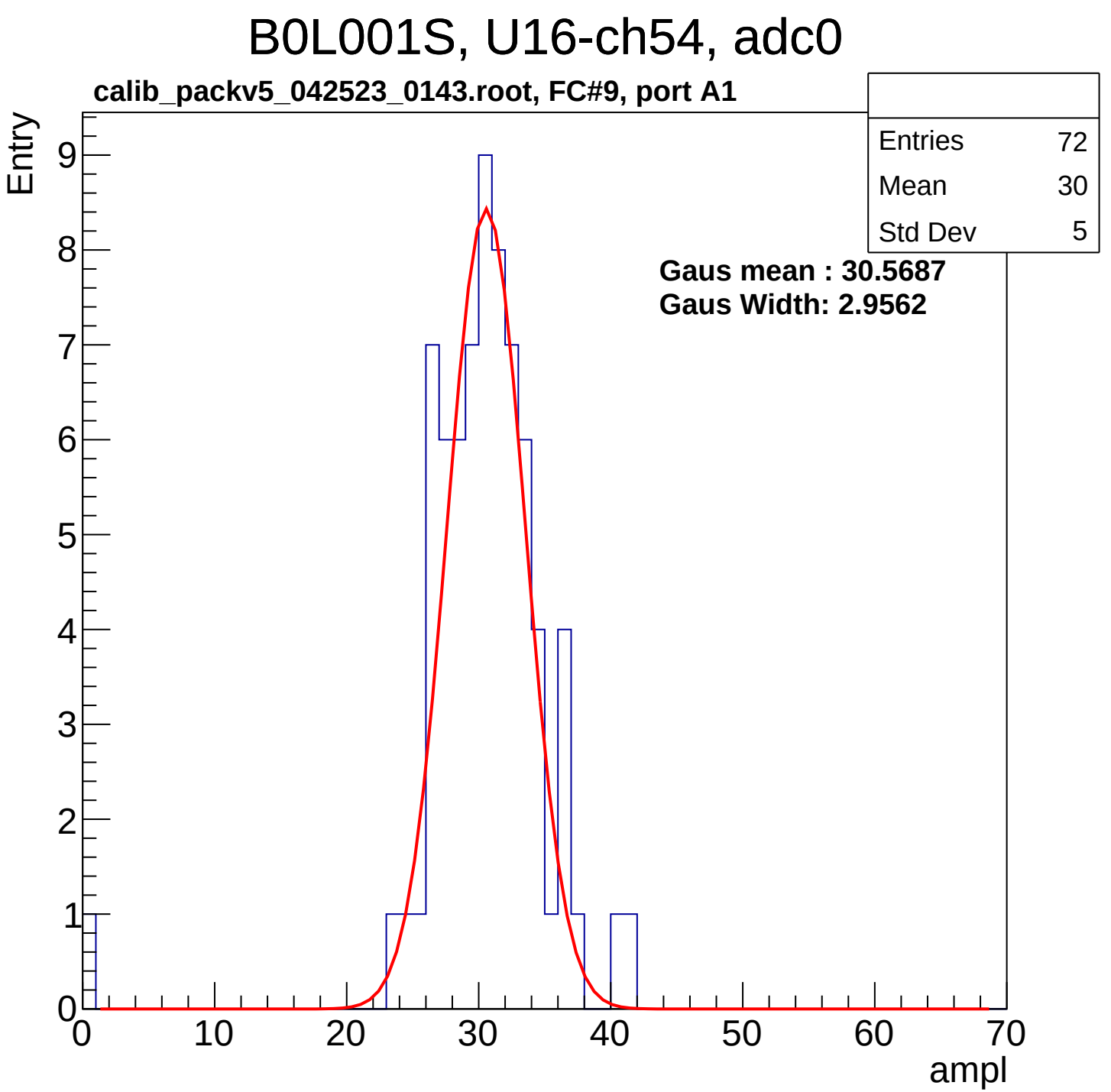
40
50
60
70


B0L001S, U16-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30
Std Dev 5
Gaus mean : 30.5687
Gaus Width: 2.9562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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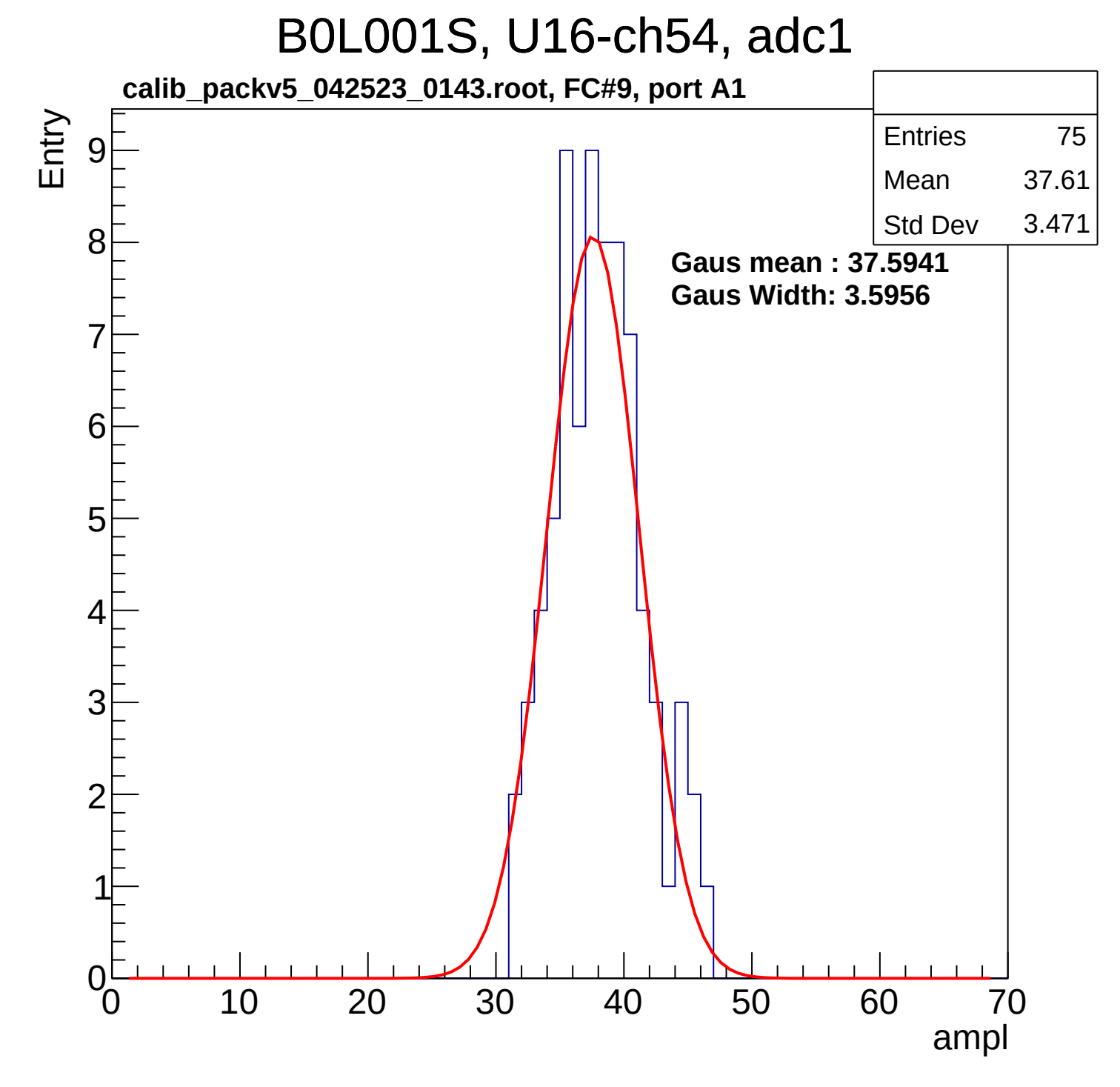
B0L001S, U16-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.61
Std Dev 3.471
Gaus mean : 37.5941
Gaus Width: 3.5956
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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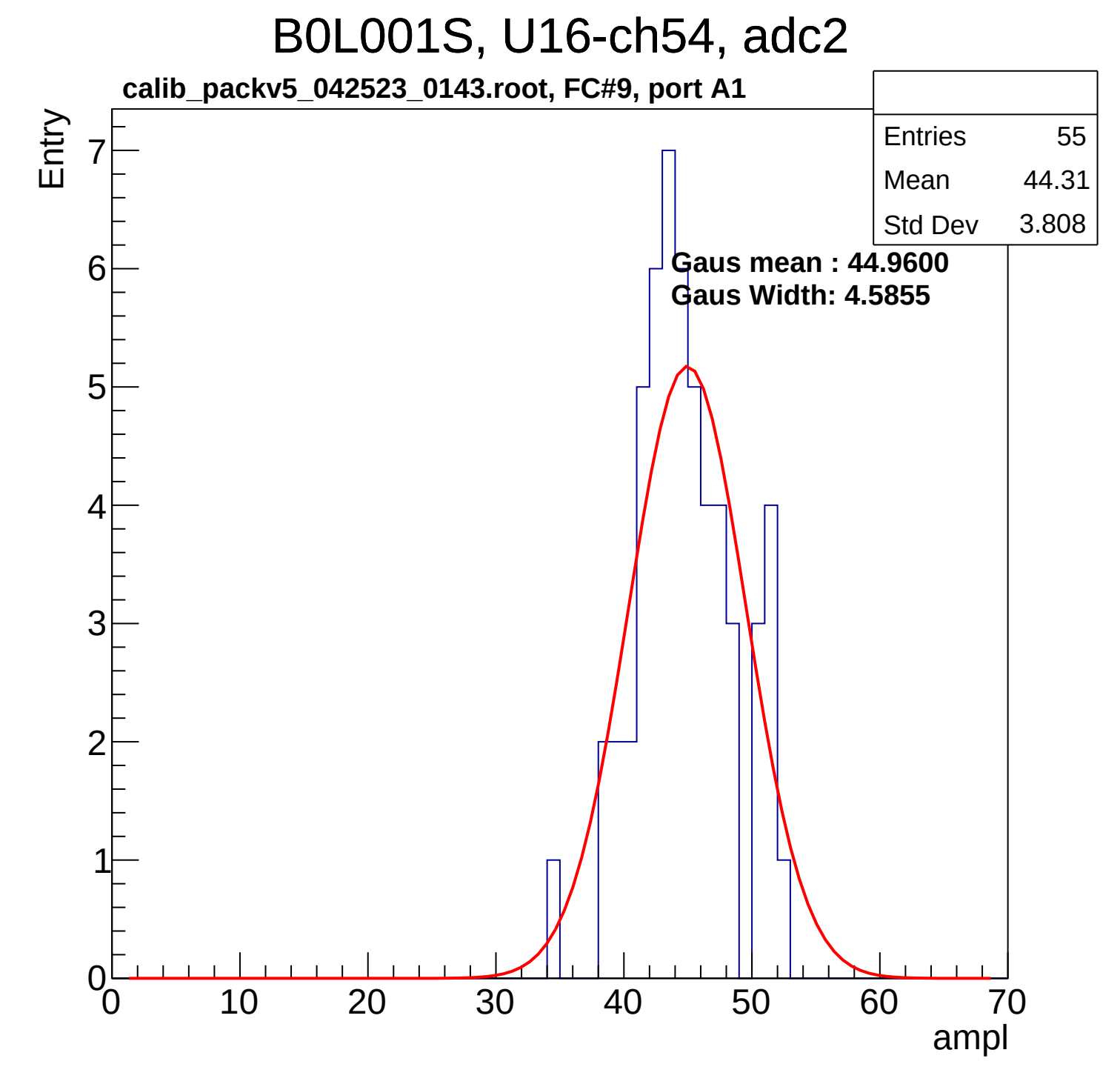
B0L001S, U16-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.31
Std Dev 3.808
Gaus mean : 44.9600
Gaus Width: 4.5855
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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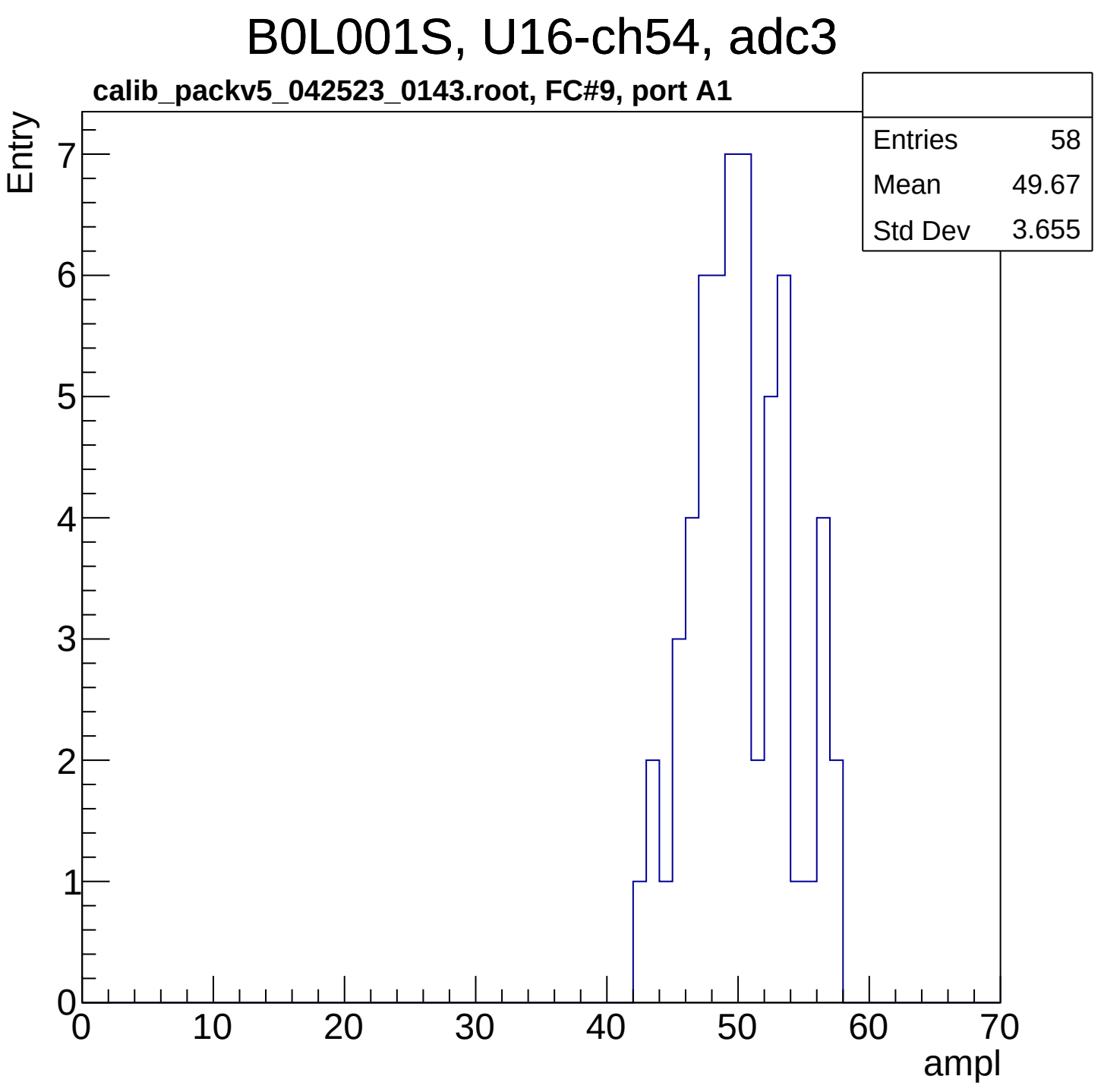
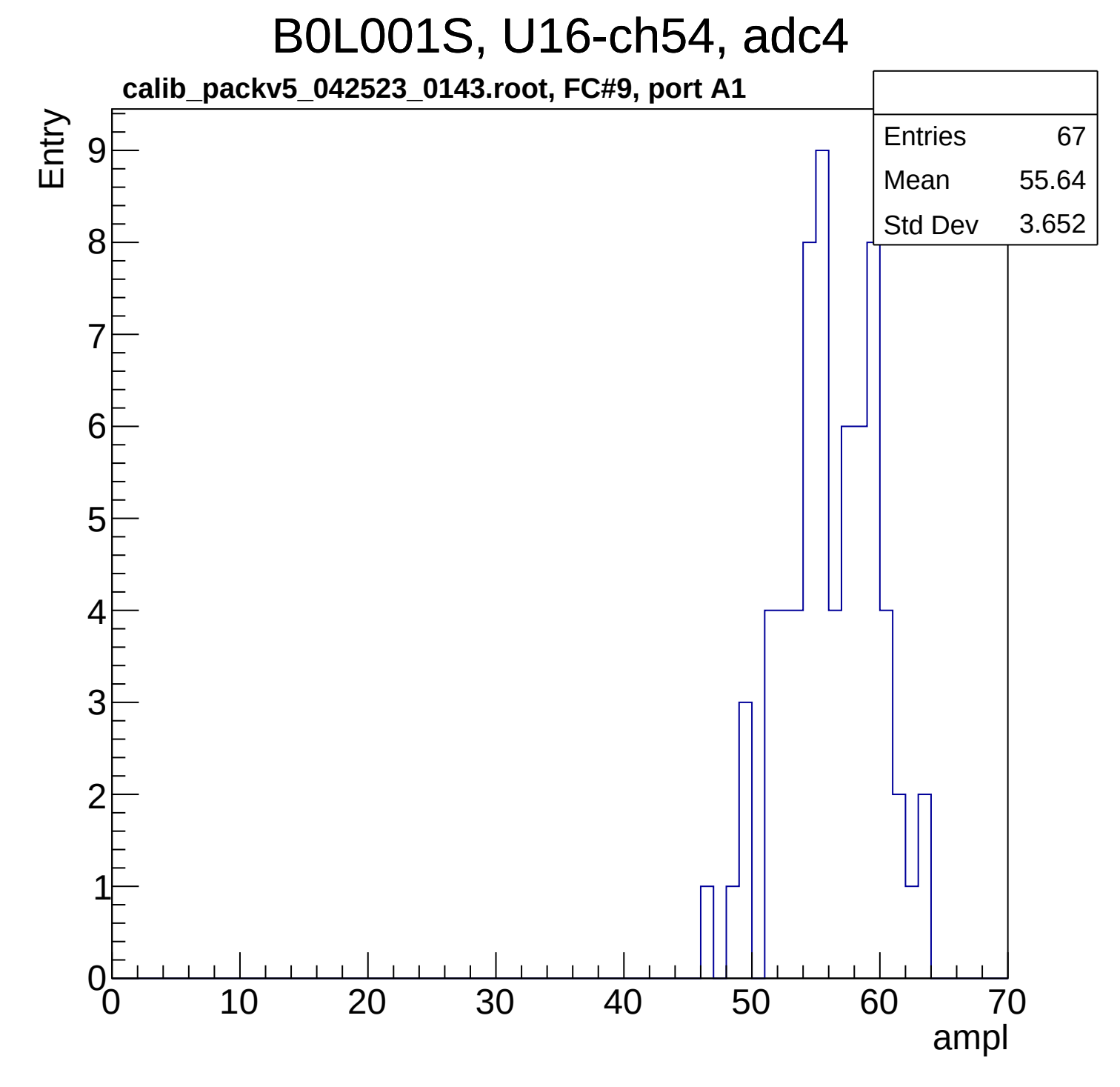
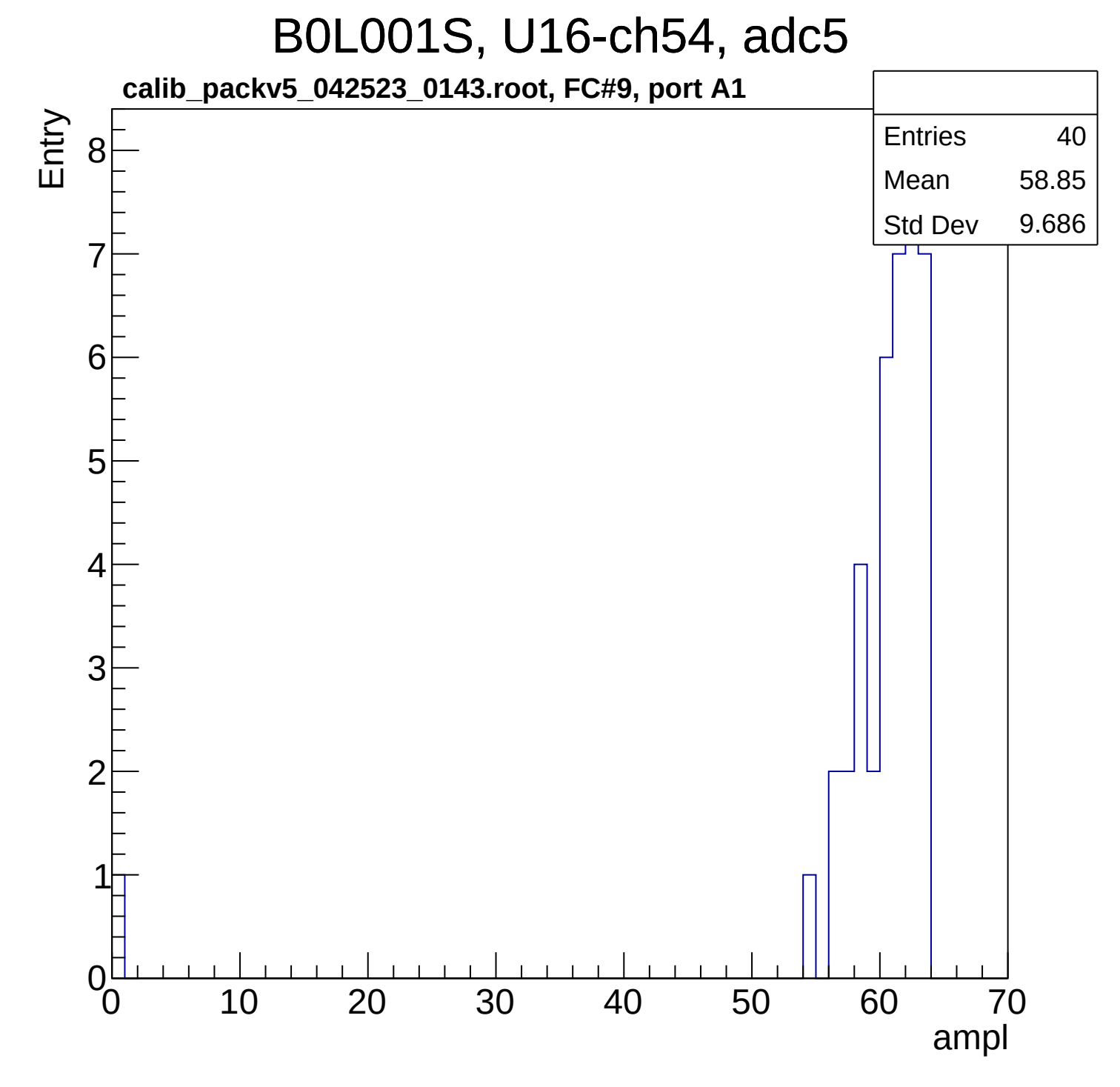

B0L001S, U16-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.67
Std Dev 3.655
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.64
Std Dev 3.652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.85
Std Dev 9.686
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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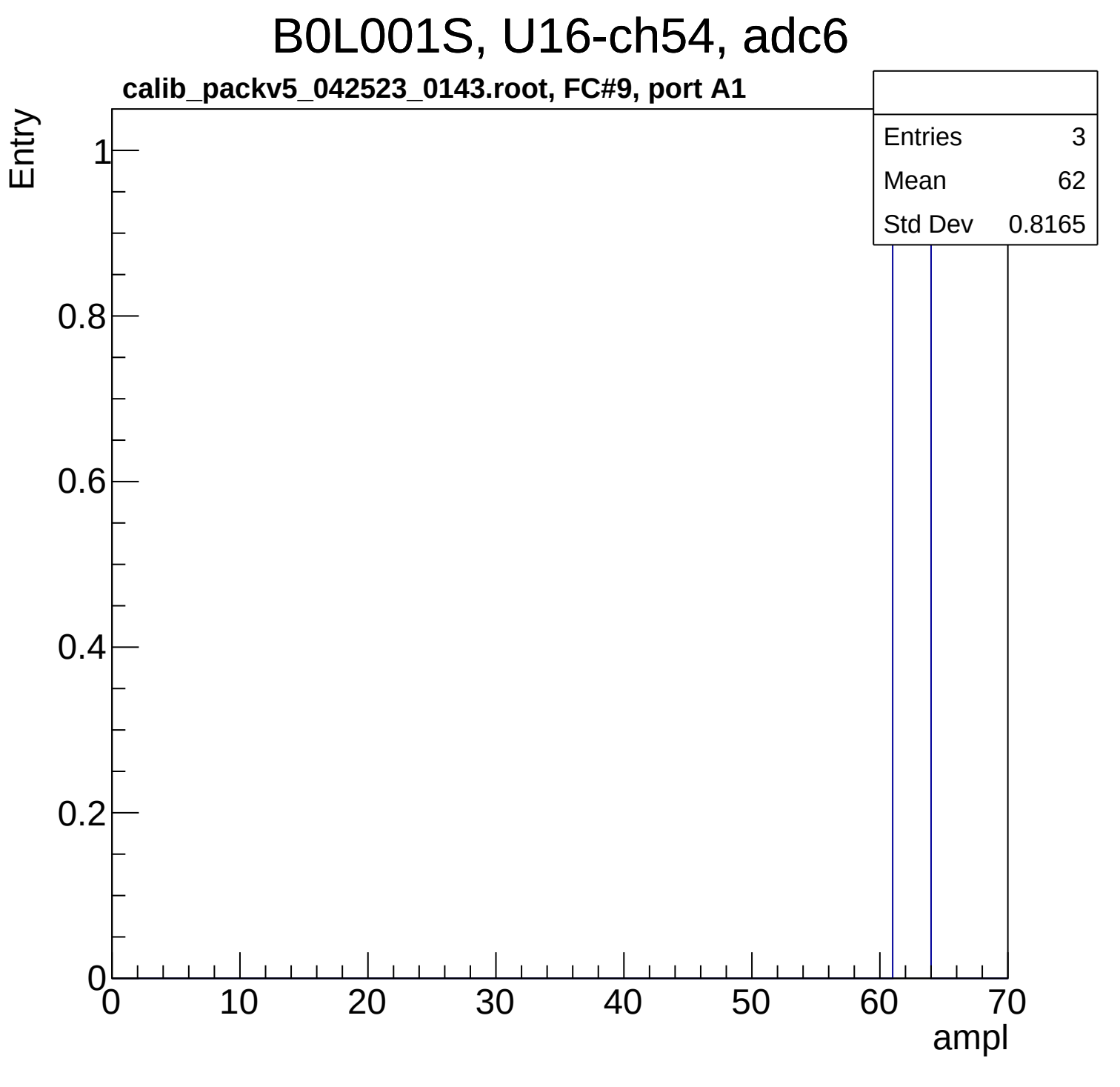
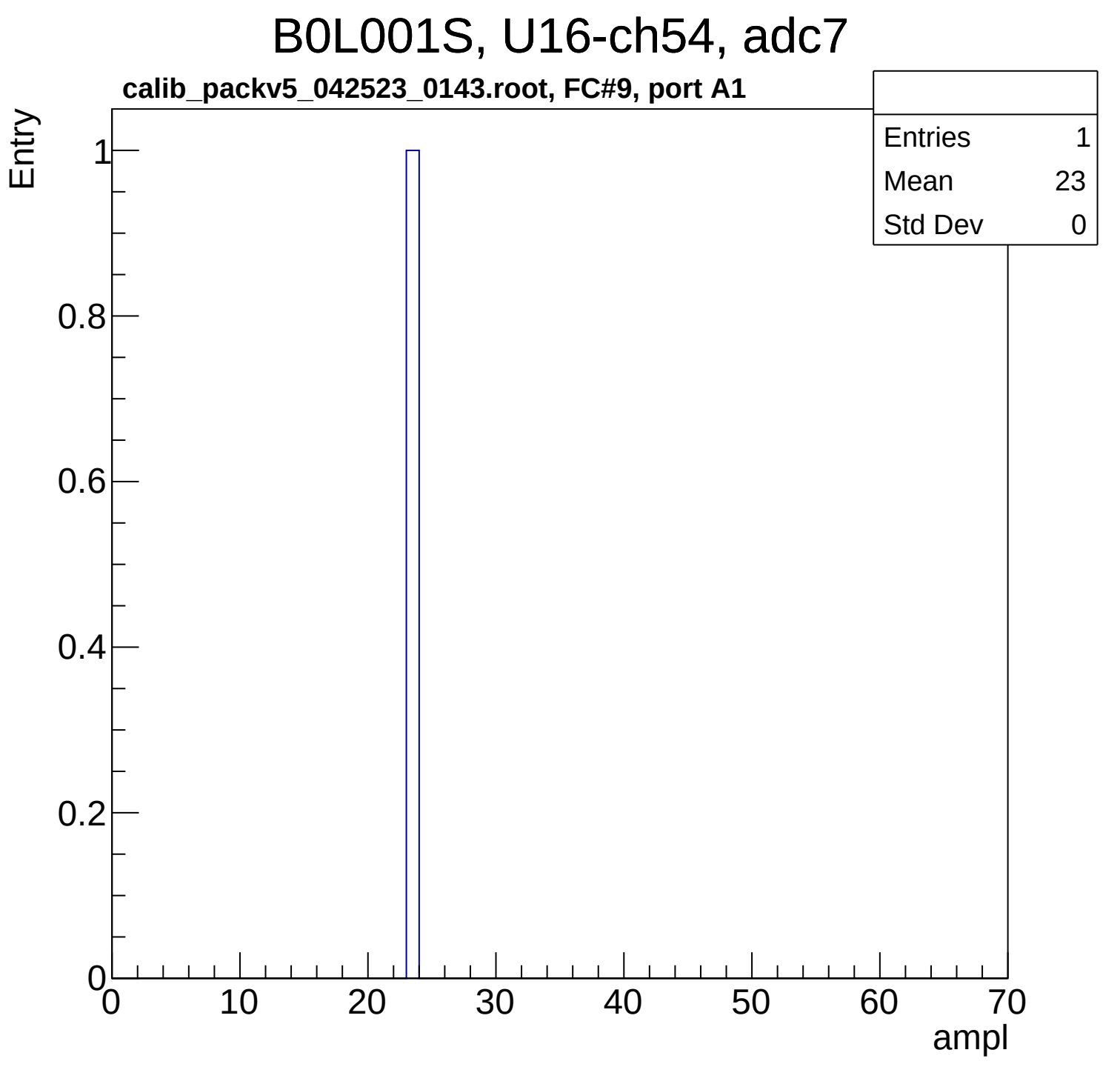
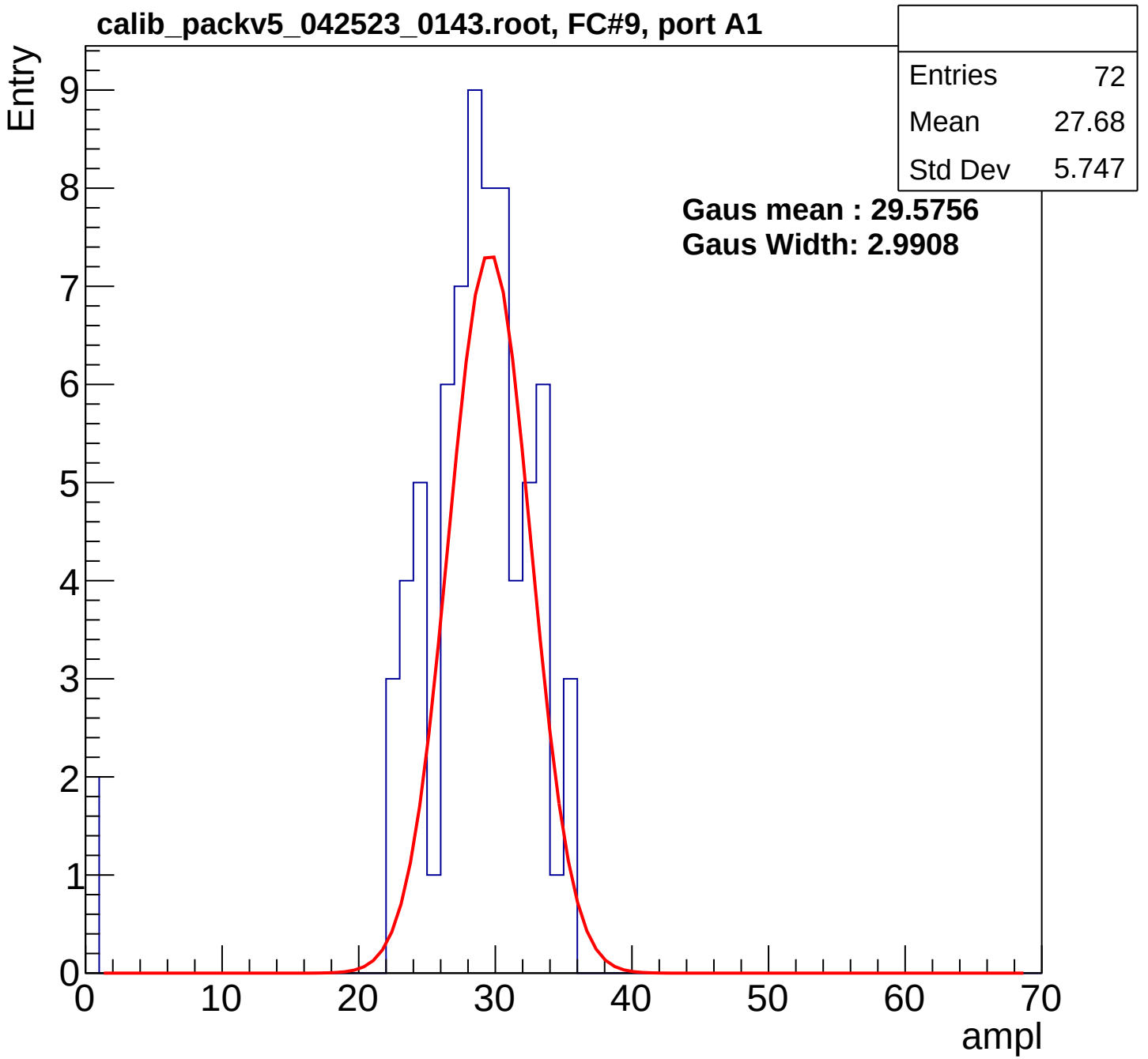
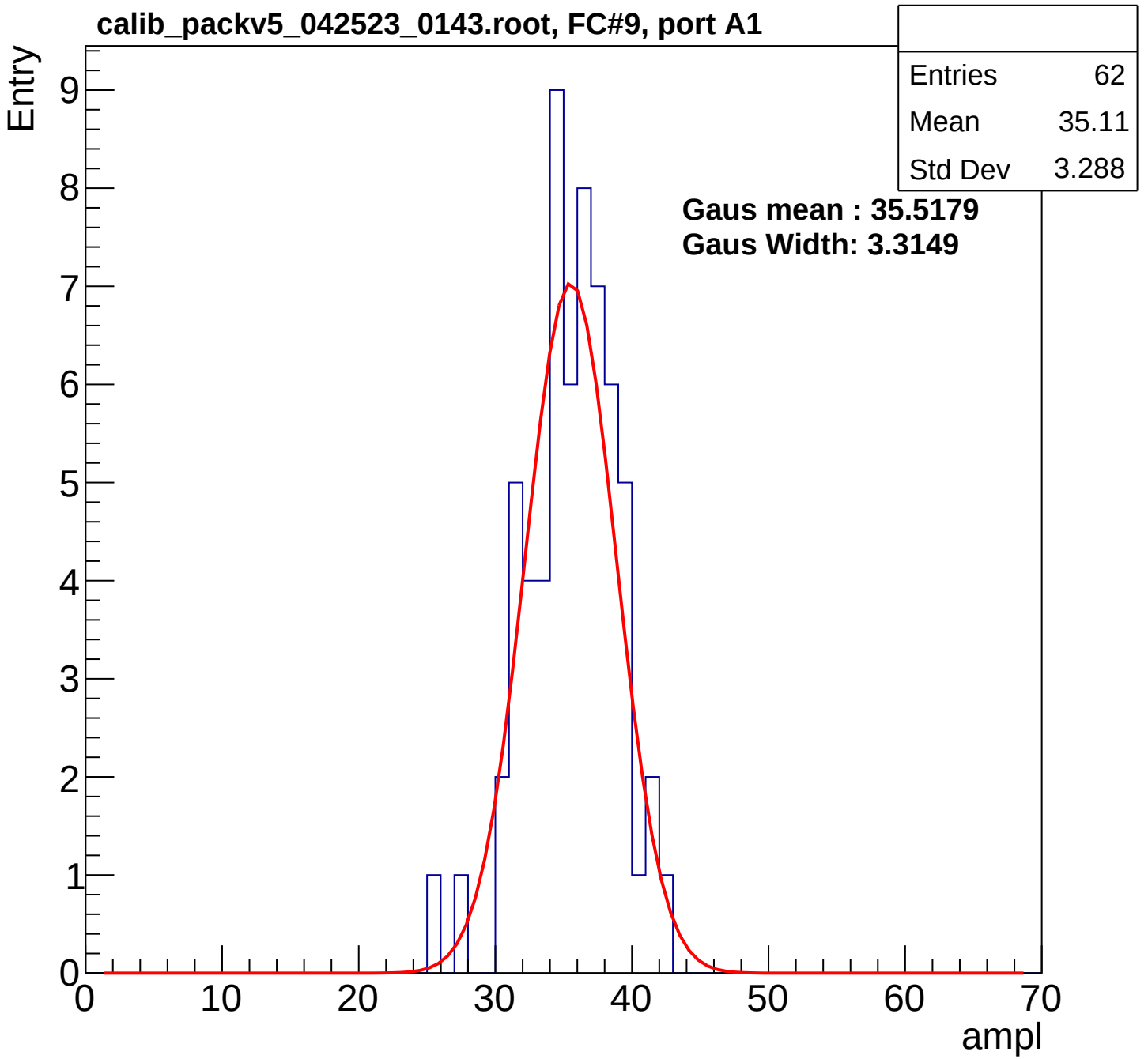
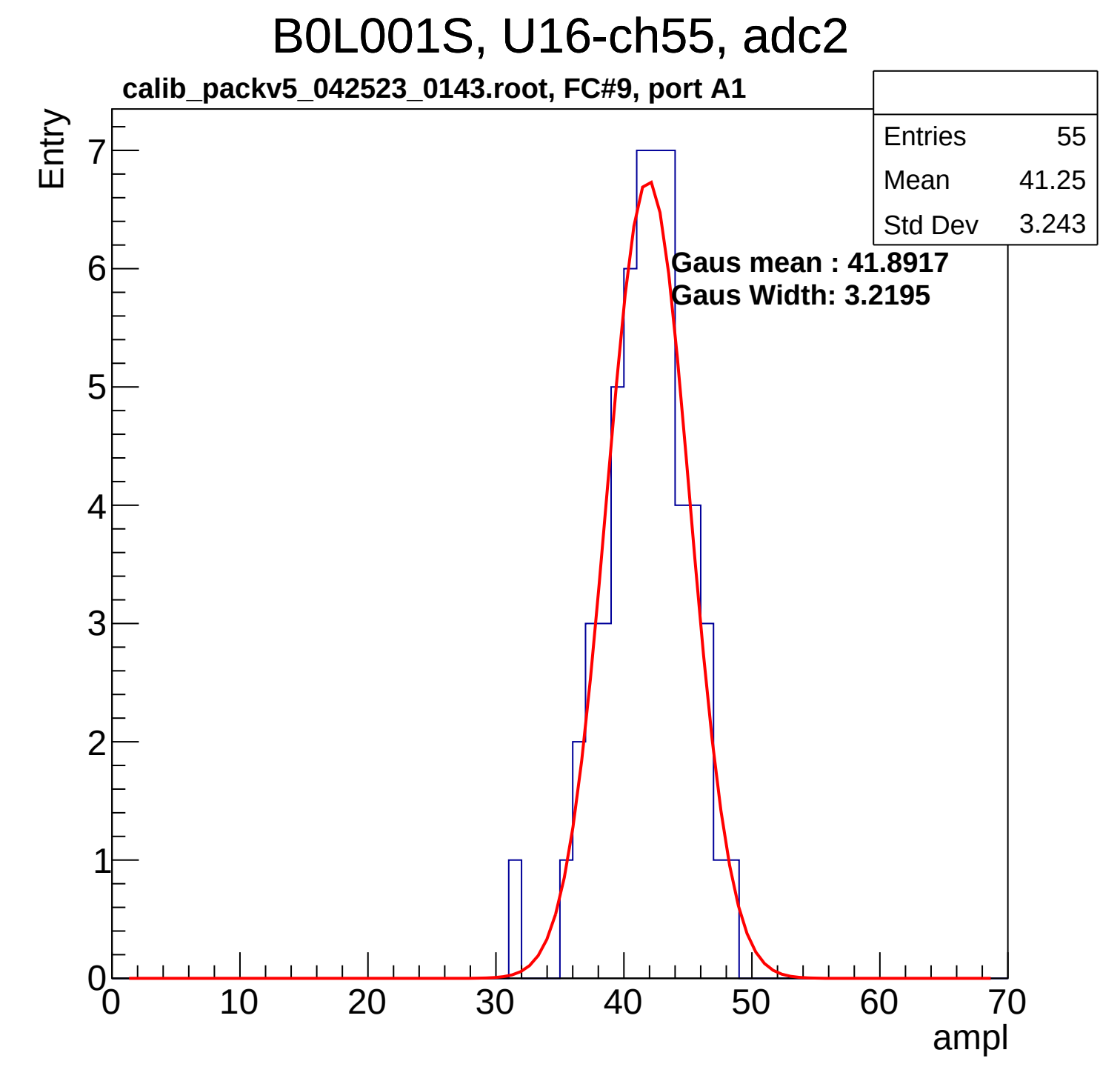

B0L001S, U16-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 27.68
Std Dev 5.747
Gaus mean : 29.5756
Gaus Width: 2.9908
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 35.11
Std Dev 3.288
Gaus mean : 35.5179
Gaus Width: 3.3149
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U16-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 41.25
Std Dev 3.243
Gaus mean : 41.8917
Gaus Width: 3.2195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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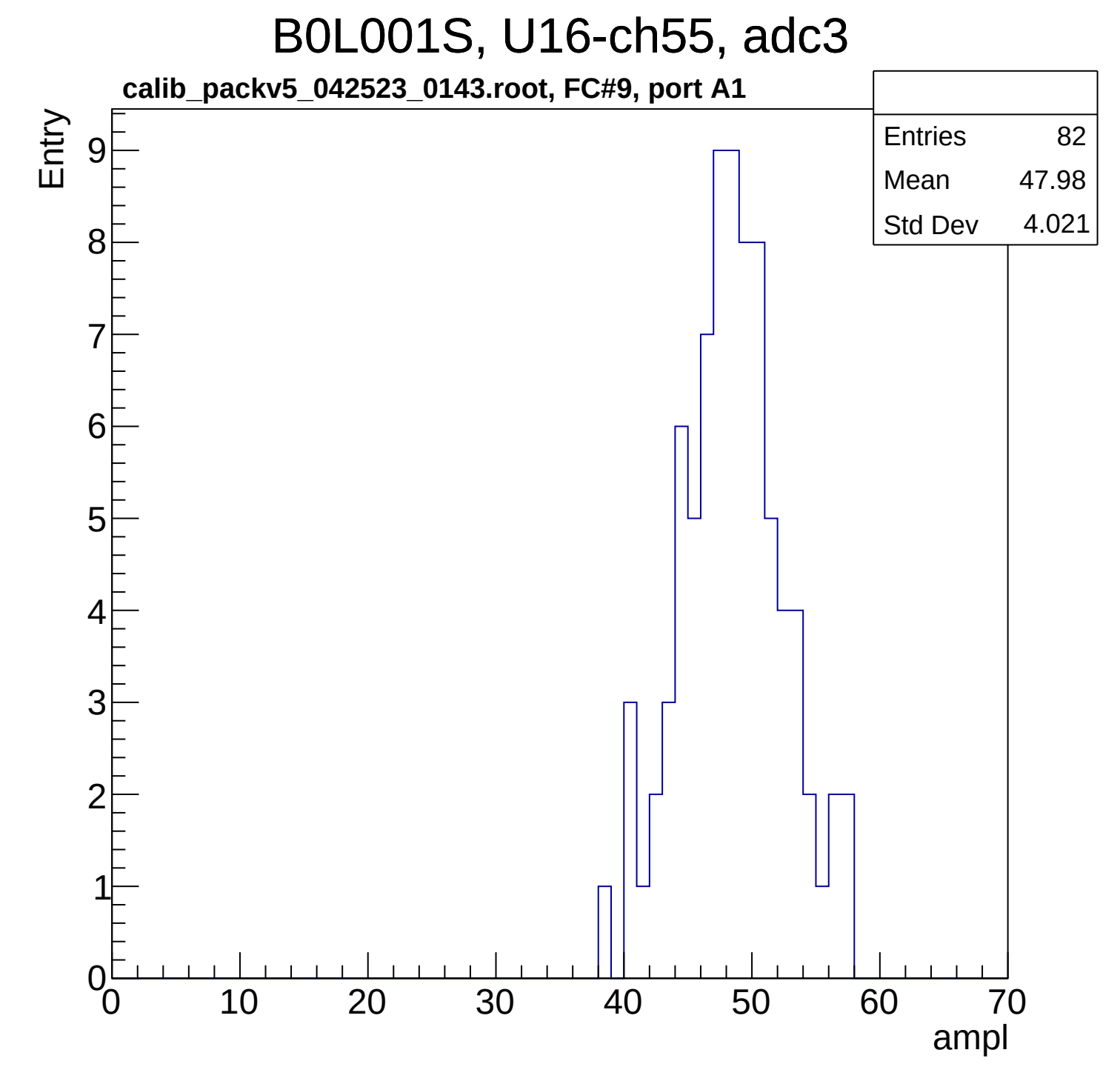
B0L001S, U16-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 47.98
Std Dev 4.021
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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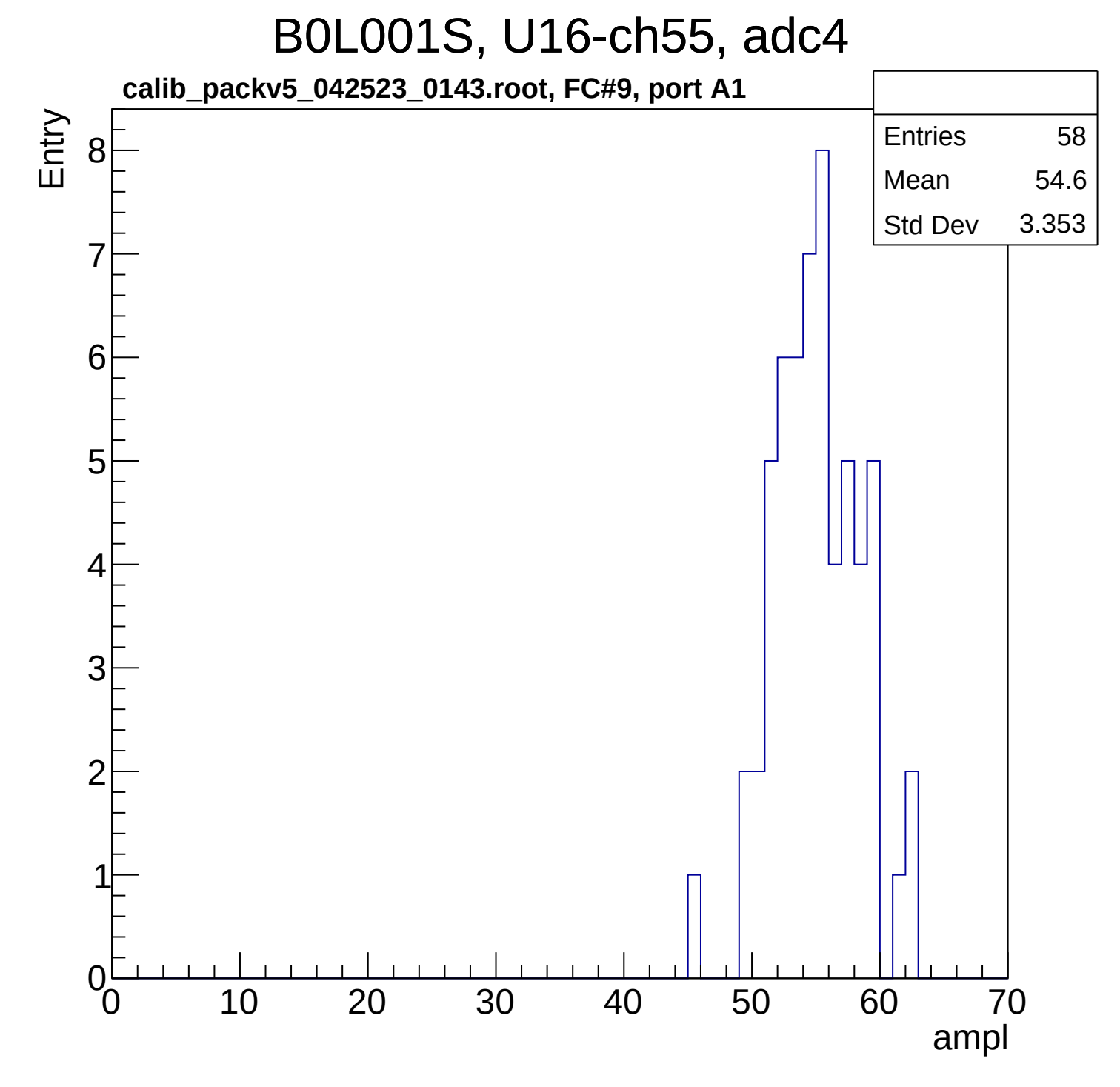
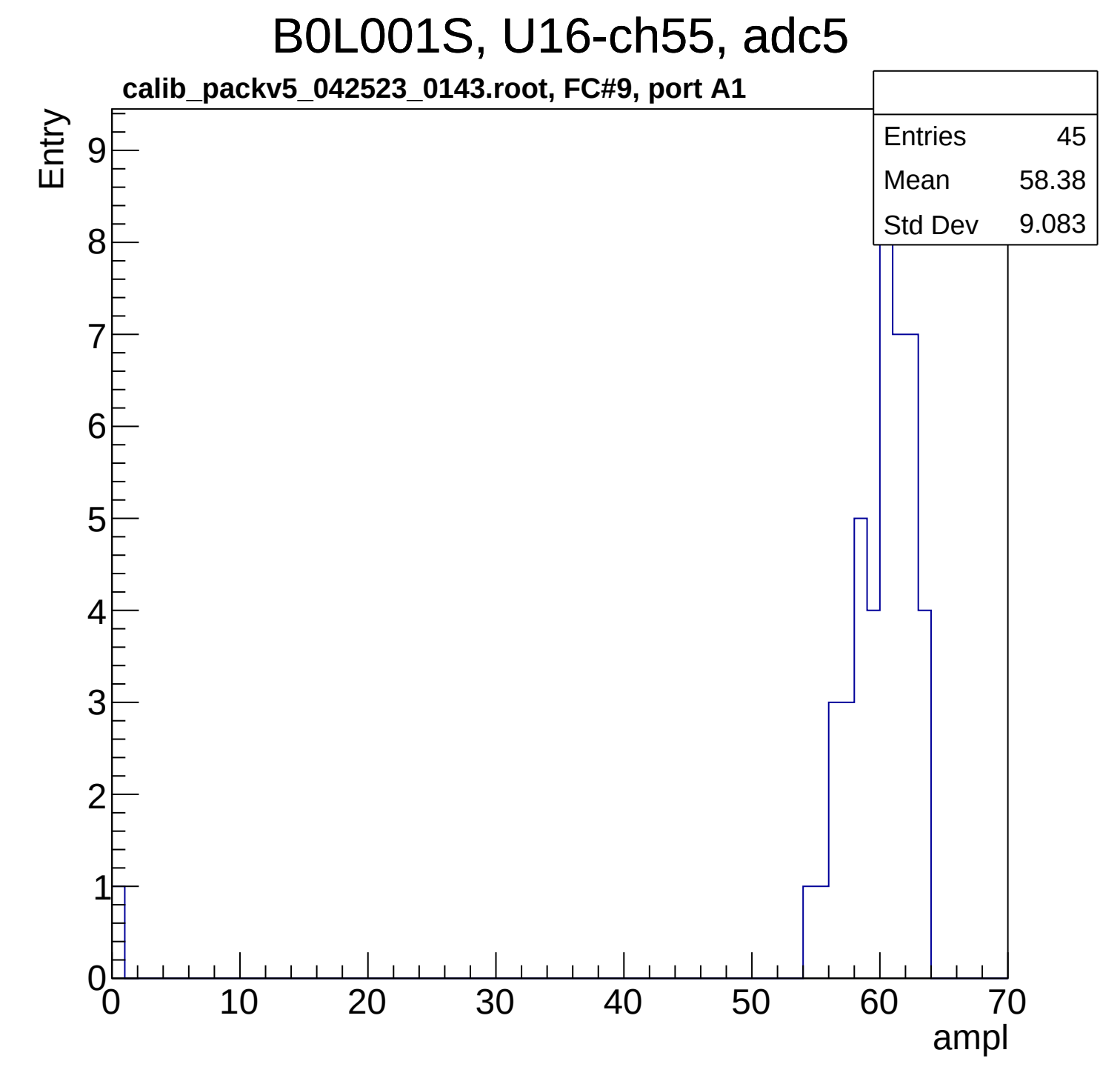

B0L001S, U16-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 54.6
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.38
Std Dev 9.083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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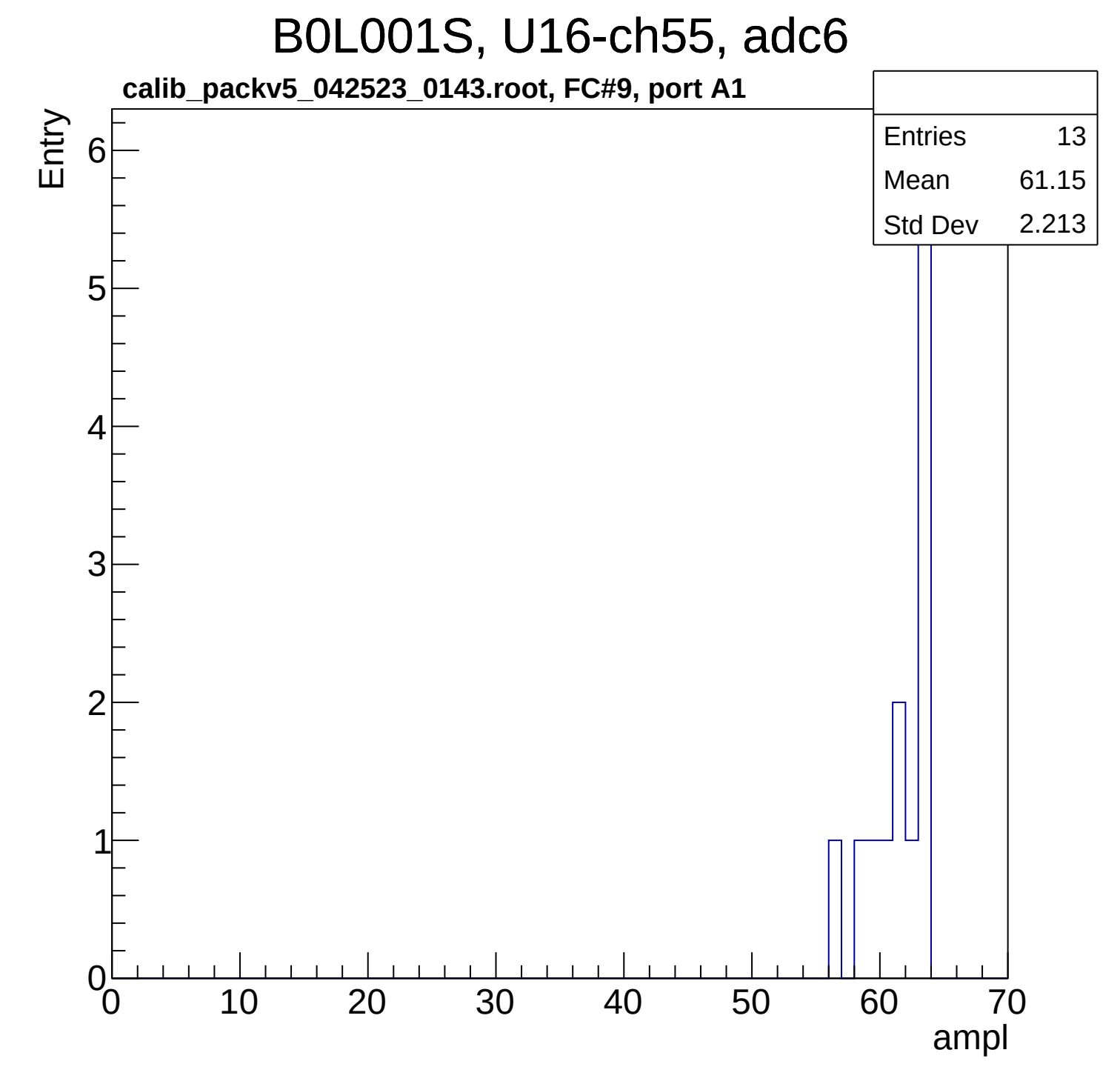
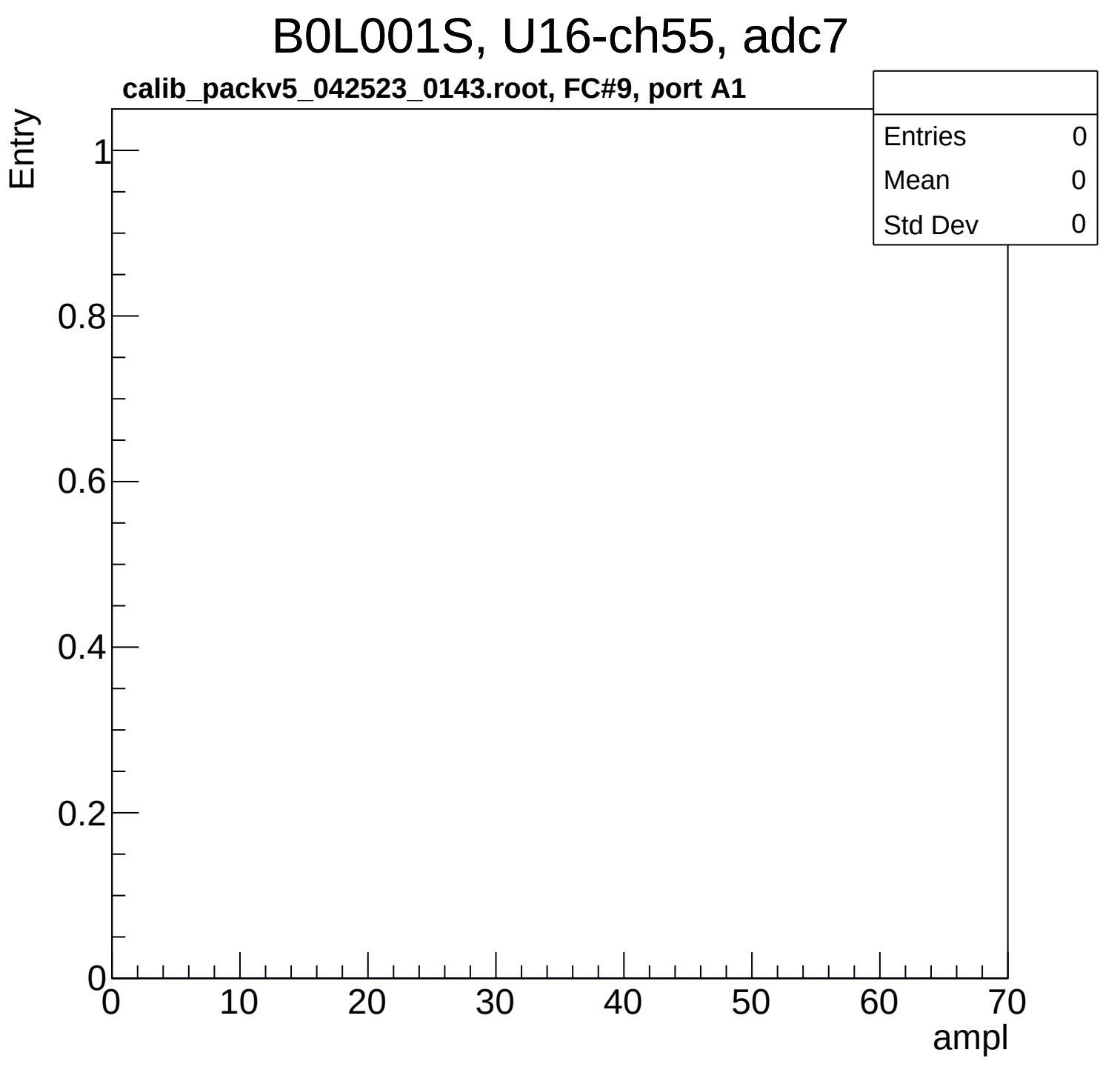
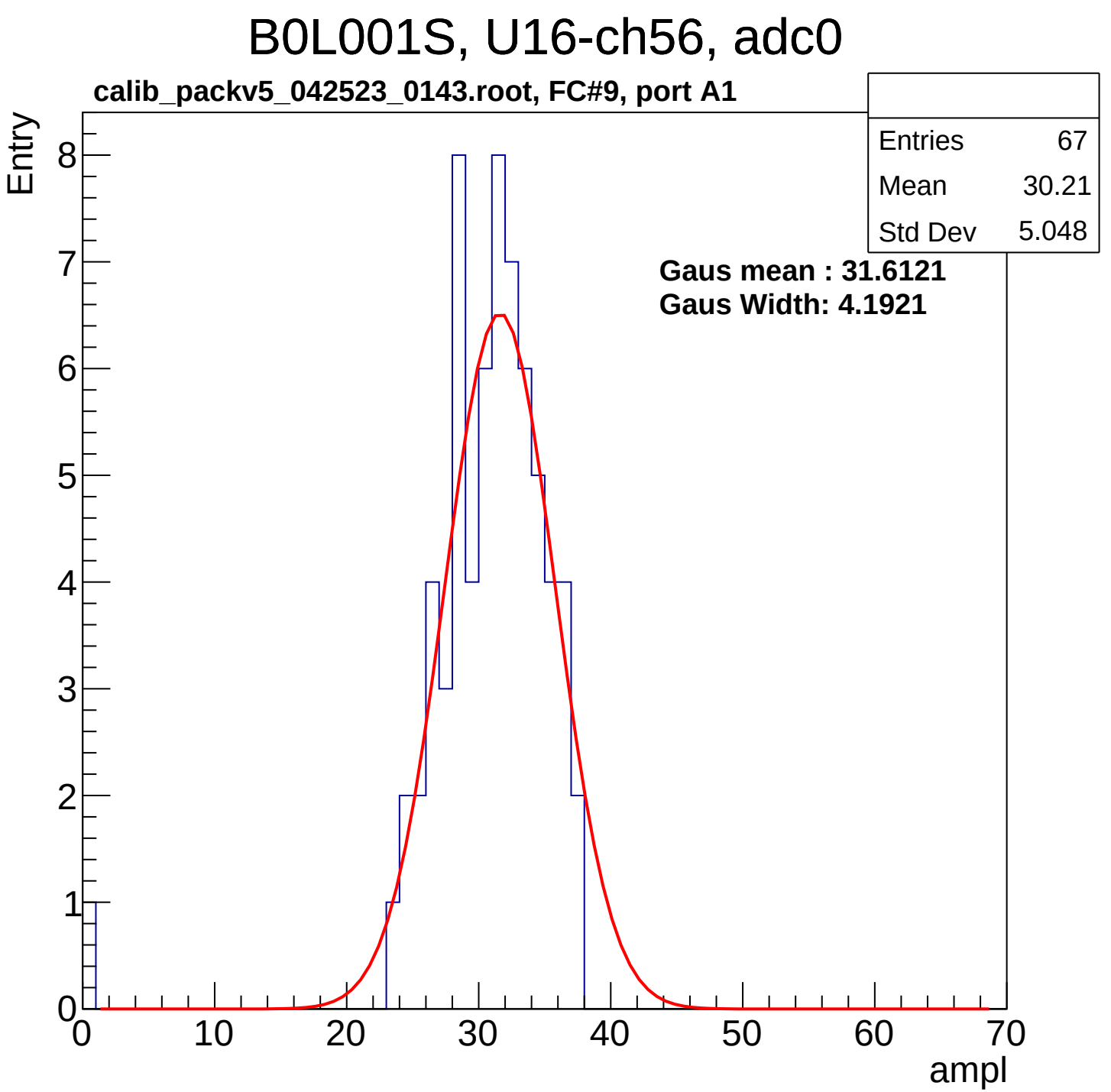
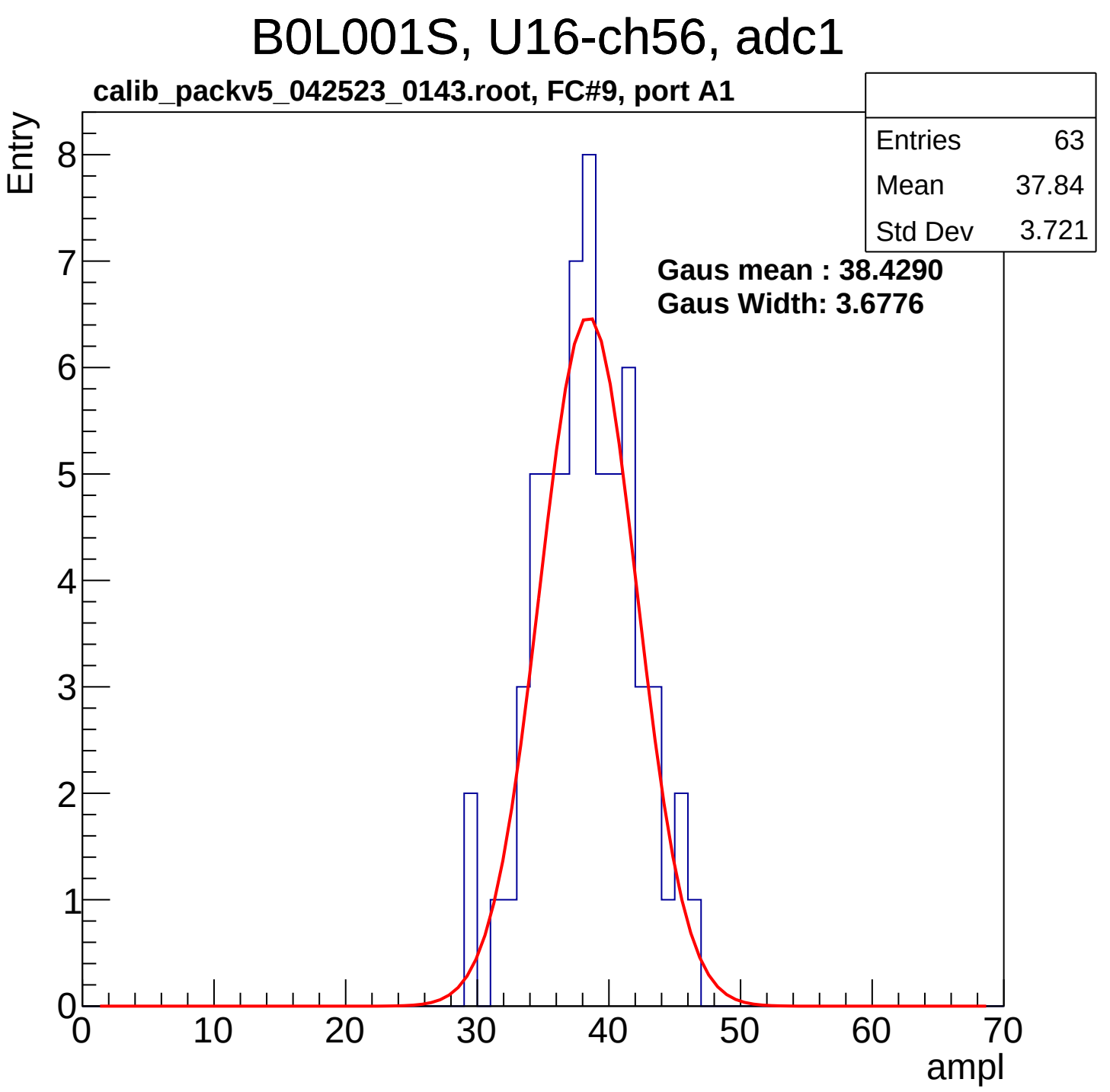
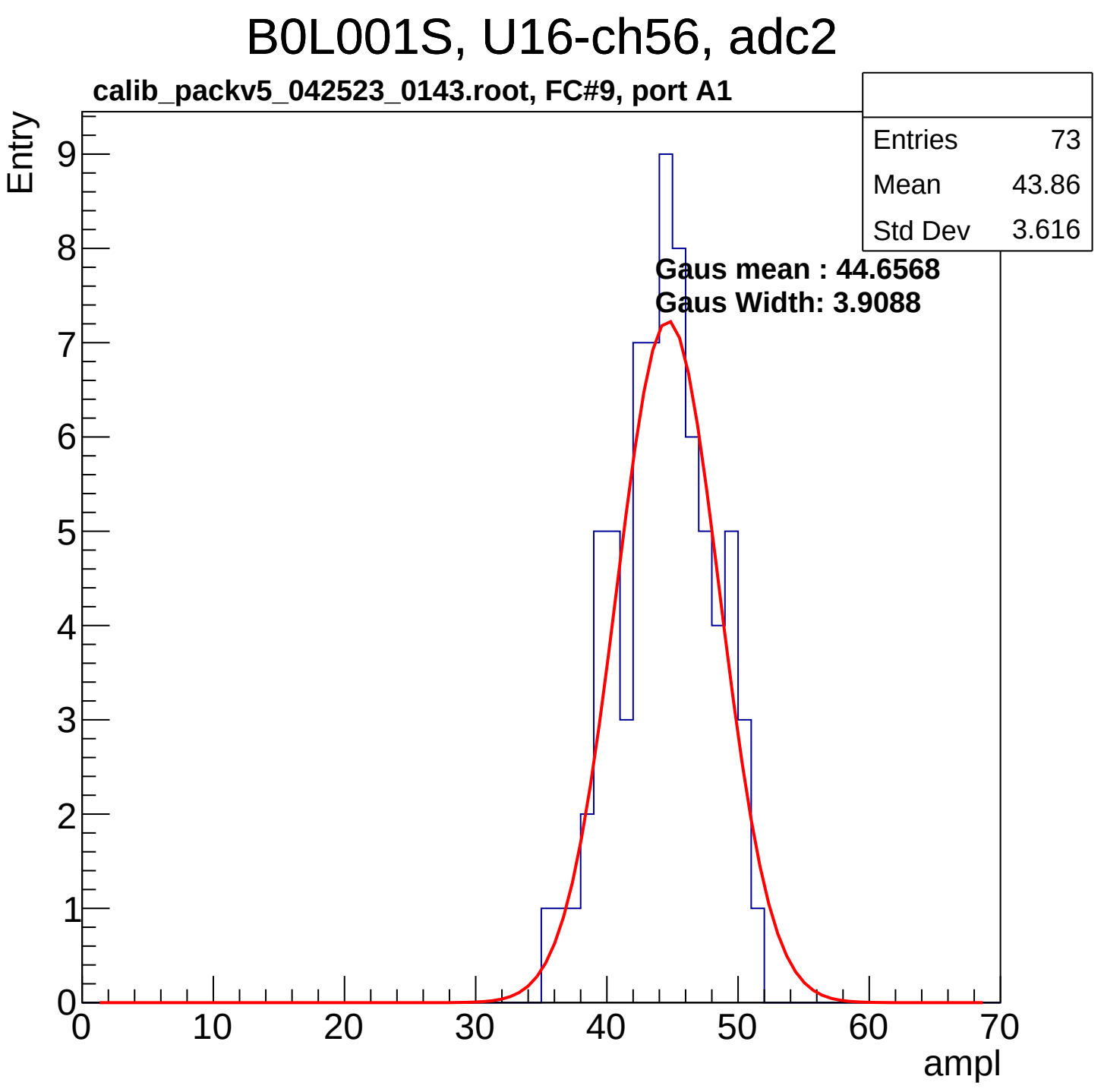
40
50
60
70

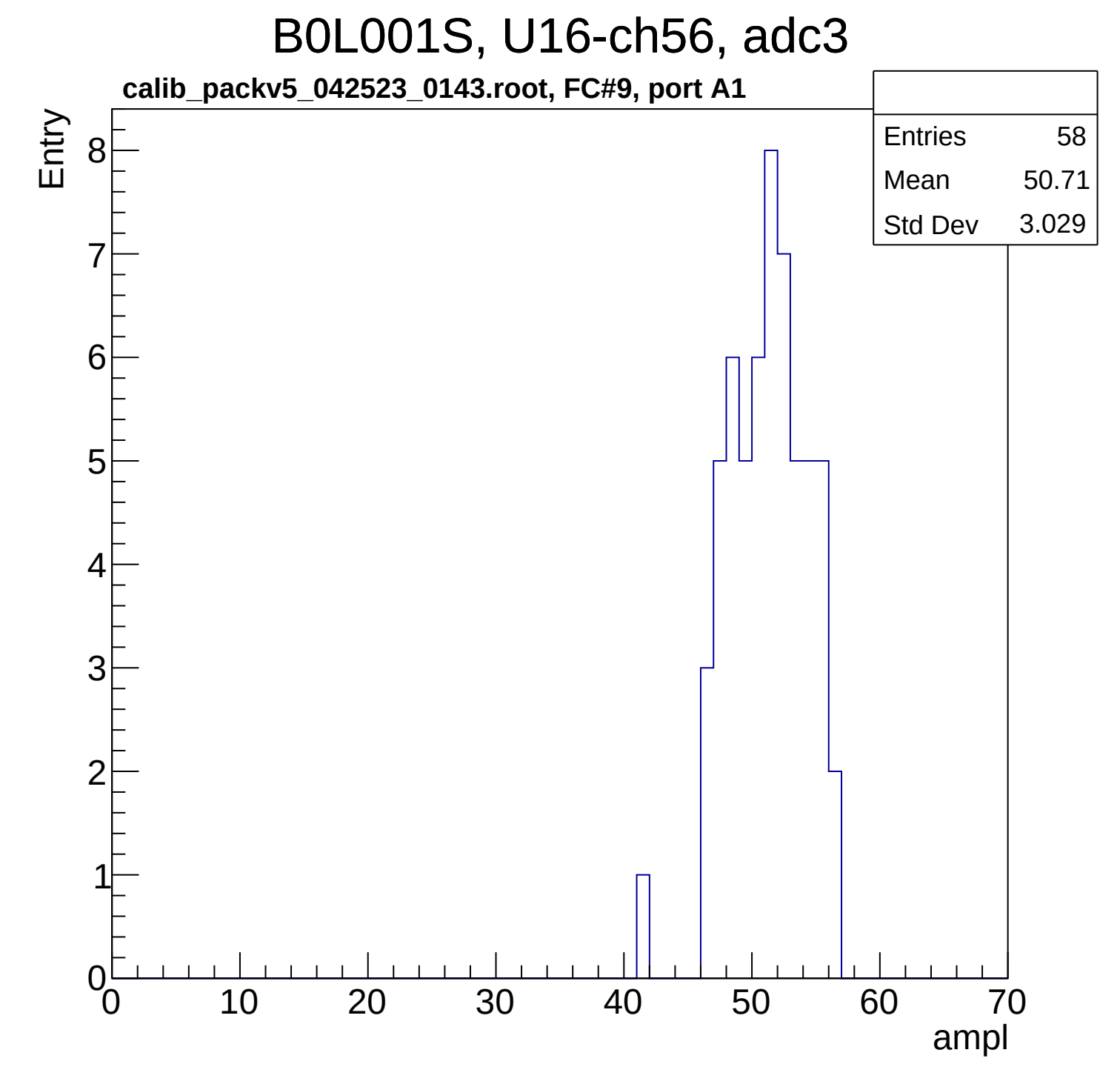

B0L001S, U16-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.15
Std Dev 2.213
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.21
Std Dev 5.048
Gaus mean : 31.6121
Gaus Width: 4.1921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.84
Std Dev 3.721
Gaus mean : 38.4290
Gaus Width: 3.6776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.86
Std Dev 3.616
Gaus mean : 44.6568
Gaus Width: 3.9088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 50.71
Std Dev 3.029
Entry
ampl
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2
3
4
5
6
7
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0
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40
50
60
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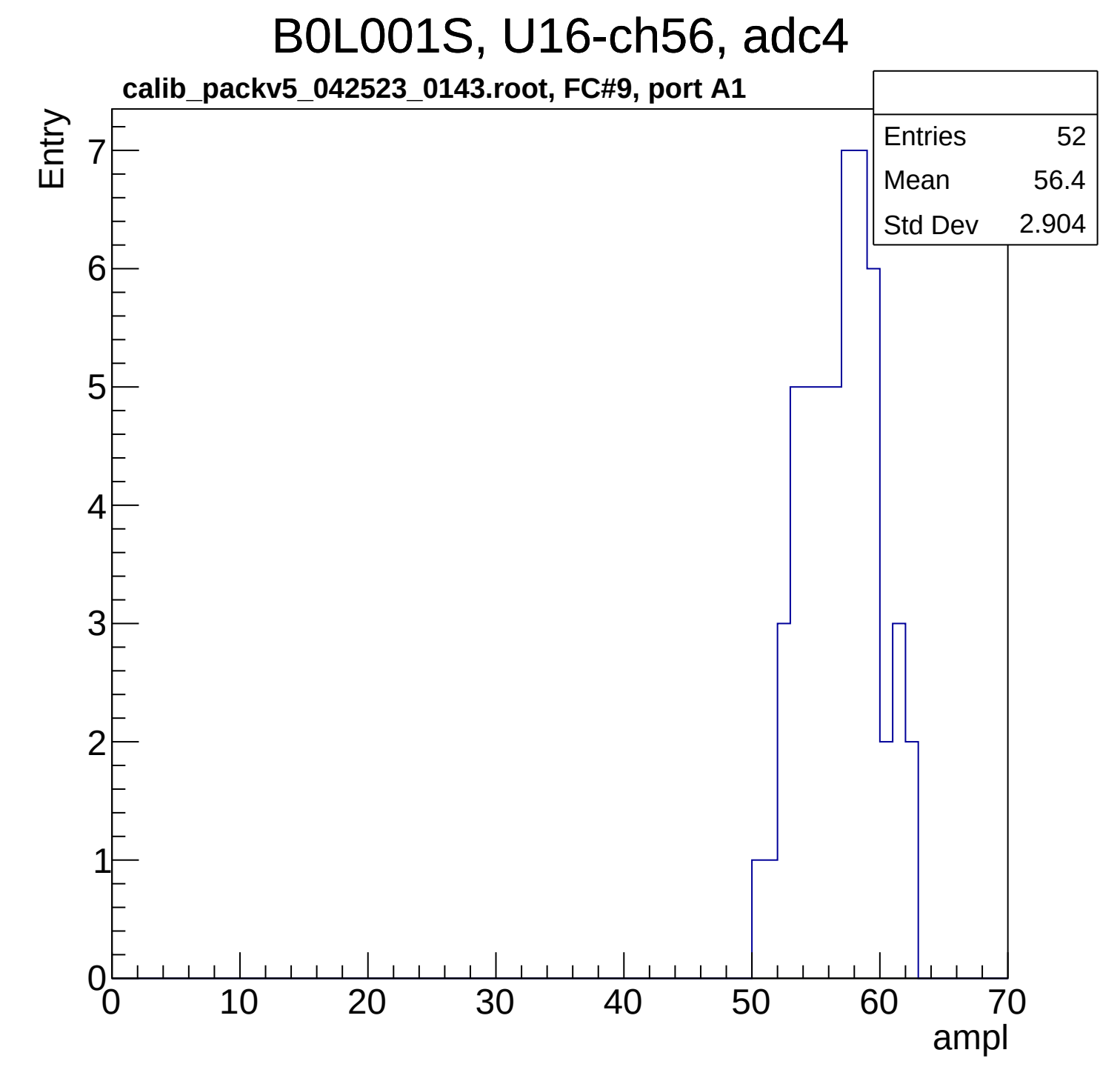
B0L001S, U16-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.4
Std Dev 2.904
Entry
ampl
0
1
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3
4
5
6
7
0
10
20
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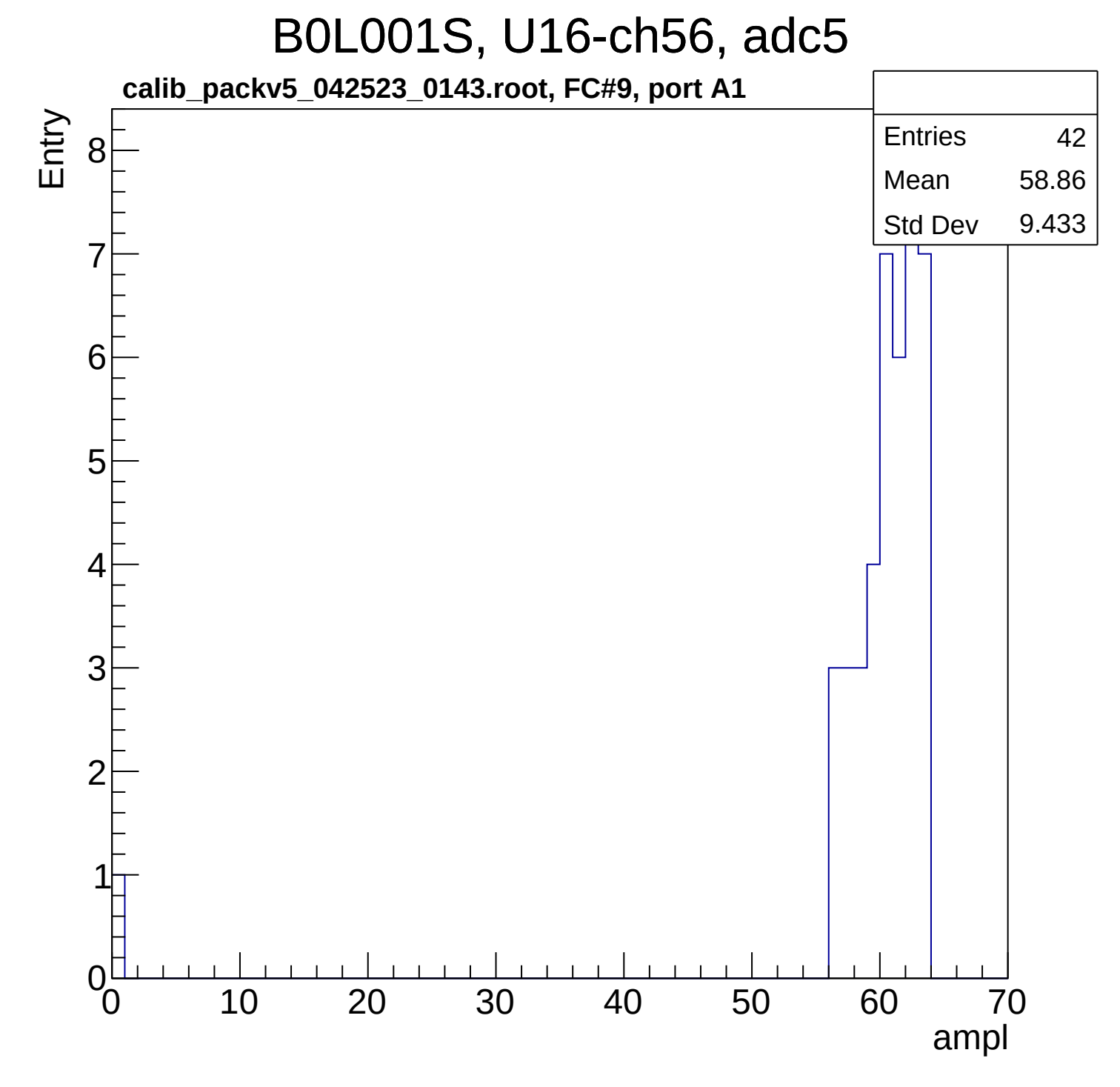
60
70


B0L001S, U16-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.86
Std Dev 9.433
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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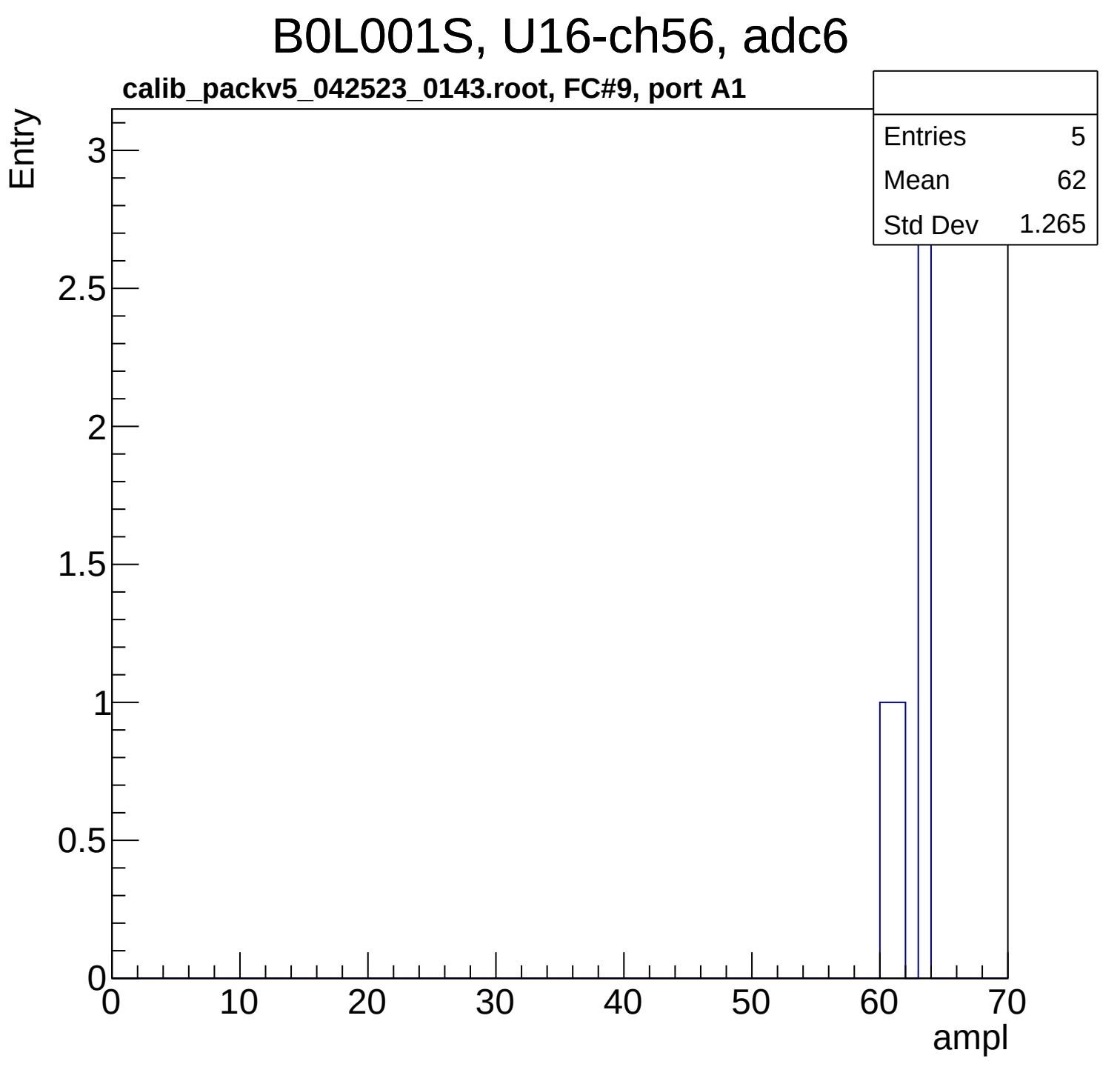
40
50
60
70


B0L001S, U16-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
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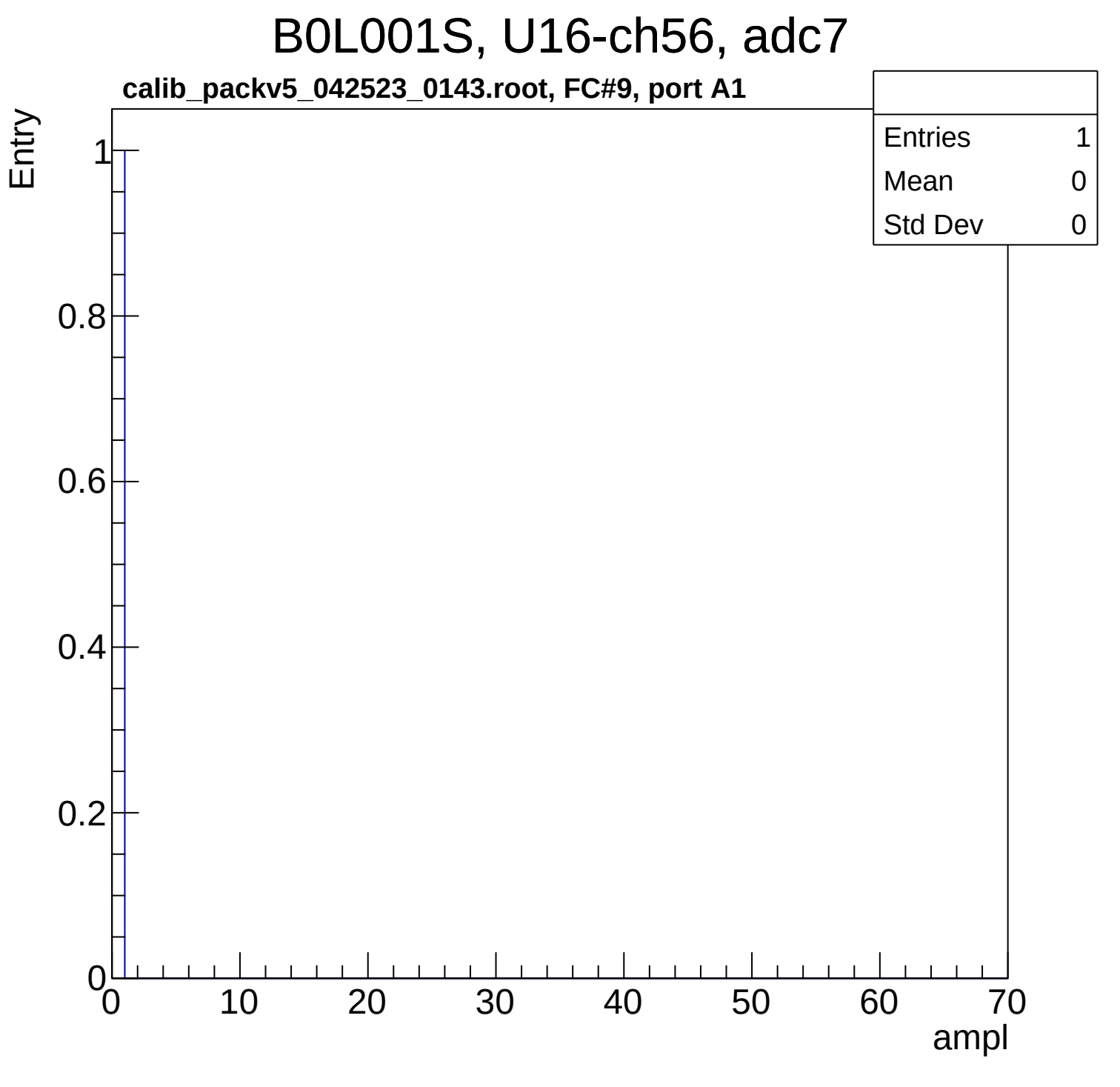
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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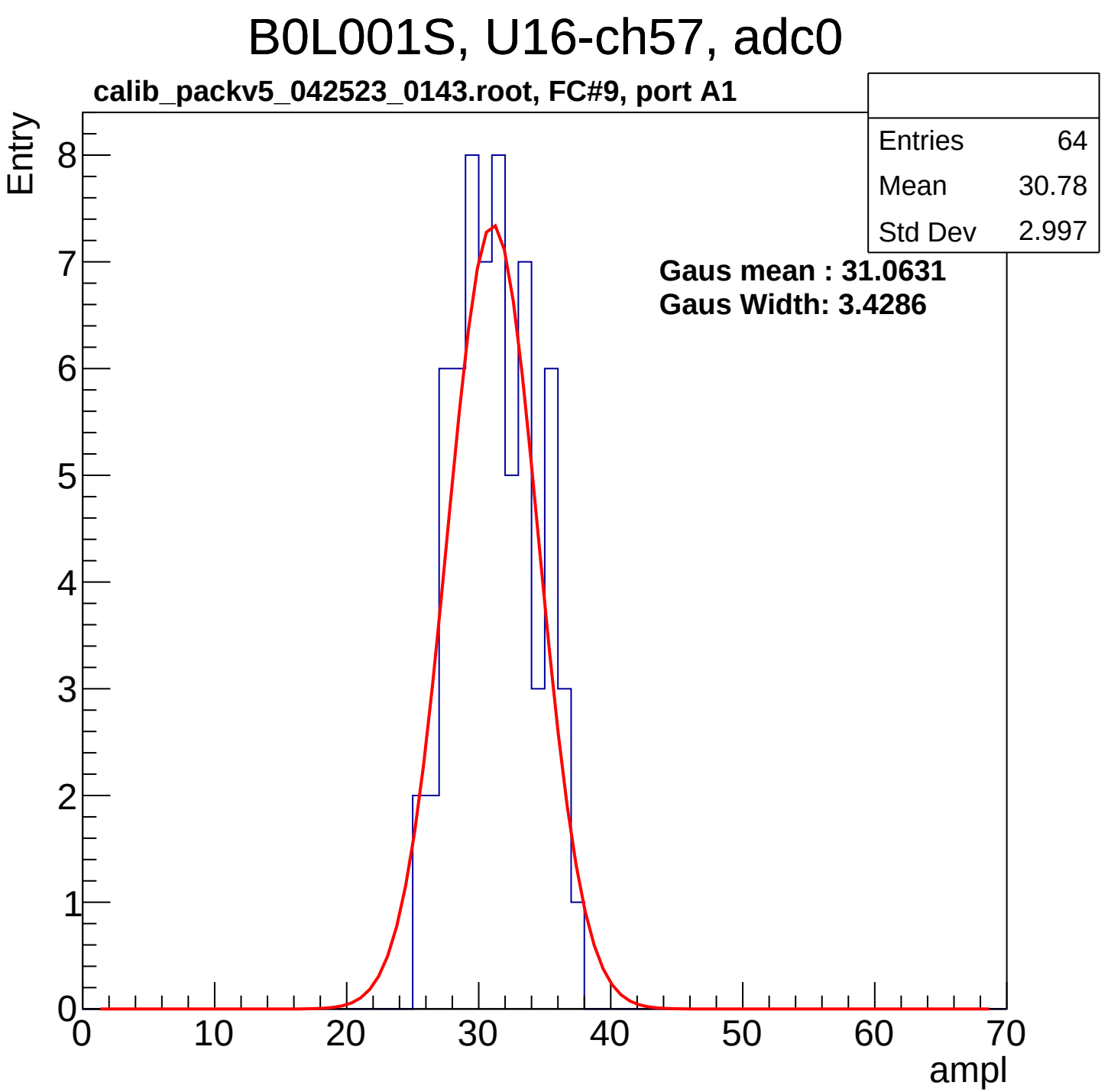
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.78
Std Dev 2.997
Gaus mean : 31.0631
Gaus Width: 3.4286
Entry
ampl
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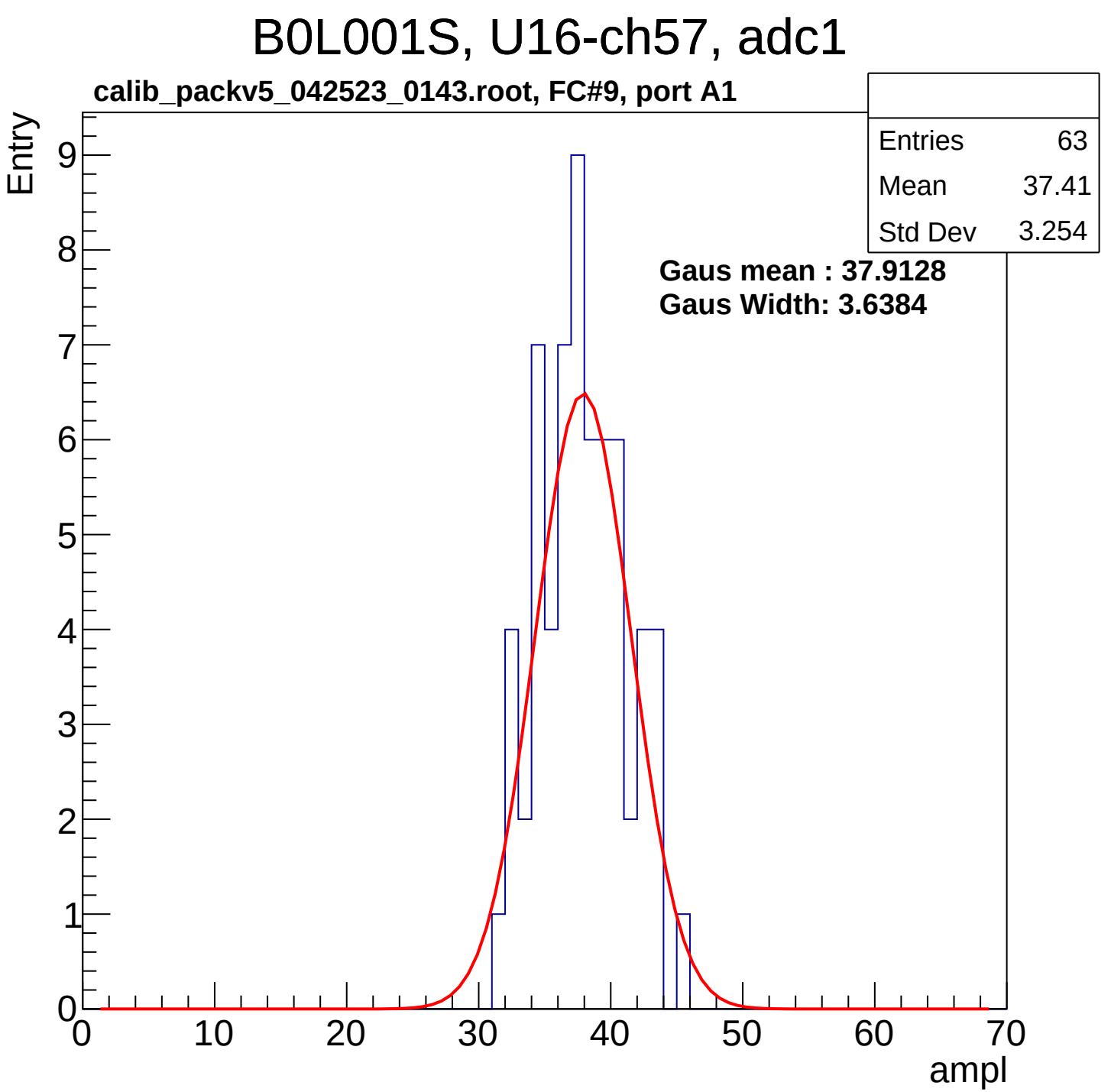
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.41
Std Dev 3.254
Gaus mean : 37.9128
Gaus Width: 3.6384
Entry
ampl
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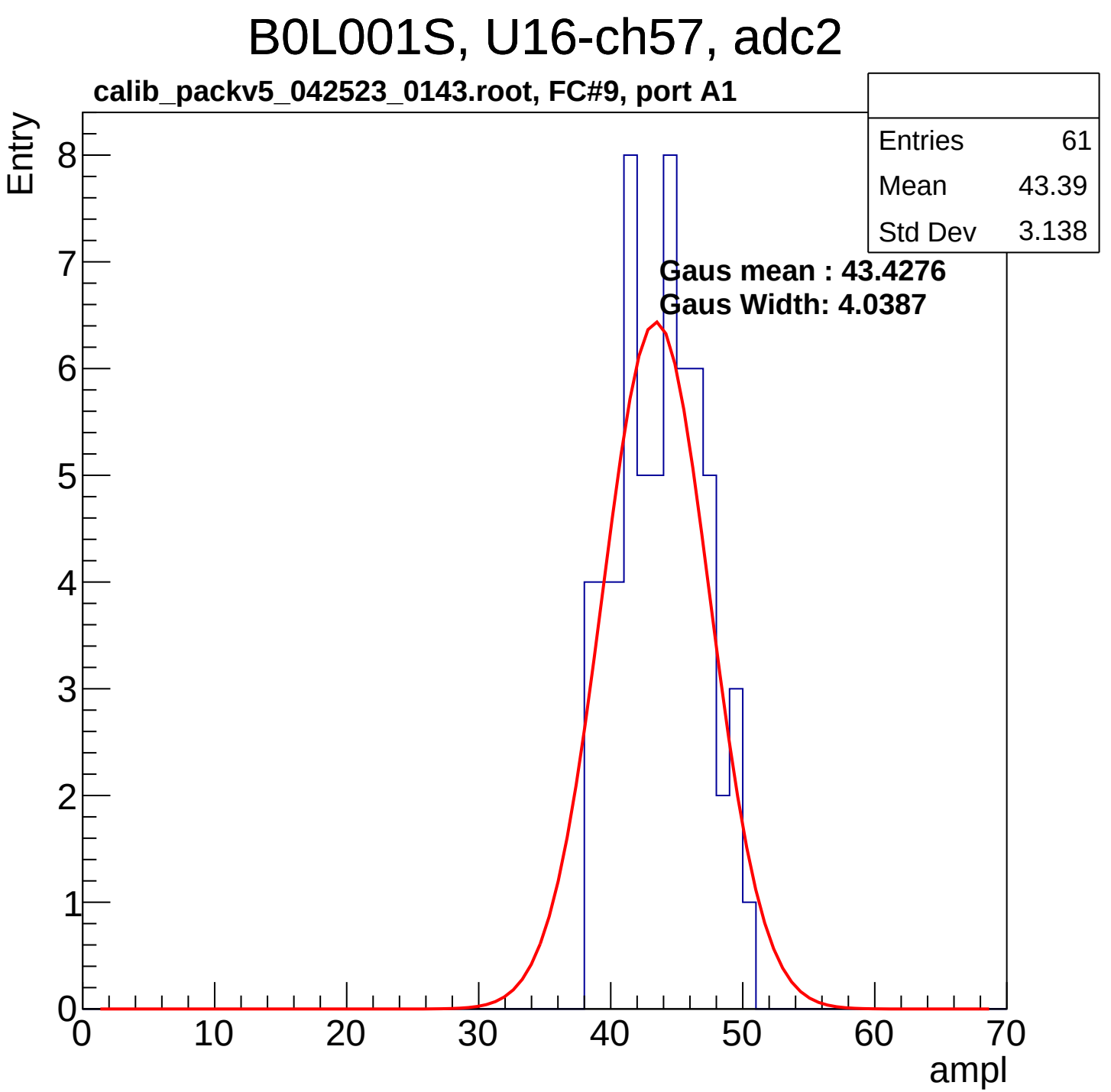
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.39
Std Dev 3.138
Gaus mean : 43.4276
Gaus Width: 4.0387
Entry
ampl
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1
2
3
4
5
6
7
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0
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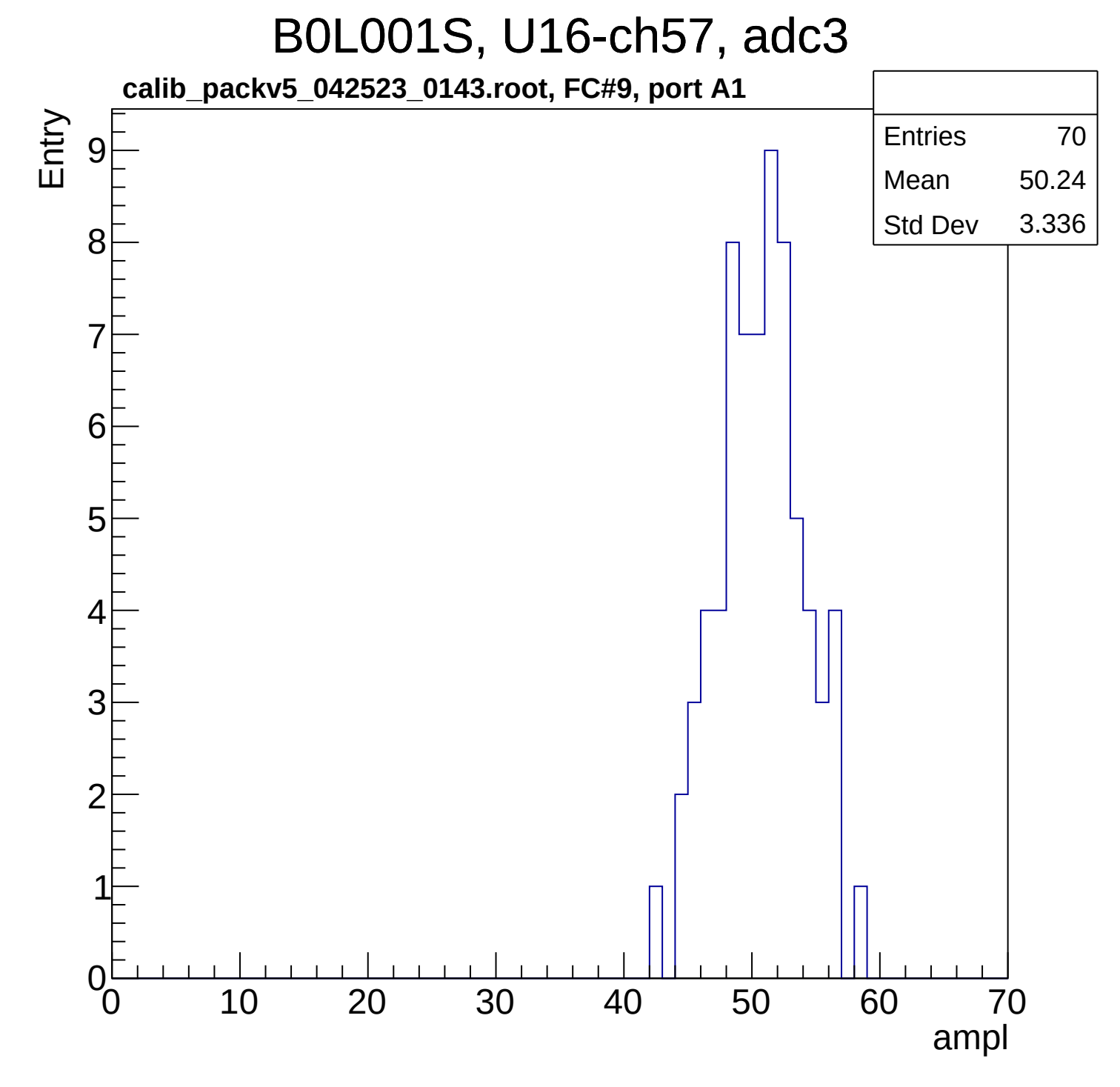
B0L001S, U16-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.24
Std Dev 3.336
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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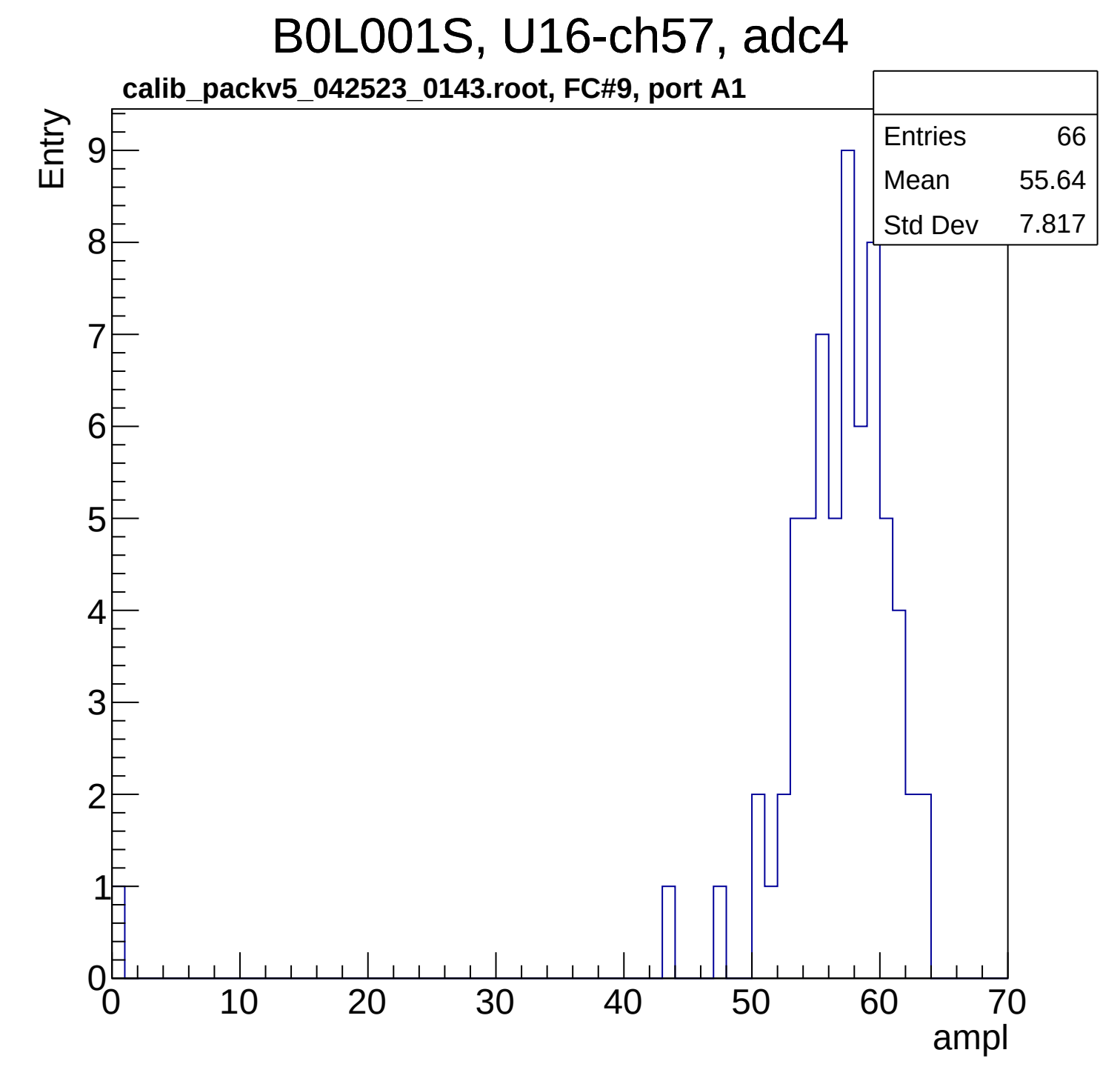
40
50
60
70


B0L001S, U16-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.64
Std Dev 7.817
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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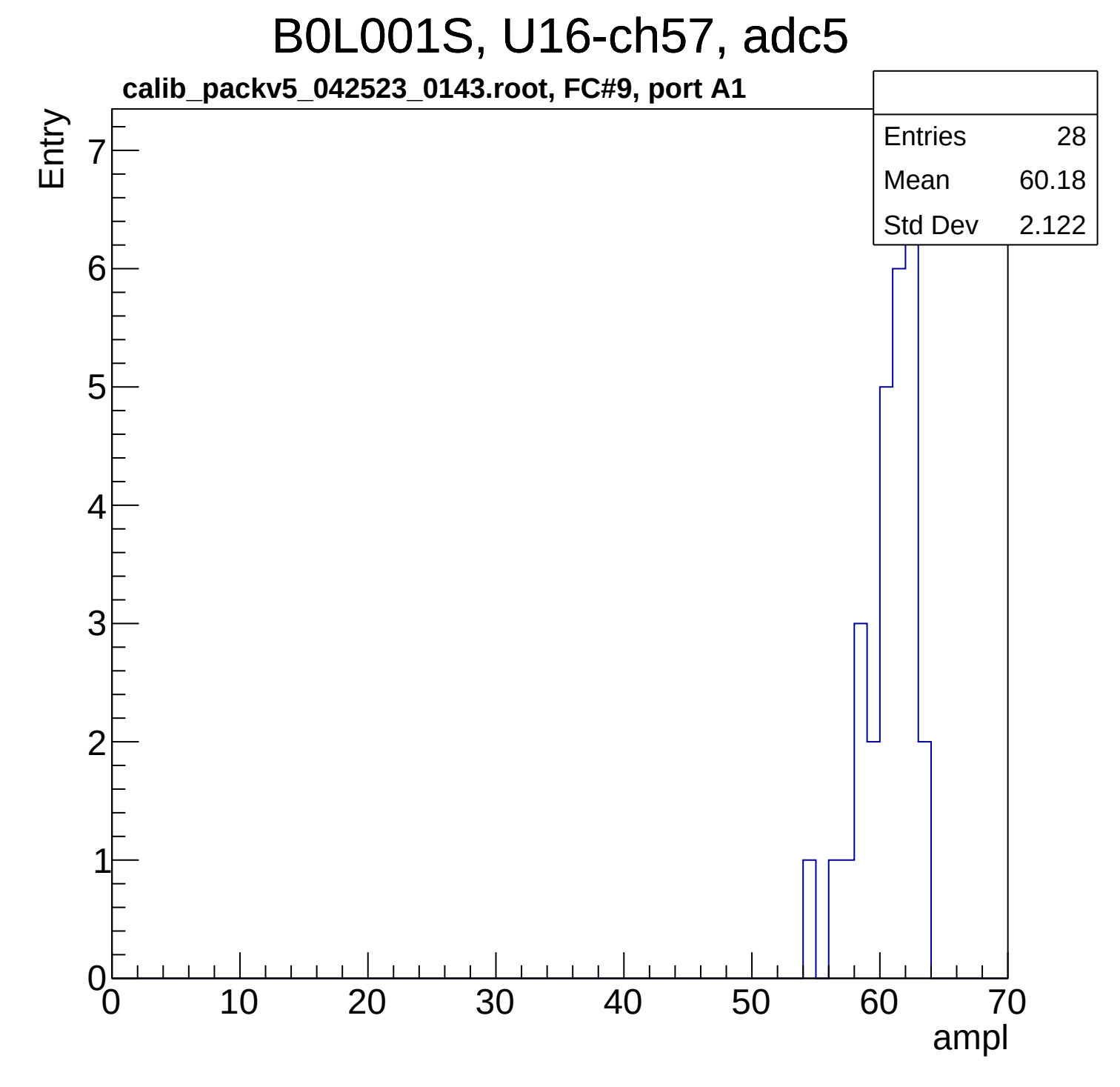
B0L001S, U16-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.18
Std Dev 2.122
Entry
0
1
2
3
4
5
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7
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10
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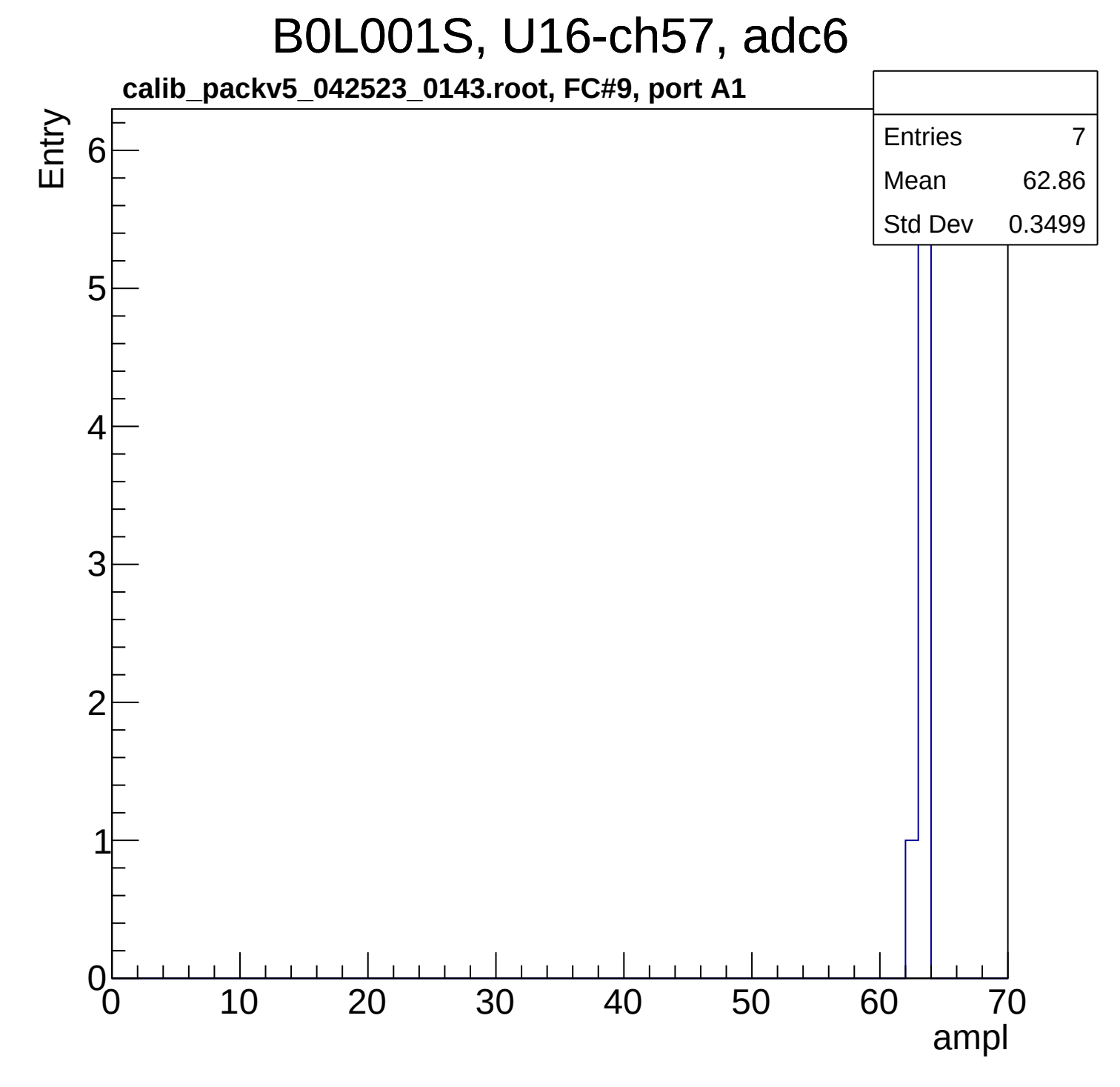
50
60
70
ampl


B0L001S, U16-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.86
Std Dev 0.3499
Entry
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1
2
3
4
5
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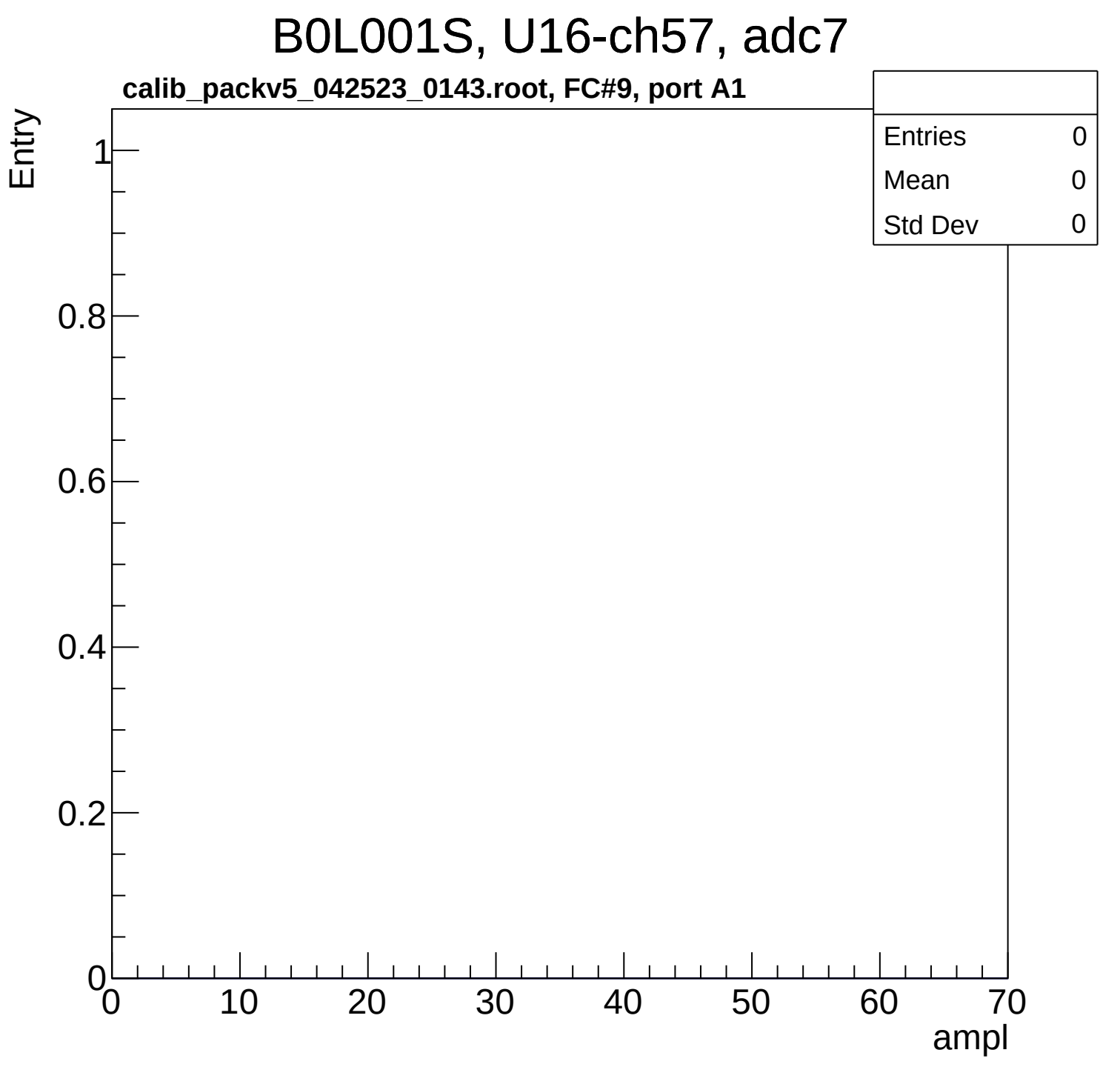
70
ampl


B0L001S, U16-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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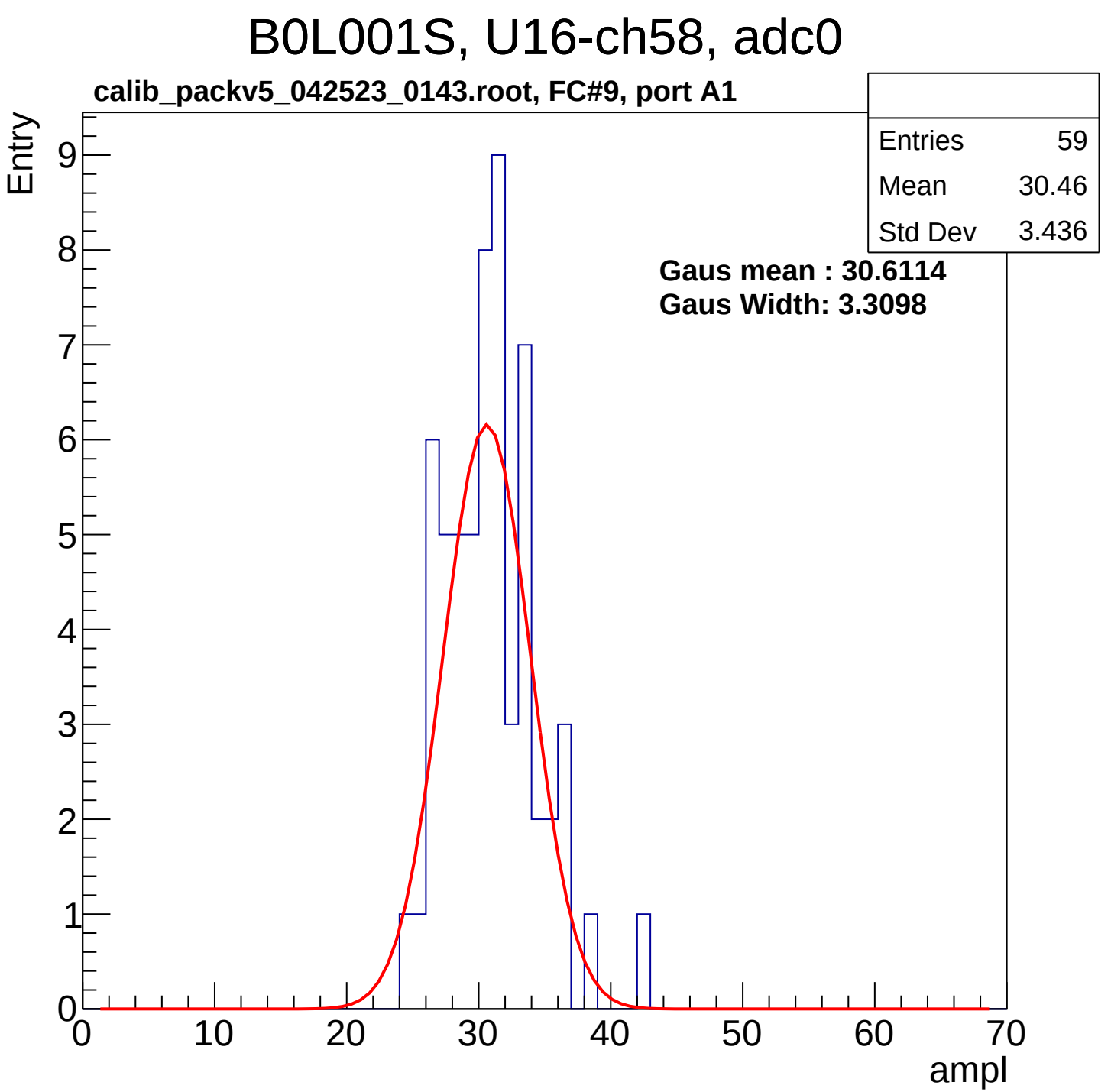
50
60
70
ampl


B0L001S, U16-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.46
Std Dev 3.436
Gaus mean : 30.6114
Gaus Width: 3.3098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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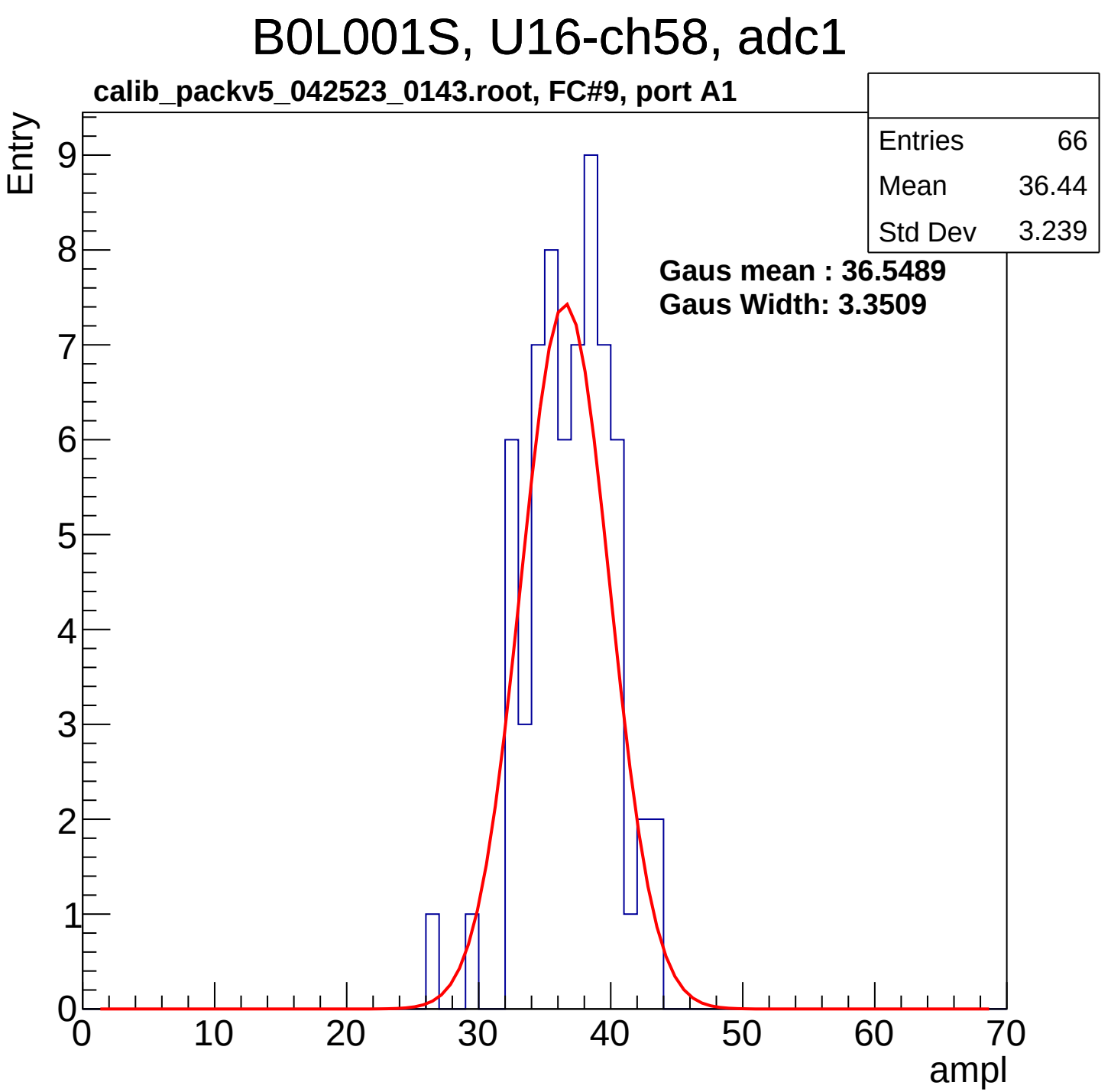
B0L001S, U16-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.44
Std Dev 3.239
Gaus mean : 36.5489
Gaus Width: 3.3509
Entry
ampl
0
10
20
30
40
50
60
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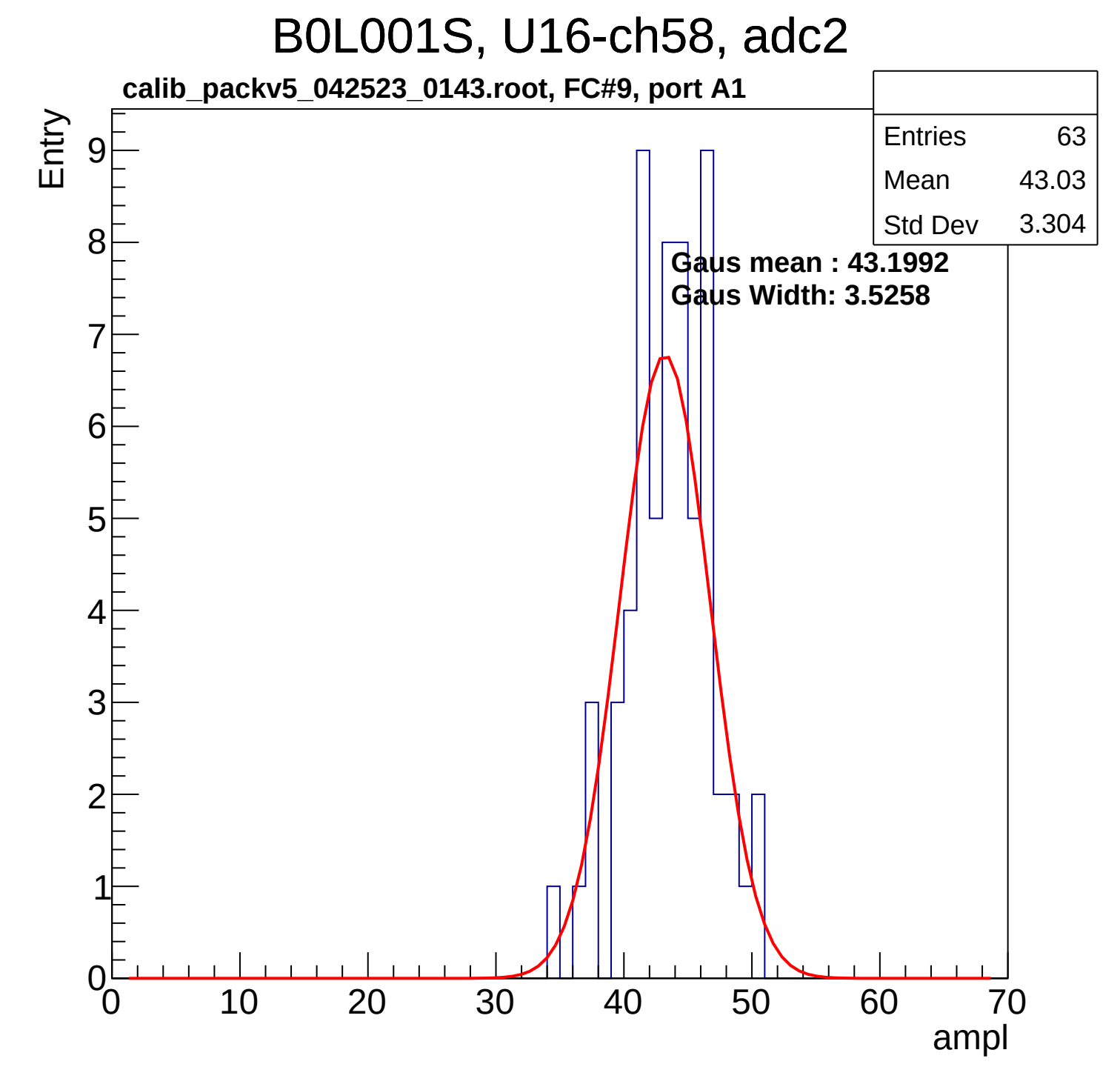
1
2
3
4
5
6
7
8
9


B0L001S, U16-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.03
Std Dev 3.304
Gaus mean : 43.1992
Gaus Width: 3.5258
Entry
ampl
0
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2
3
4
5
6
7
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9
0
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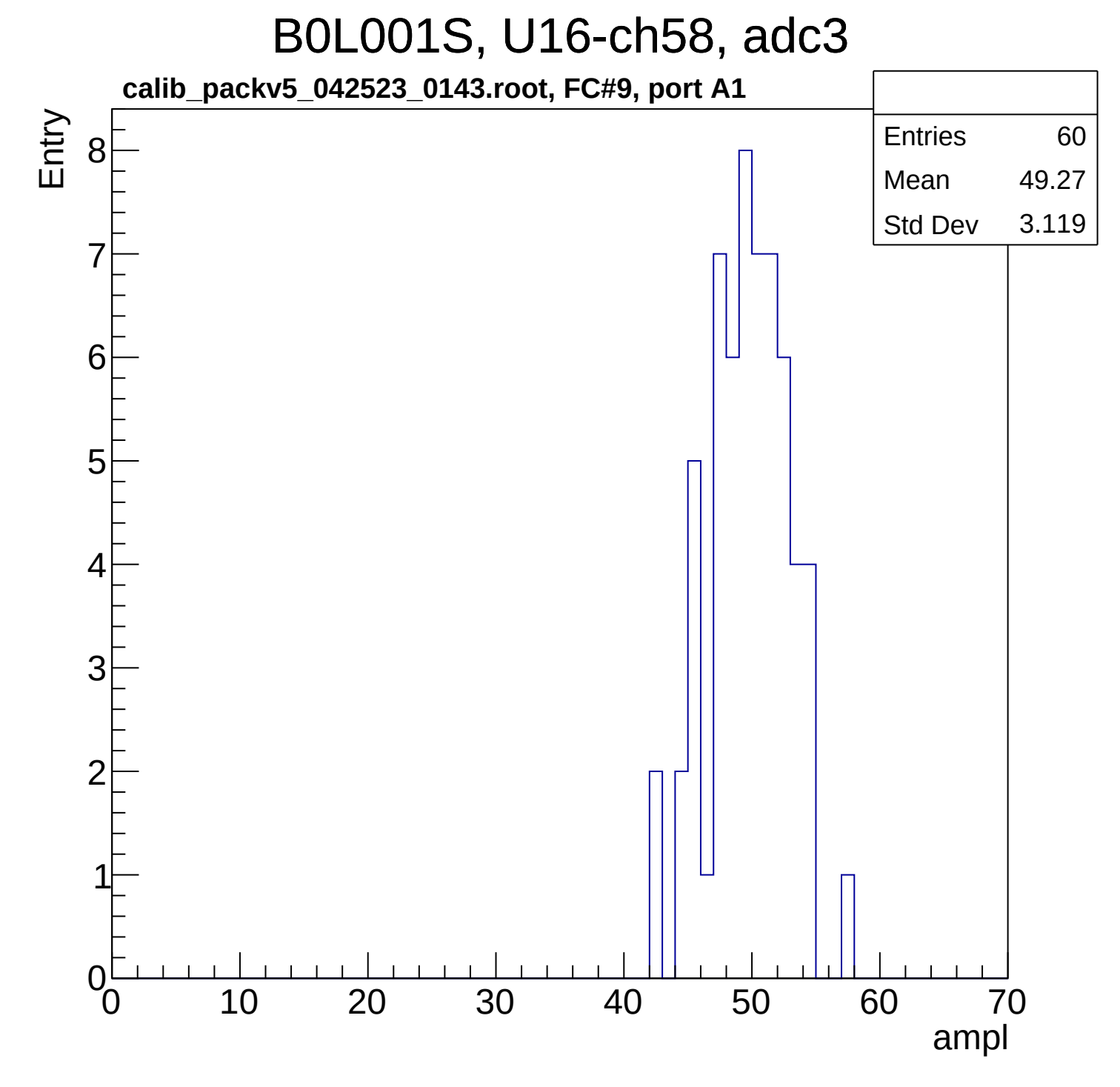
B0L001S, U16-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 49.27
Std Dev 3.119
Entry
ampl
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1
2
3
4
5
6
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60
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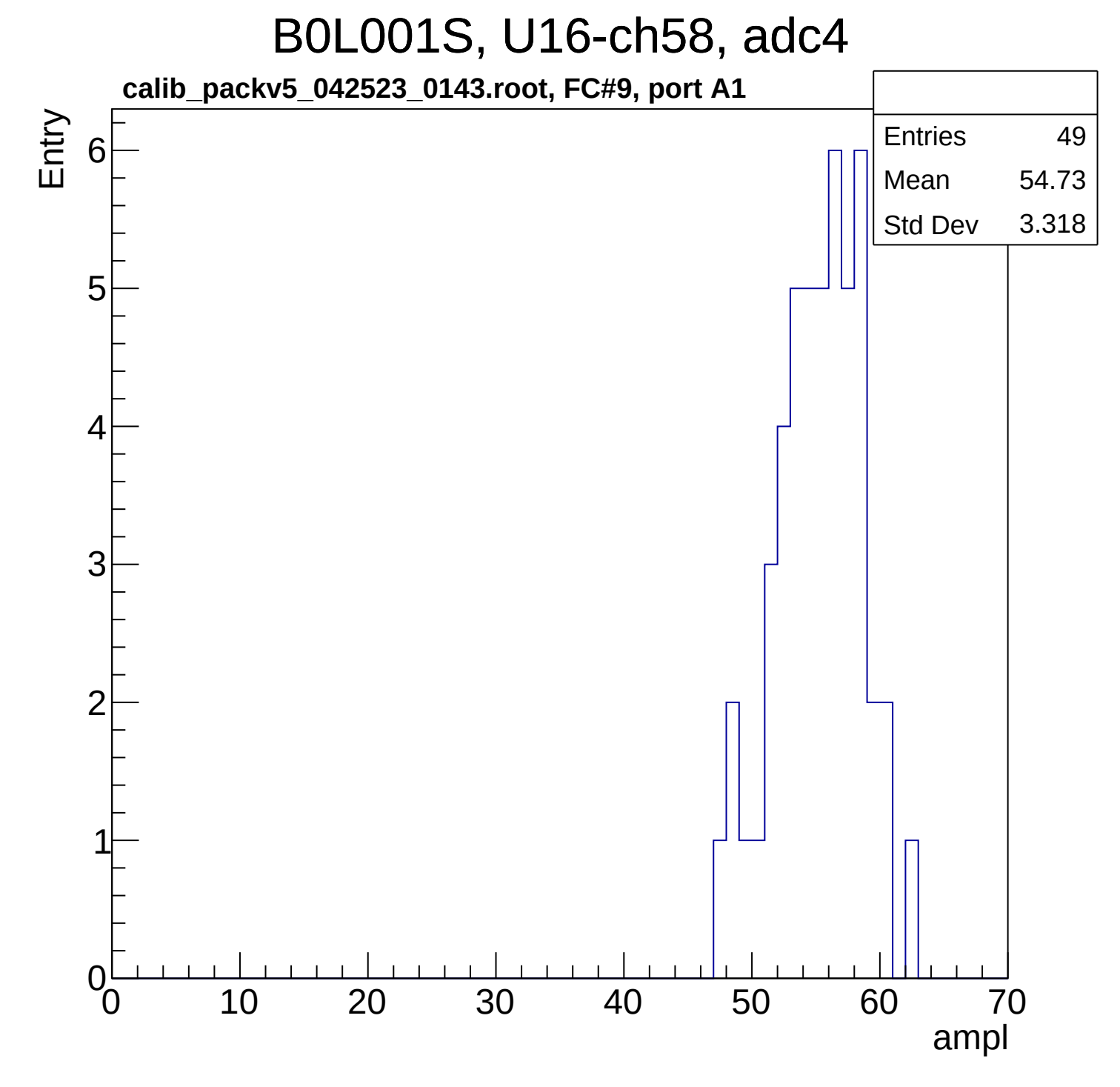
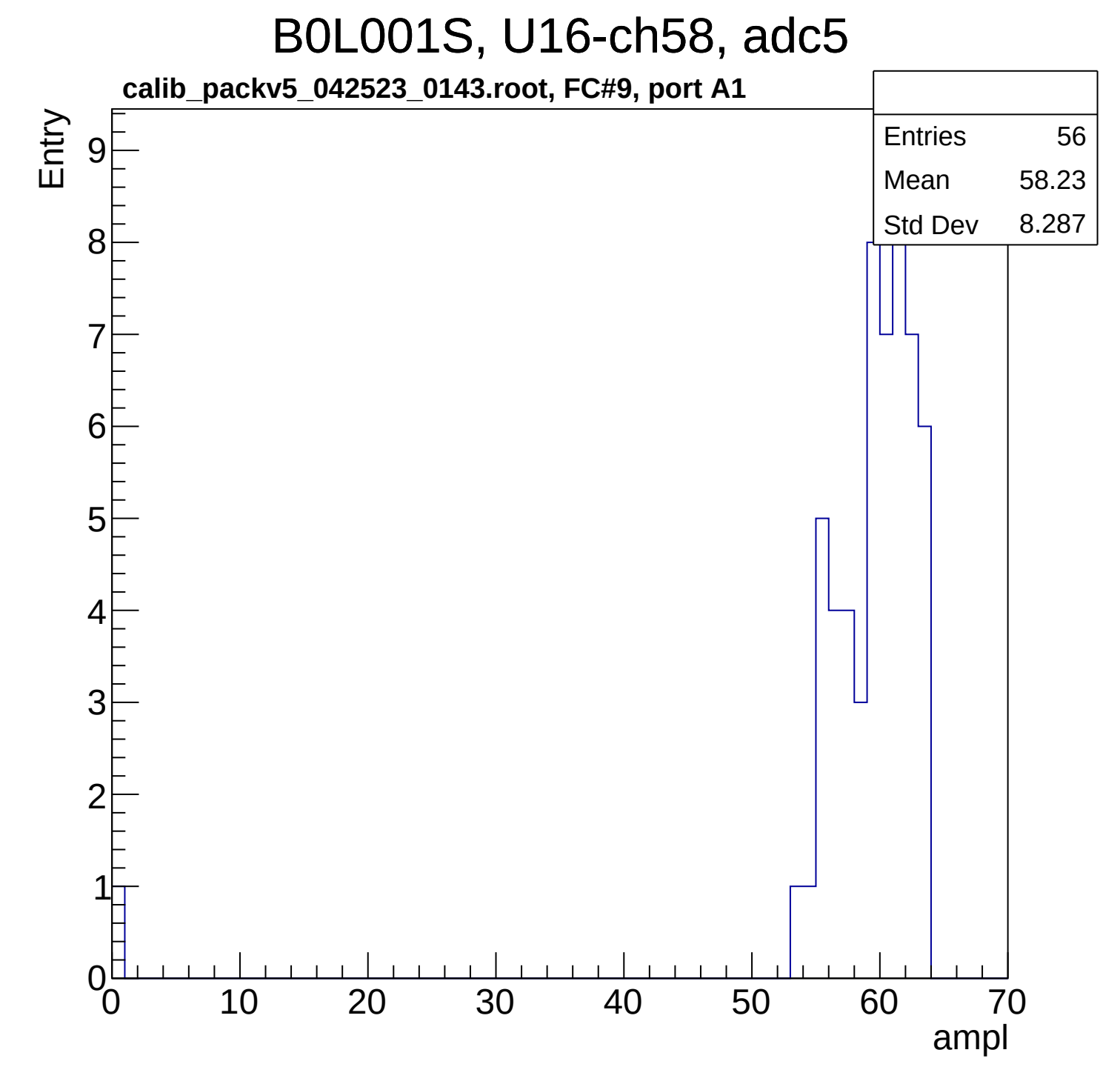

B0L001S, U16-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 54.73
Std Dev 3.318
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.23
Std Dev 8.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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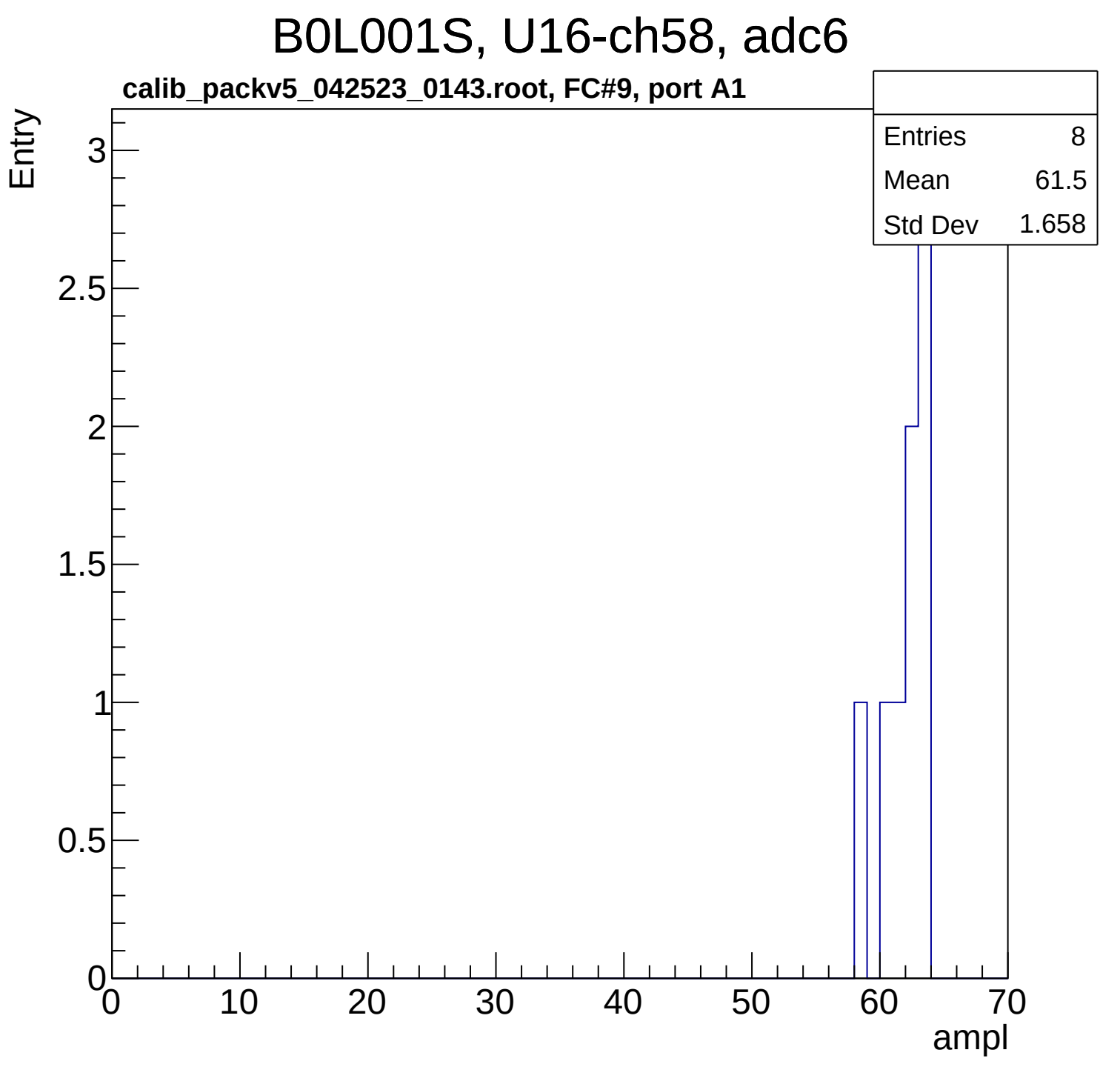
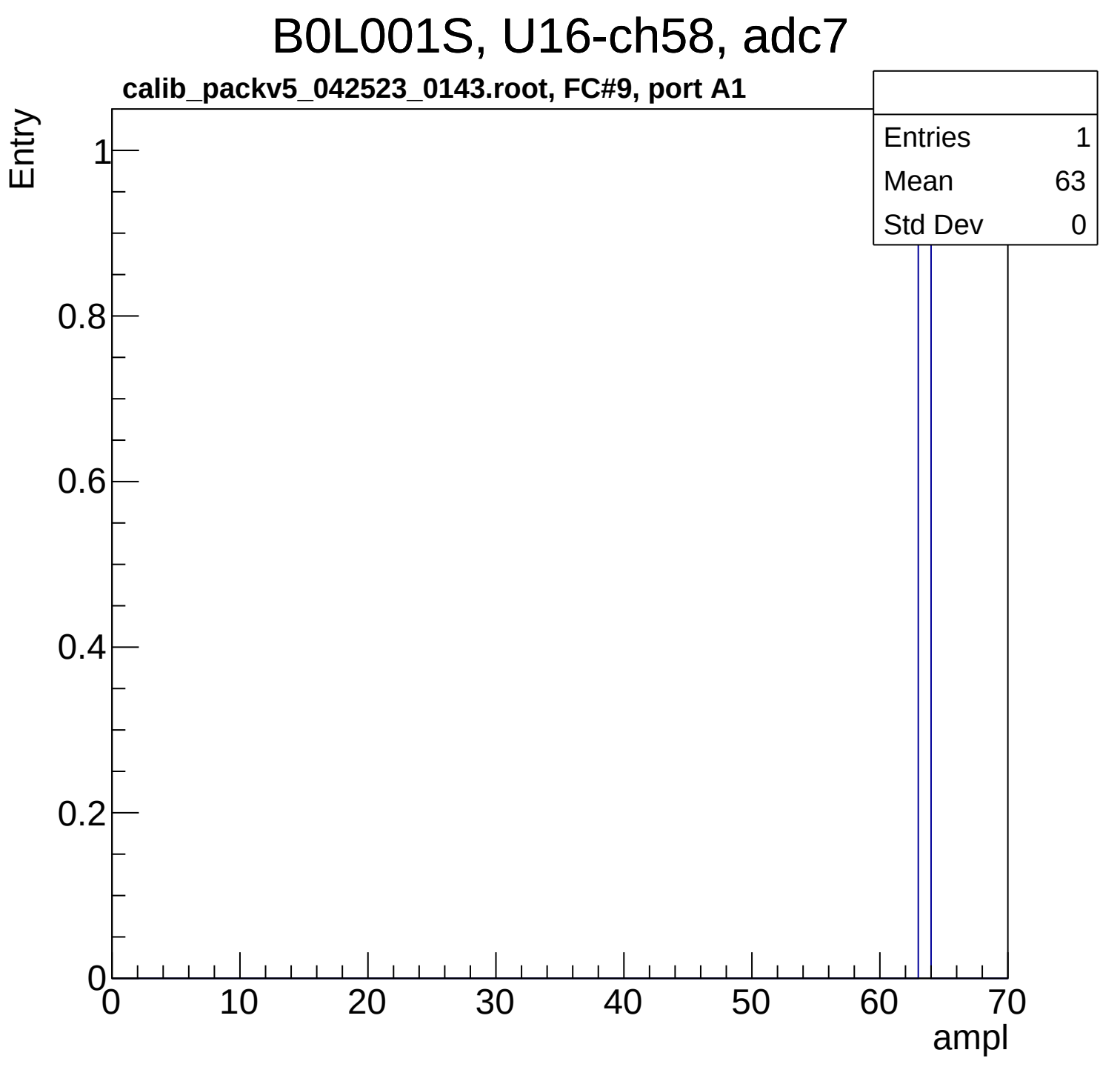
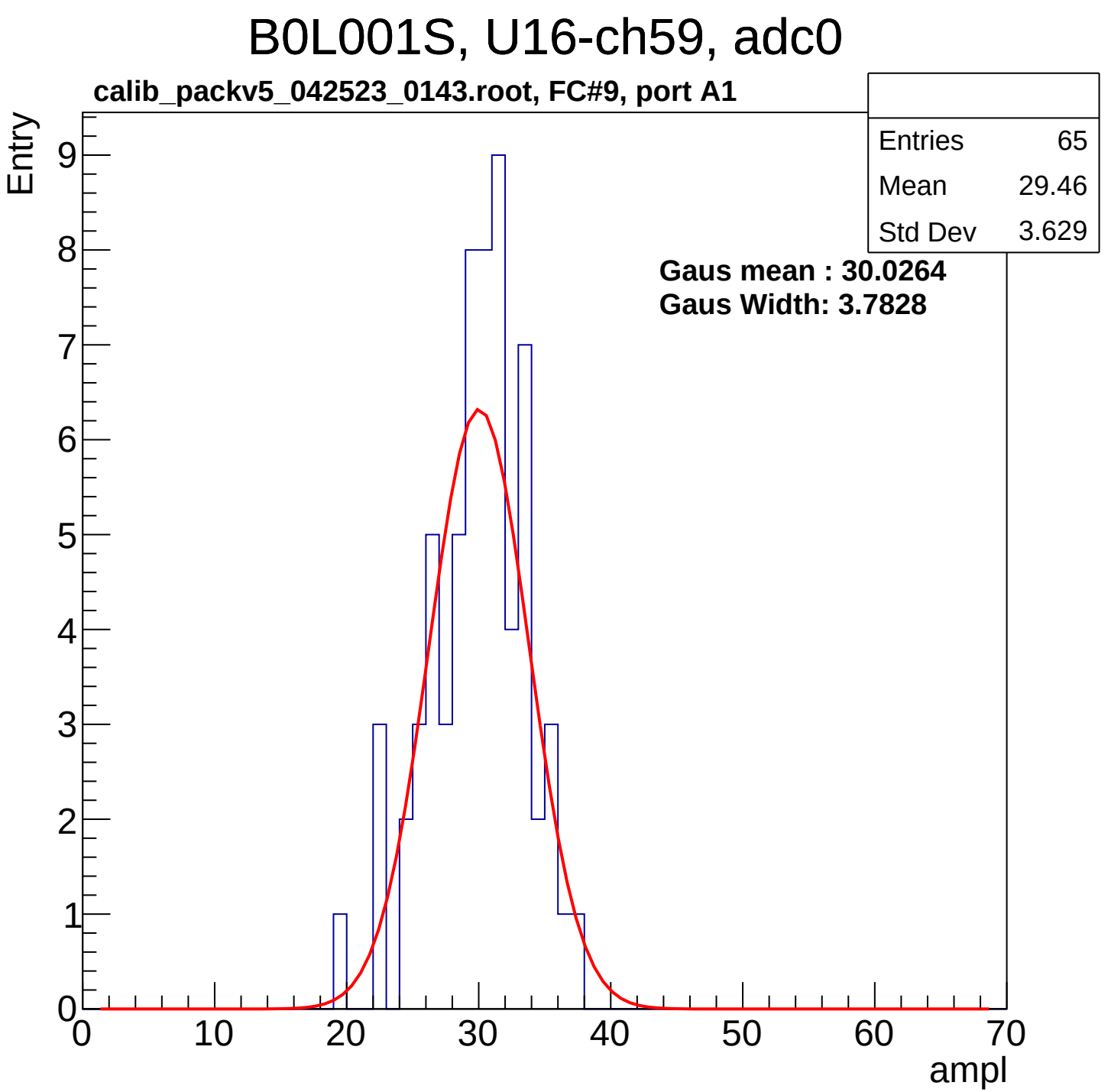
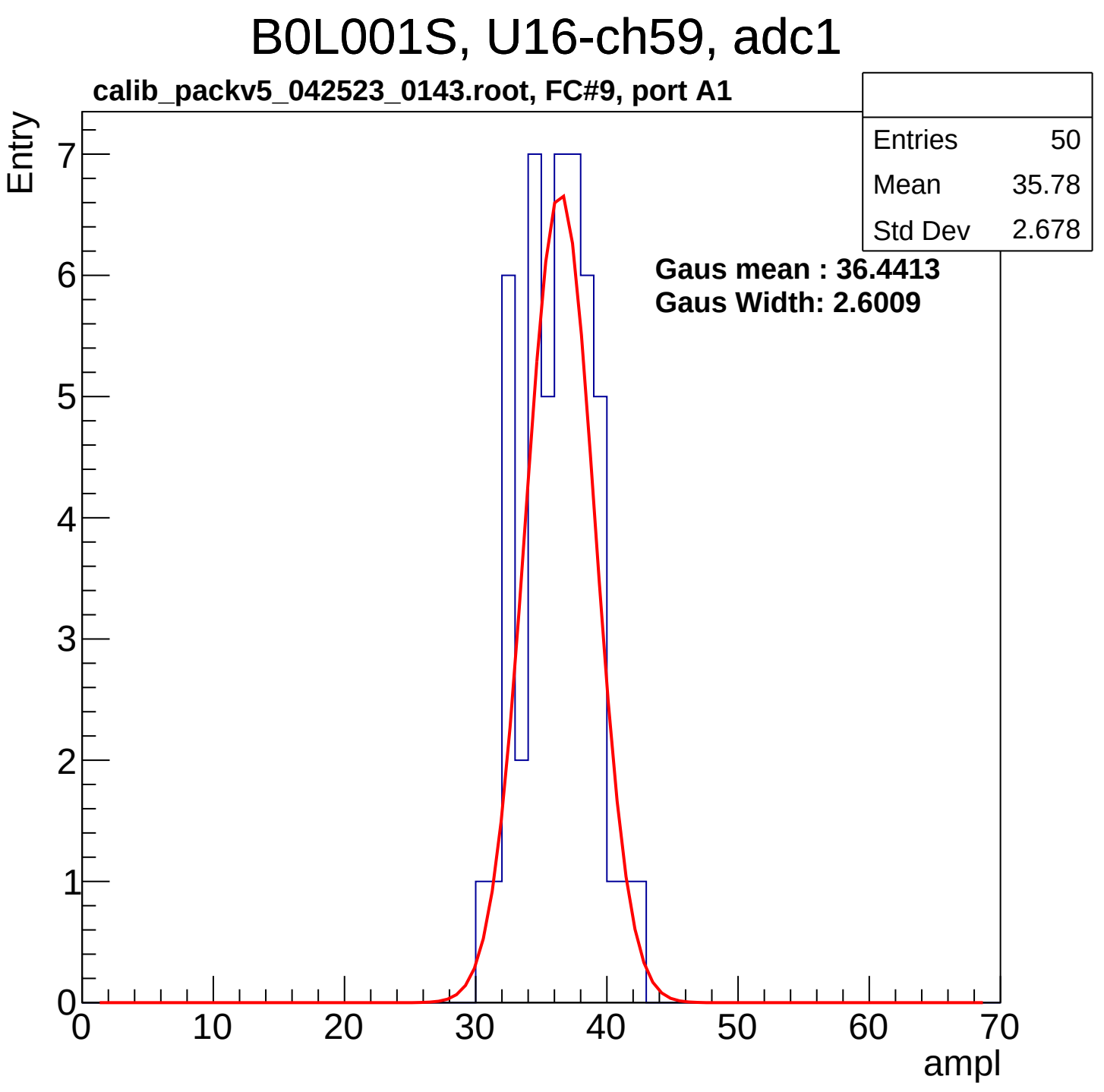
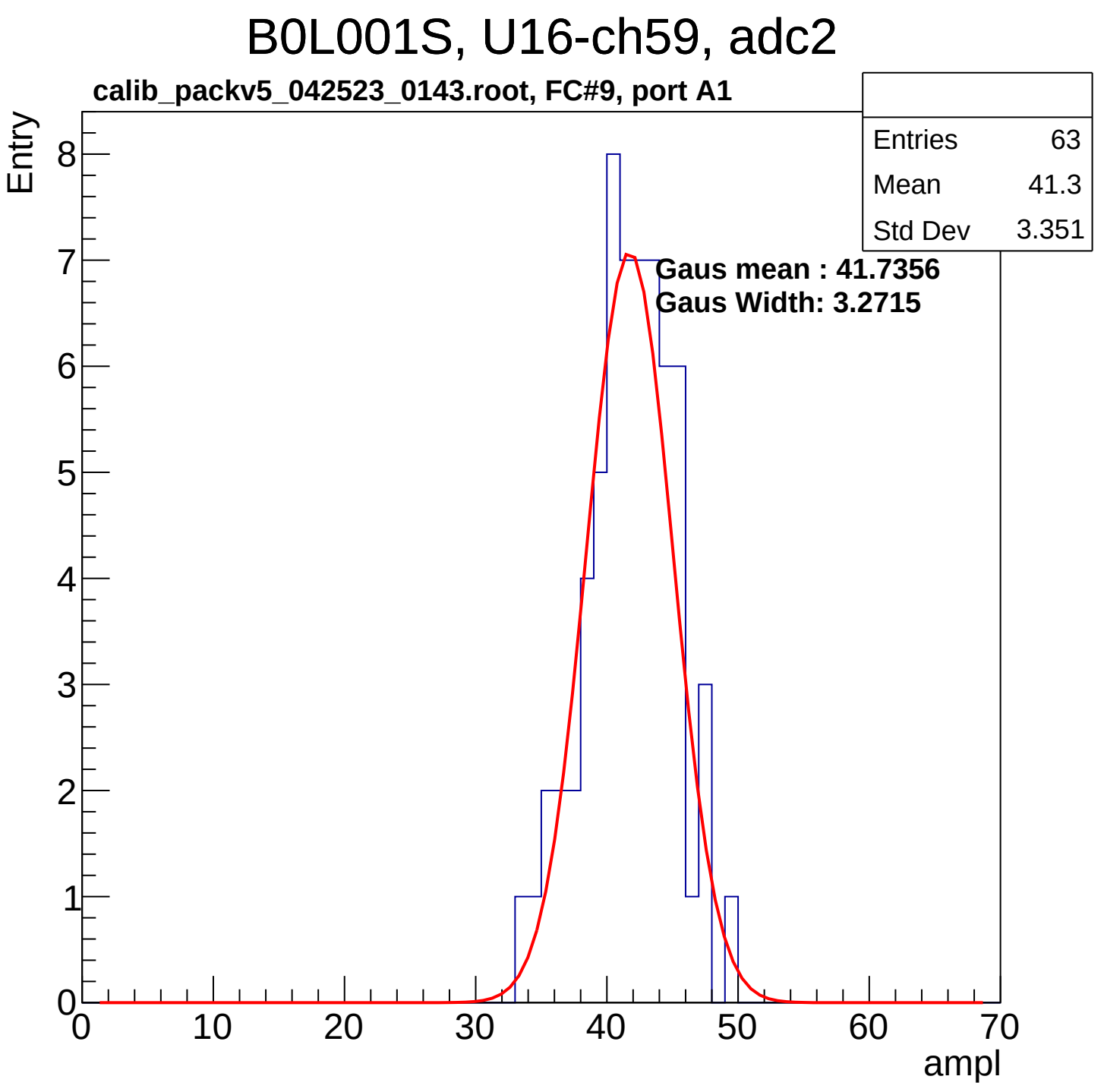
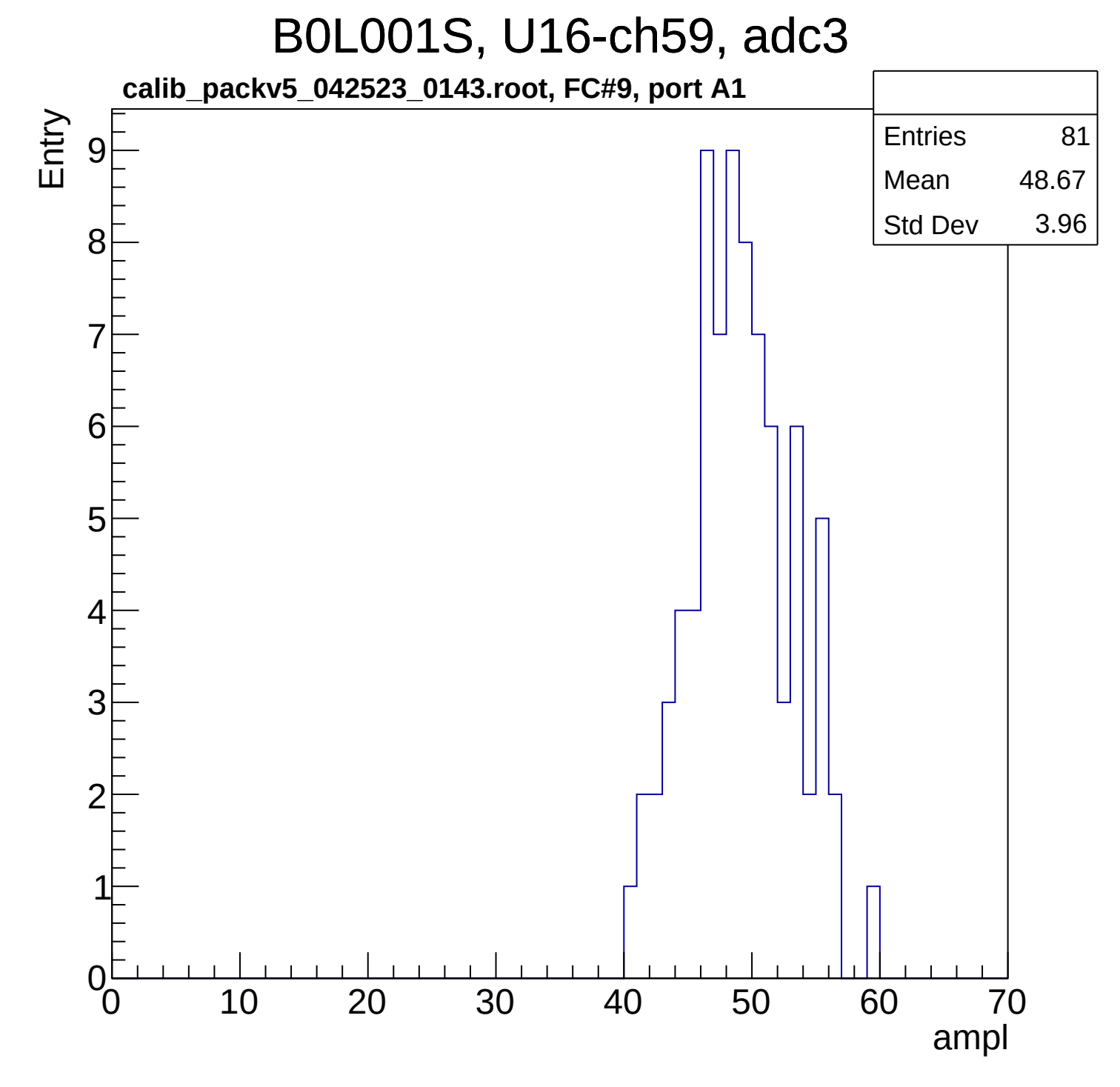

B0L001S, U16-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.46
Std Dev 3.629
Gaus mean : 30.0264
Gaus Width: 3.7828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 35.78
Std Dev 2.678
Gaus mean : 36.4413
Gaus Width: 2.6009
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 41.3
Std Dev 3.351
Gaus mean : 41.7356
Gaus Width: 3.2715
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 48.67
Std Dev 3.96
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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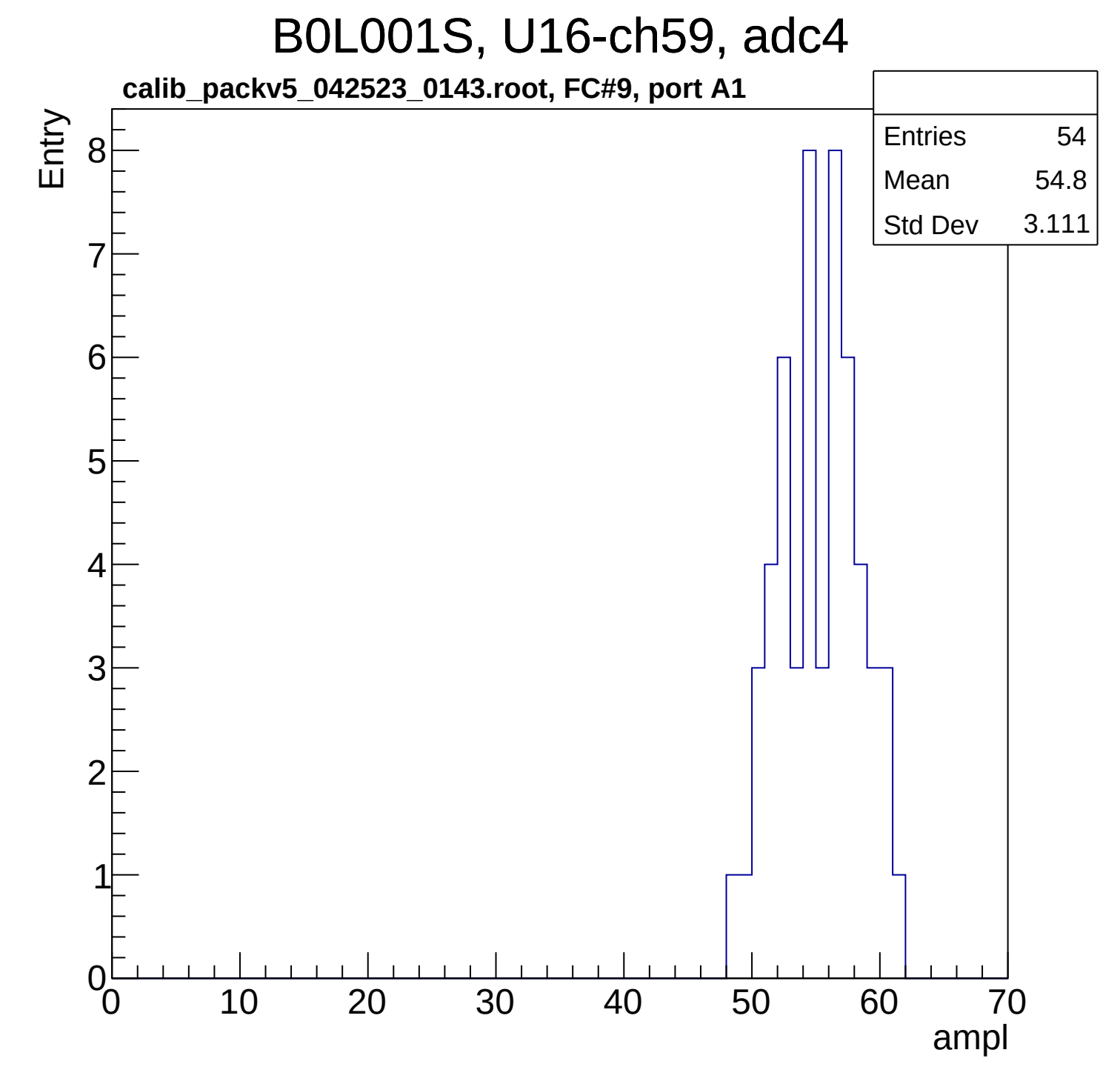
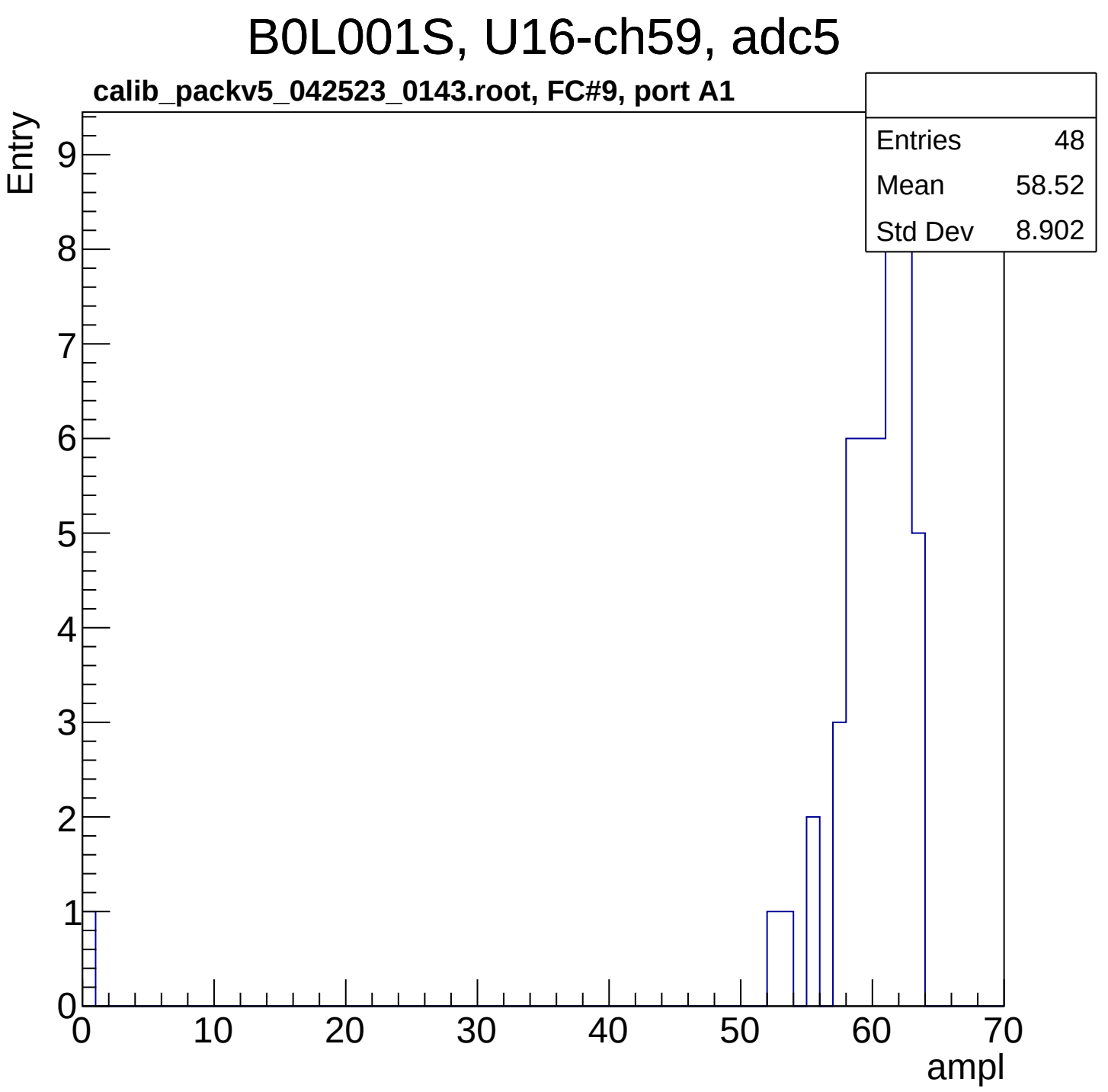
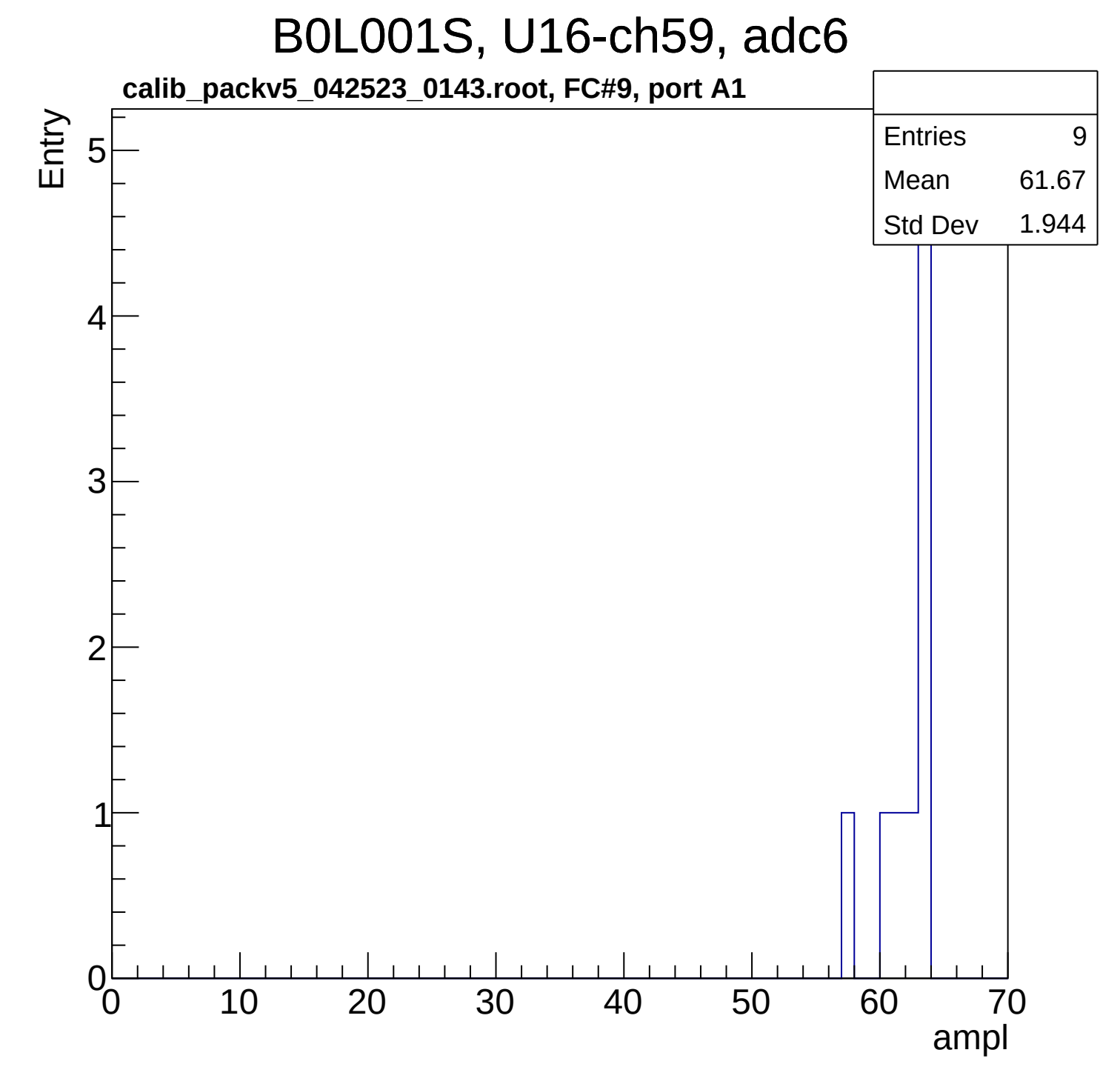
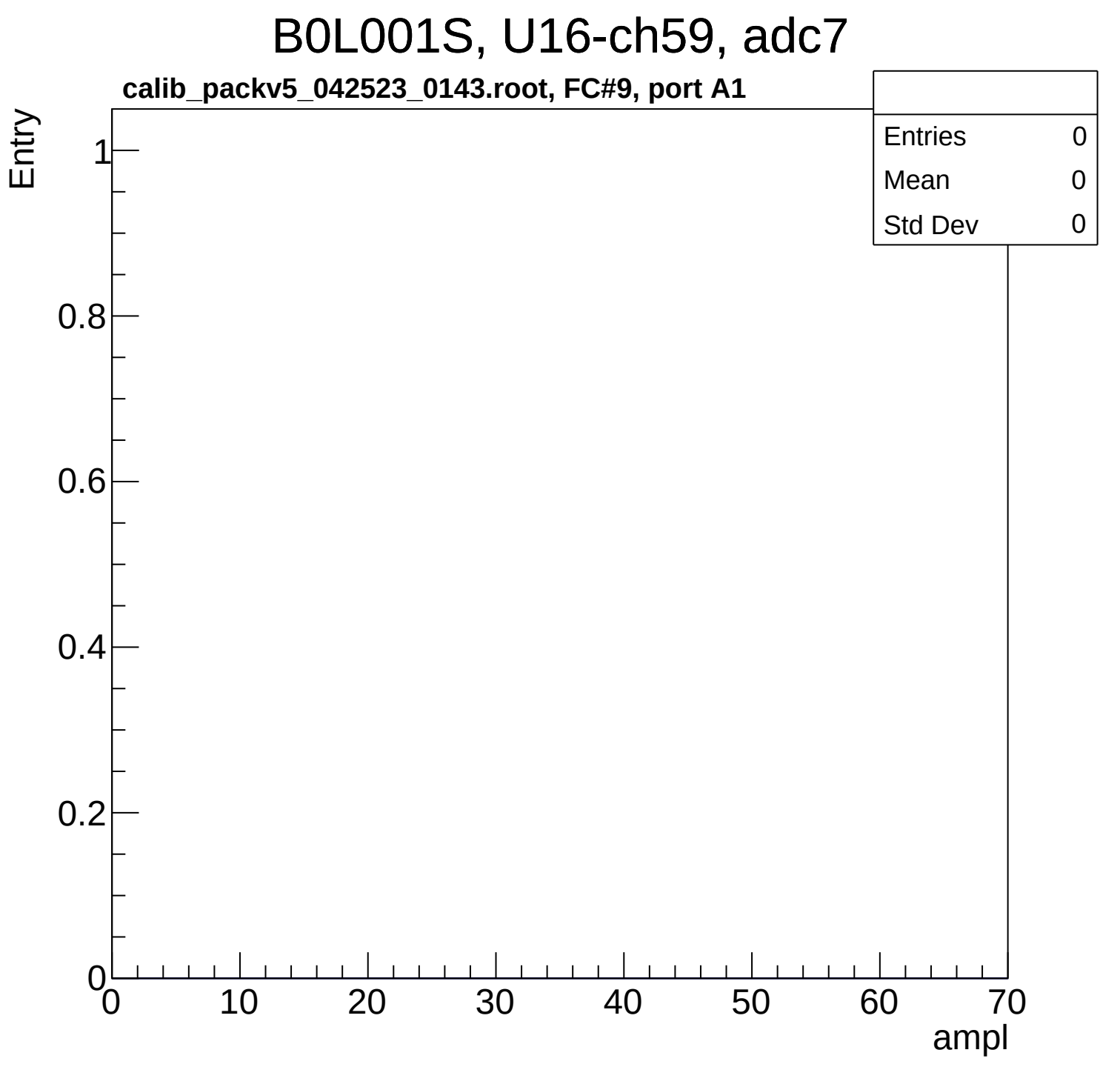
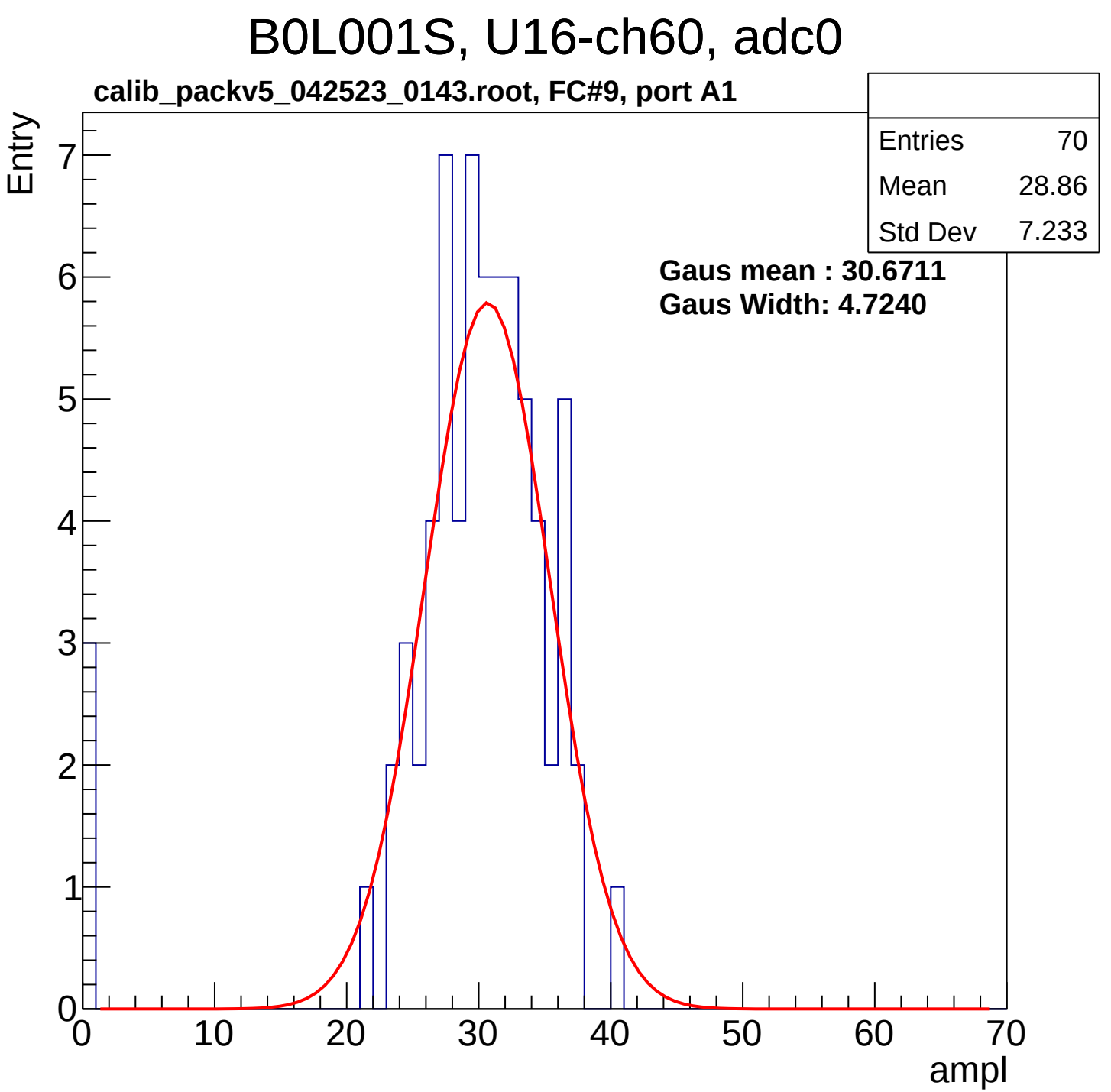
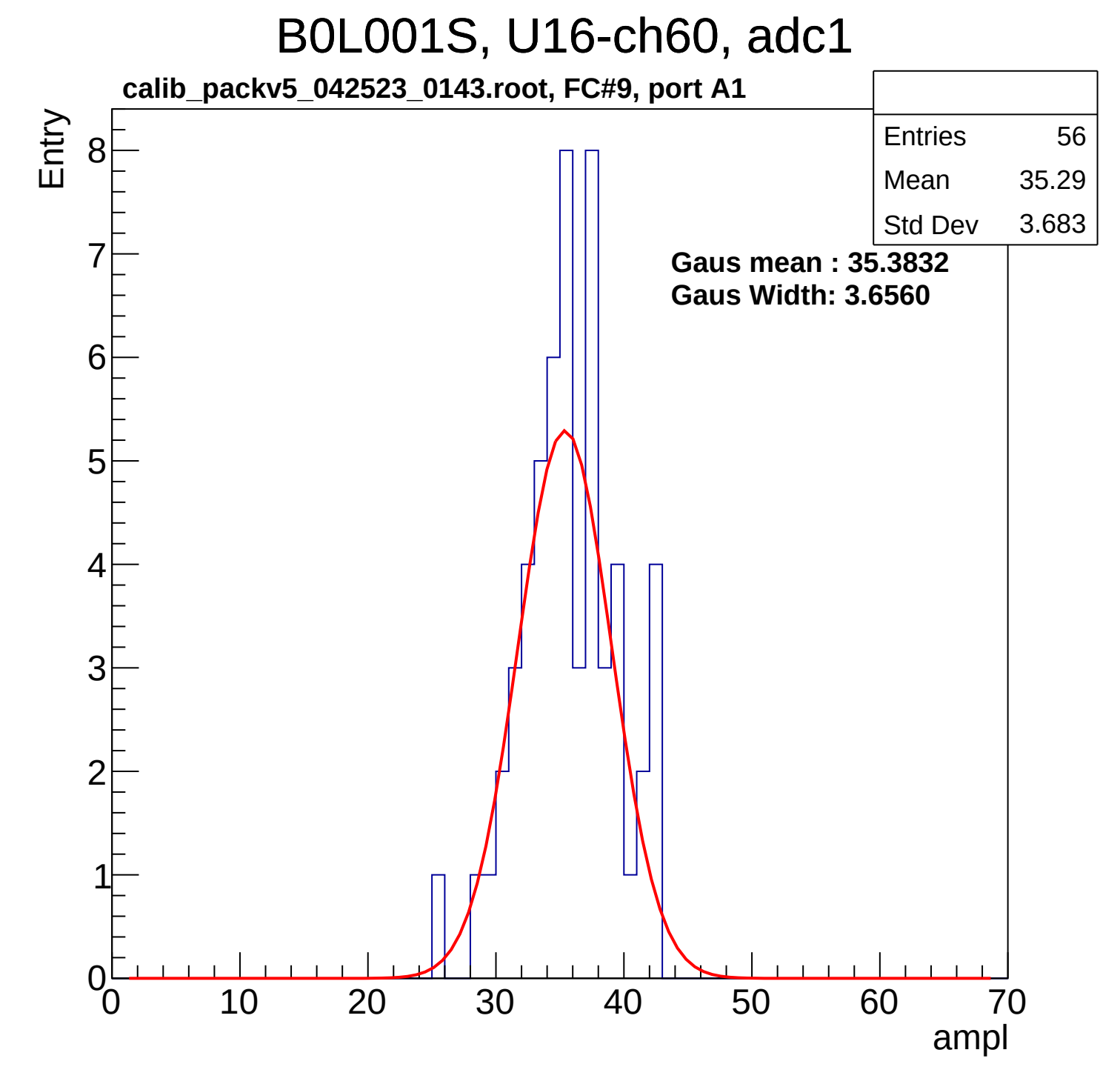

B0L001S, U16-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 54.8
Std Dev 3.111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

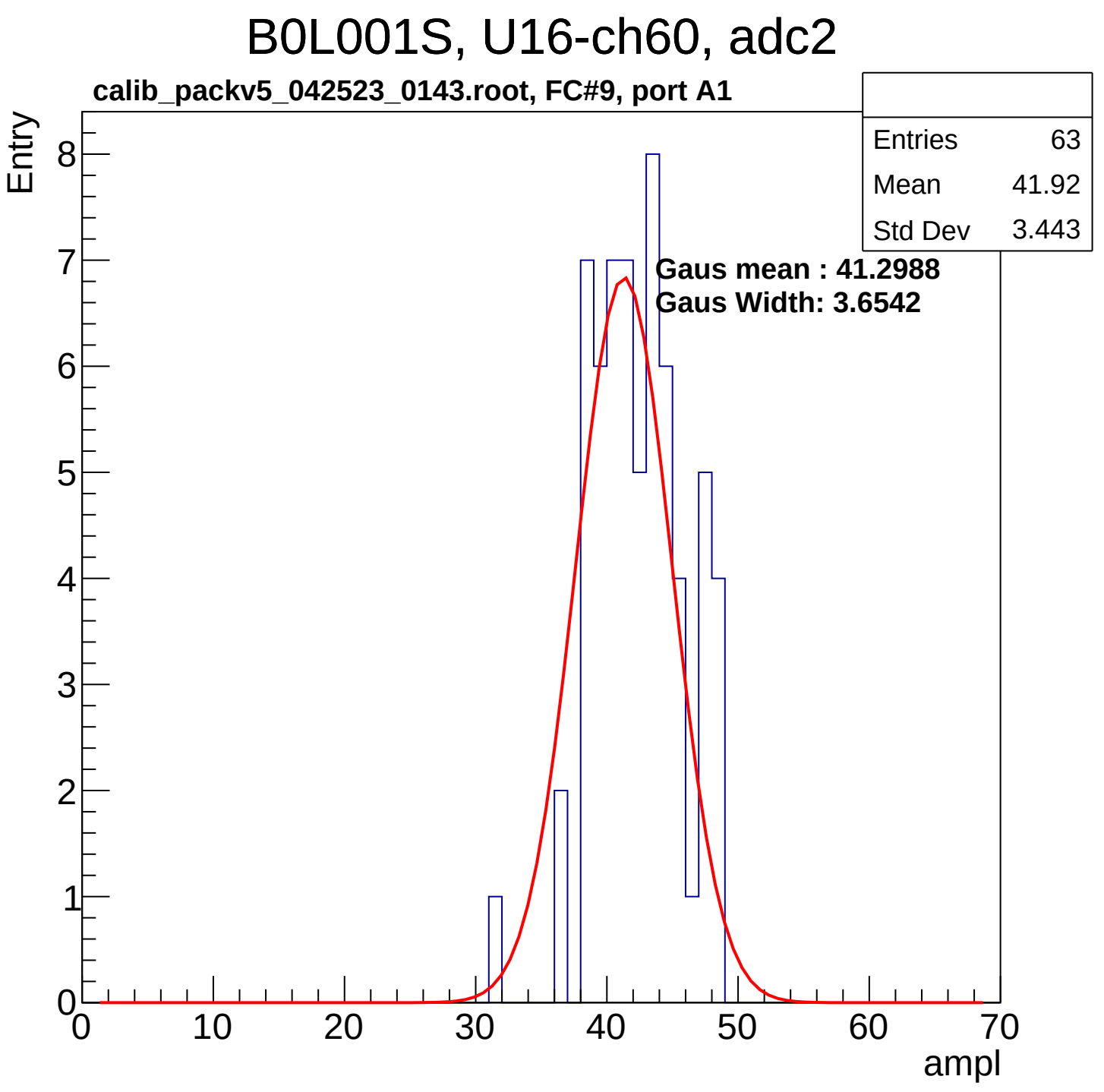

B0L001S, U16-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.52
Std Dev 8.902
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

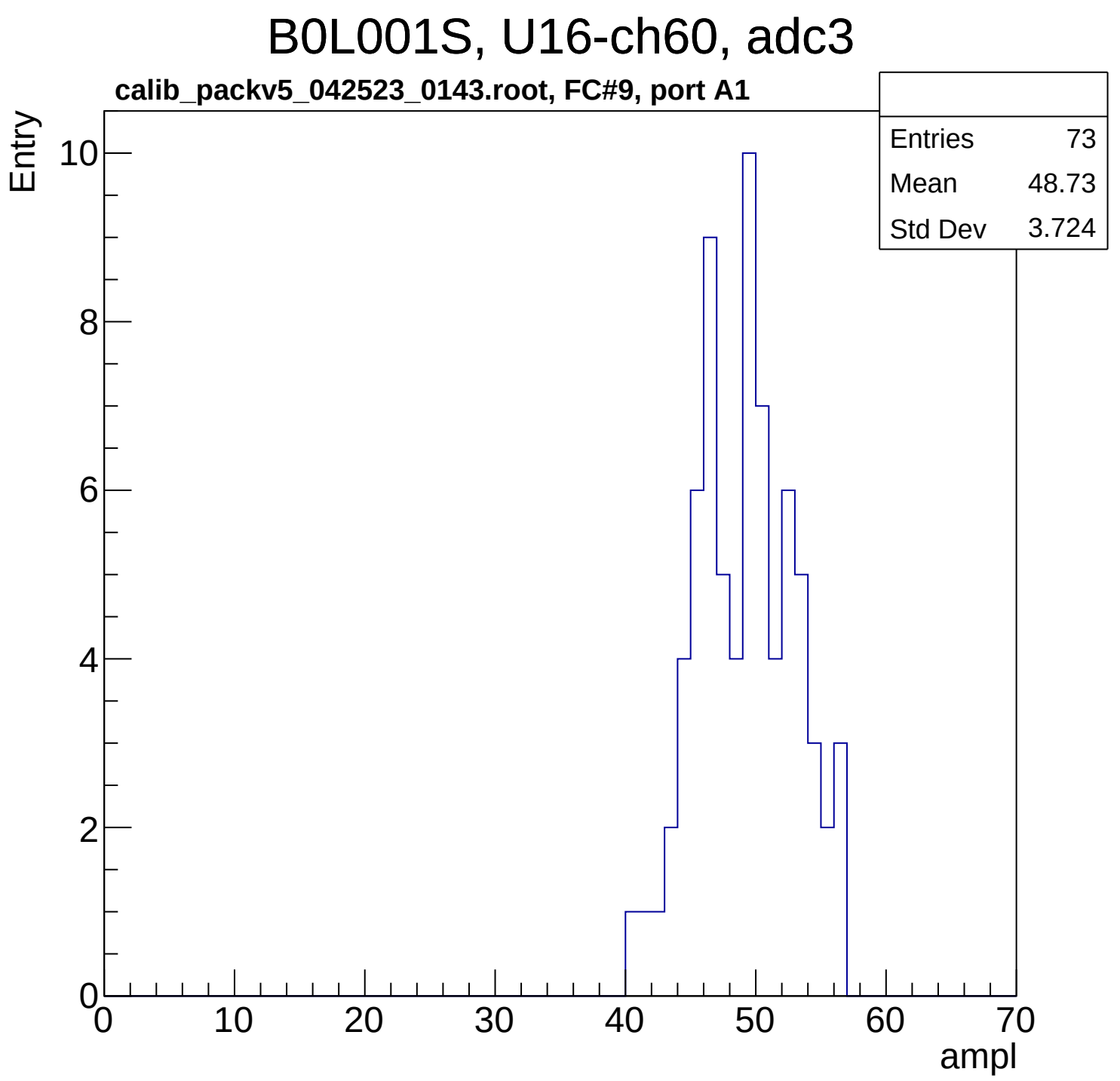

B0L001S, U16-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.67
Std Dev 1.944
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

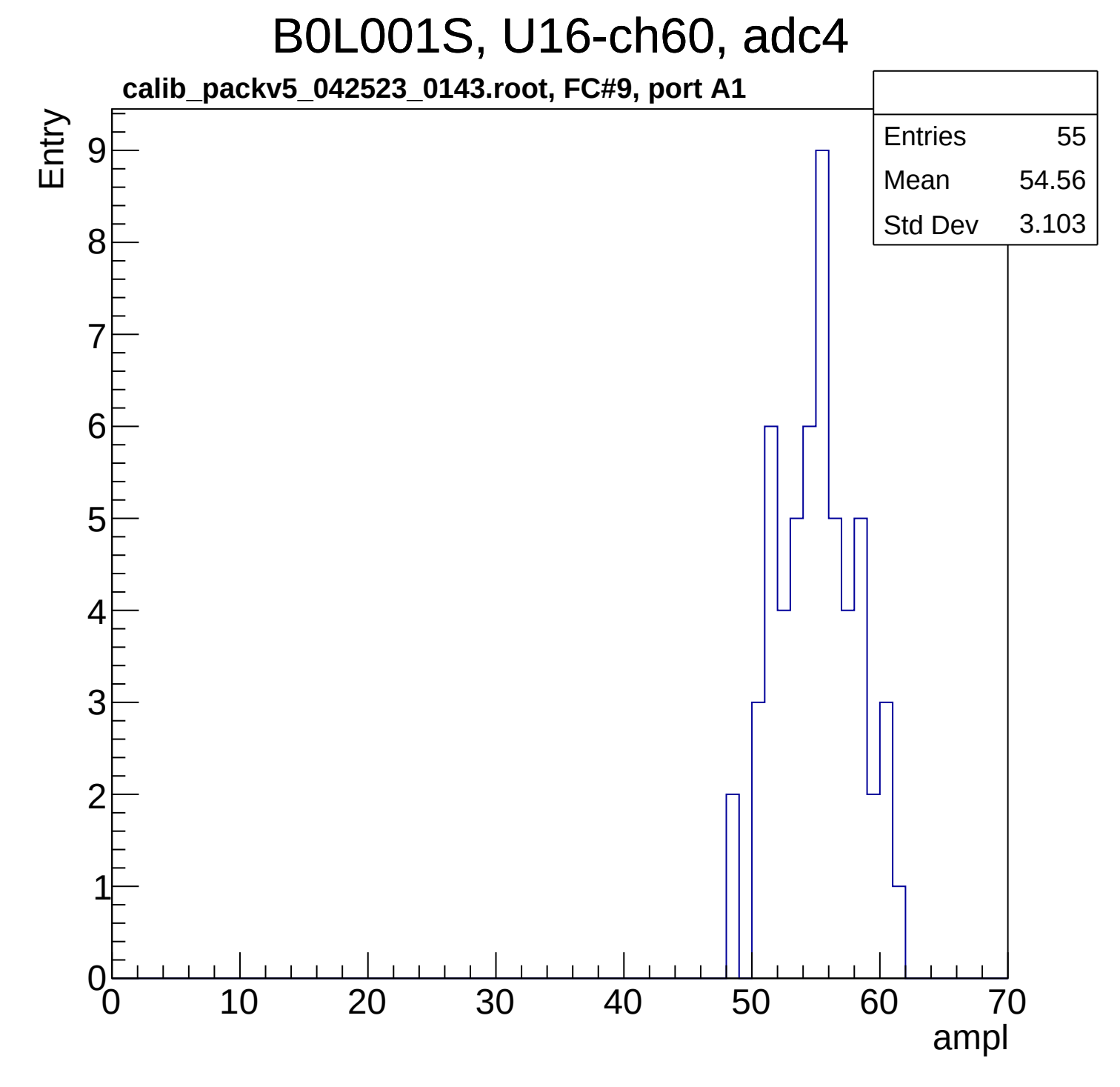

B0L001S, U16-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 28.86
Std Dev 7.233
Gaus mean : 30.6711
Gaus Width: 4.7240
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 35.29
Std Dev 3.683
Gaus mean : 35.3832
Gaus Width: 3.6560
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 41.92
Std Dev 3.443
Gaus mean : 41.2988
Gaus Width: 3.6542
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 48.73
Std Dev 3.724
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.56
Std Dev 3.103
Entry
ampl
0
1
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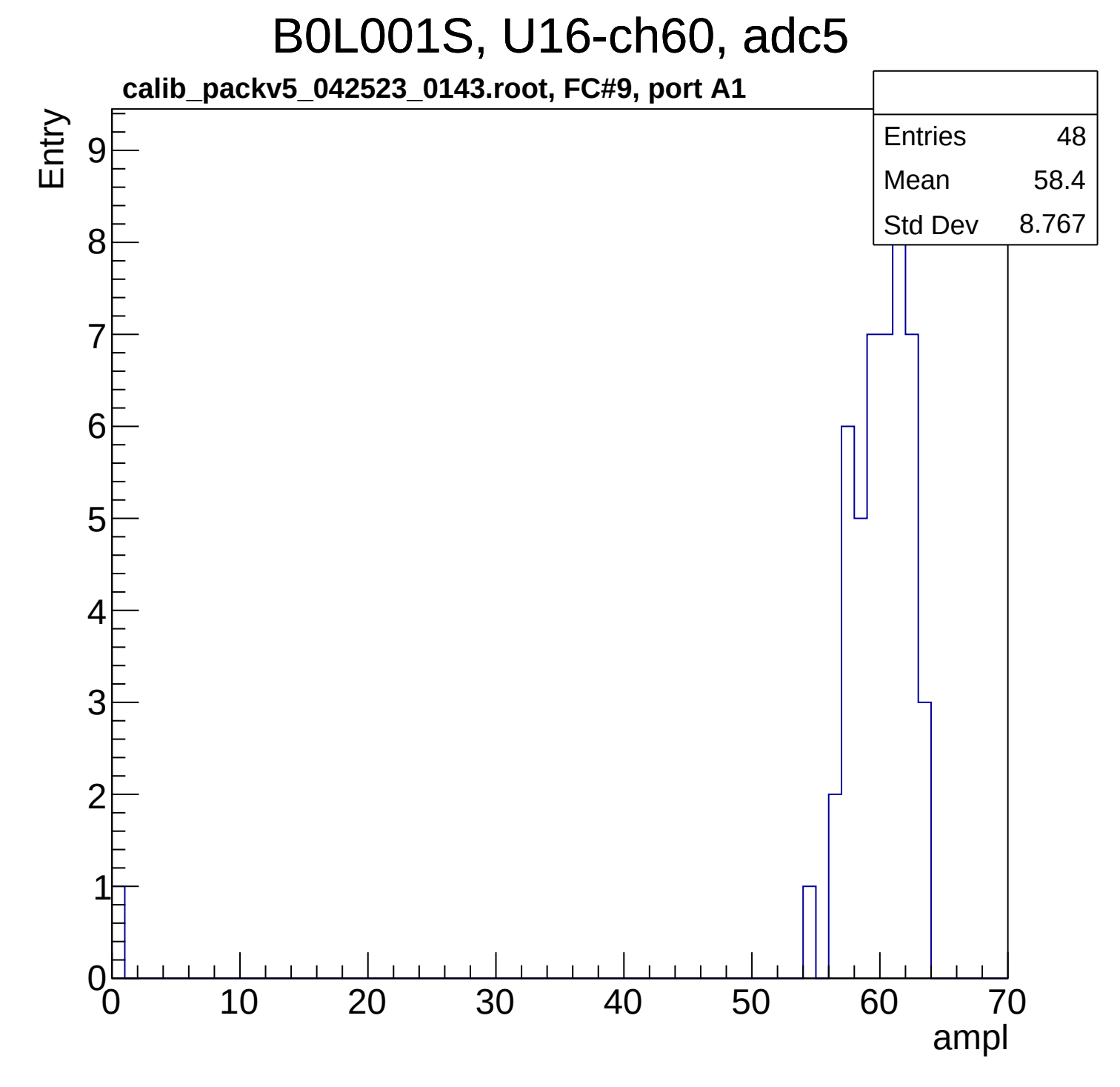
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.4
Std Dev 8.767
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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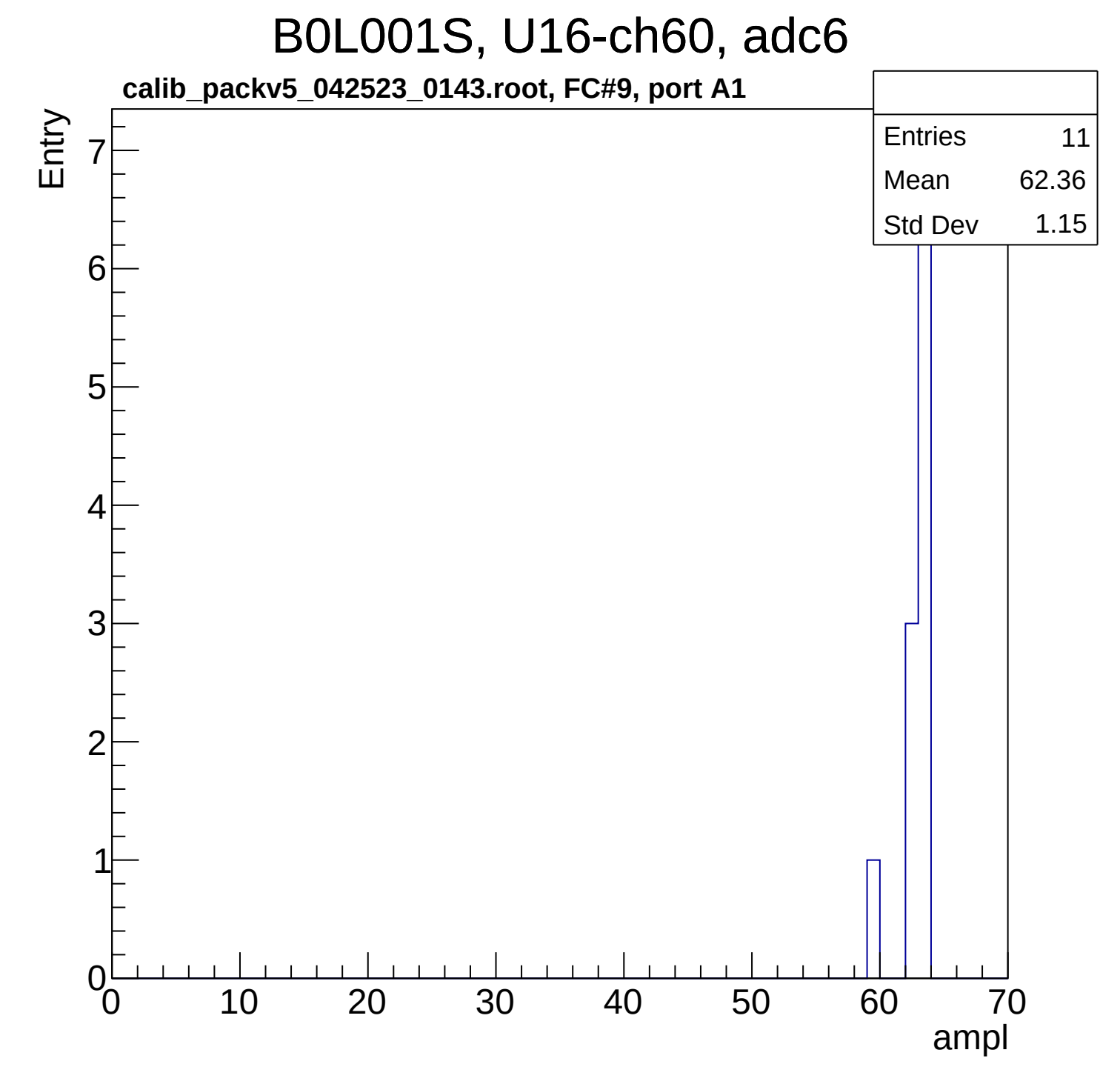
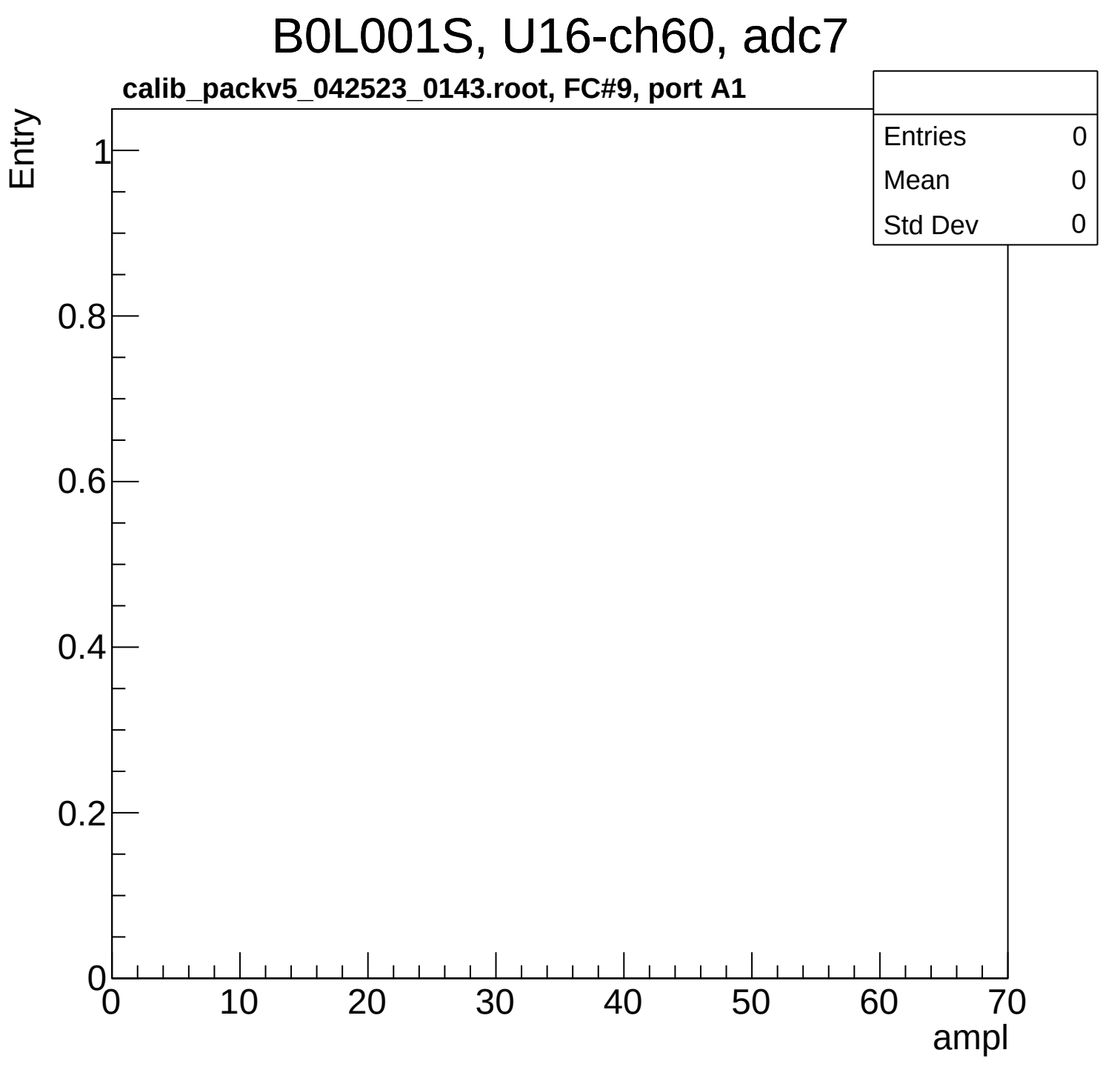
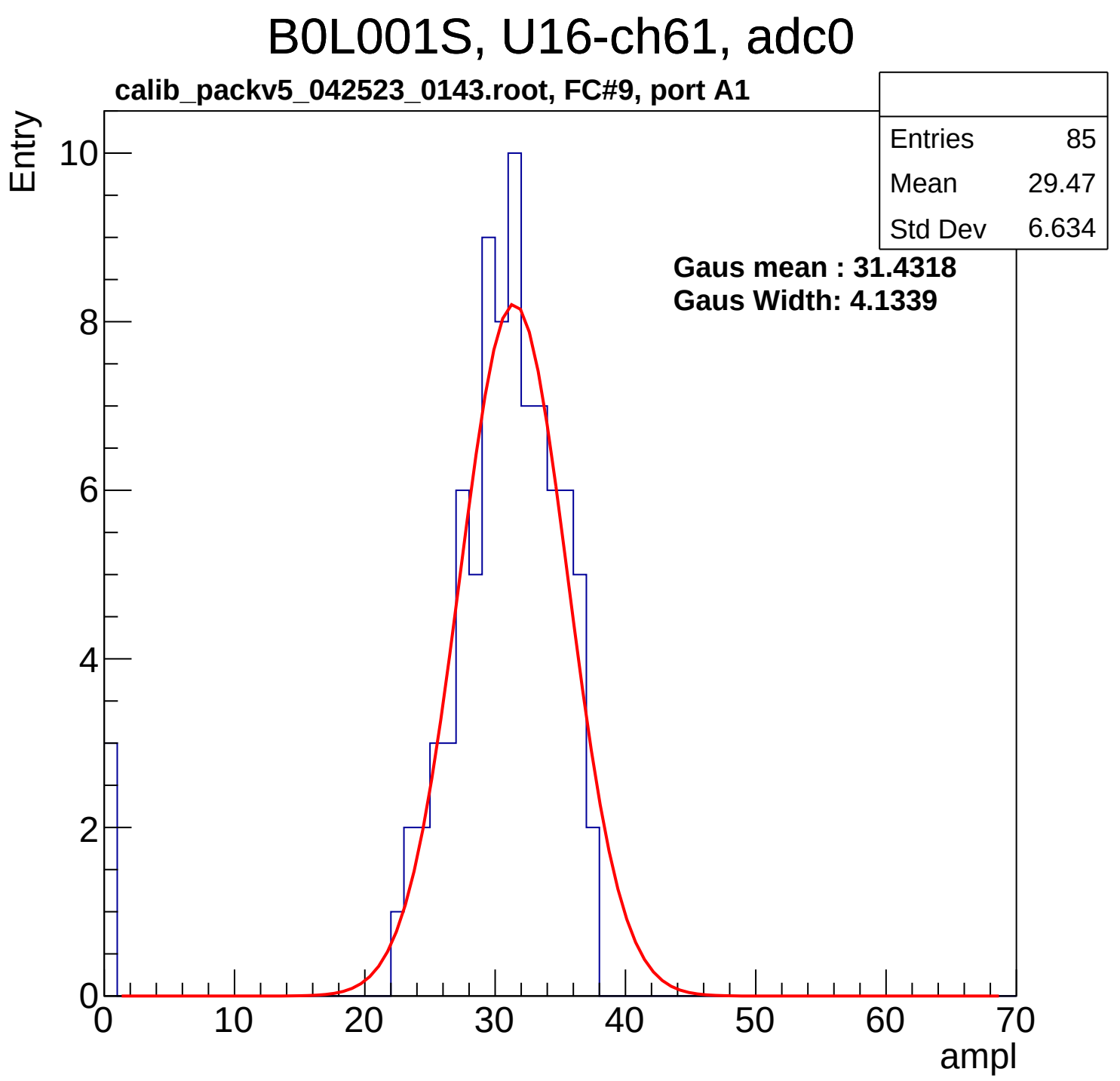
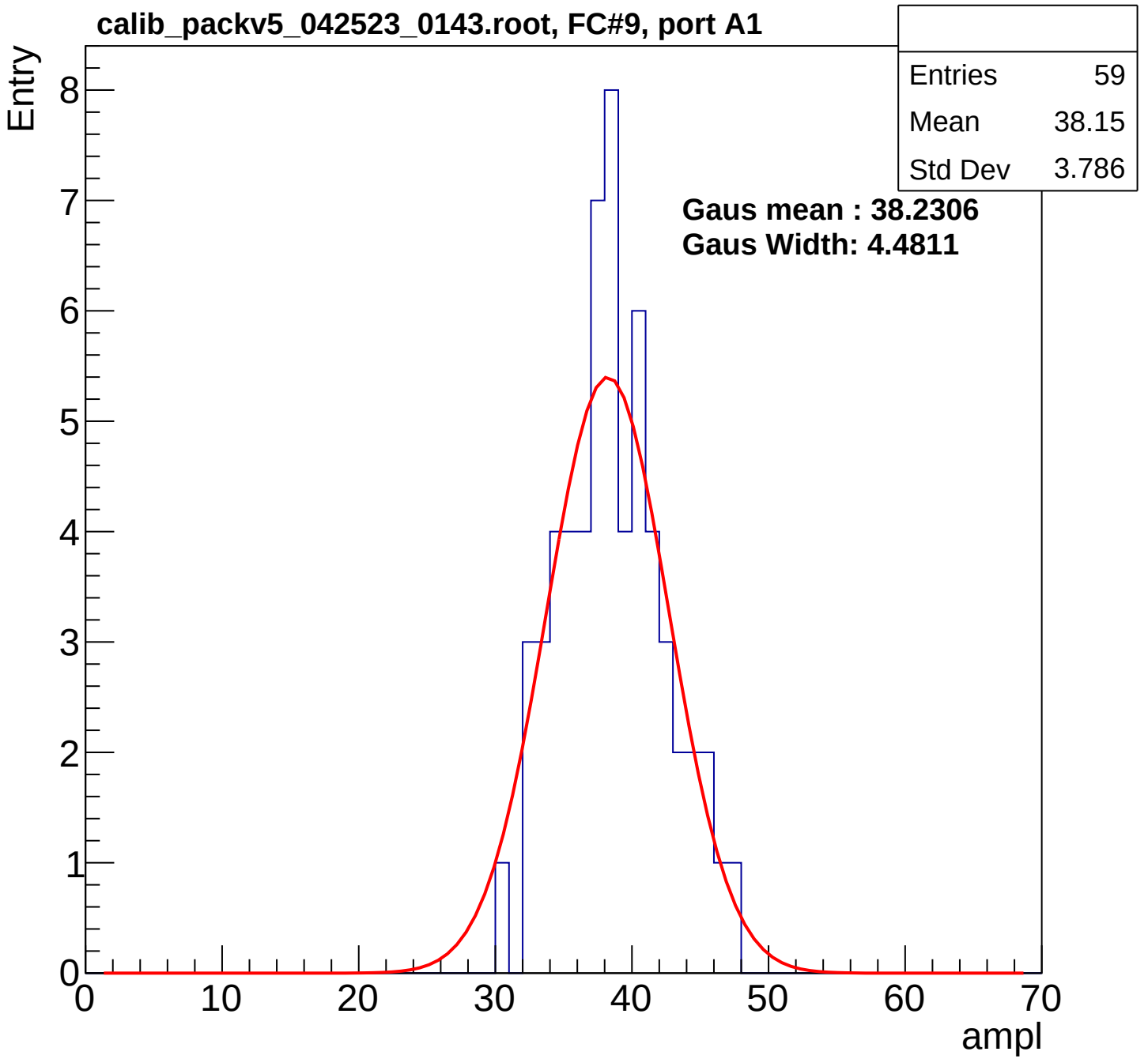

B0L001S, U16-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 62.36
Std Dev 1.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 29.47
Std Dev 6.634
Gaus mean : 31.4318
Gaus Width: 4.1339
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.15
Std Dev 3.786
Gaus mean : 38.2306
Gaus Width: 4.4811
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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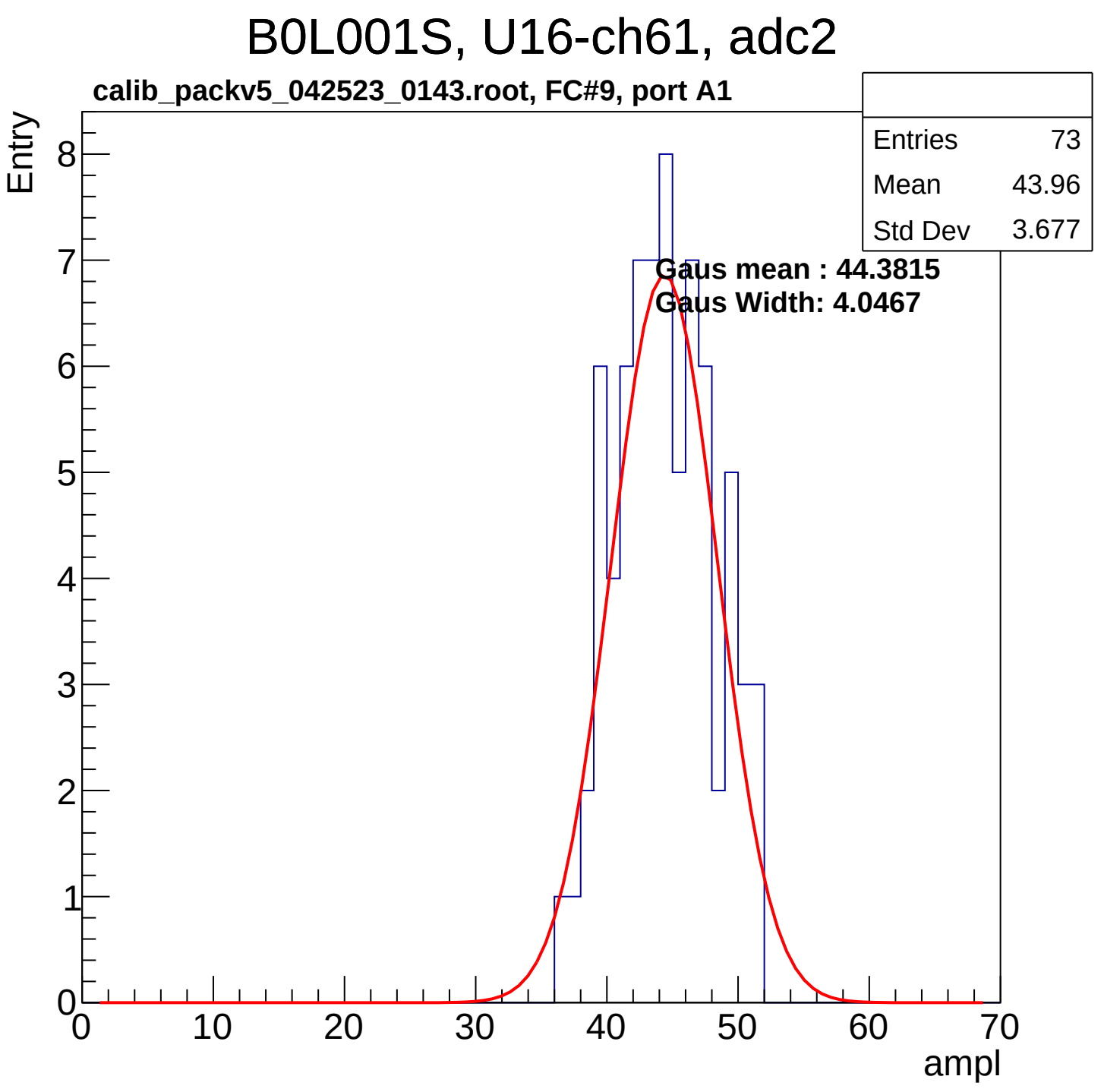
B0L001S, U16-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.96
Std Dev 3.677
Gaus mean : 44.3815
Gaus Width: 4.0467
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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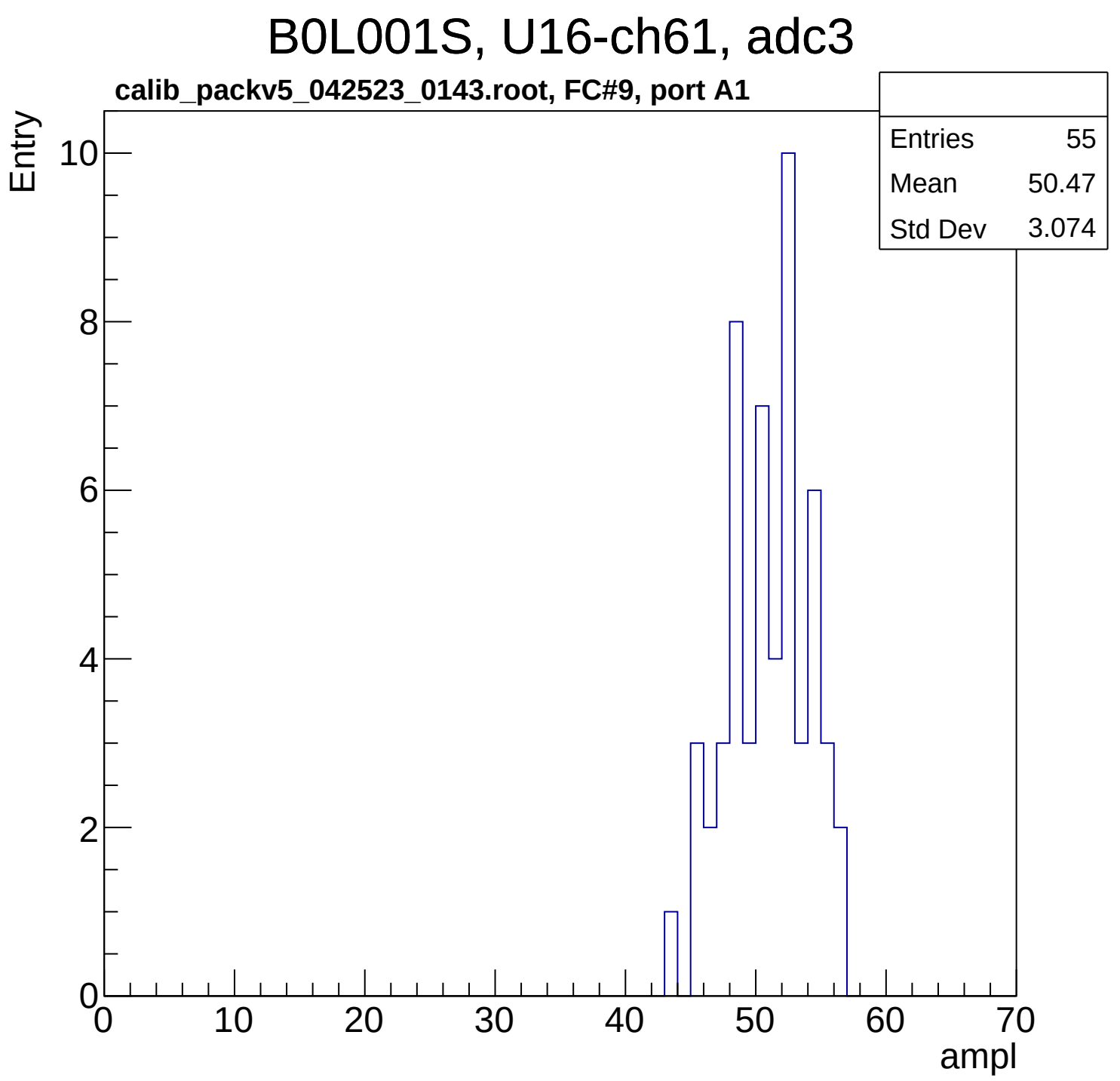
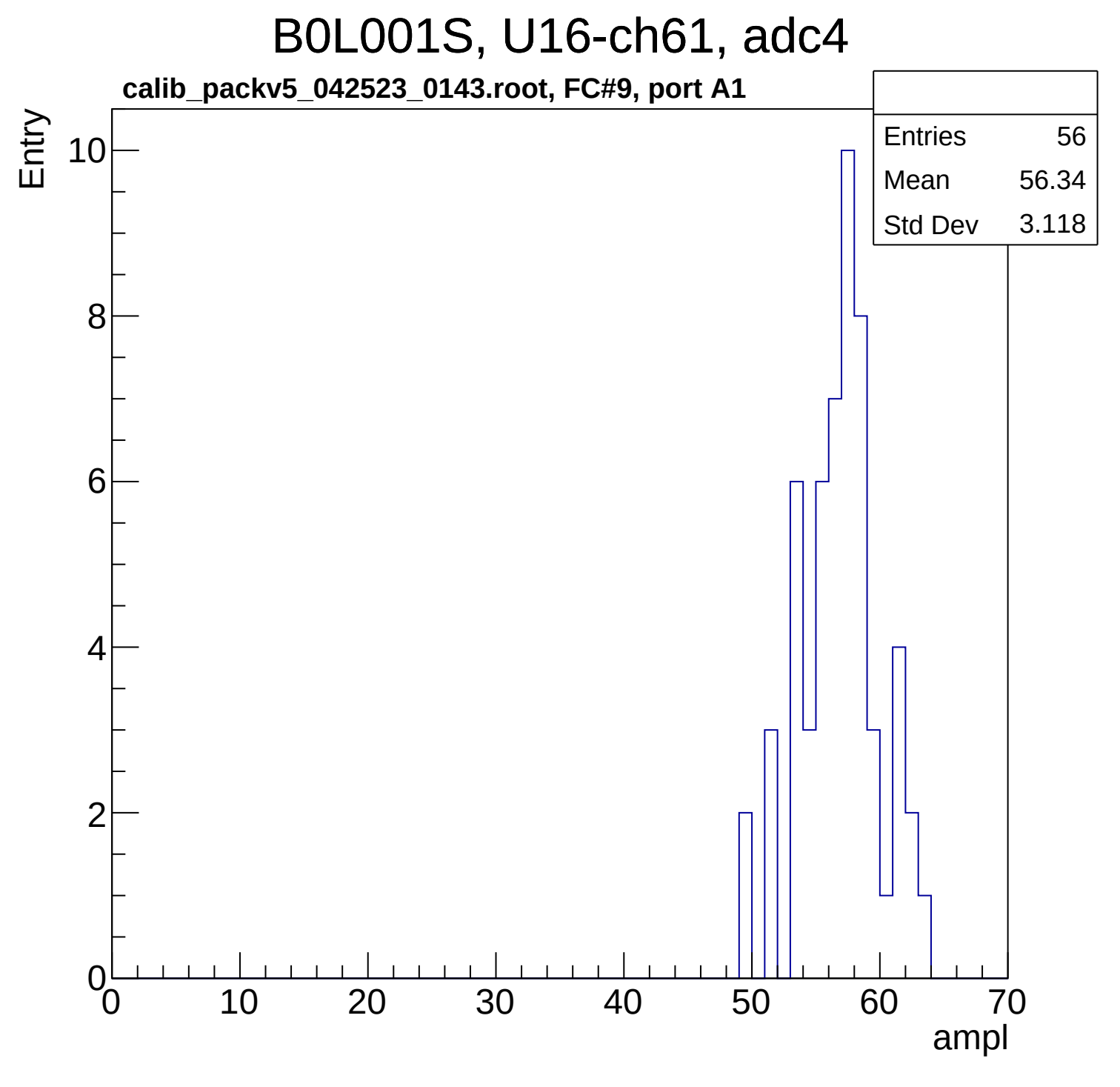
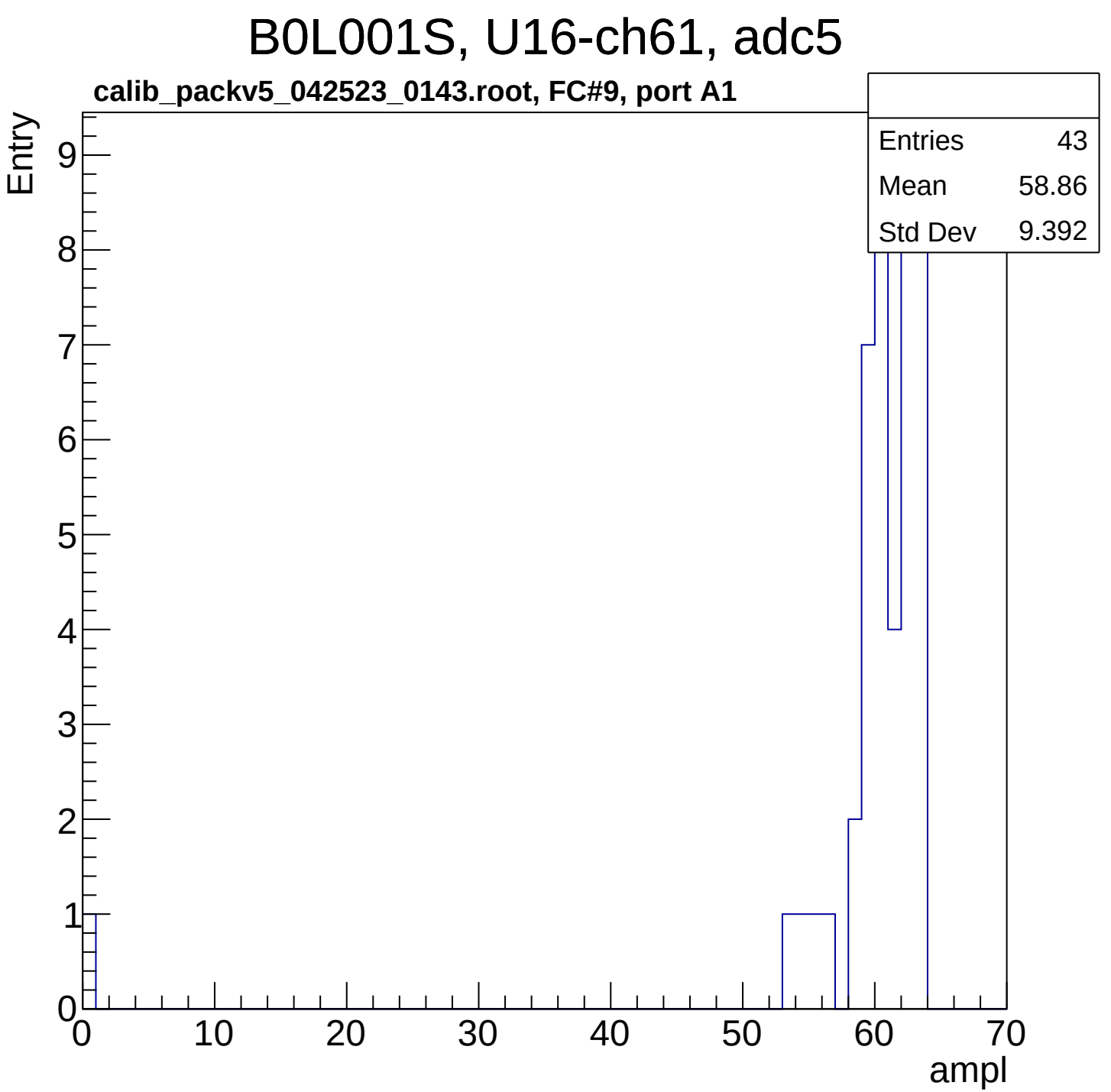

B0L001S, U16-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.47
Std Dev 3.074
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.34
Std Dev 3.118
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.86
Std Dev 9.392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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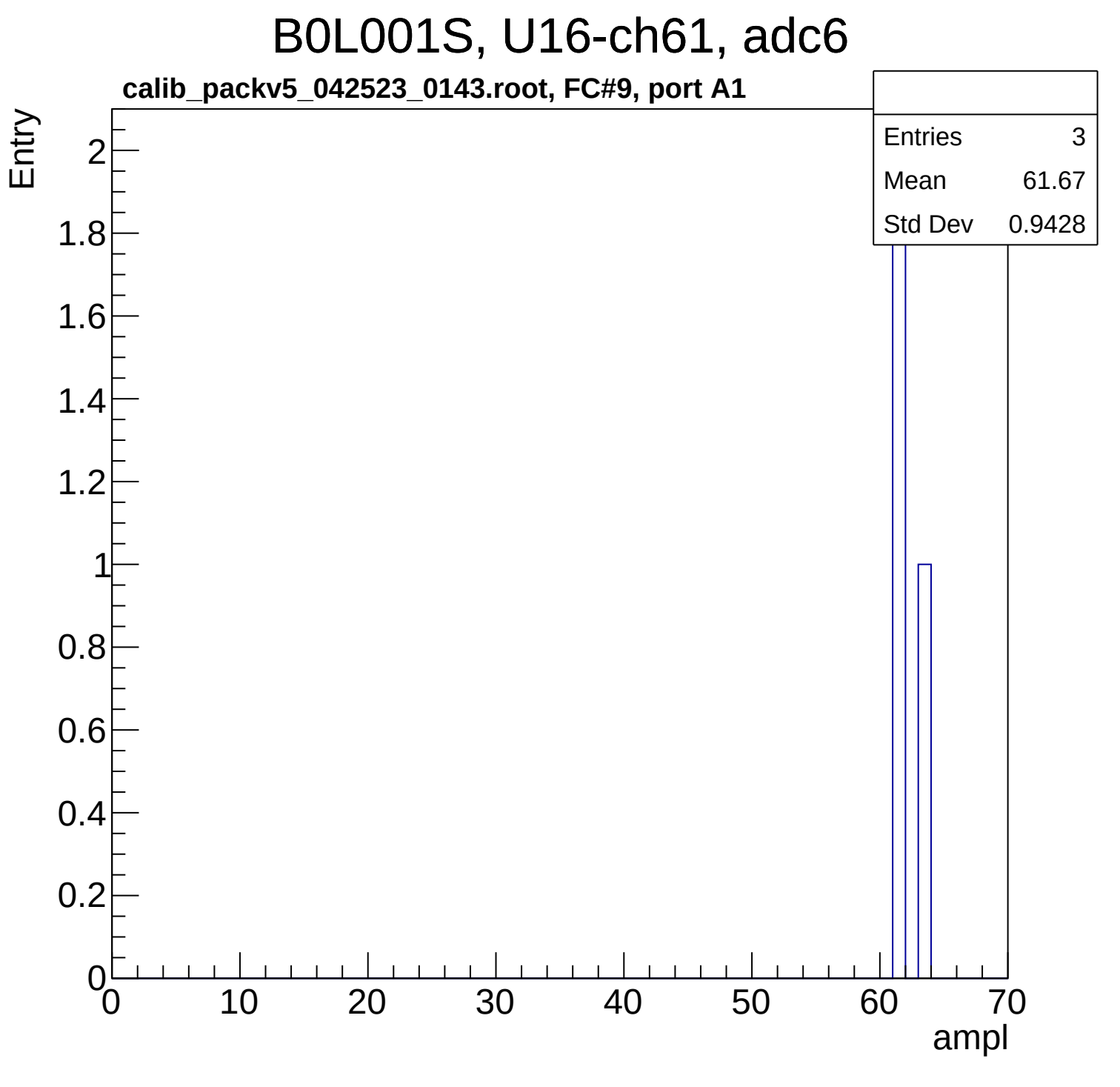
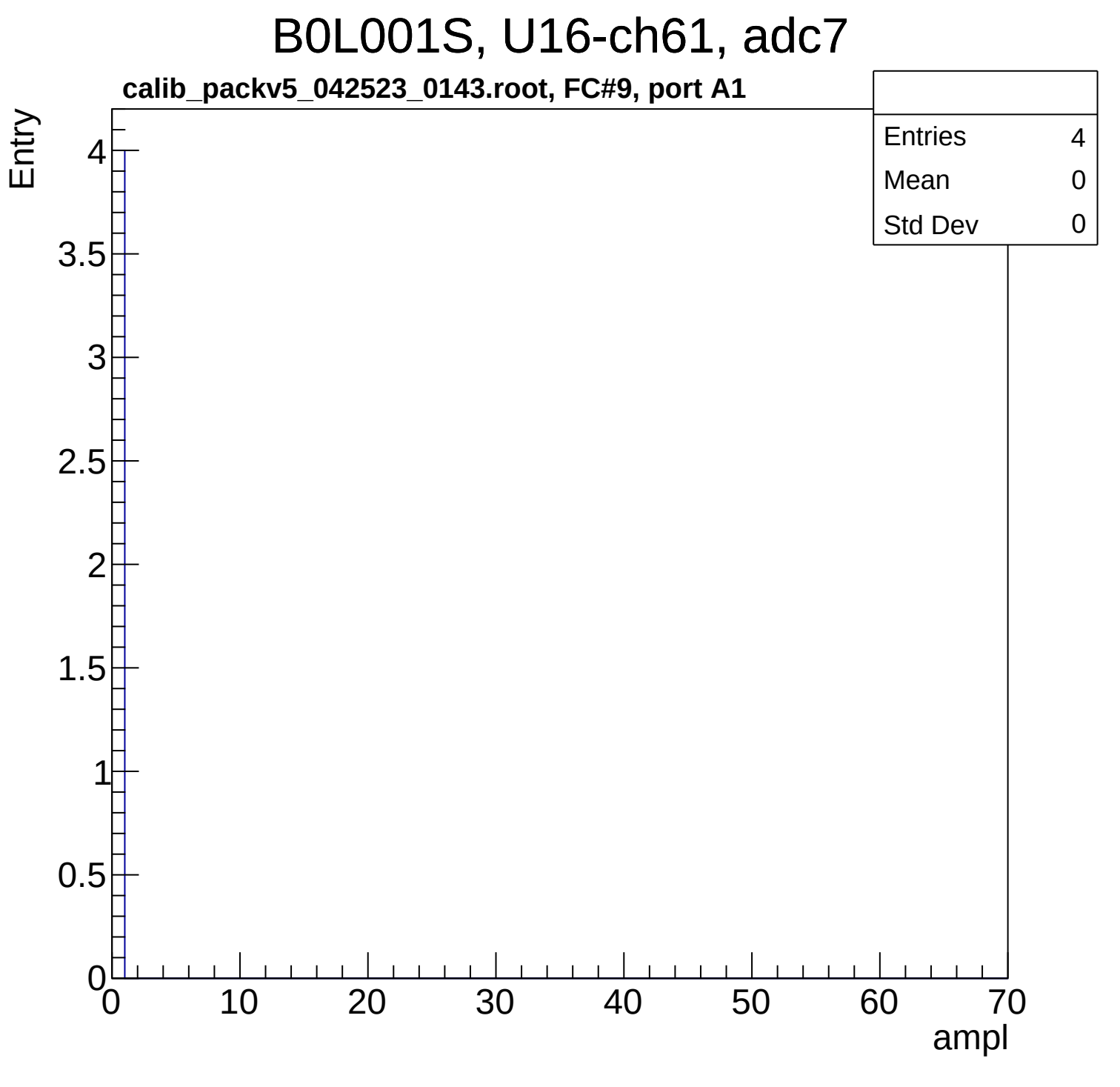
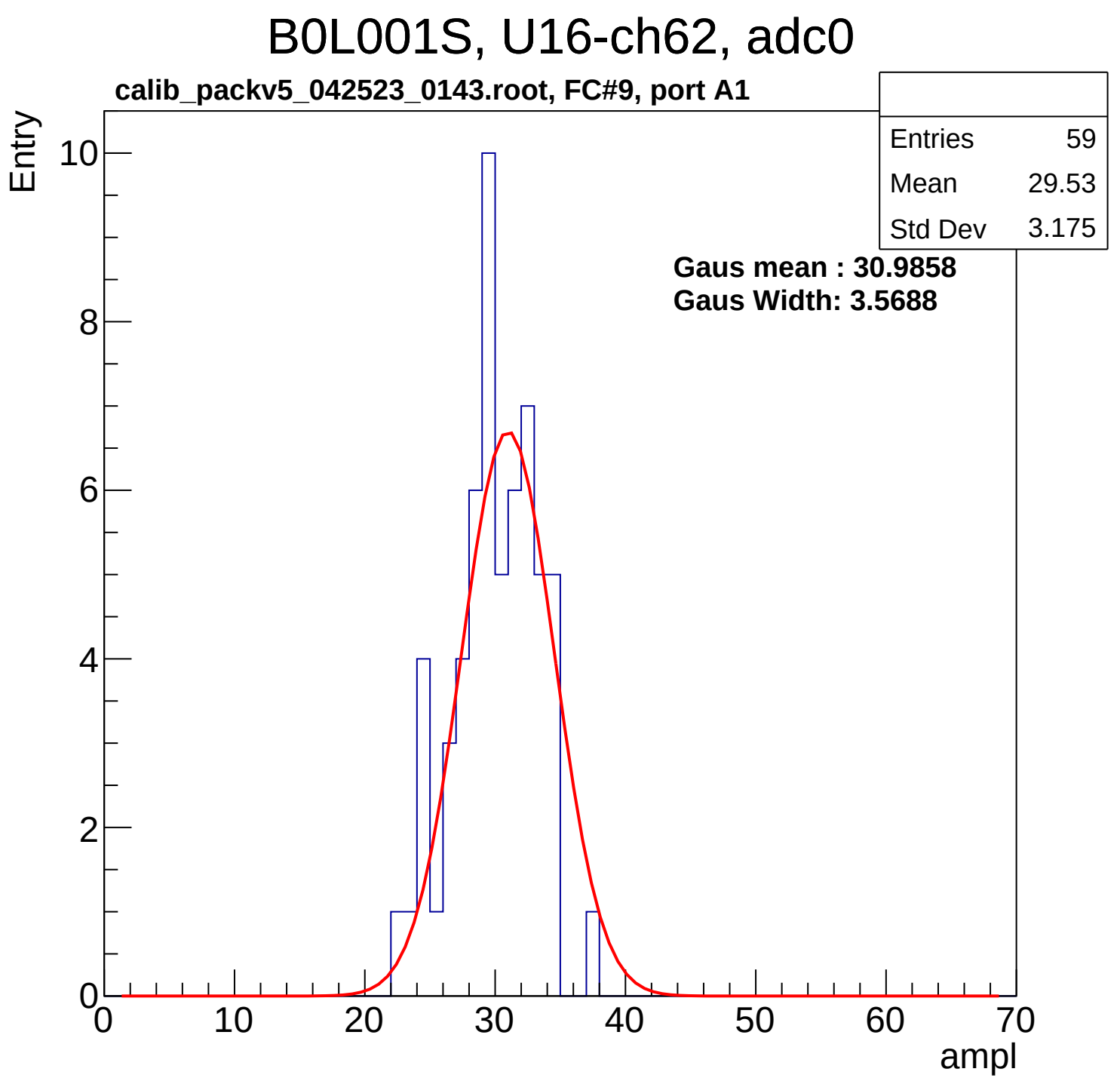
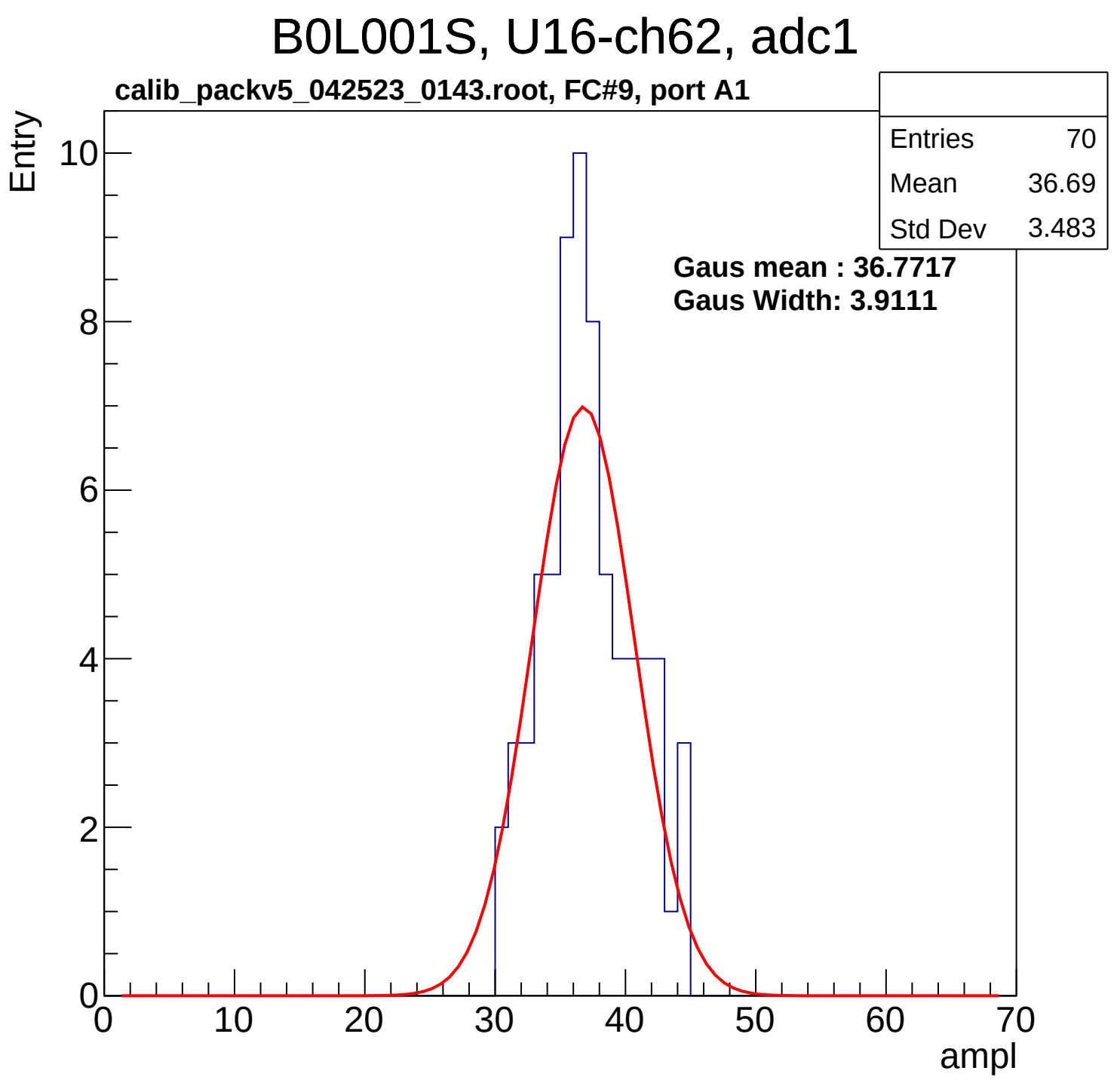
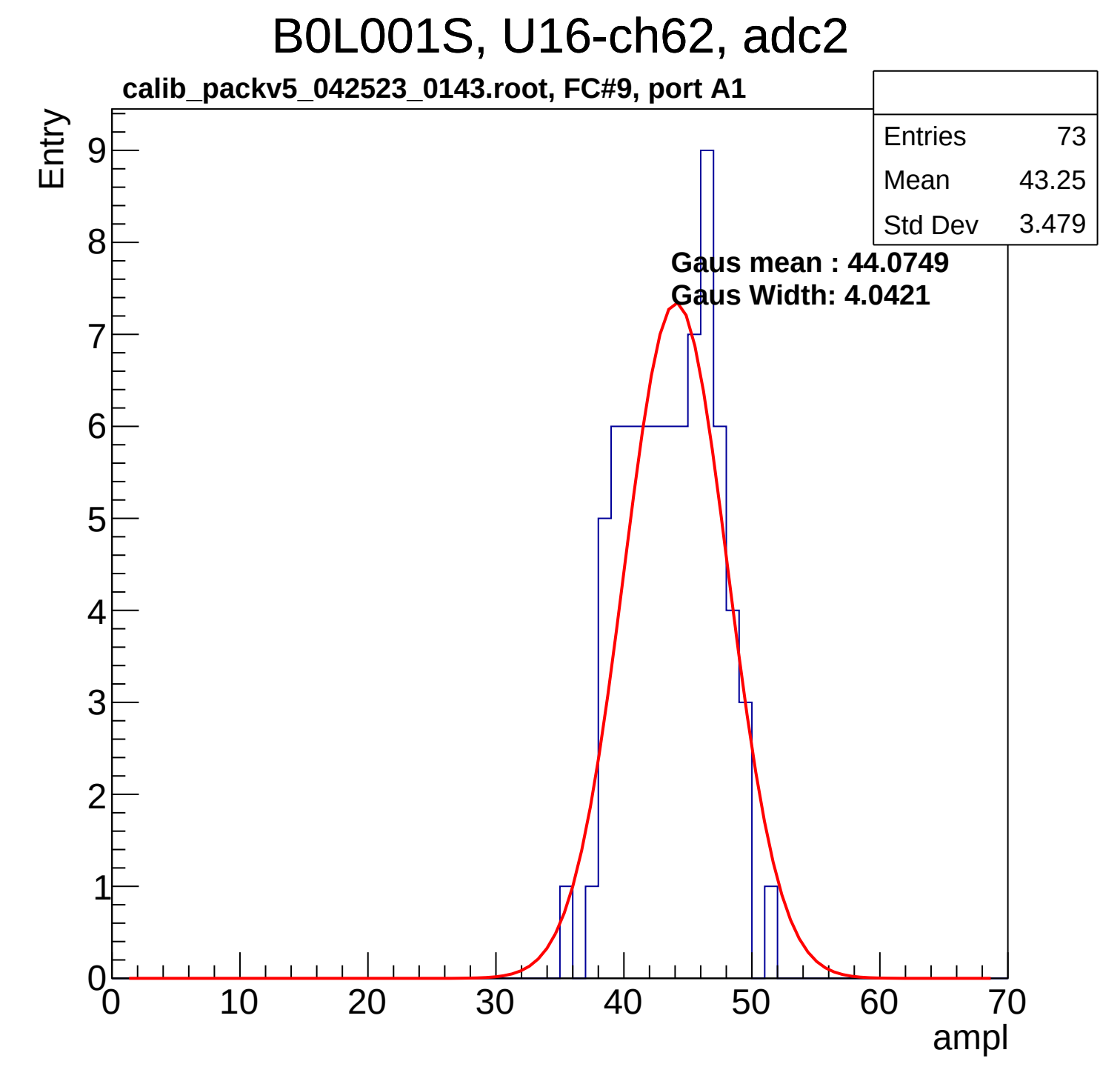
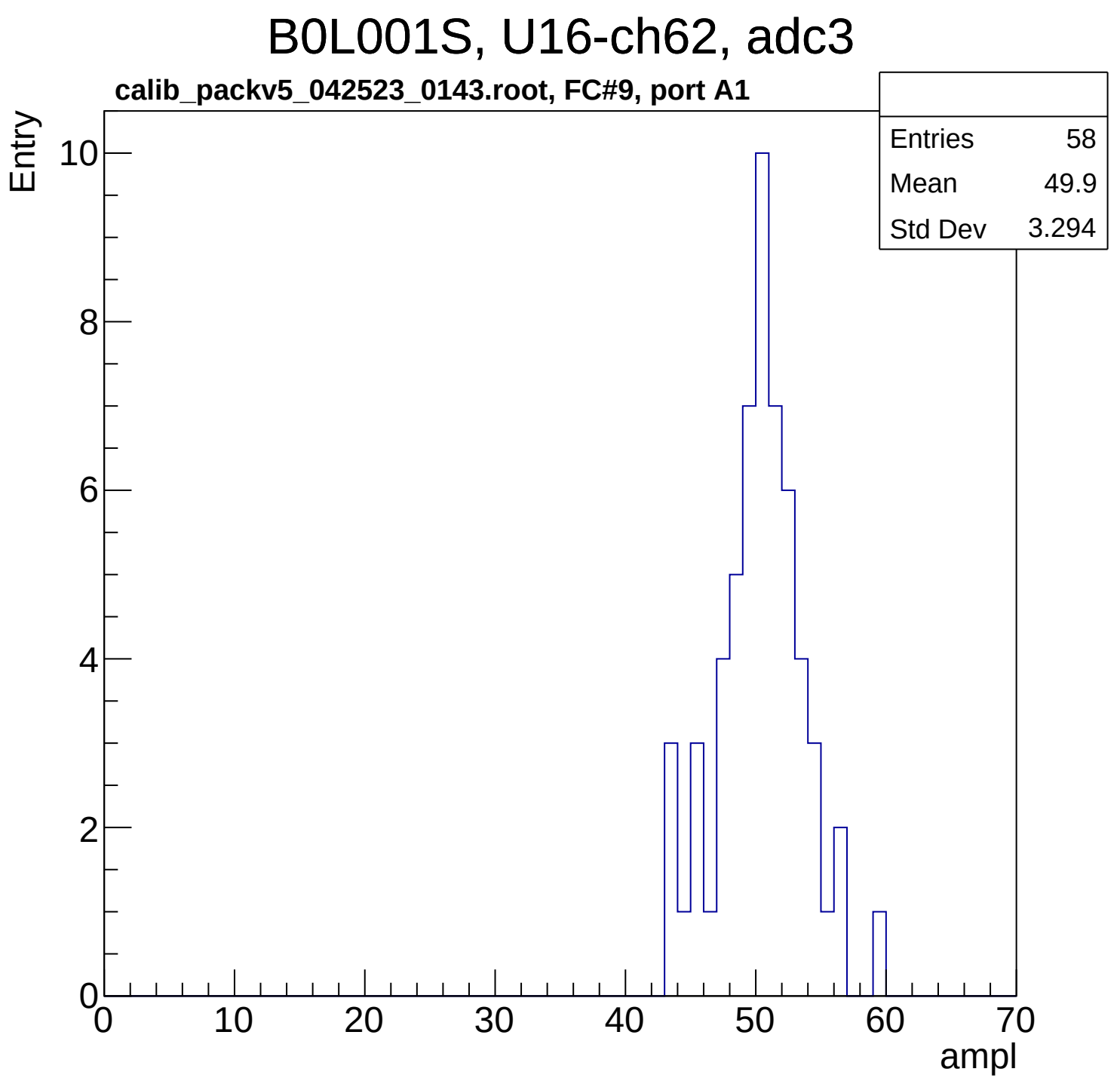

B0L001S, U16-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

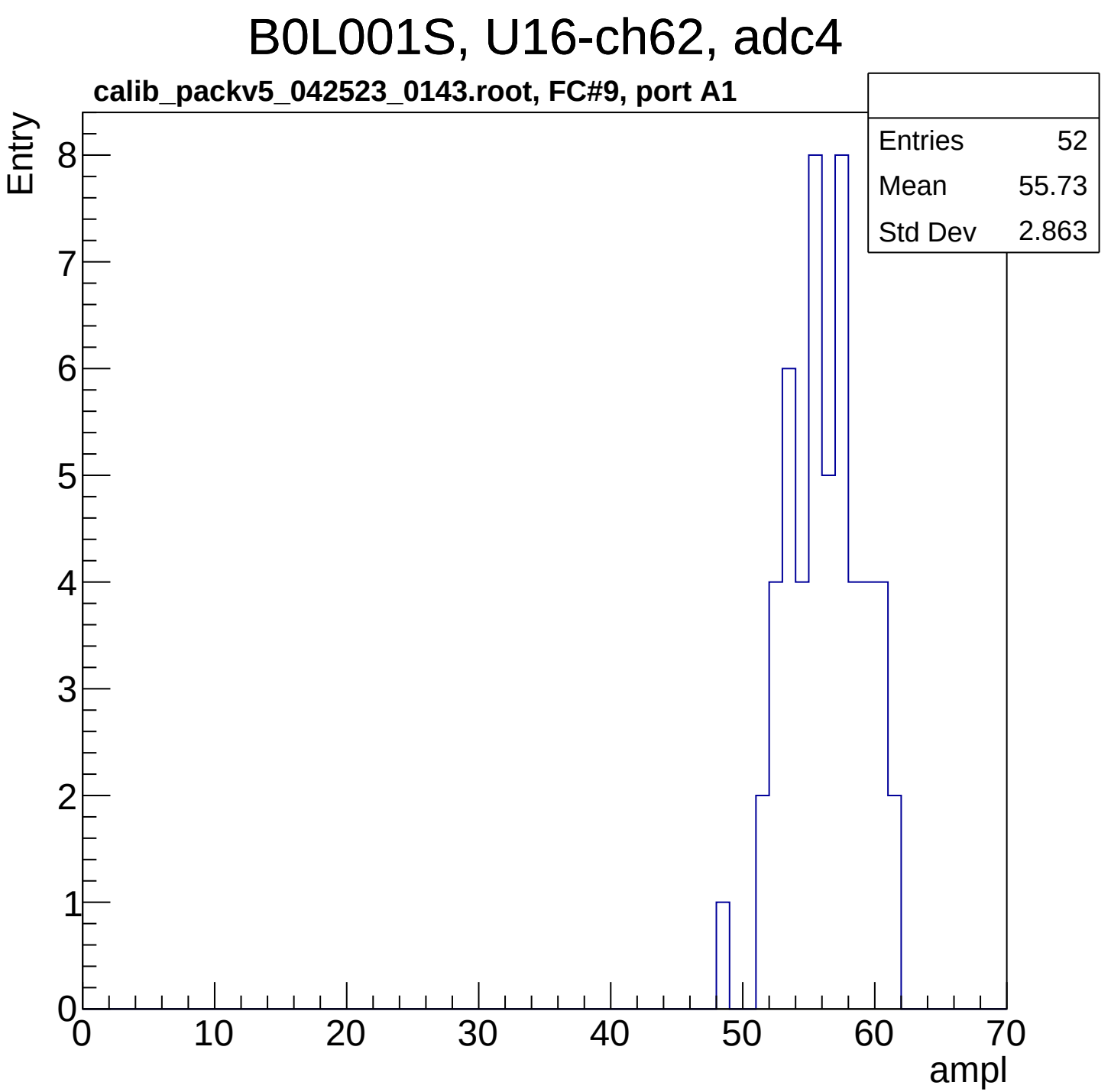

B0L001S, U16-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

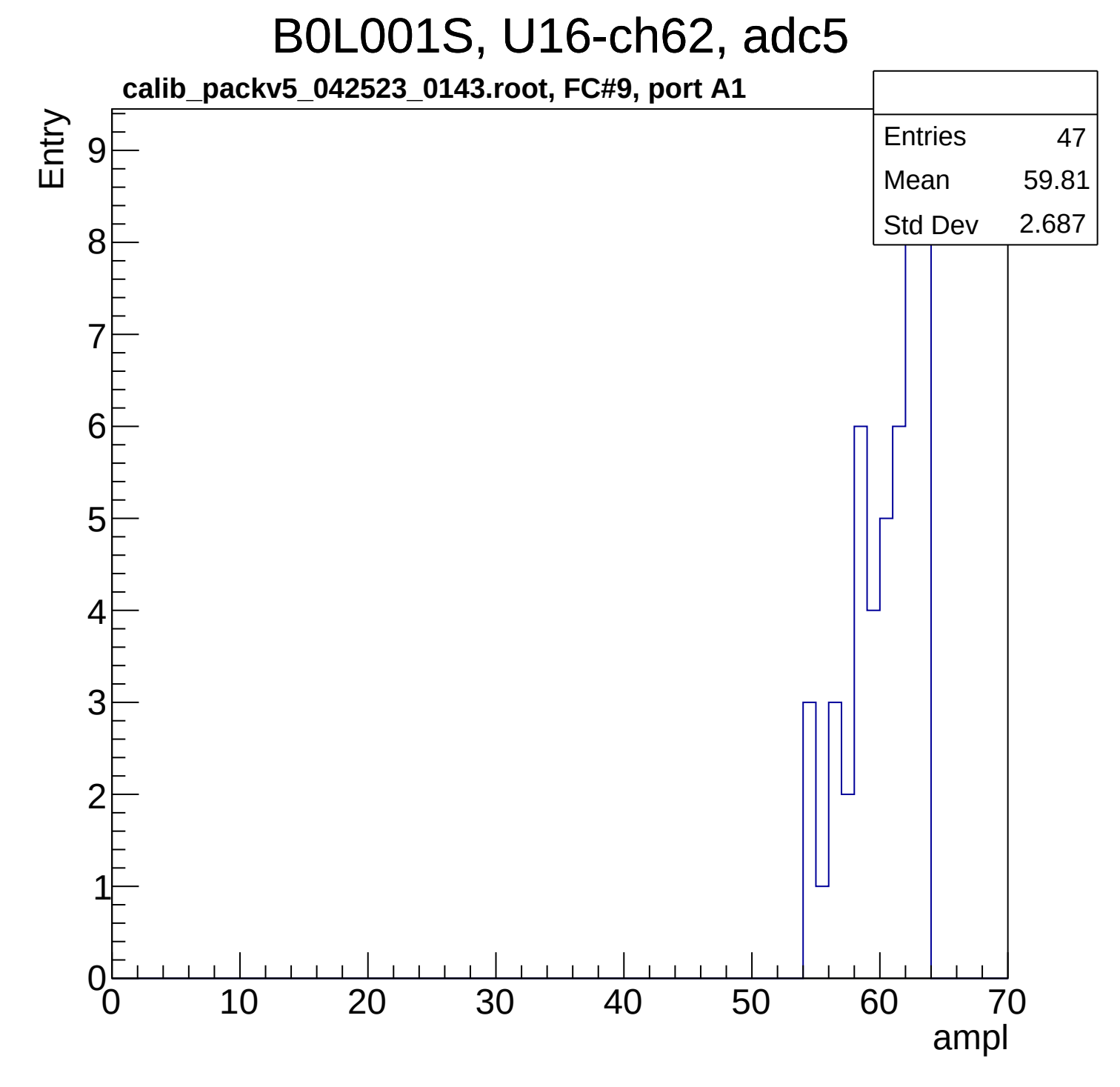

B0L001S, U16-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.53
Std Dev 3.175
Gaus mean : 30.9858
Gaus Width: 3.5688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

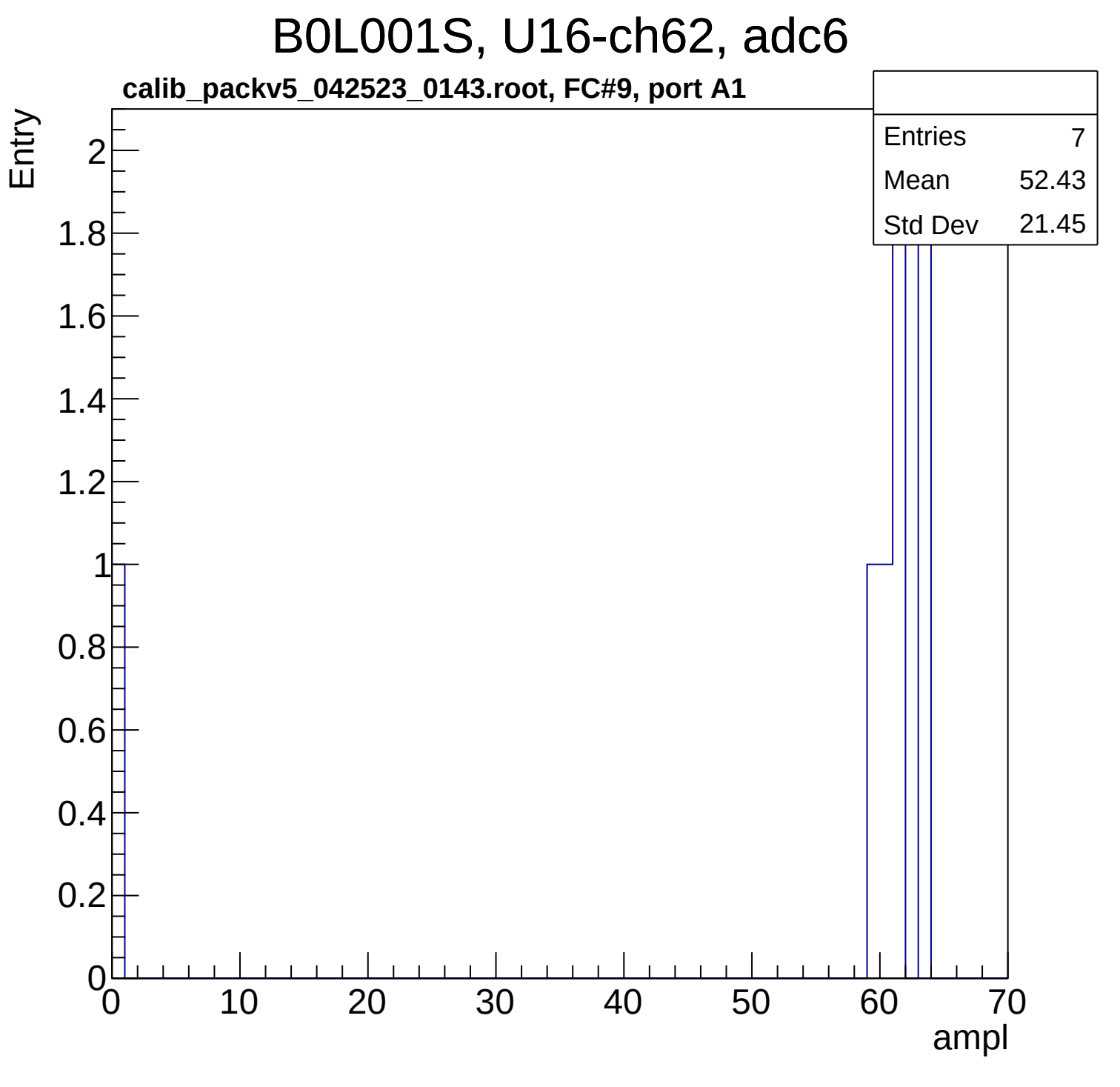

B0L001S, U16-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.69
Std Dev 3.483
Gaus mean : 36.7717
Gaus Width: 3.9111
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

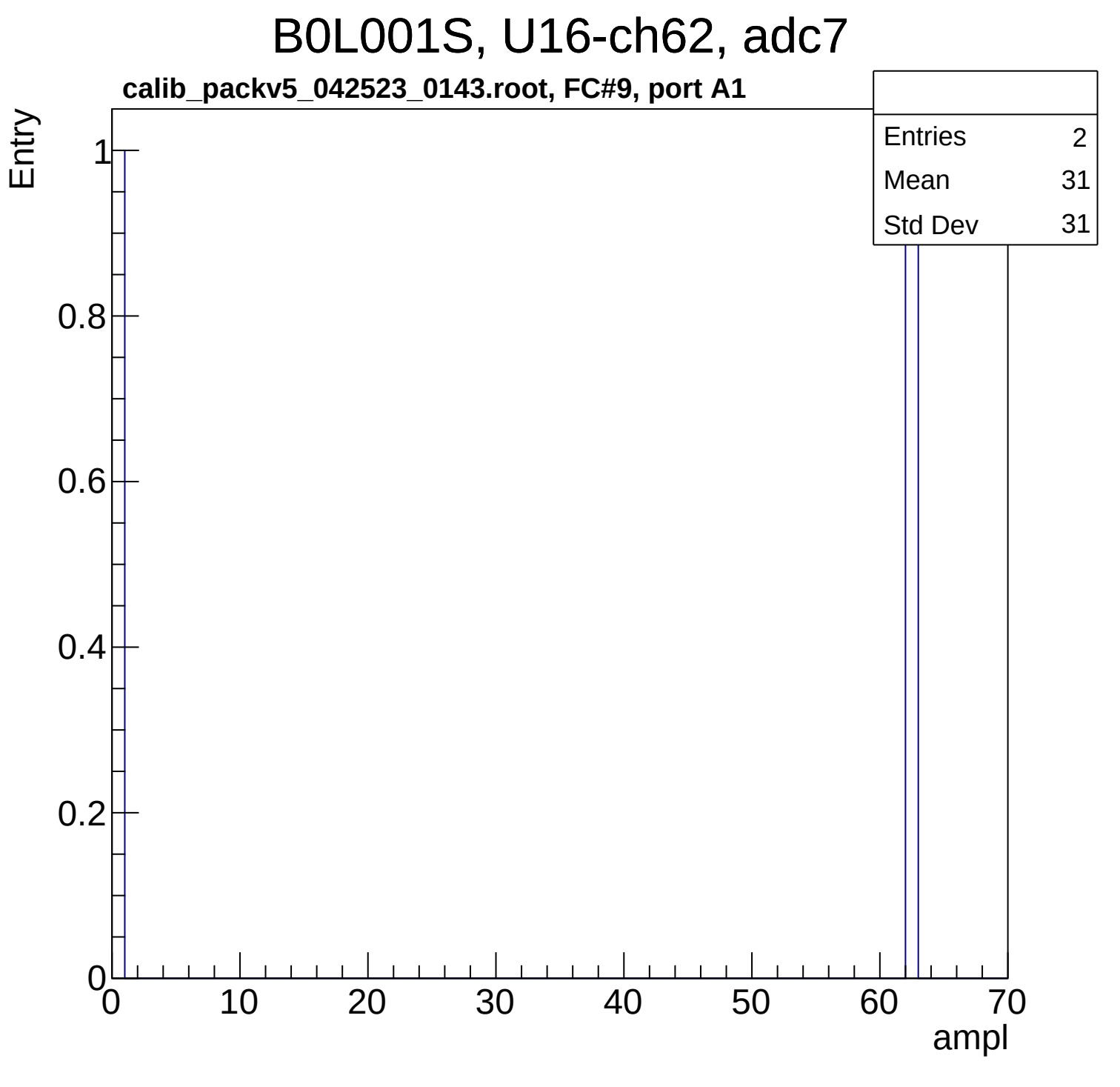

B0L001S, U16-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.25
Std Dev 3.479
Gaus mean : 44.0749
Gaus Width: 4.0421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

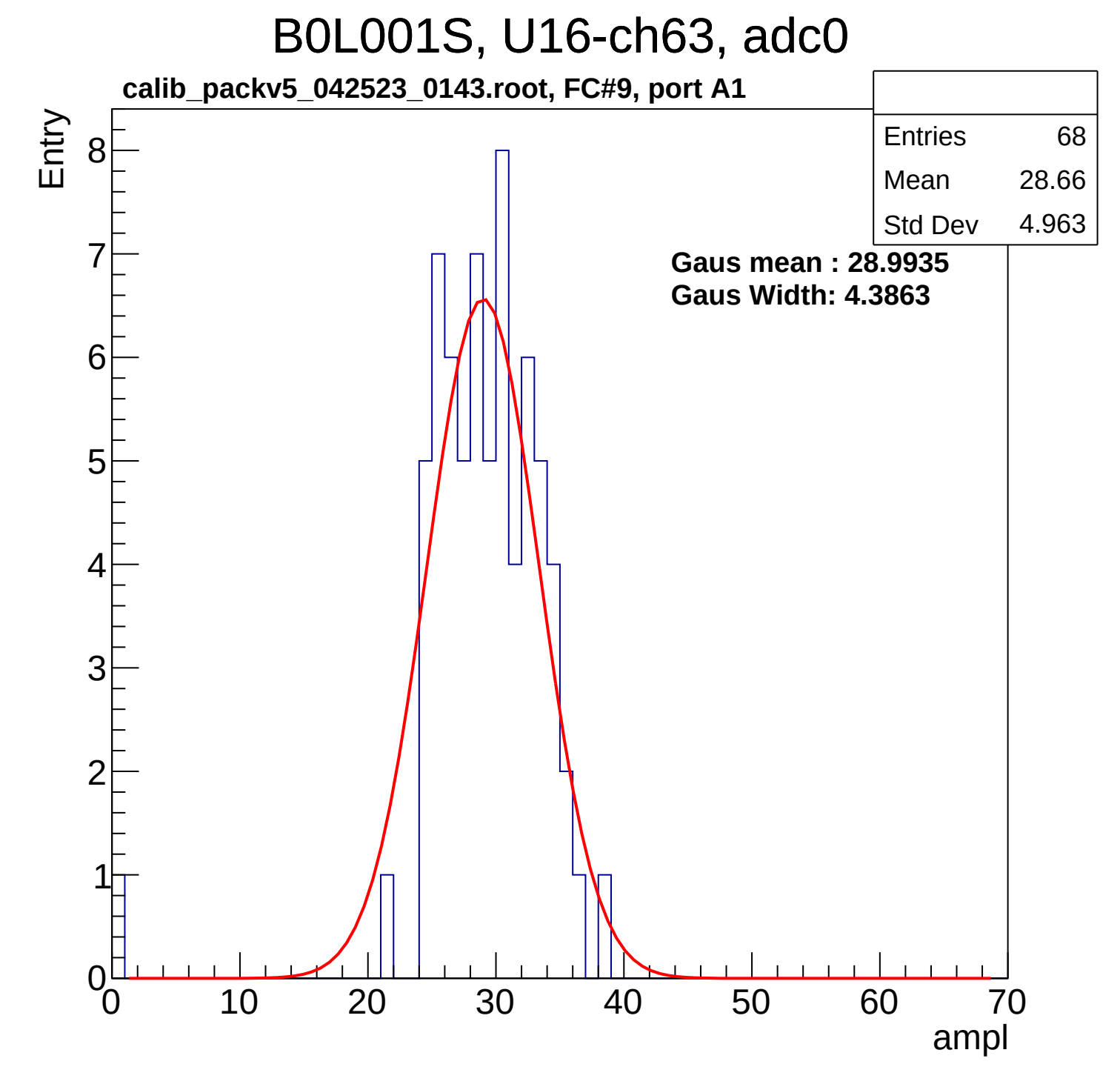

B0L001S, U16-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.9
Std Dev 3.294
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

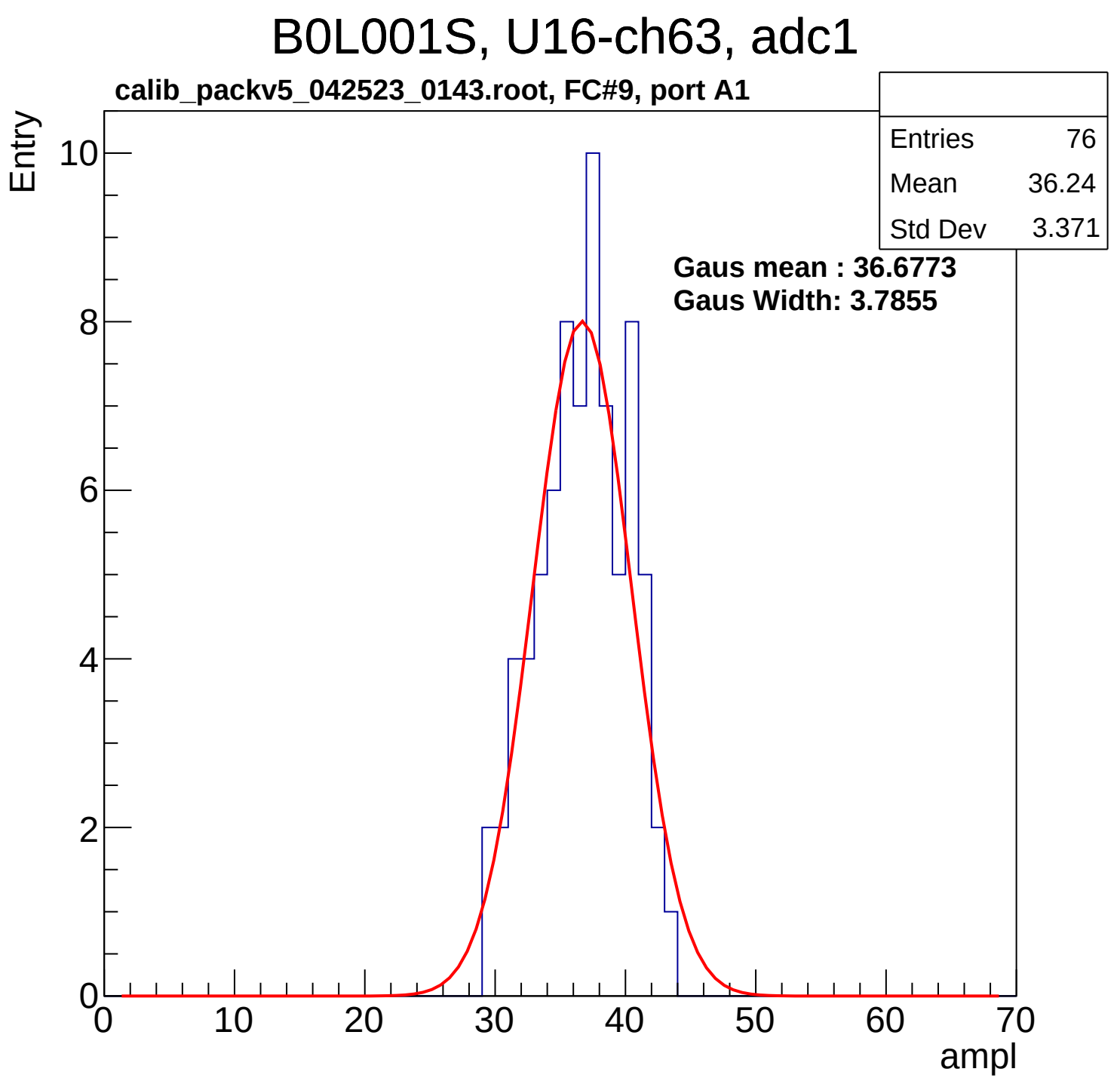

B0L001S, U16-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.73
Std Dev 2.863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

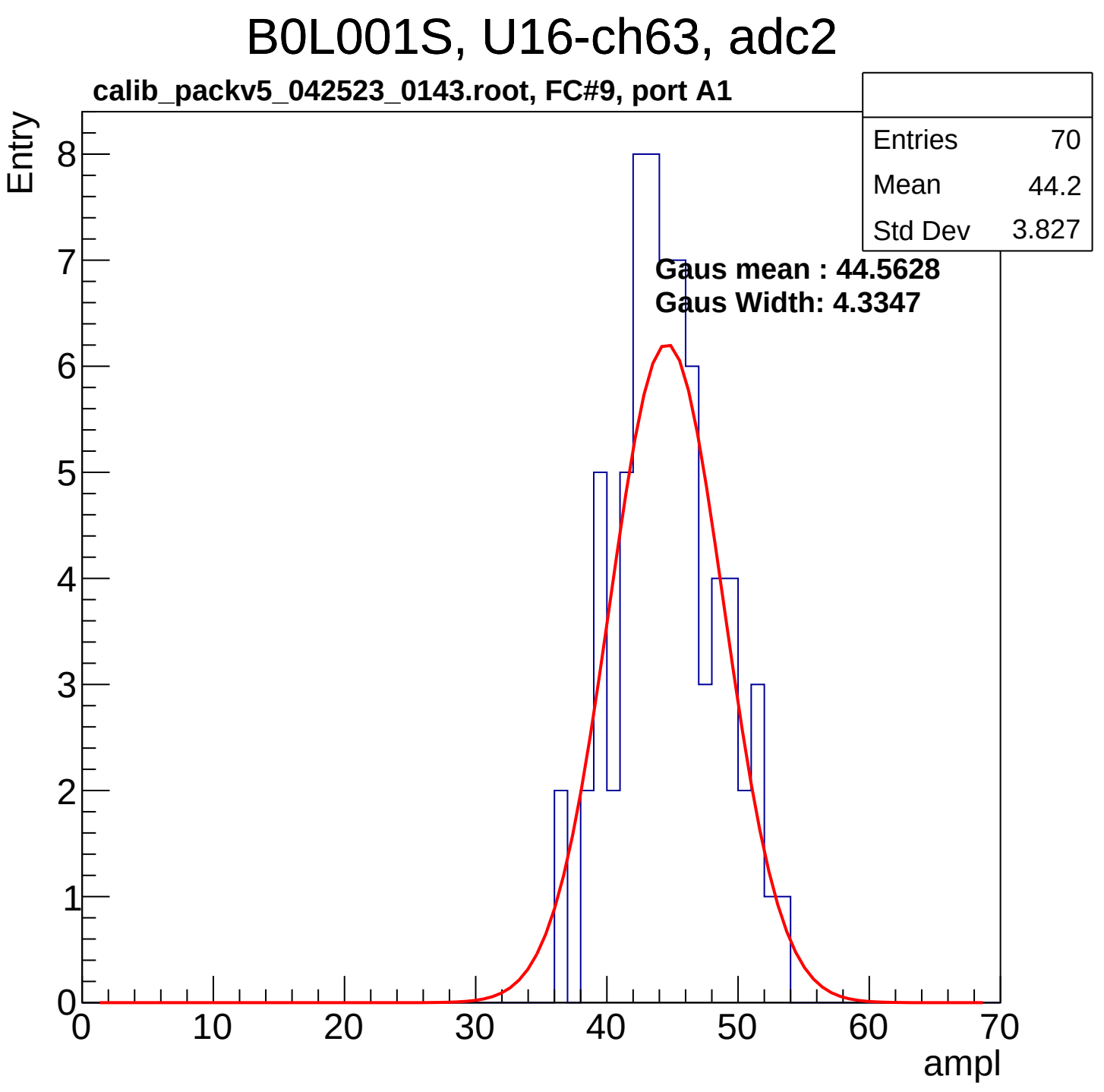

B0L001S, U16-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.81
Std Dev 2.687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

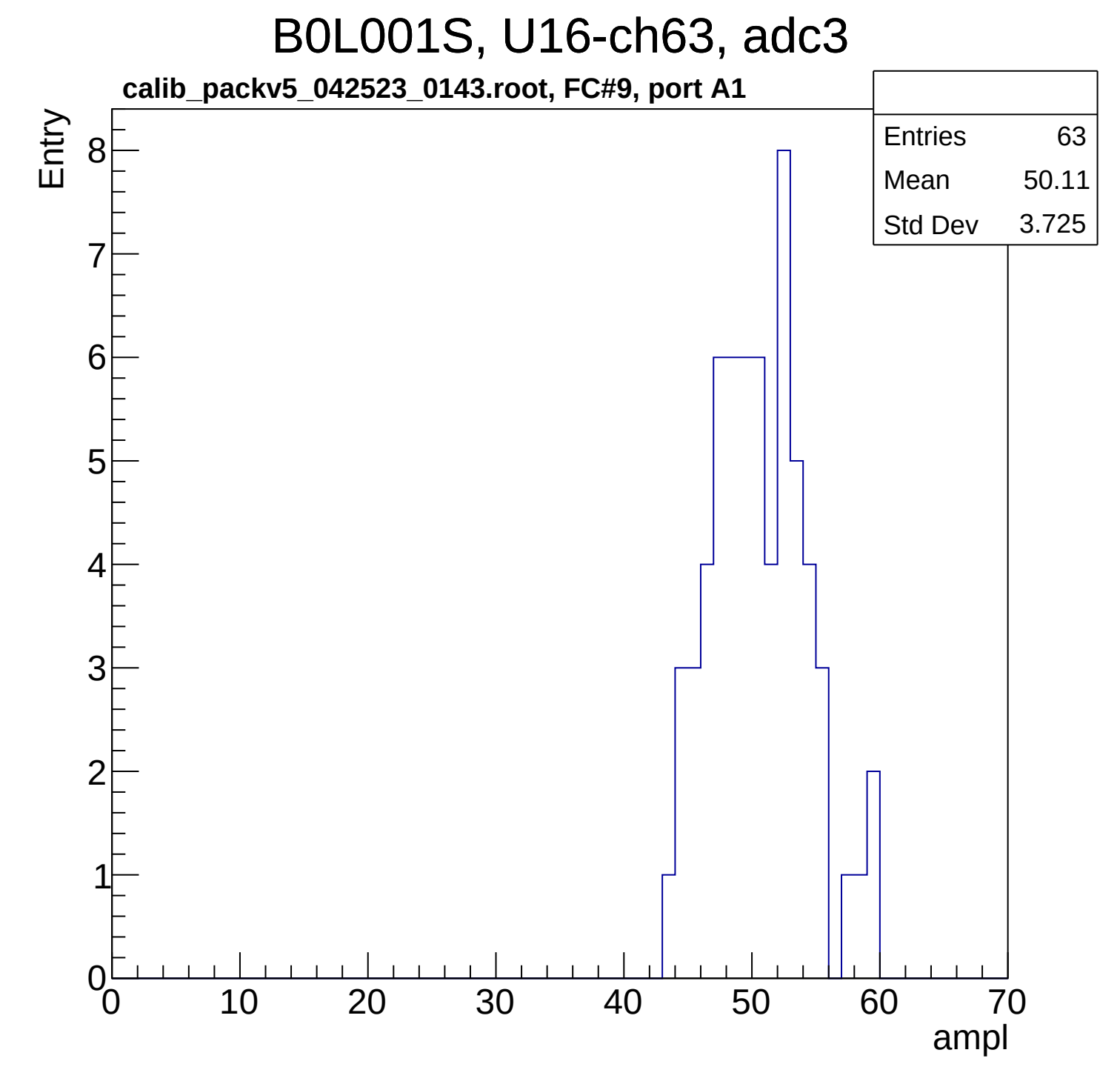

B0L001S, U16-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 52.43
Std Dev 21.45
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

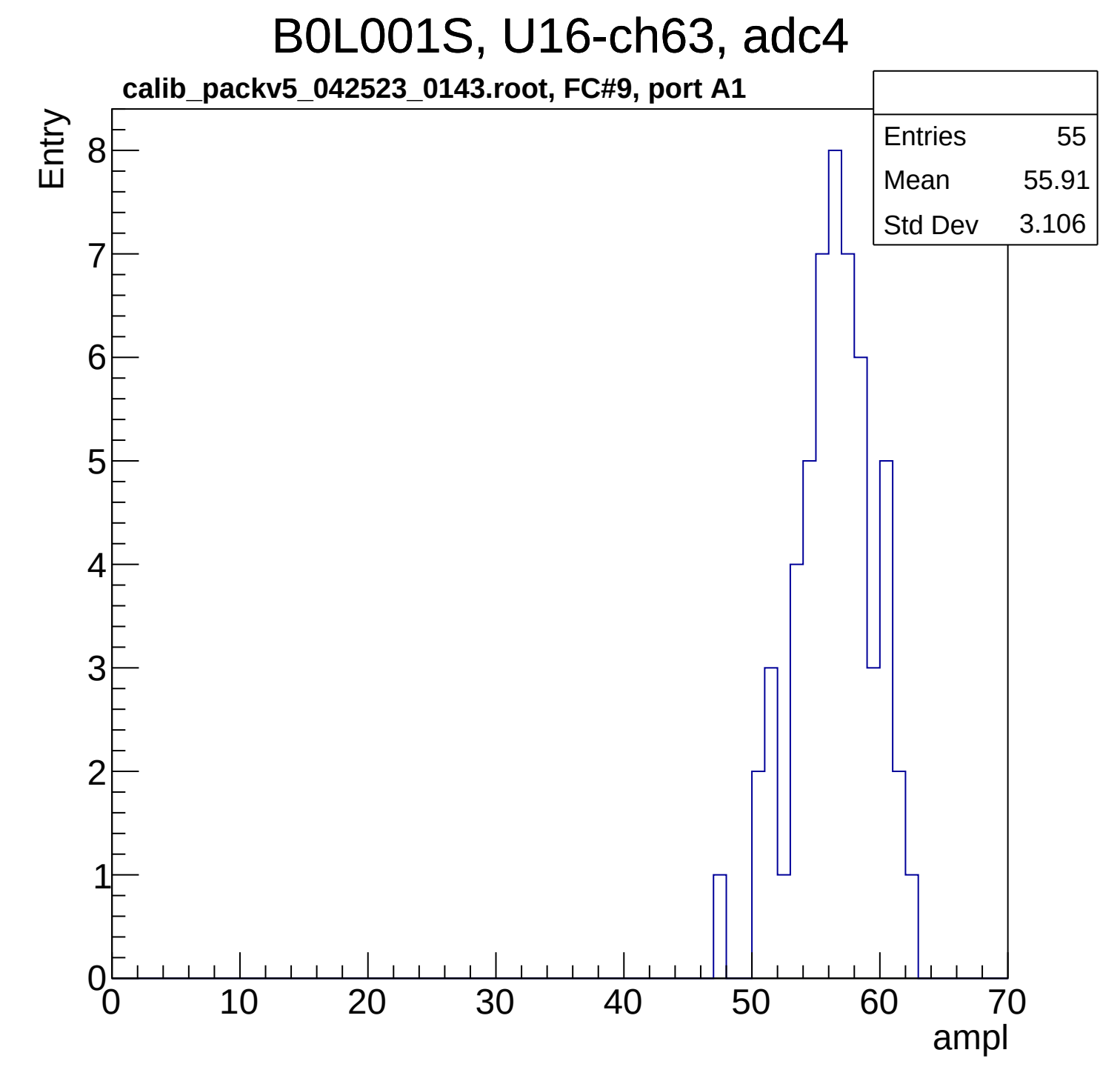

B0L001S, U16-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 28.66
Std Dev 4.963
Gaus mean : 28.9935
Gaus Width: 4.3863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 36.24
Std Dev 3.371
Gaus mean : 36.6773
Gaus Width: 3.7855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.2
Std Dev 3.827
Gaus mean : 44.5628
Gaus Width: 4.3347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.11
Std Dev 3.725
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 55.91
Std Dev 3.106
Entry
ampl
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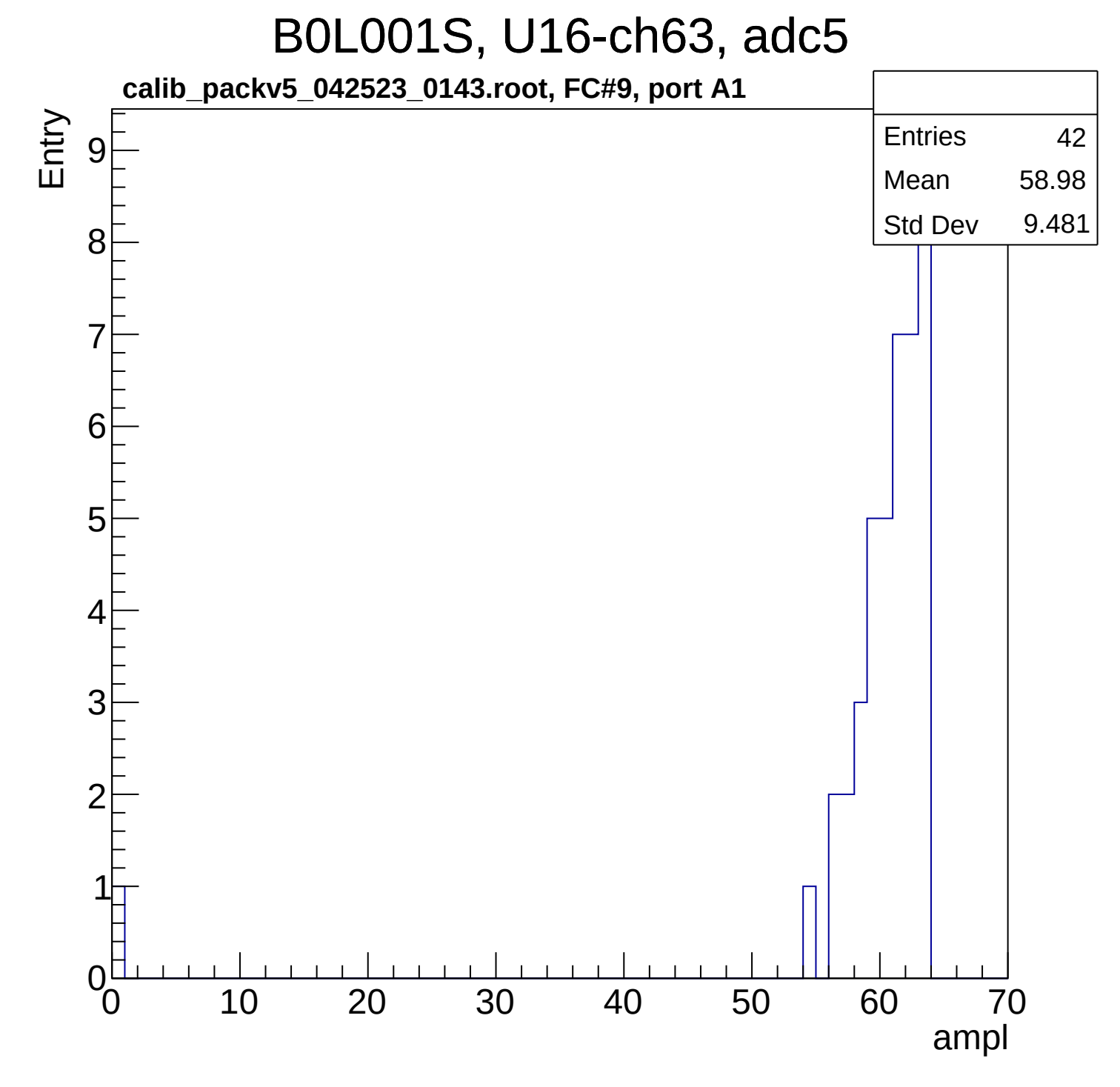
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.98
Std Dev 9.481
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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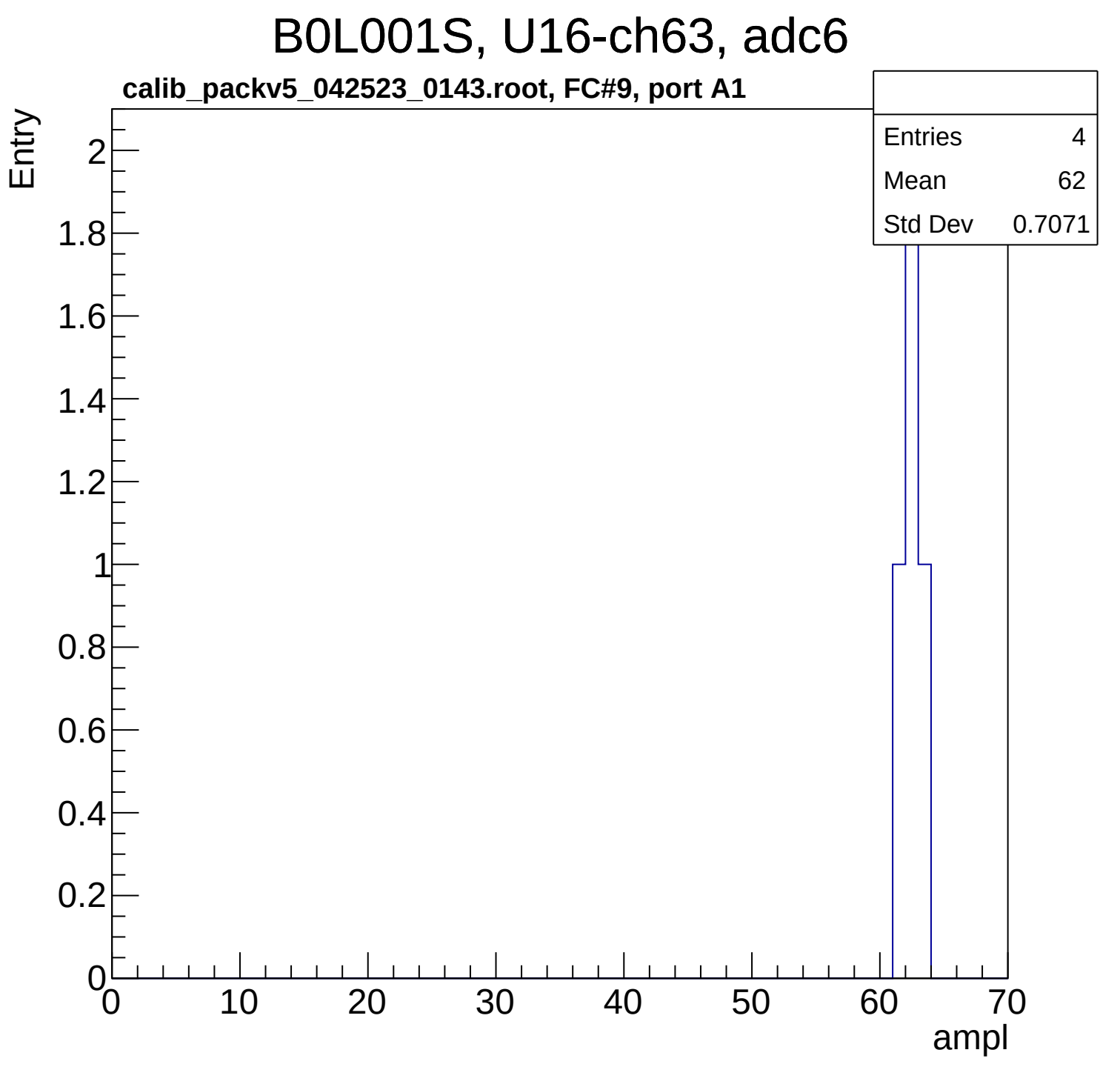
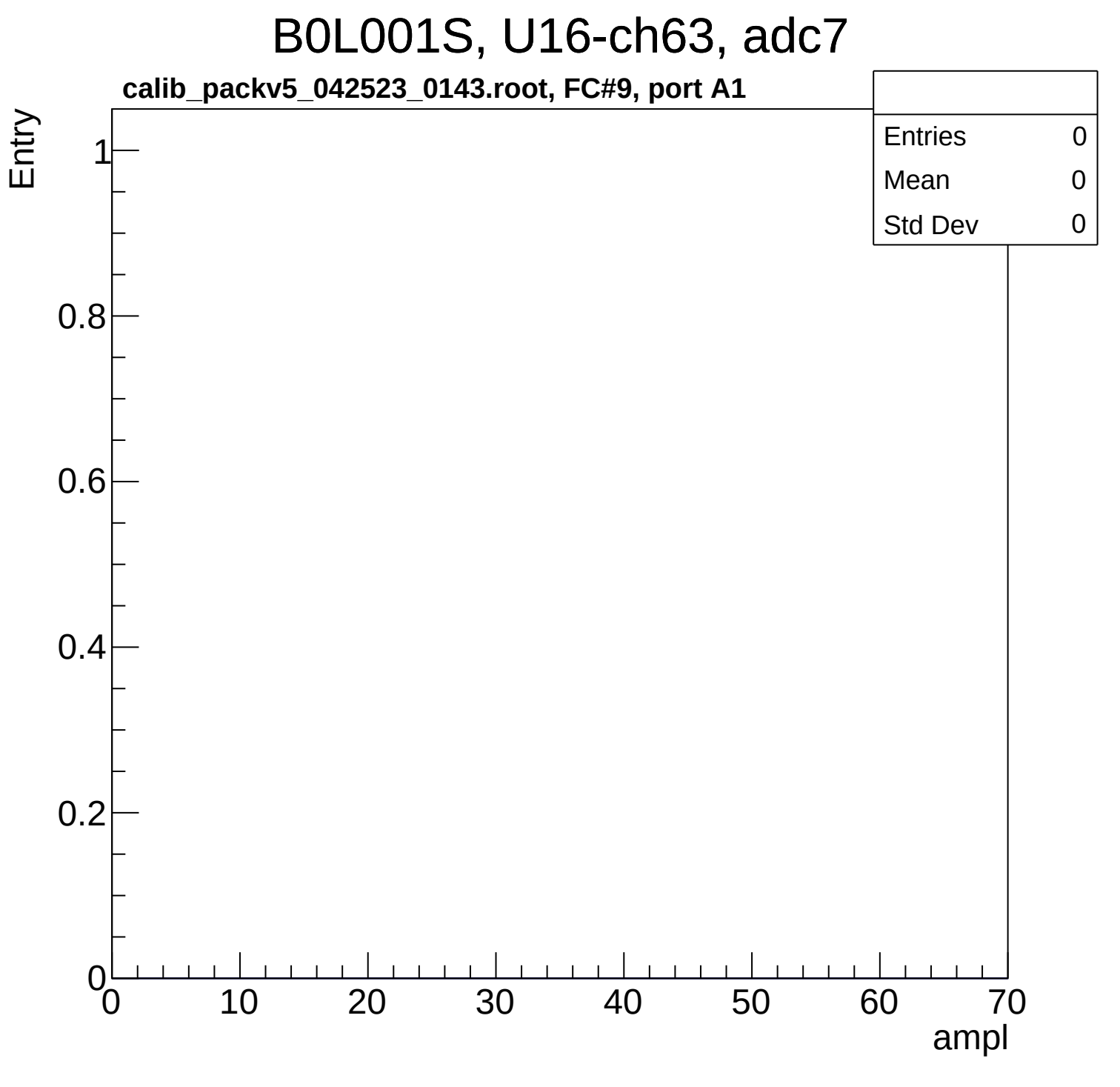
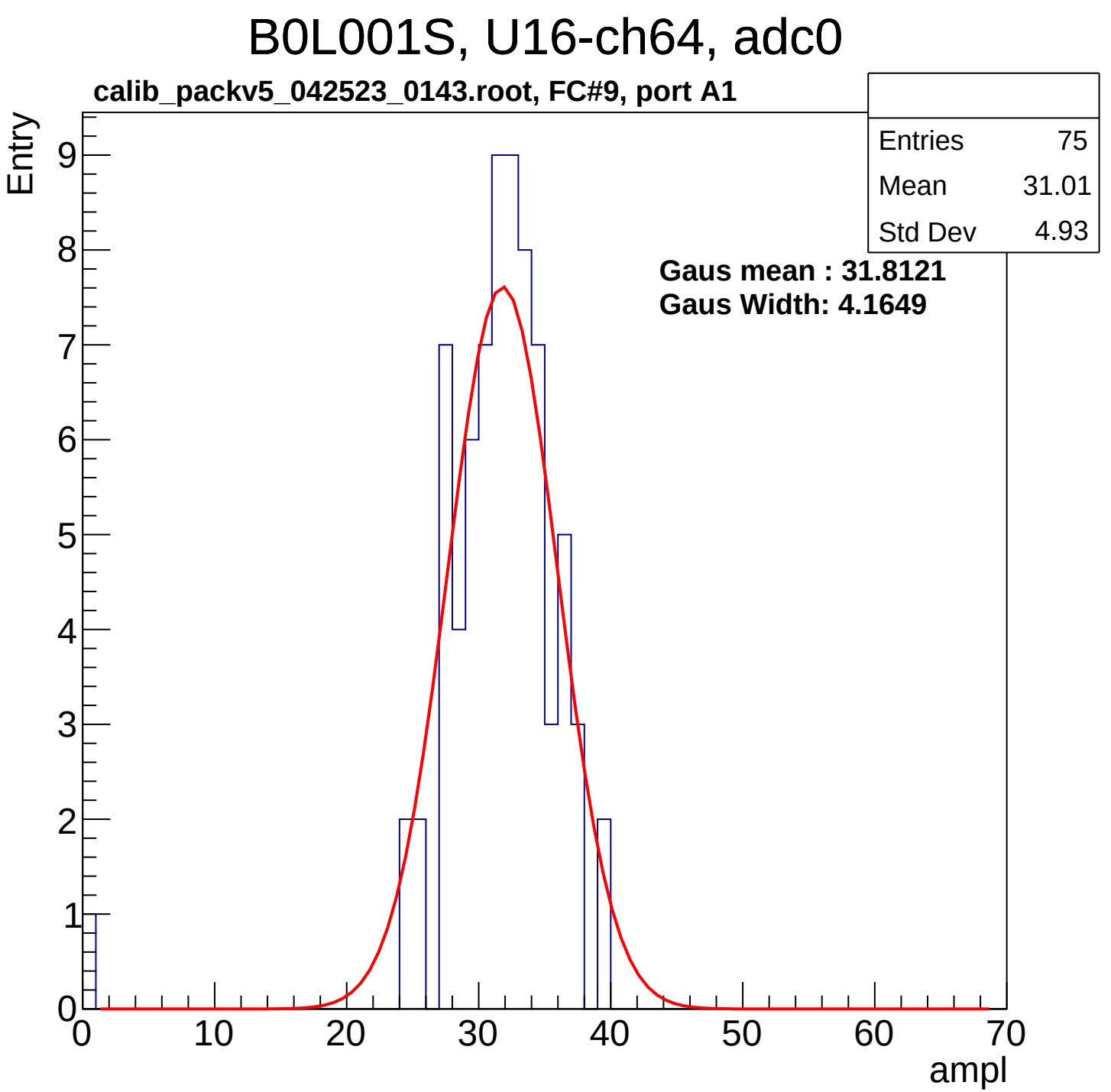
40
50
60
70


B0L001S, U16-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 31.01
Std Dev 4.93
Gaus mean : 31.8121
Gaus Width: 4.1649
Entry
ampl
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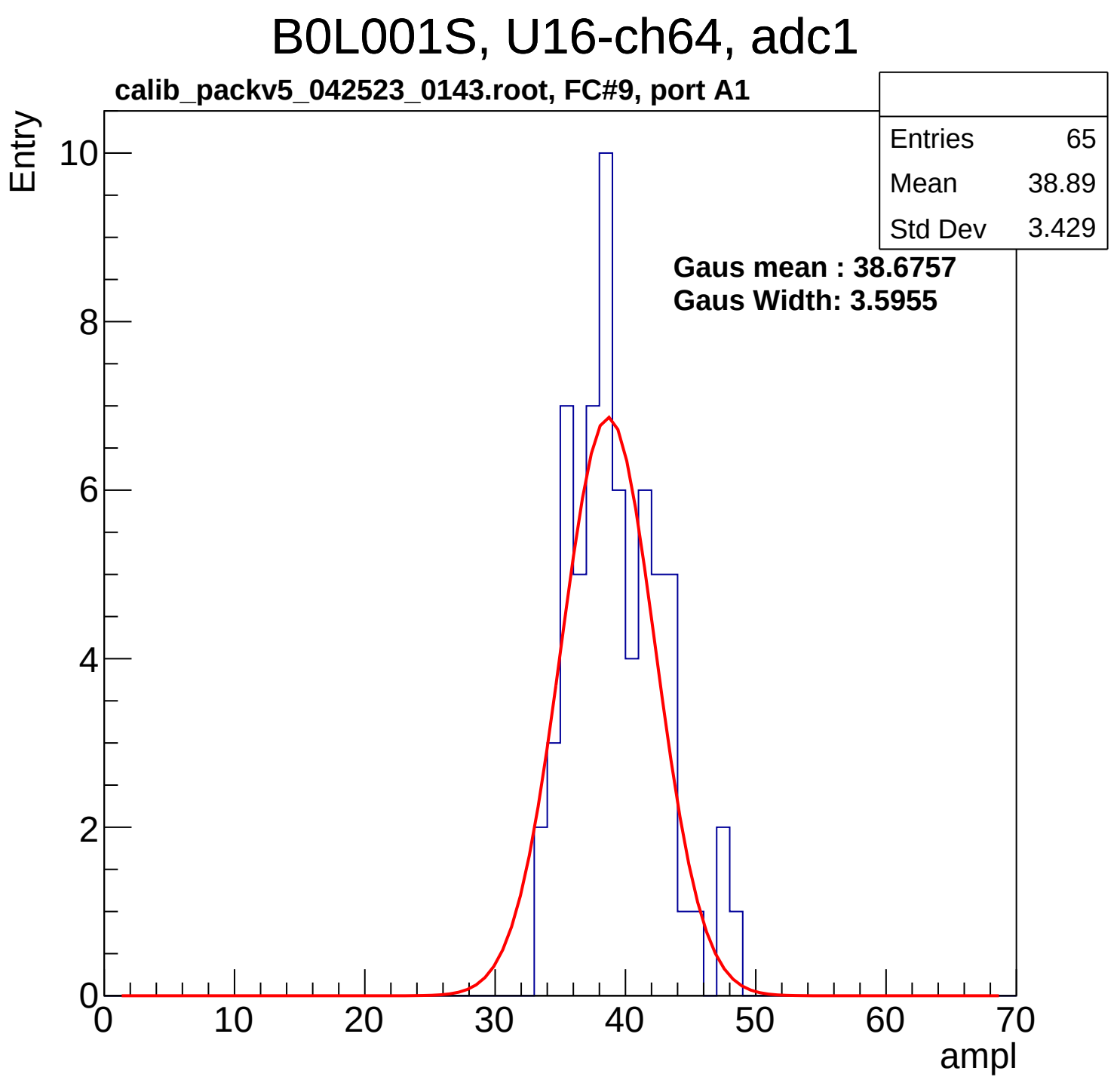
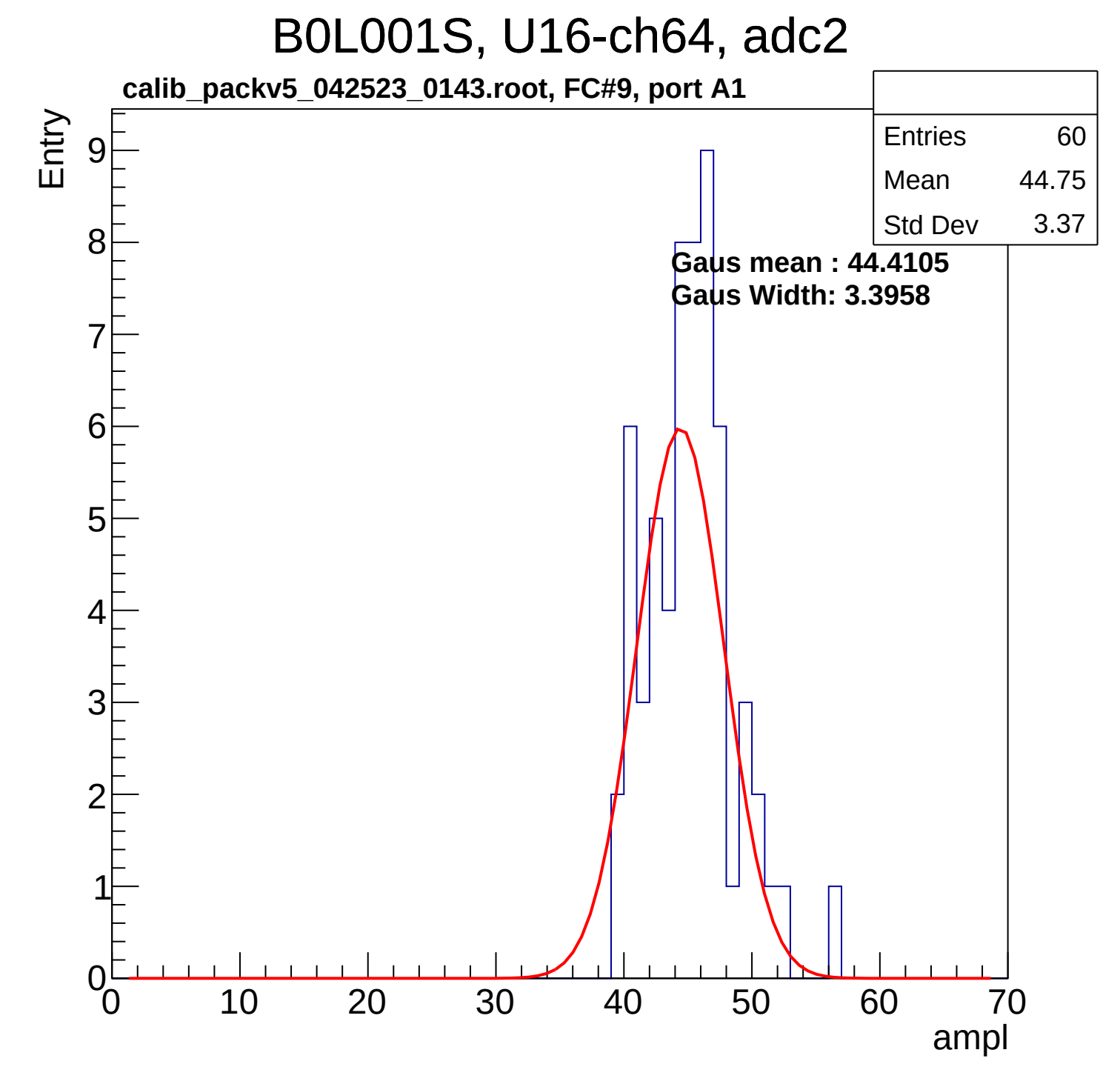
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.89
Std Dev 3.429
Gaus mean : 38.6757
Gaus Width: 3.5955
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.75
Std Dev 3.37
Gaus mean : 44.4105
Gaus Width: 3.3958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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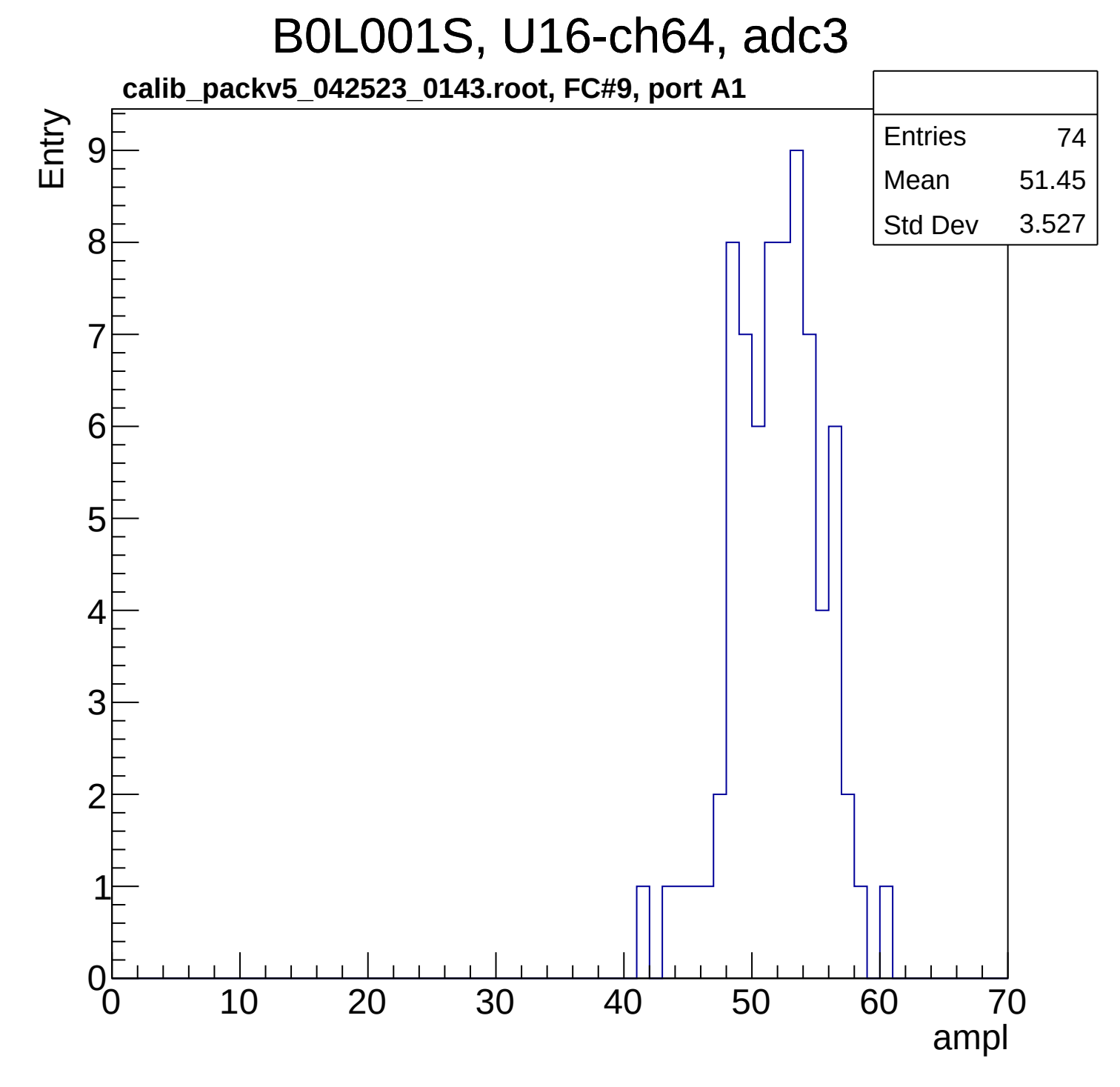
B0L001S, U16-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.45
Std Dev 3.527
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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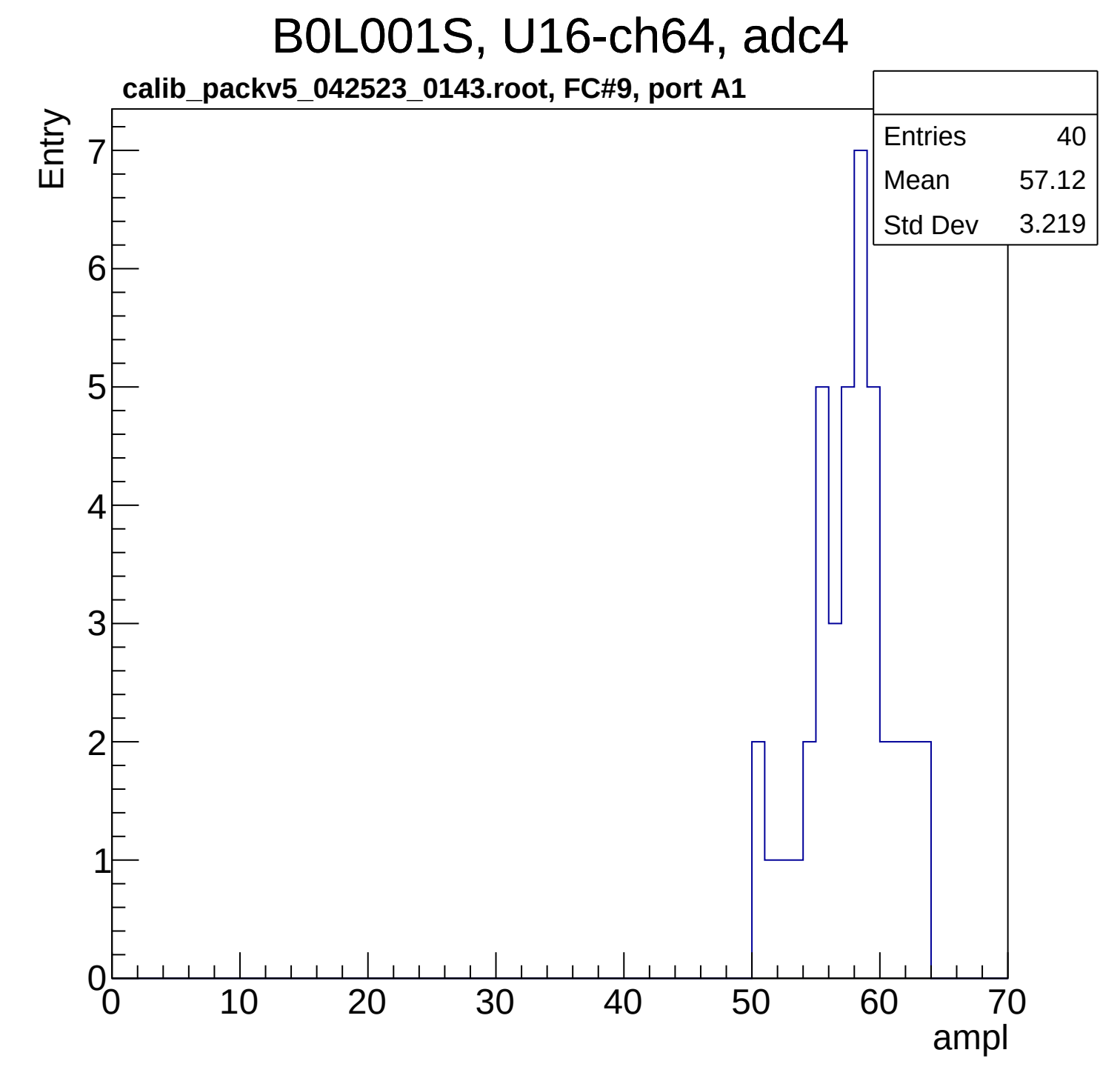
B0L001S, U16-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 57.12
Std Dev 3.219
Entry
ampl
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1
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3
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5
6
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0
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20
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40
50
60
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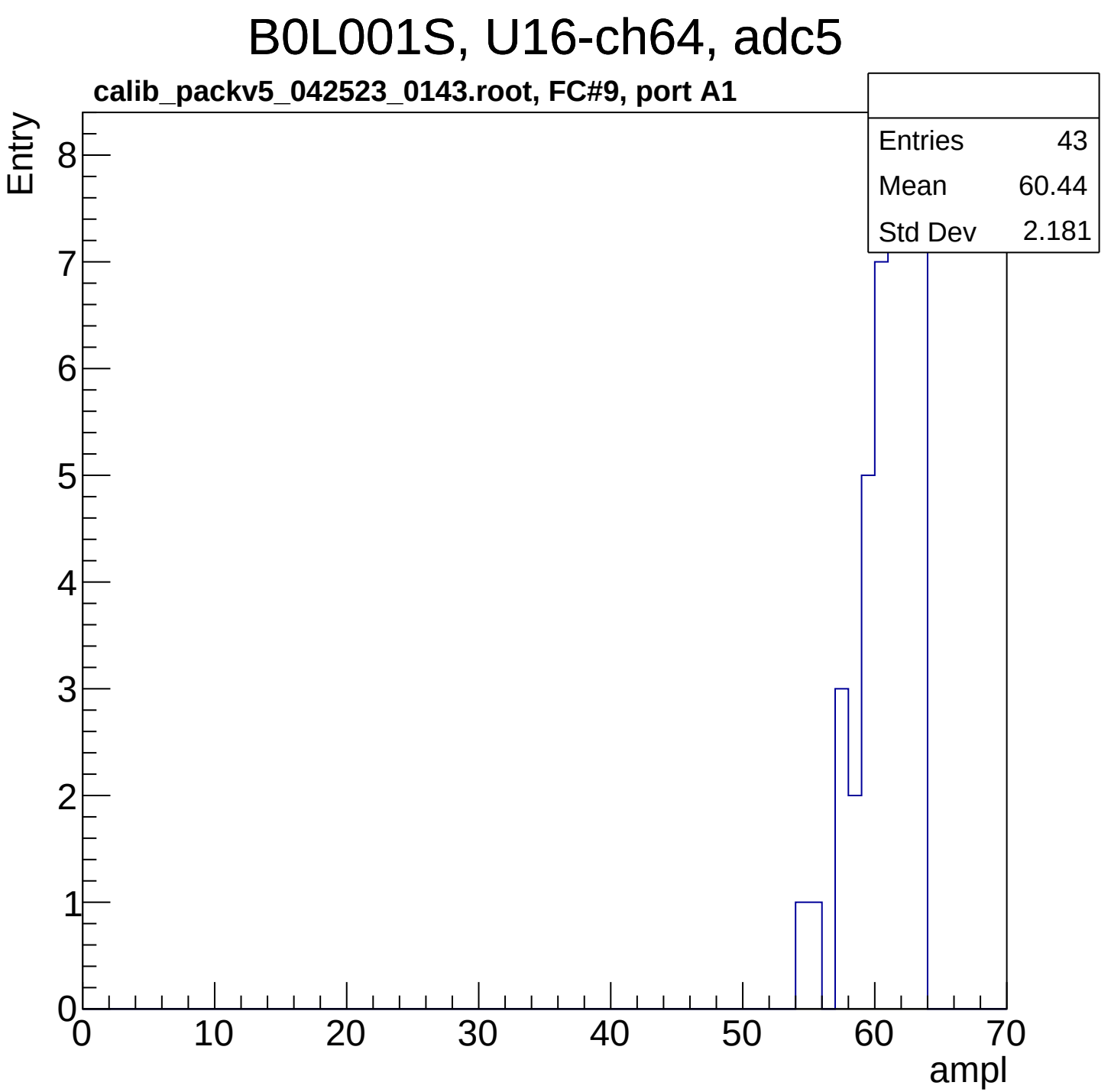
B0L001S, U16-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 60.44
Std Dev 2.181
Entry
0
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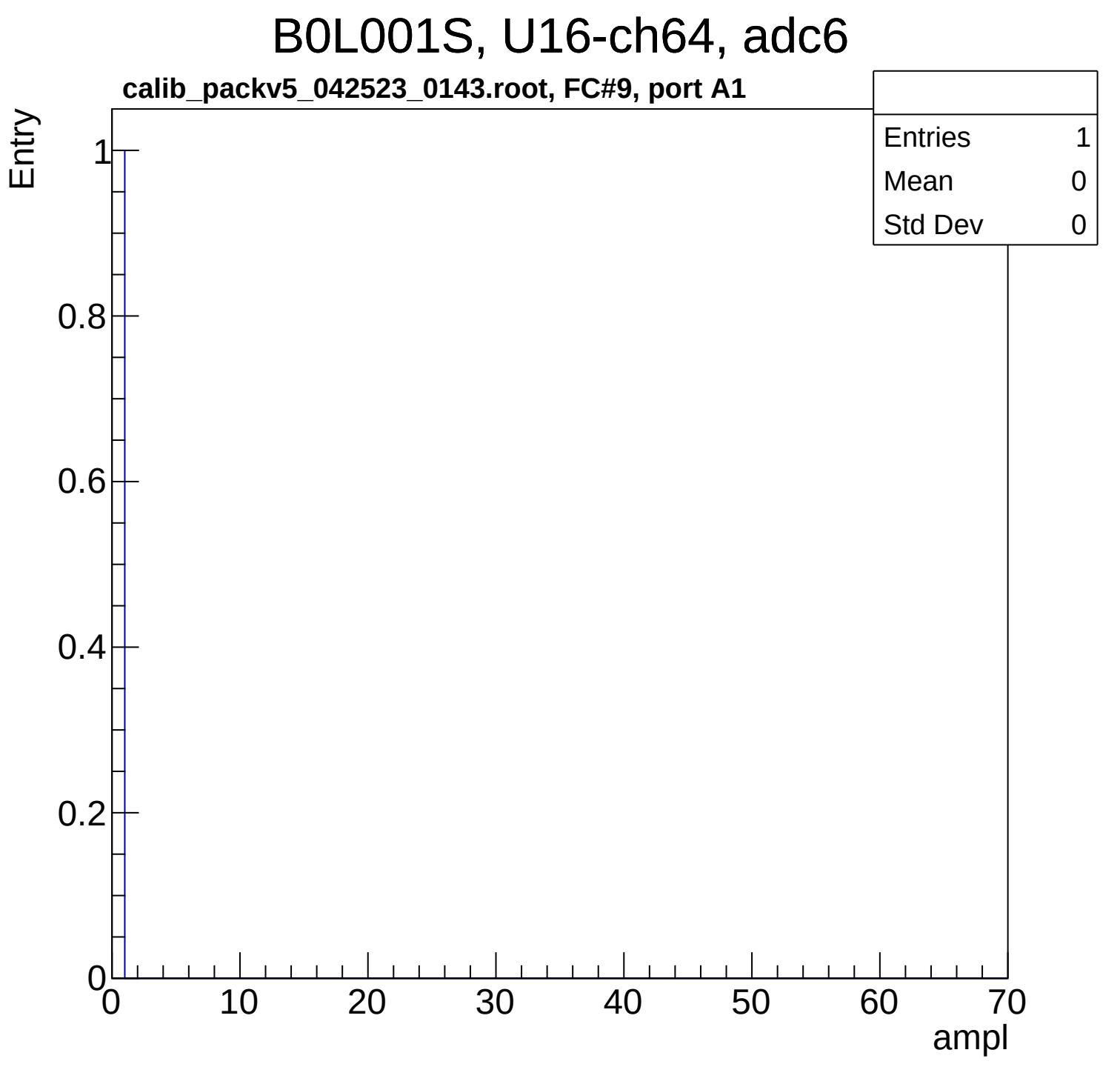
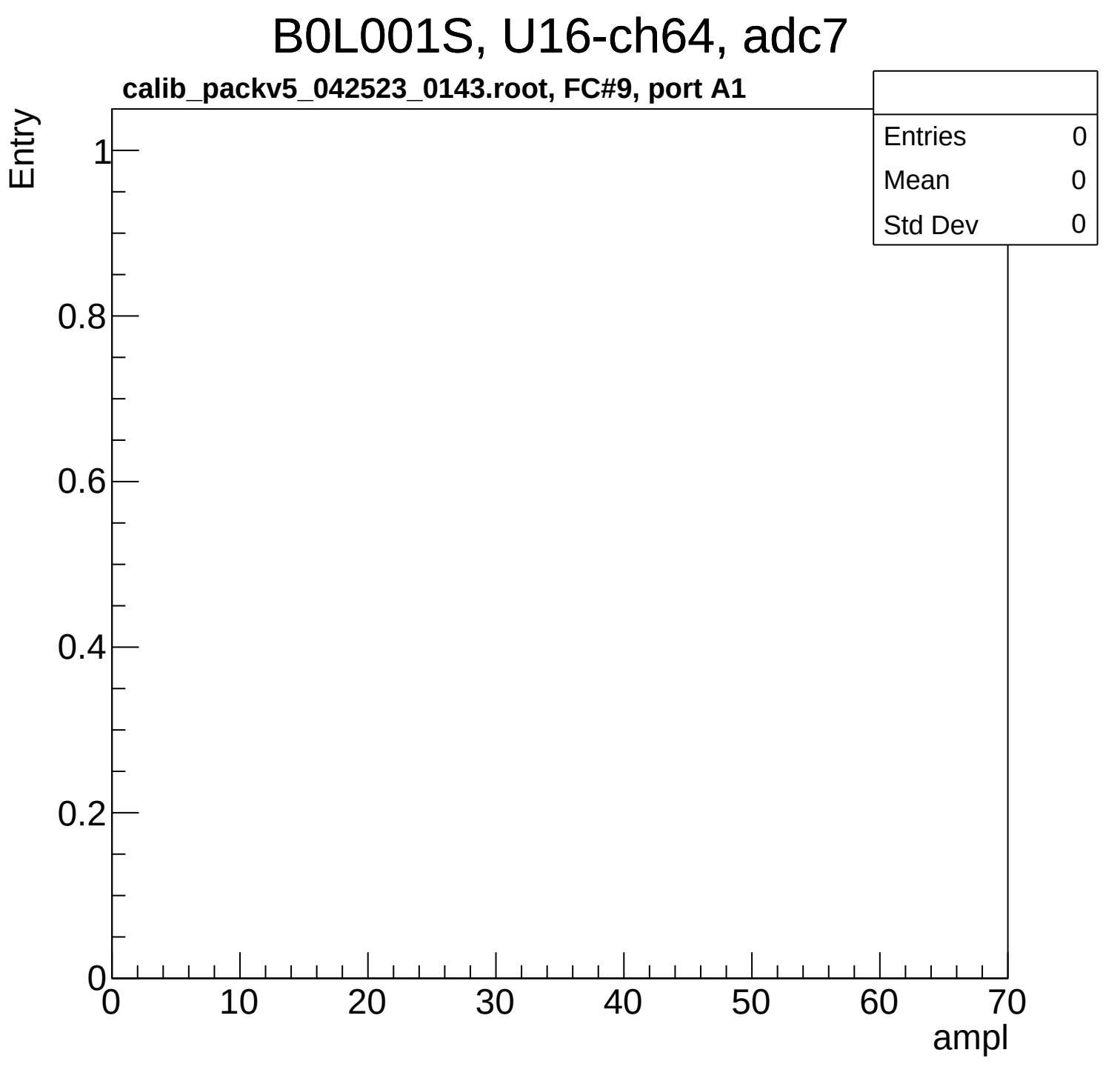
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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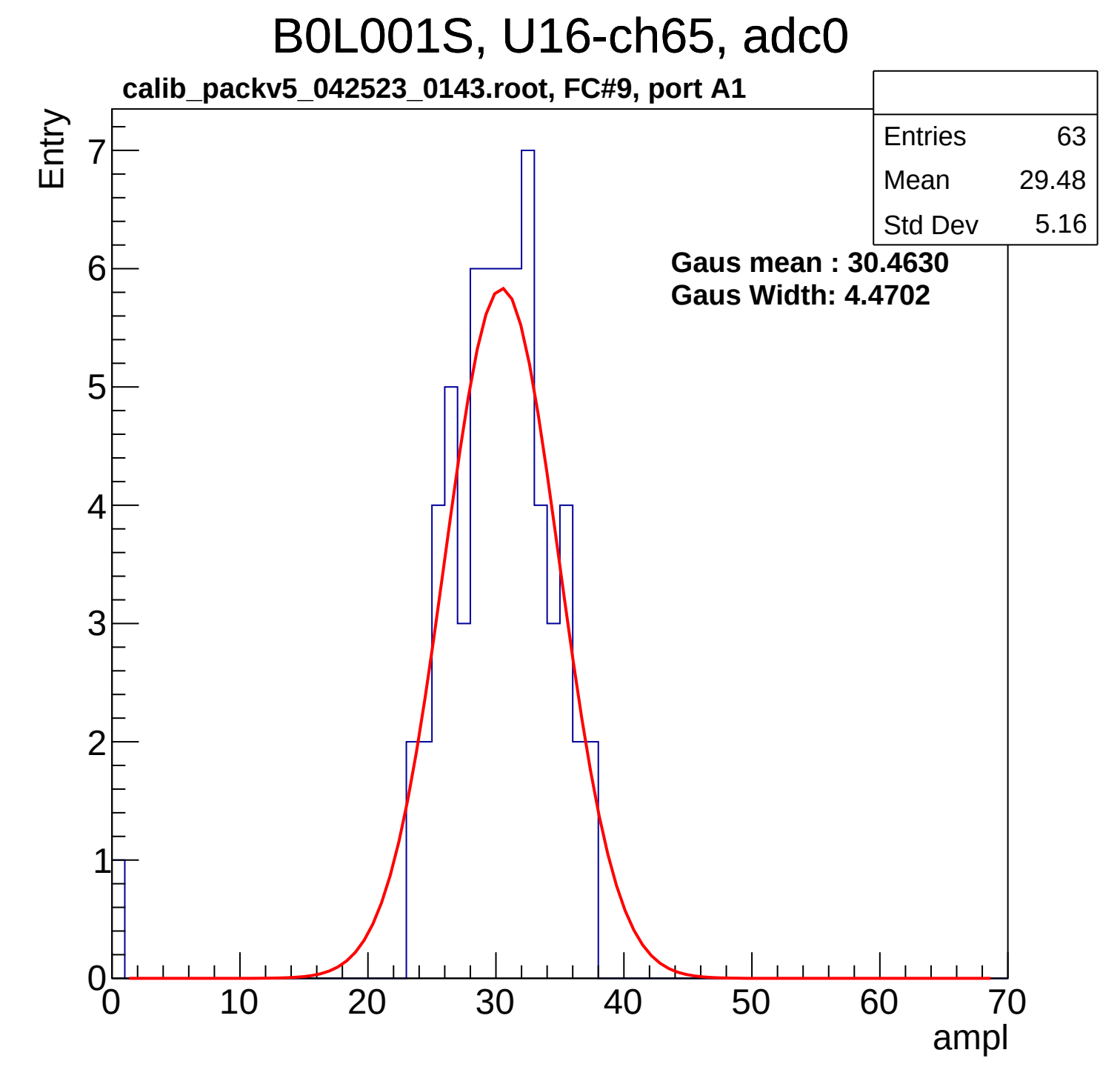
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.48
Std Dev 5.16
Gaus mean : 30.4630
Gaus Width: 4.4702
Entry
ampl
0
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3
4
5
6
7
0
10
20
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40
50
60
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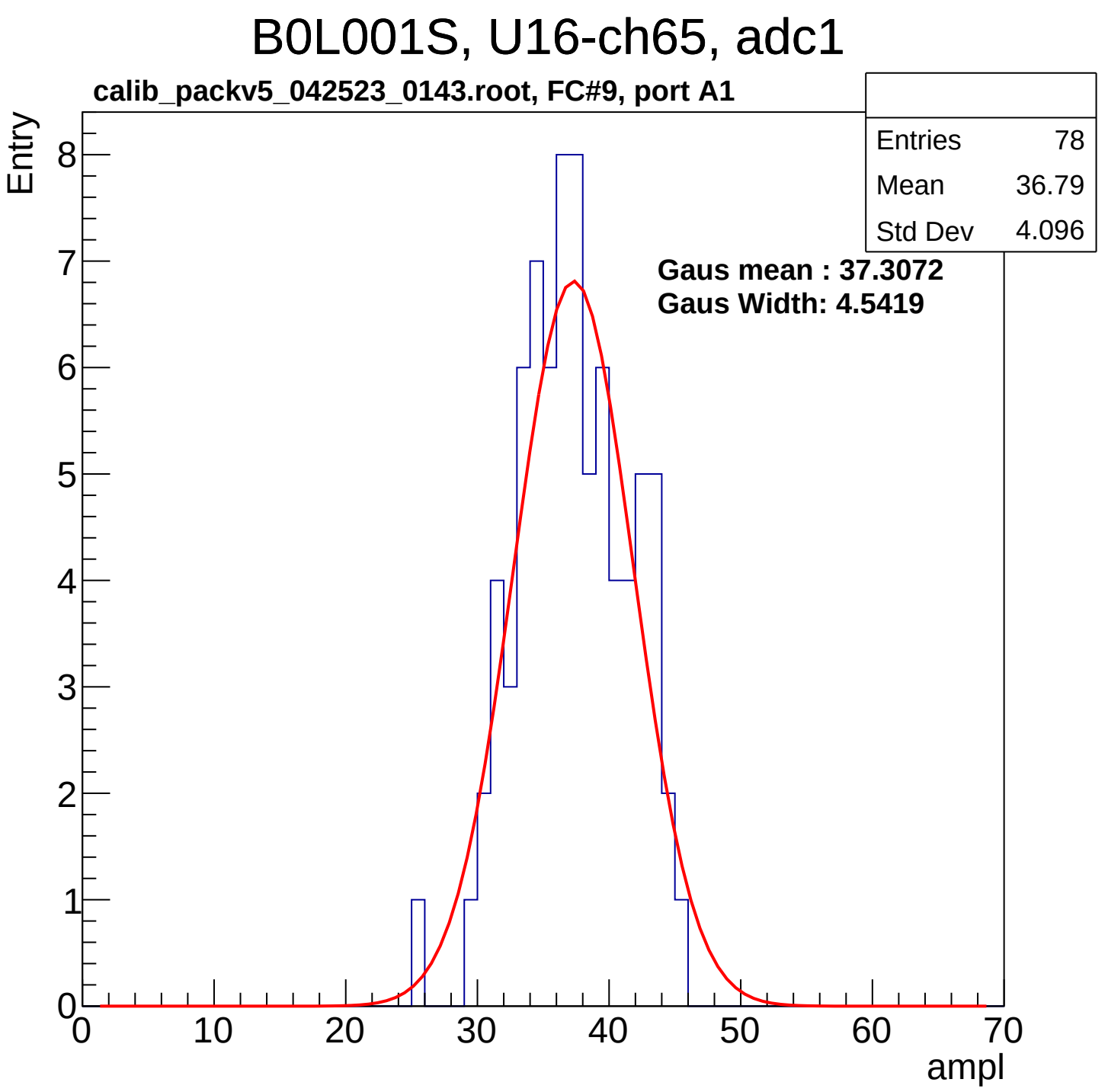
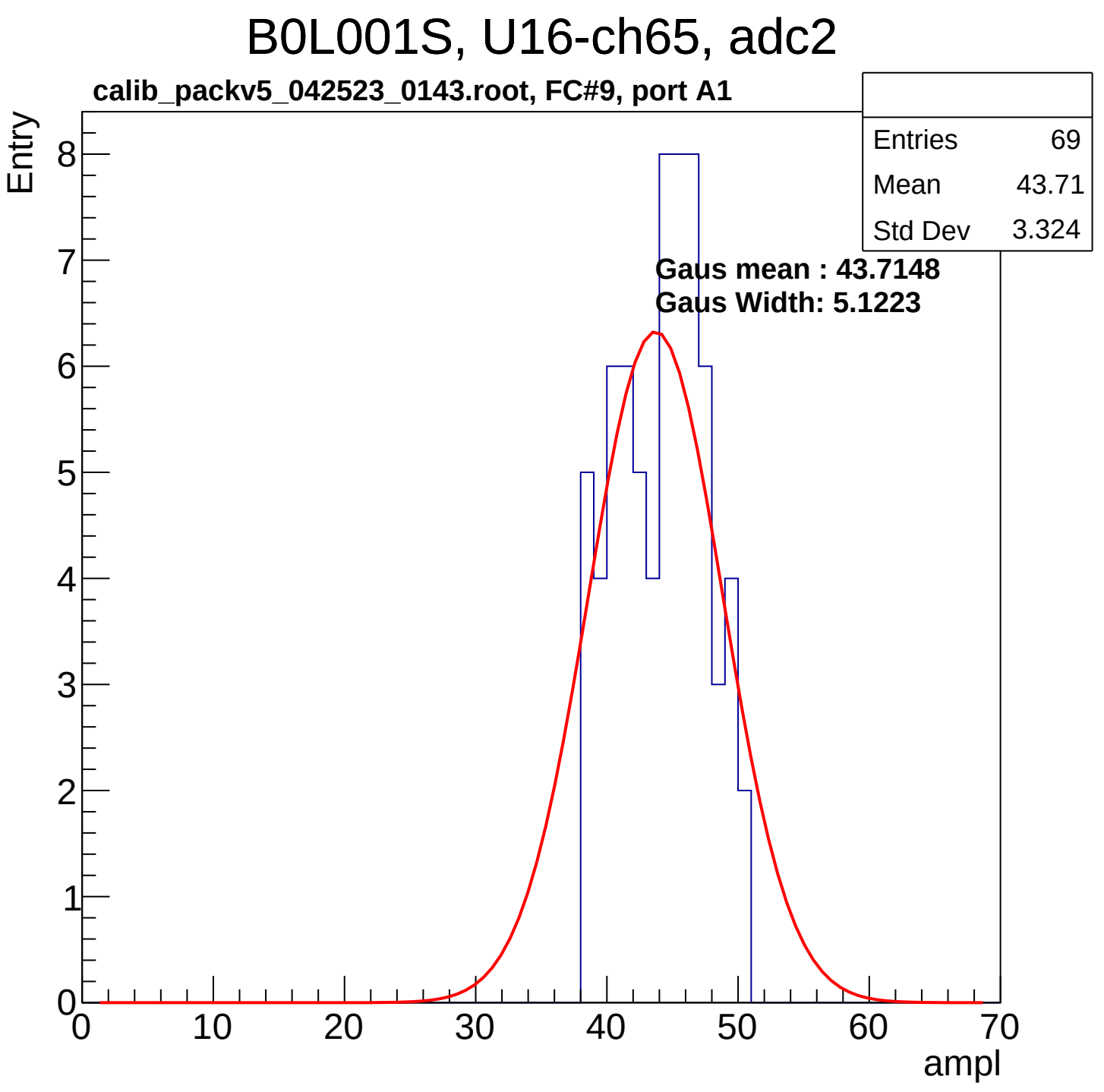
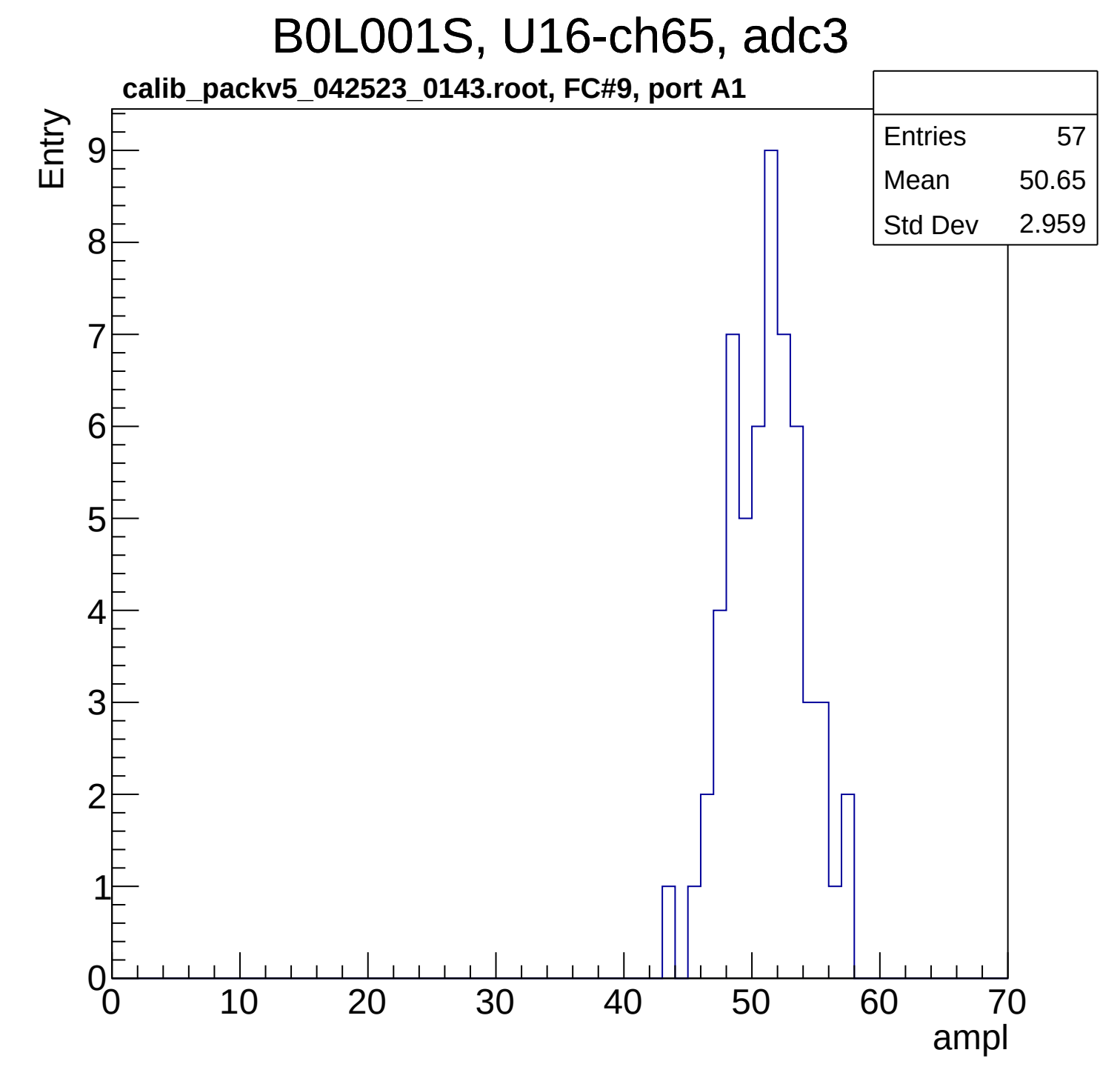
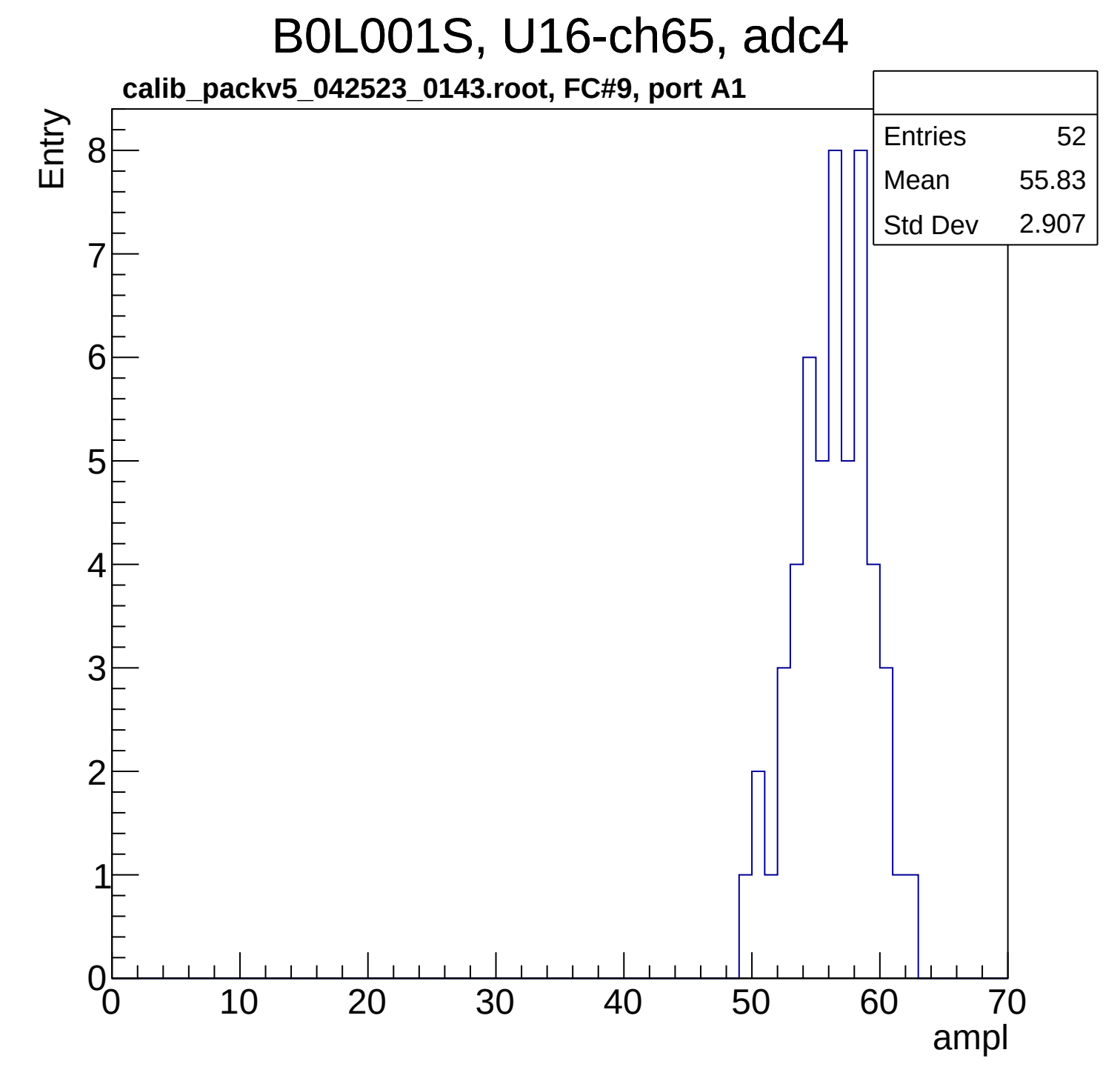

B0L001S, U16-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 36.79
Std Dev 4.096
Gaus mean : 37.3072
Gaus Width: 4.5419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.71
Std Dev 3.324
Gaus mean : 43.7148
Gaus Width: 5.1223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.65
Std Dev 2.959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.83
Std Dev 2.907
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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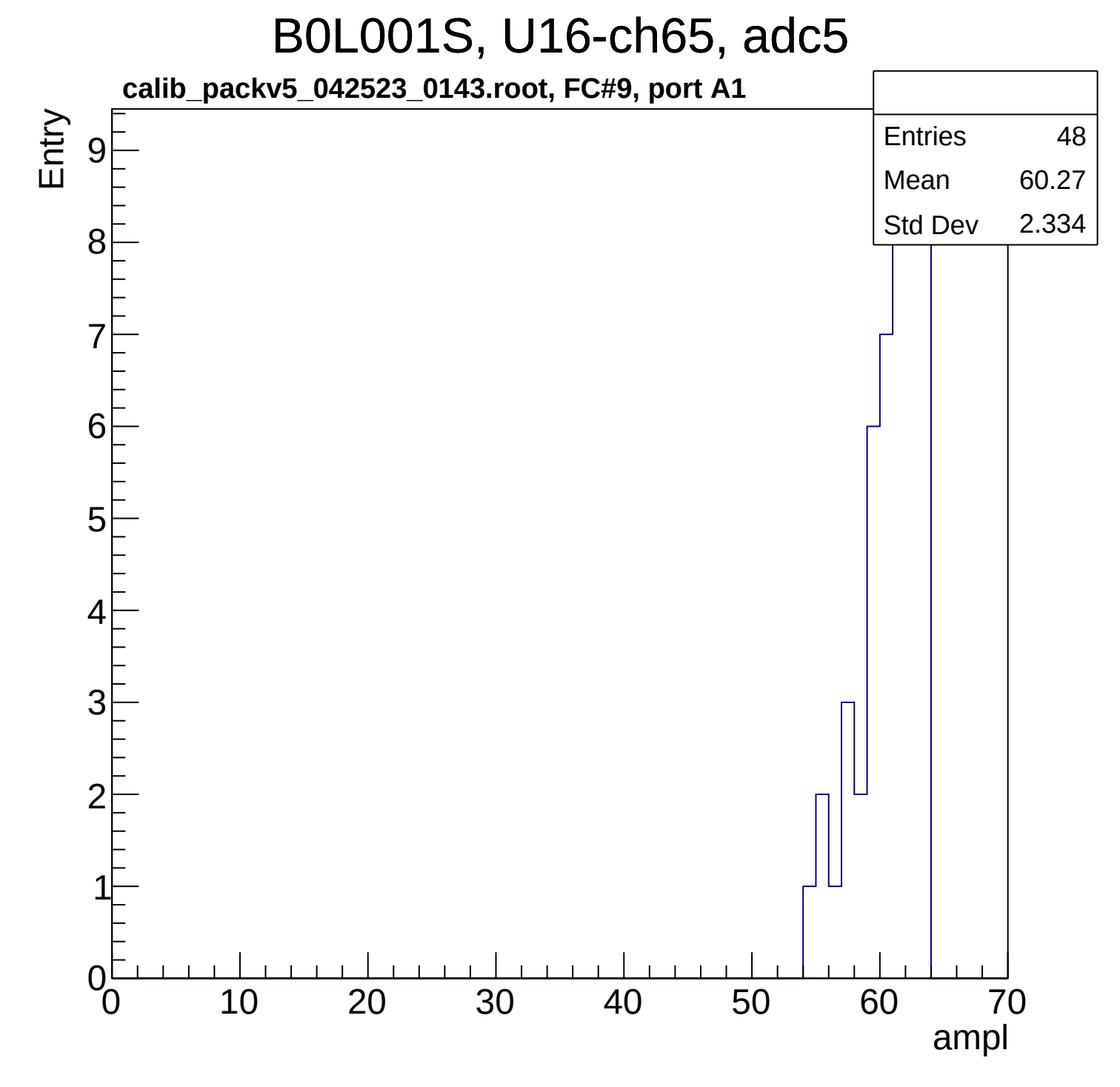
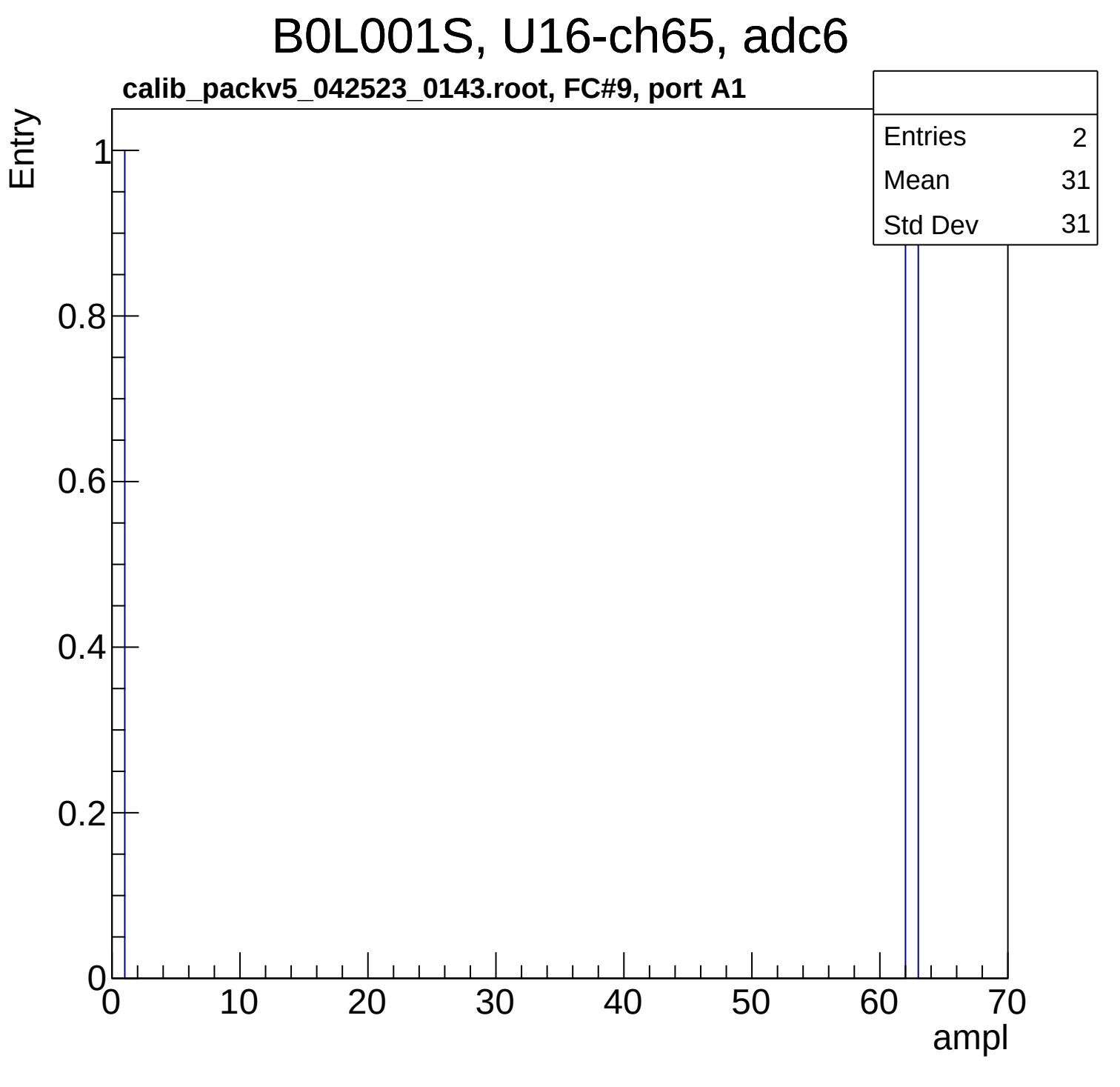
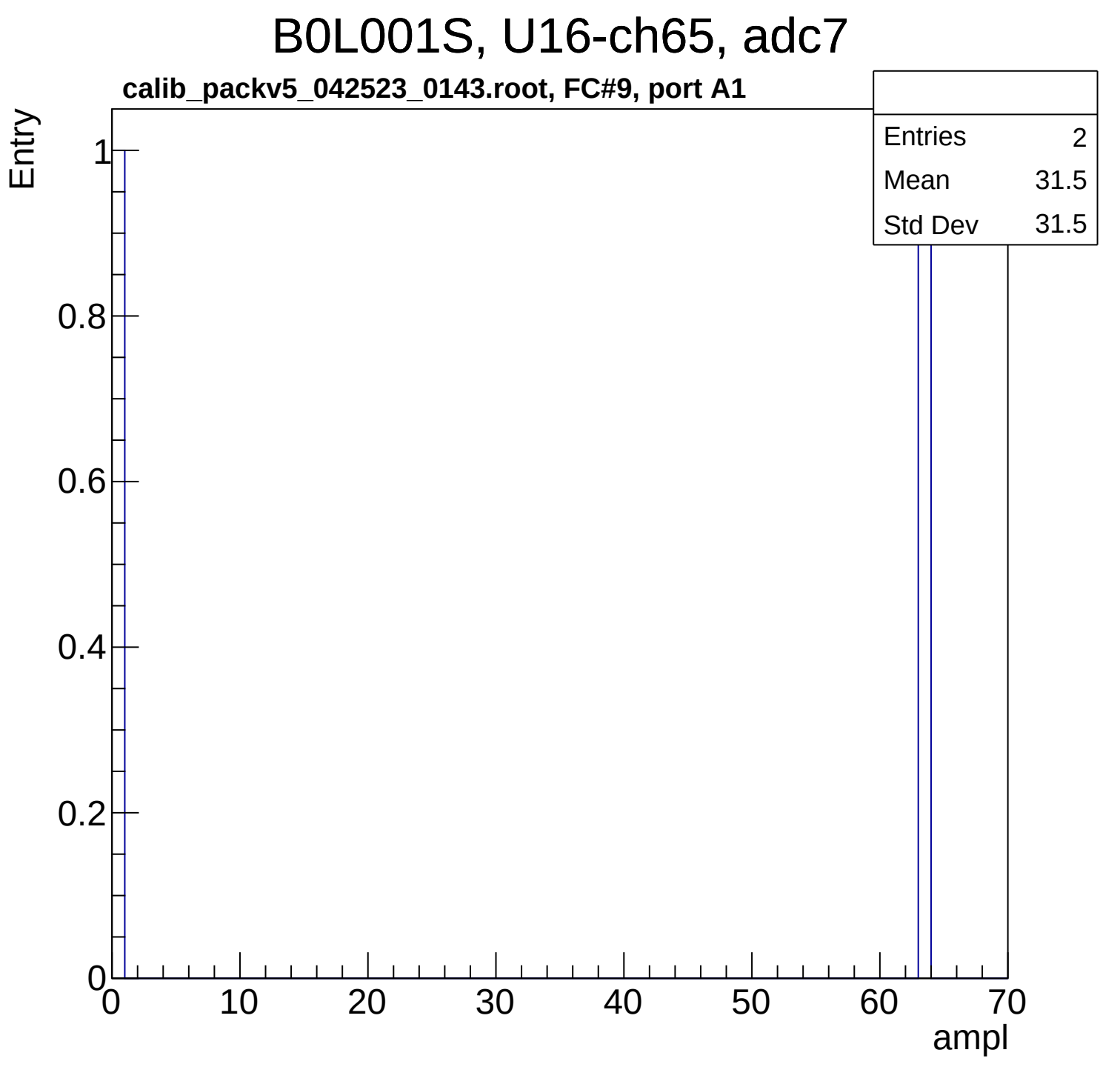
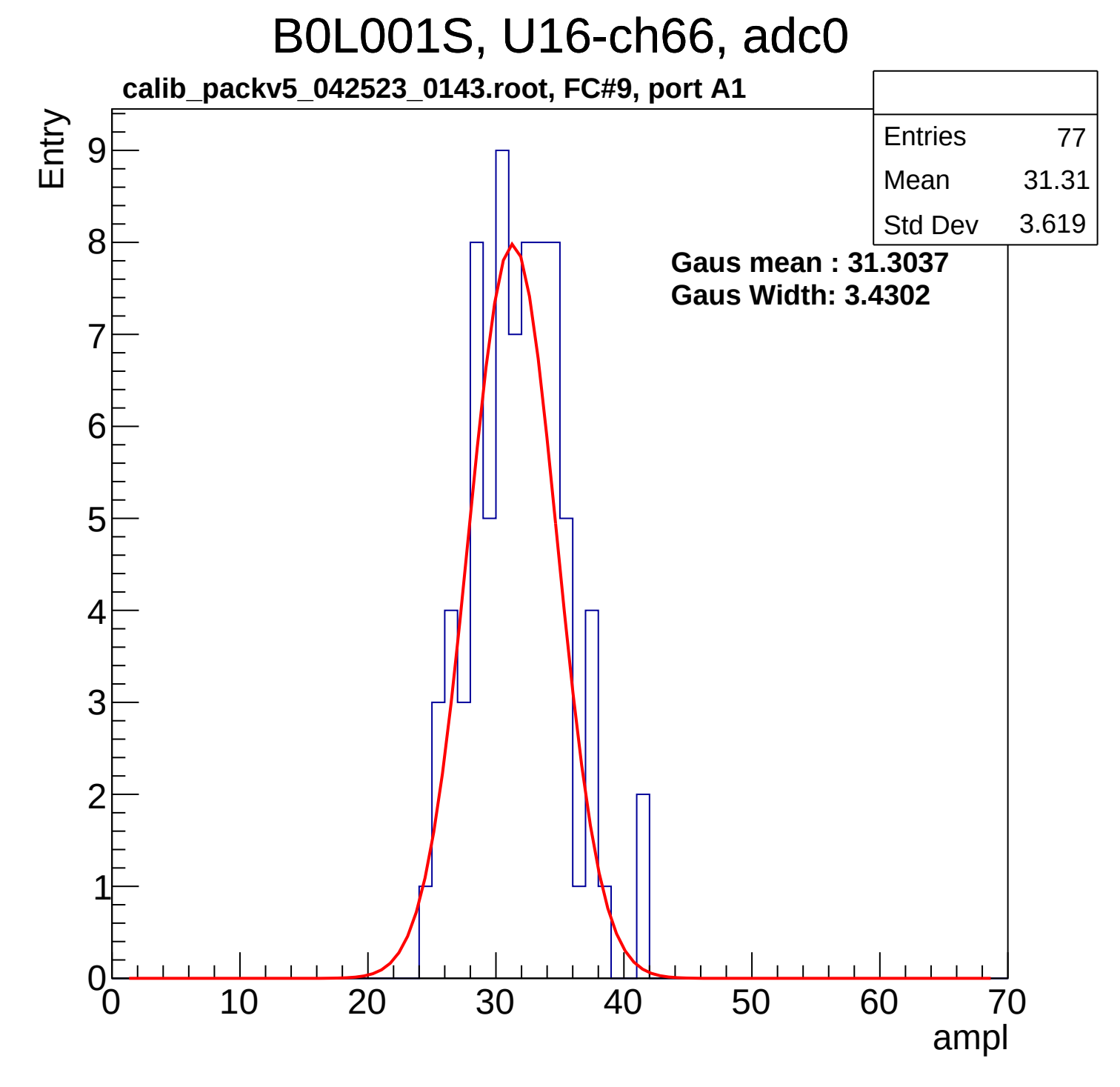
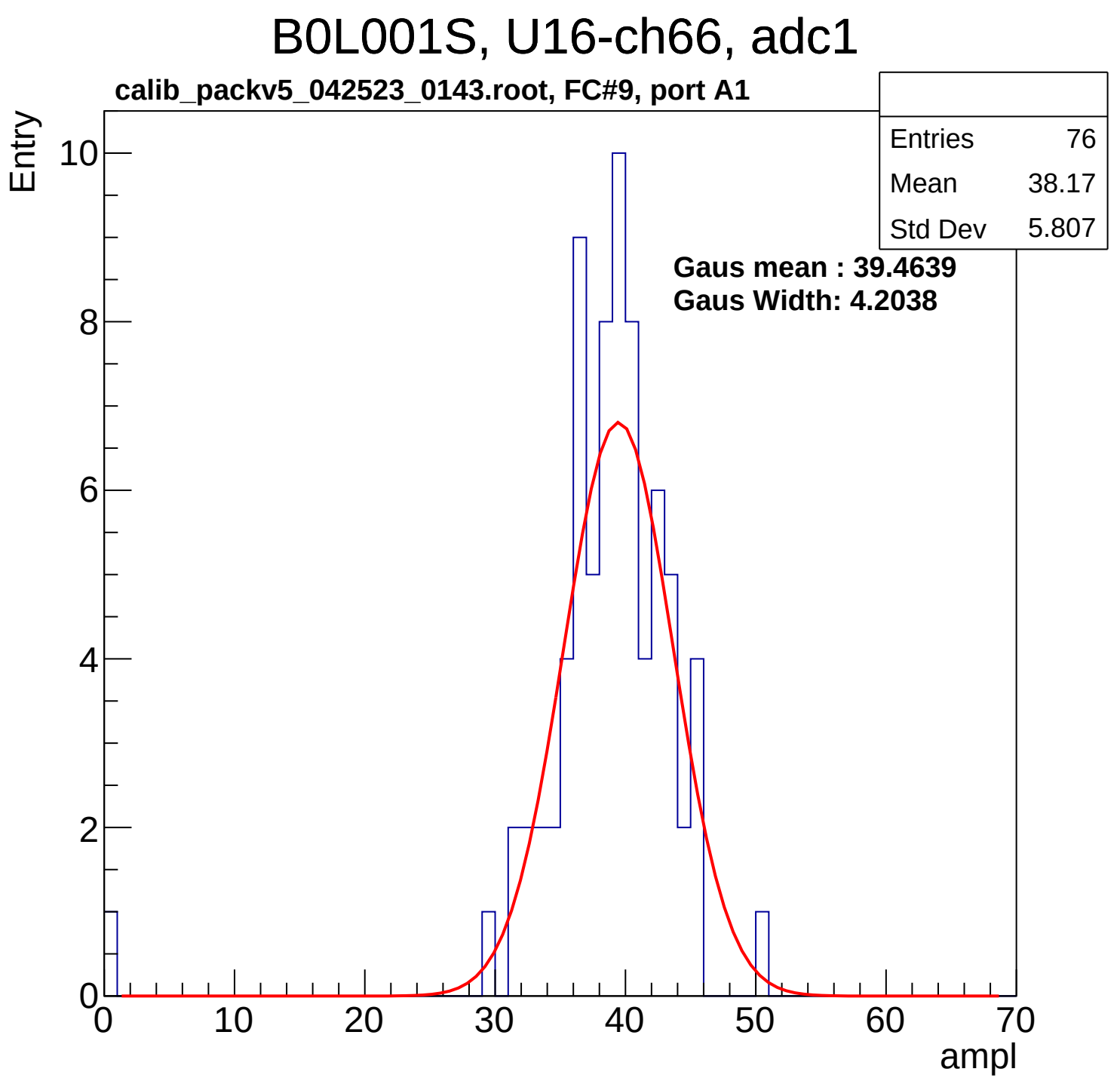

B0L001S, U16-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 60.27
Std Dev 2.334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.31
Std Dev 3.619
Gaus mean : 31.3037
Gaus Width: 3.4302
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.17
Std Dev 5.807
Gaus mean : 39.4639
Gaus Width: 4.2038
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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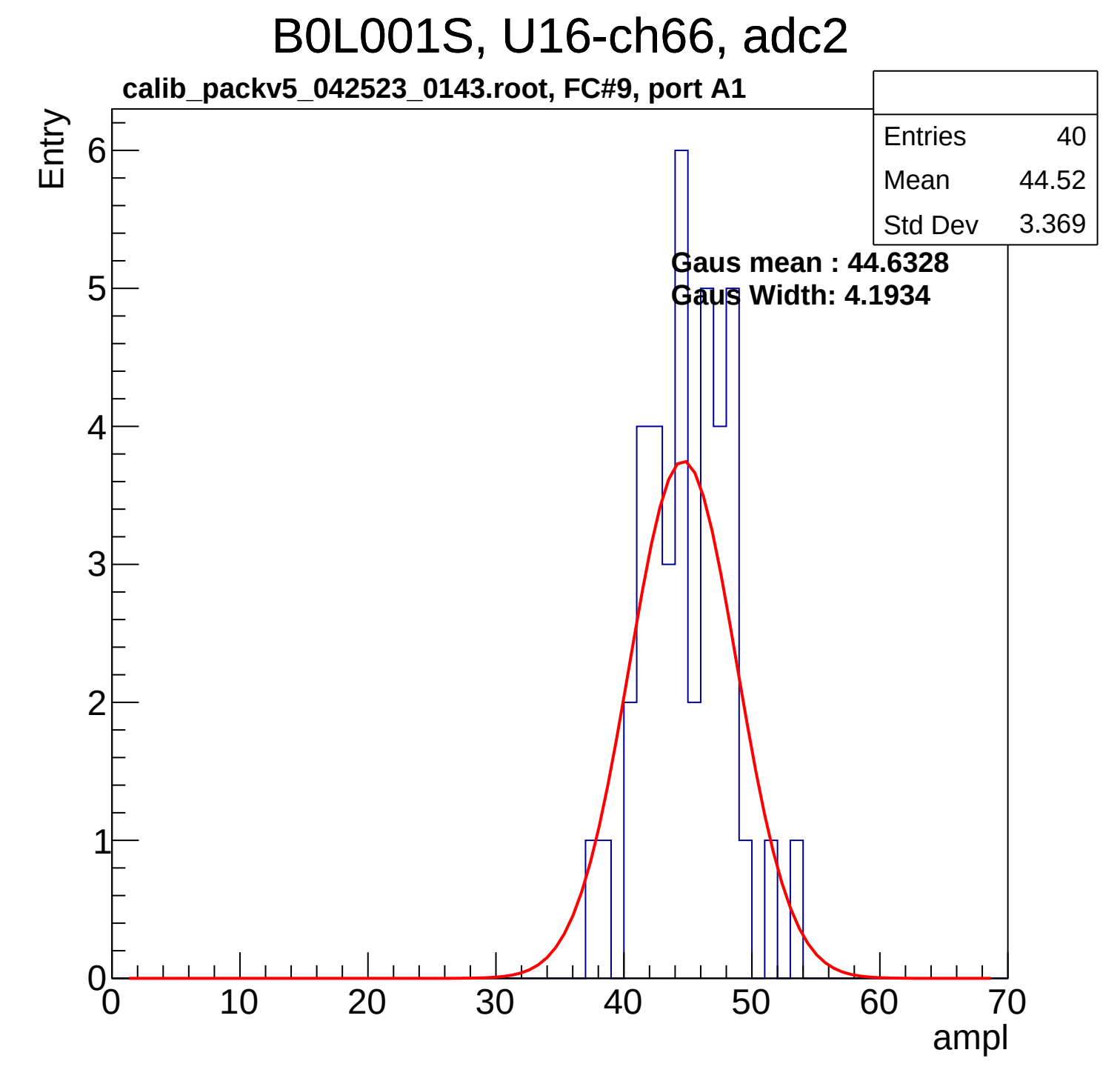
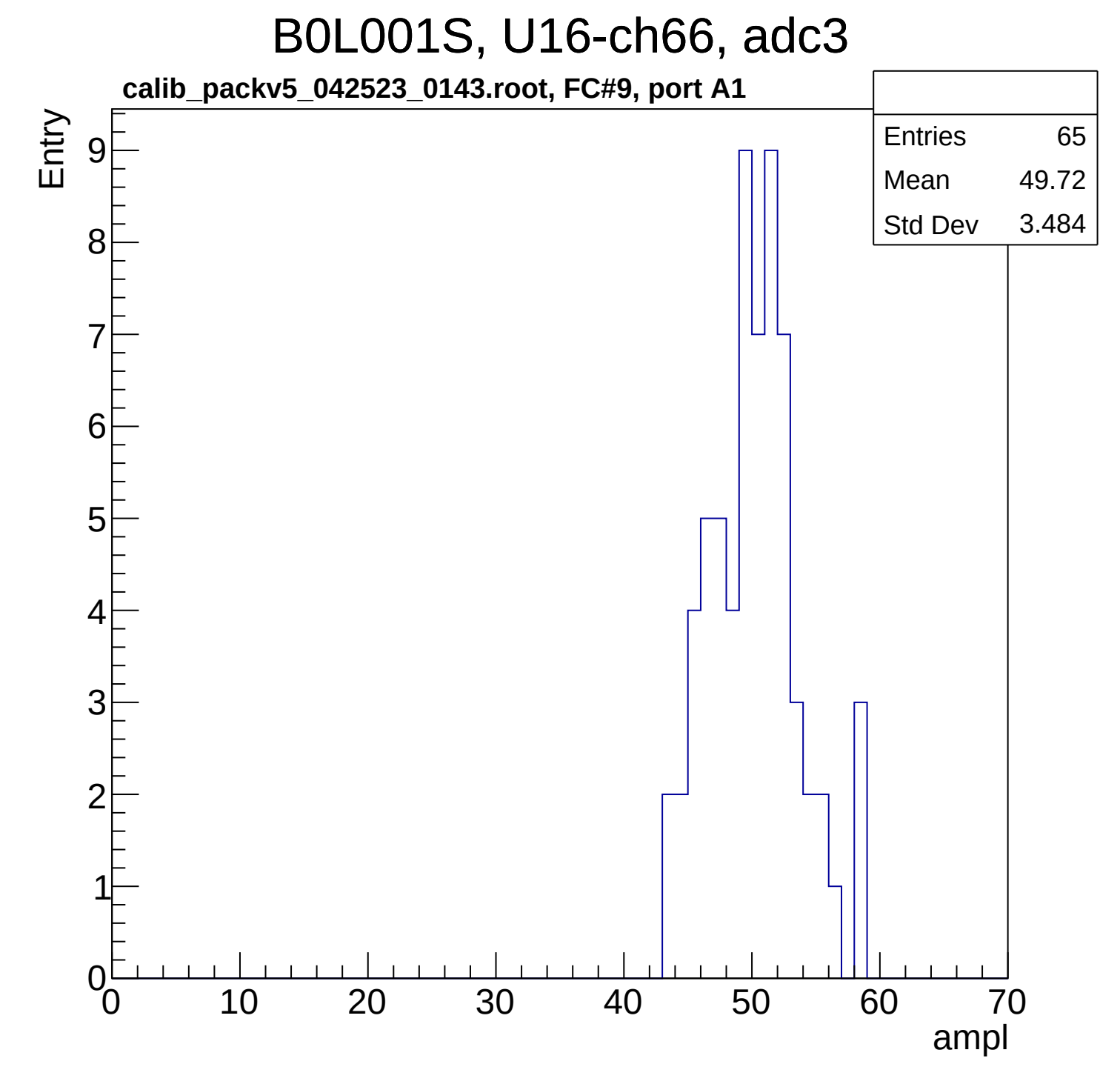
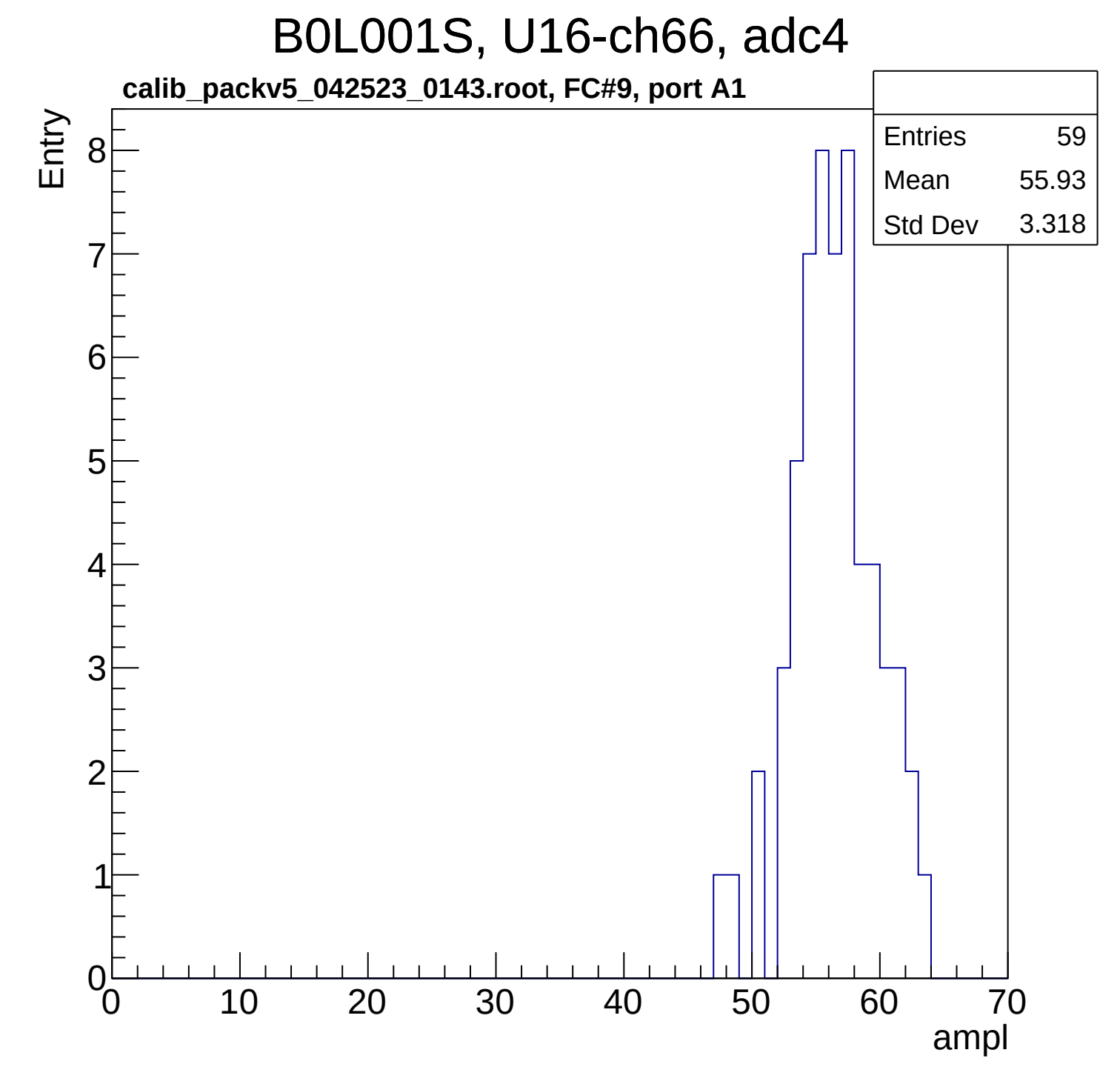
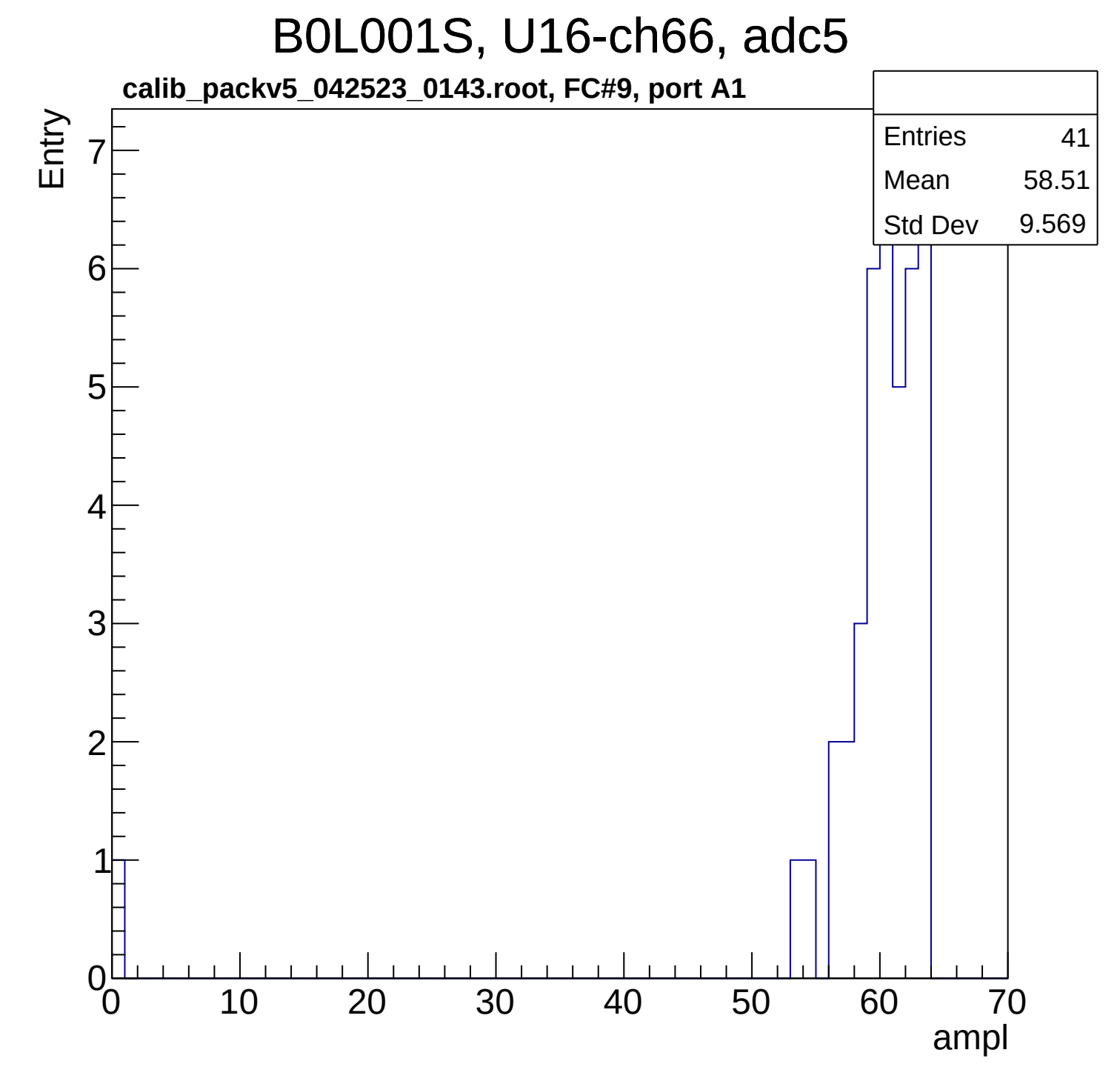
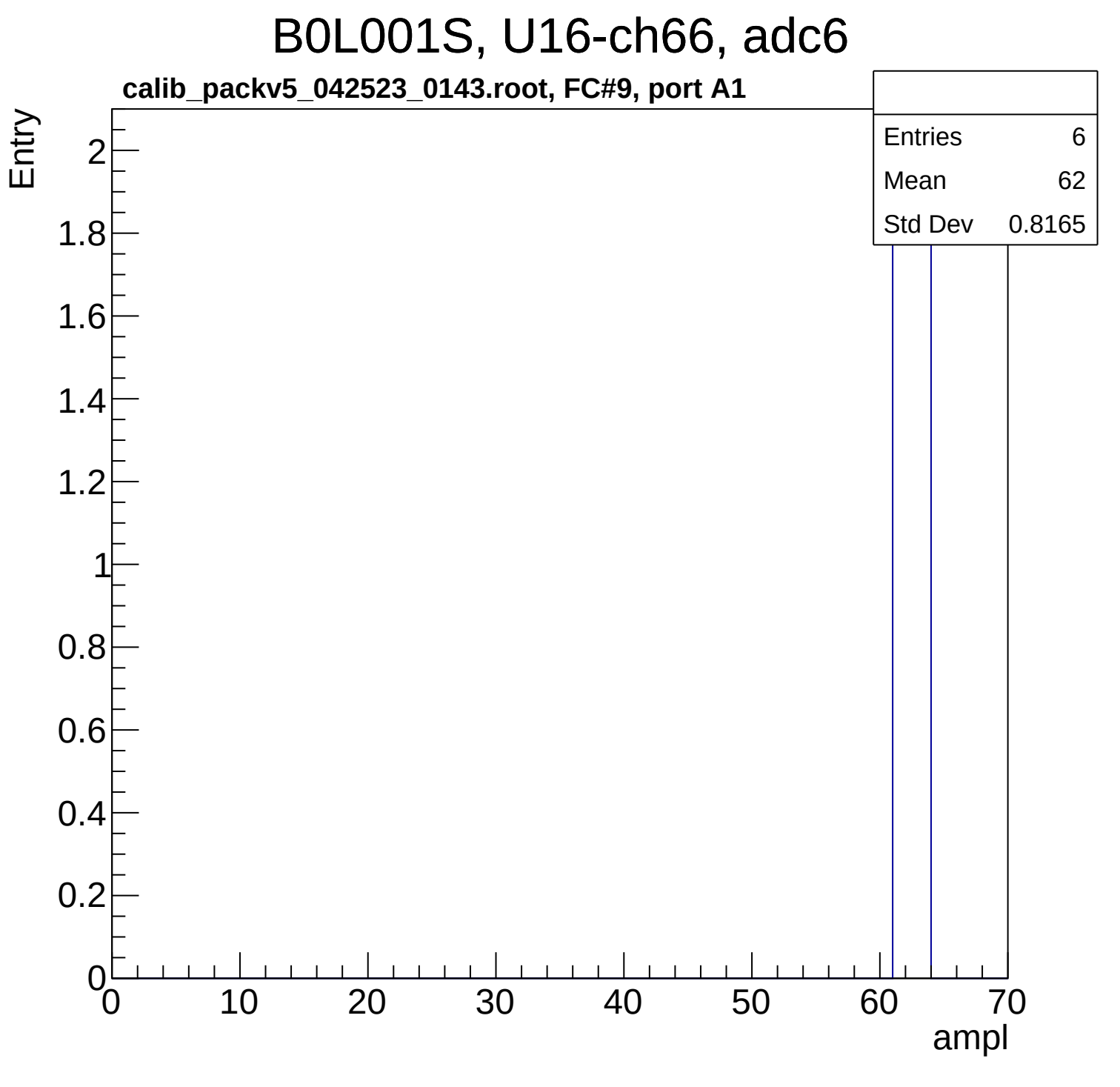
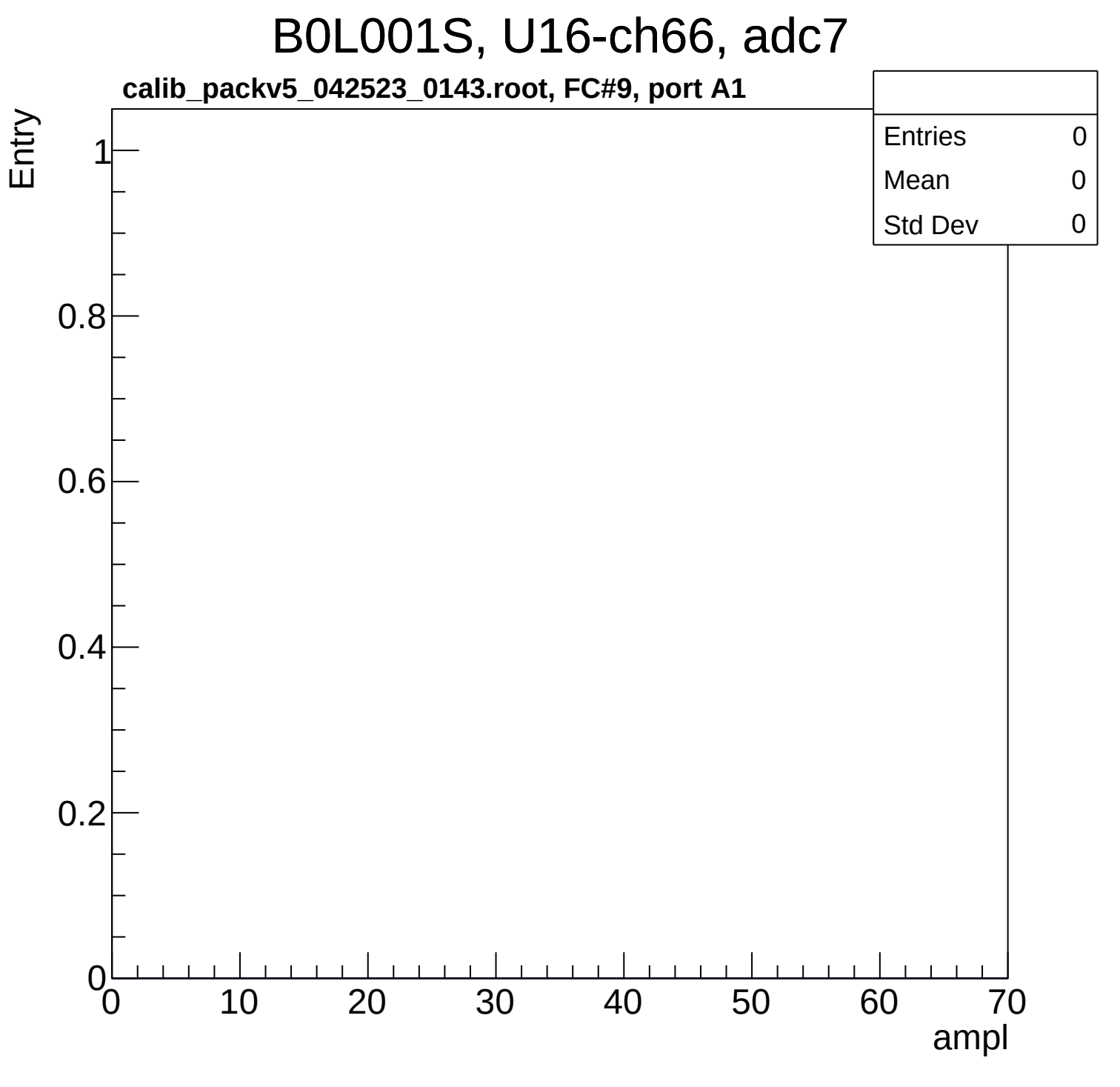

B0L001S, U16-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 44.52
Std Dev 3.369
Gaus mean : 44.6328
Gaus Width: 4.1934
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

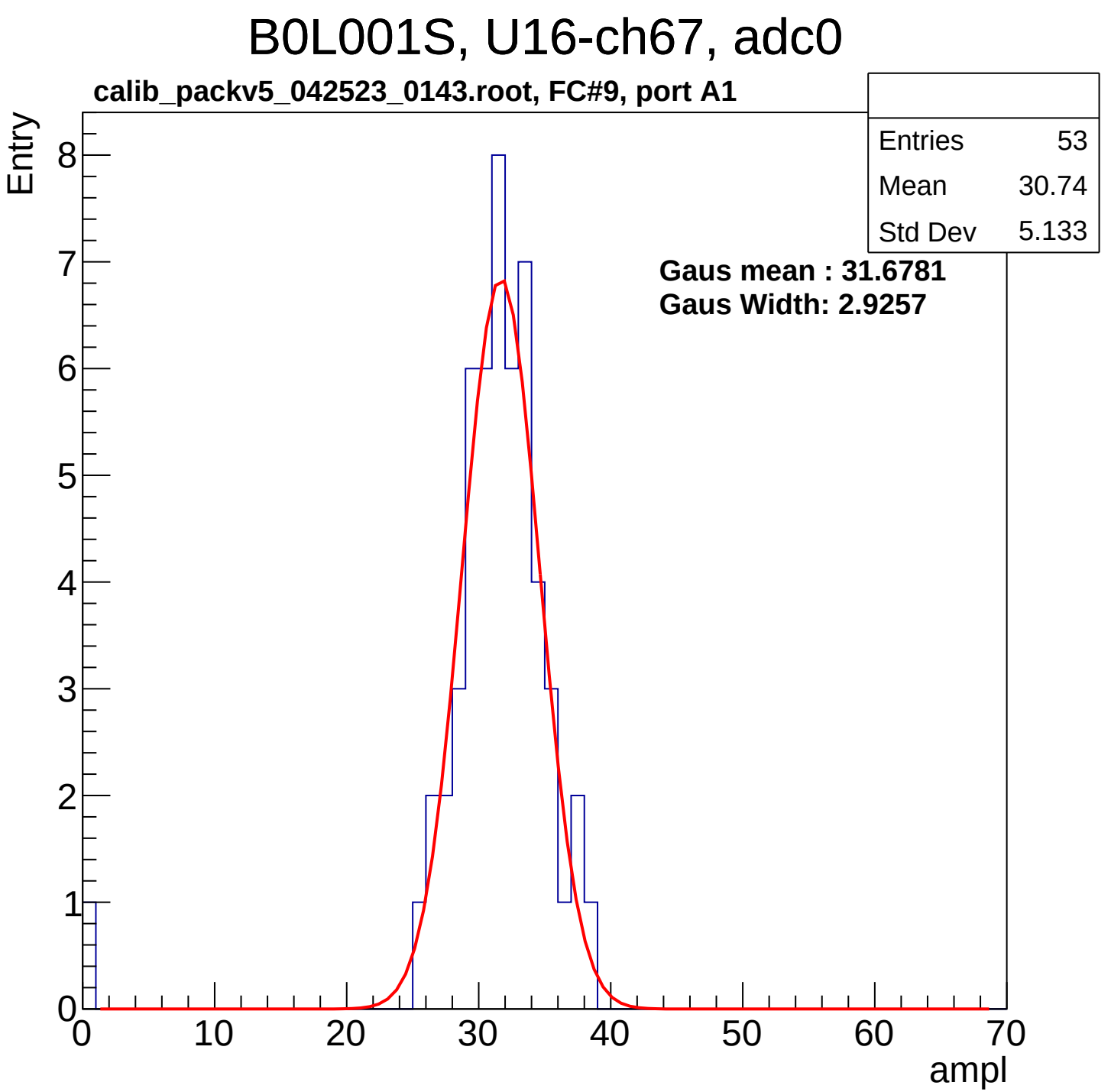

B0L001S, U16-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.72
Std Dev 3.484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

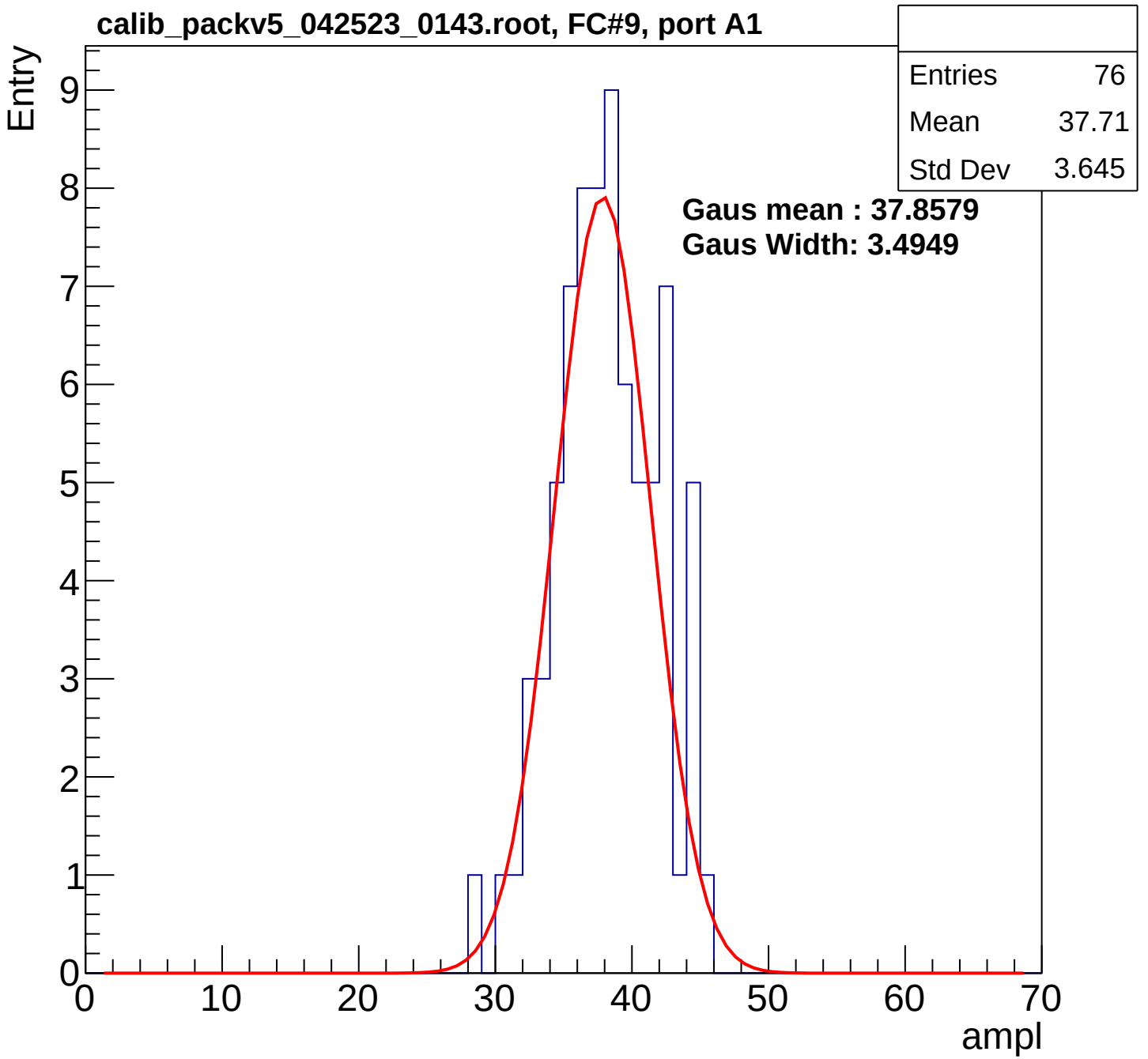

B0L001S, U16-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.93
Std Dev 3.318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

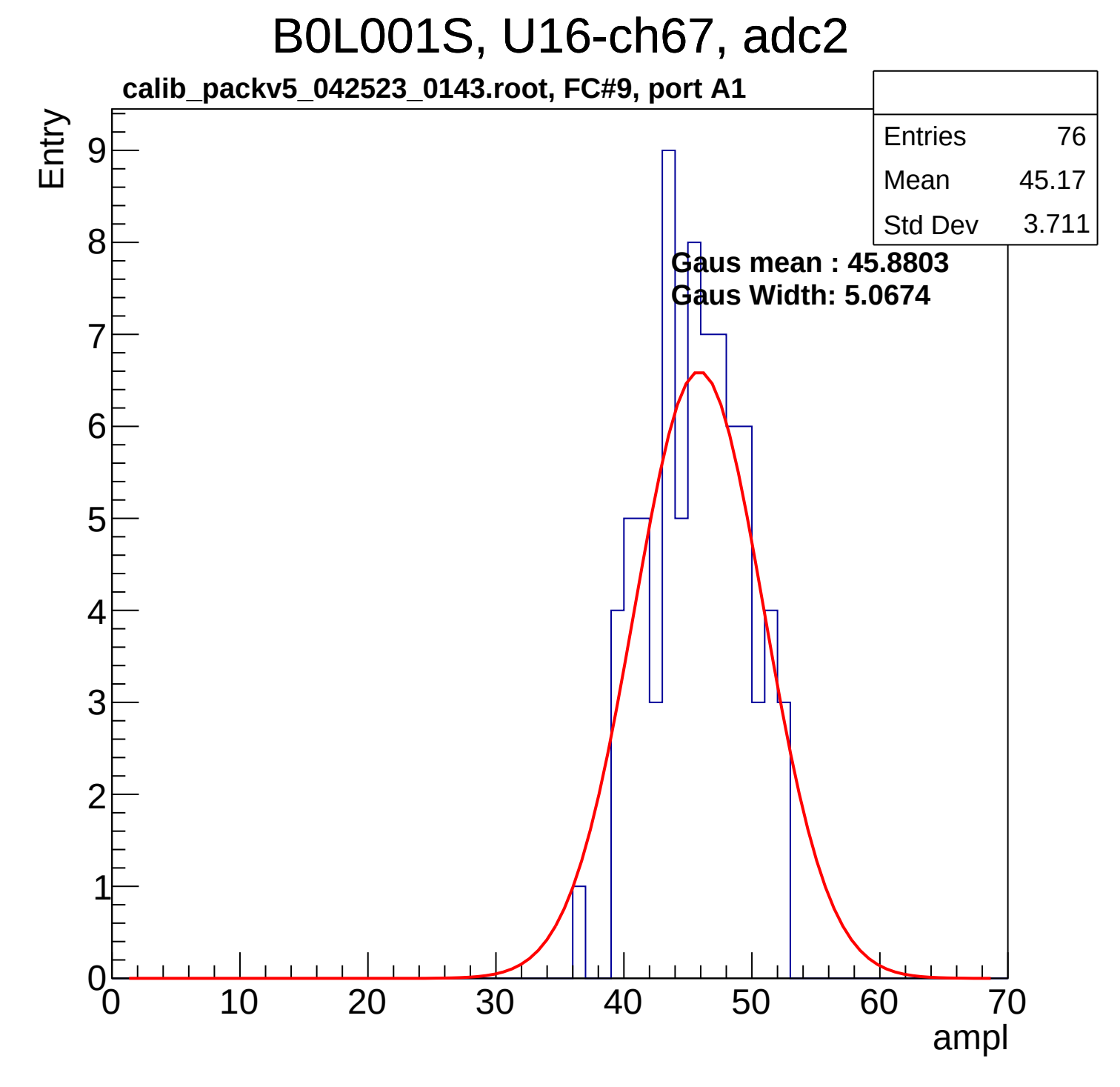

B0L001S, U16-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.51
Std Dev 9.569
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 30.74
Std Dev 5.133
Gaus mean : 31.6781
Gaus Width: 2.9257
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.71
Std Dev 3.645
Gaus mean : 37.8579
Gaus Width: 3.4949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 45.17
Std Dev 3.711
Gaus mean : 45.8803
Gaus Width: 5.0674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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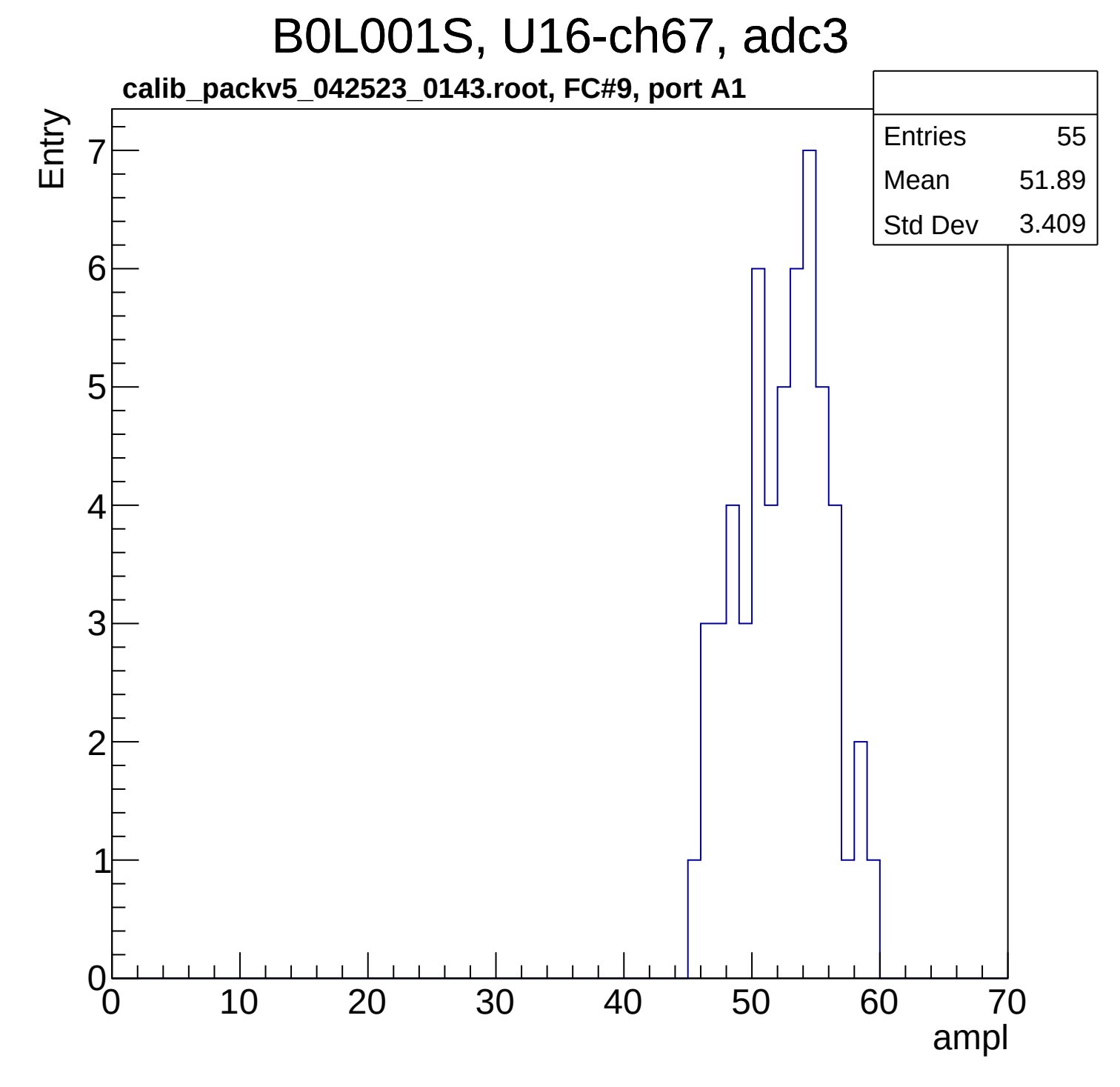
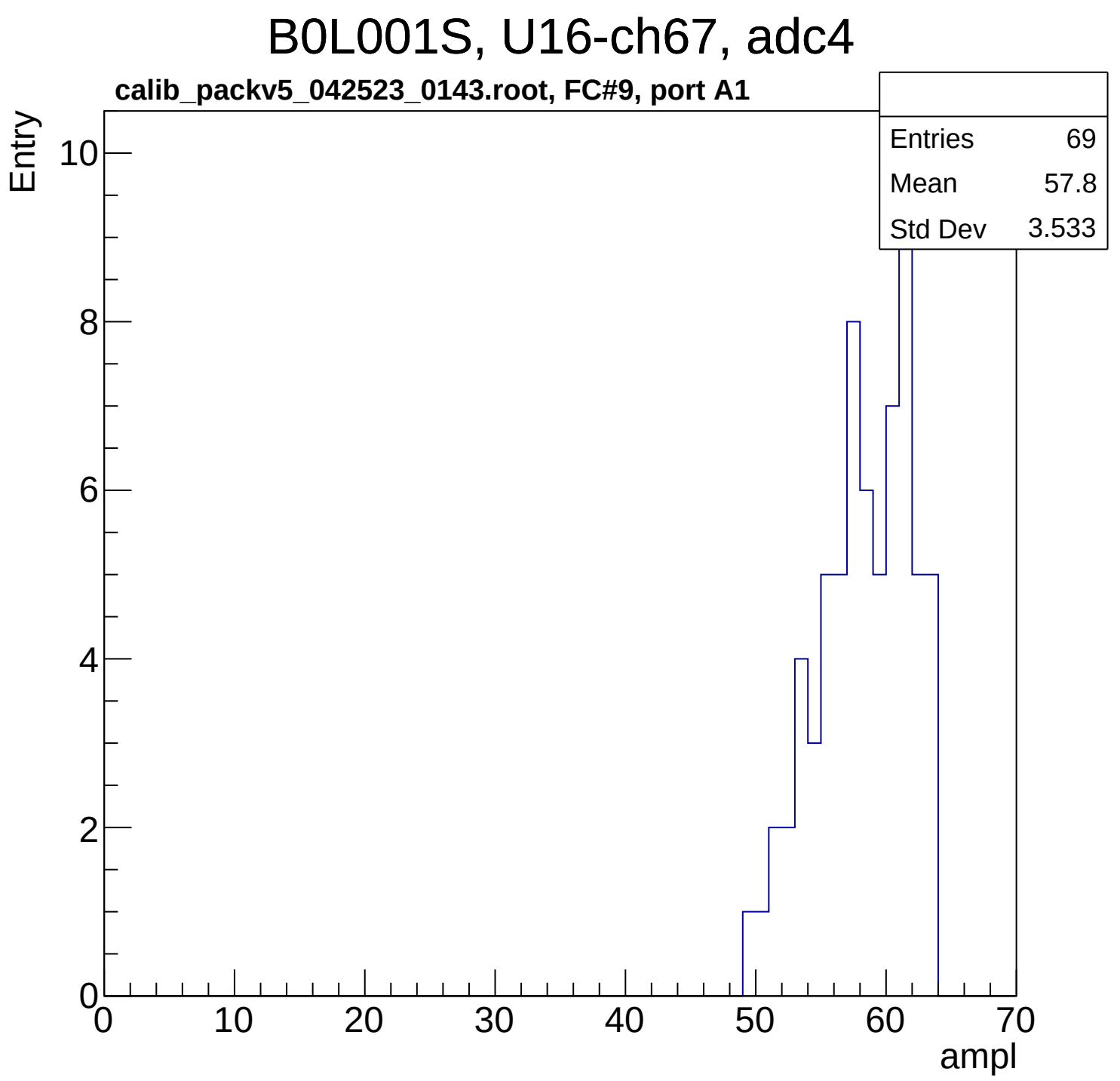
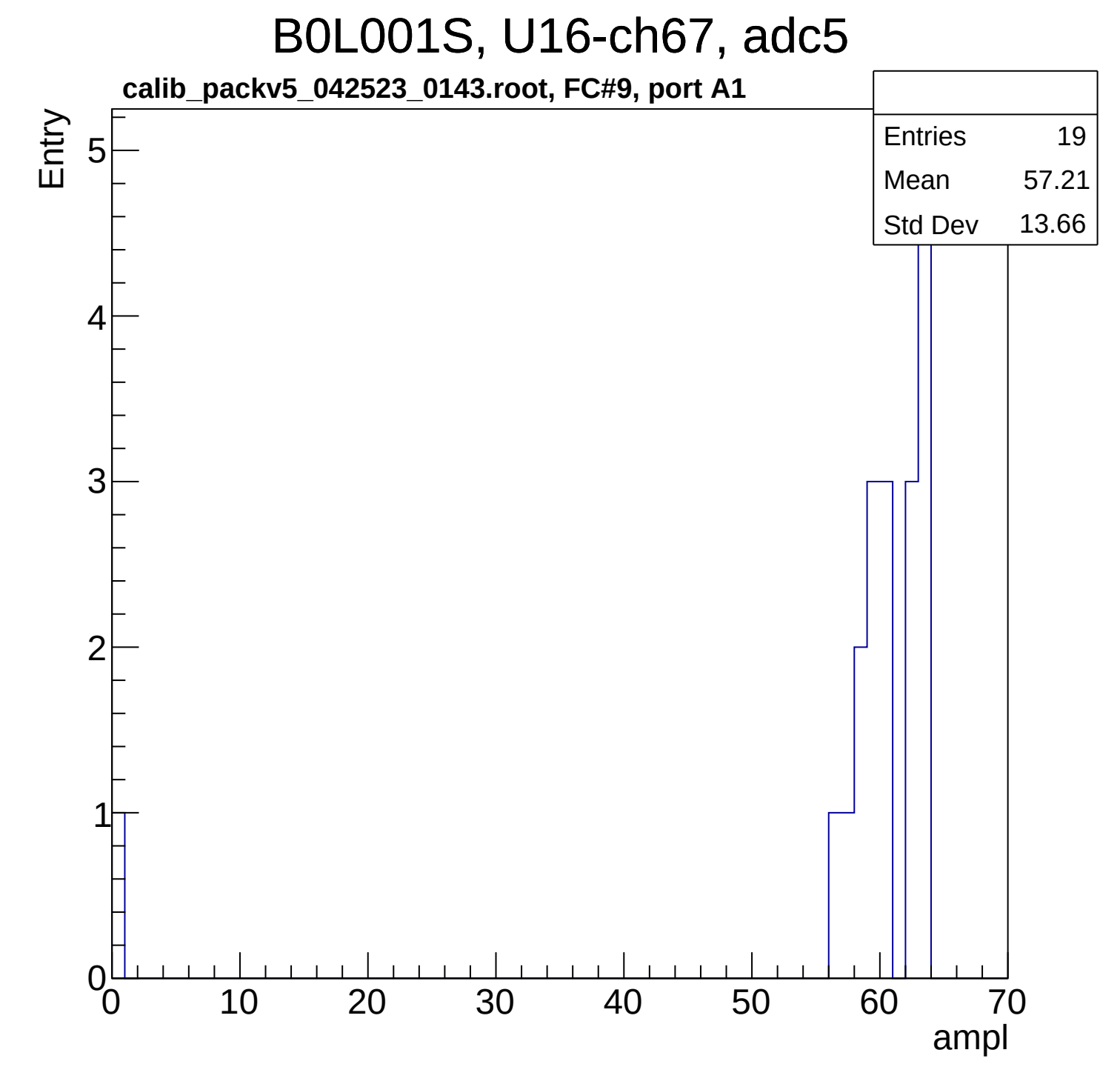
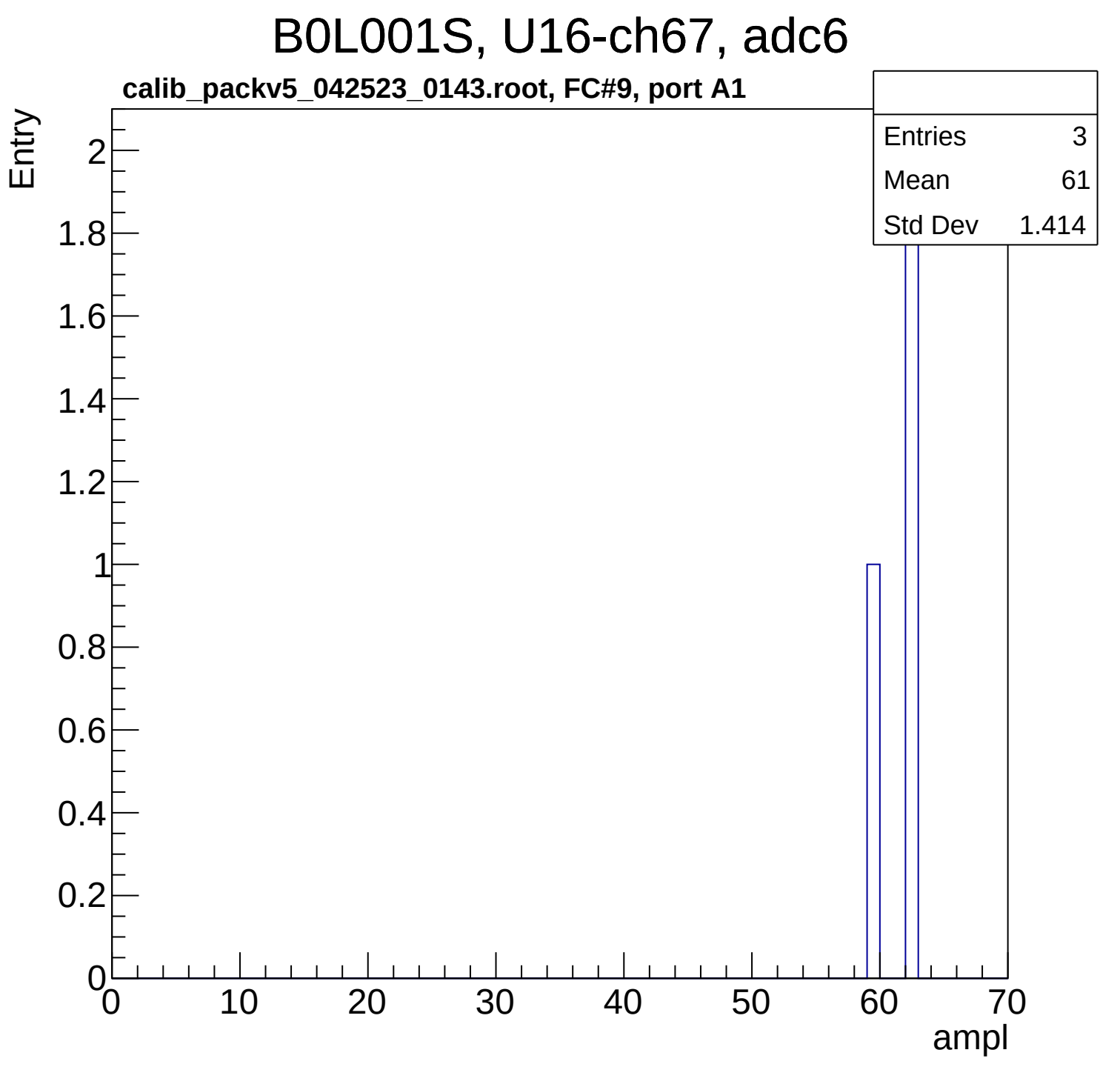
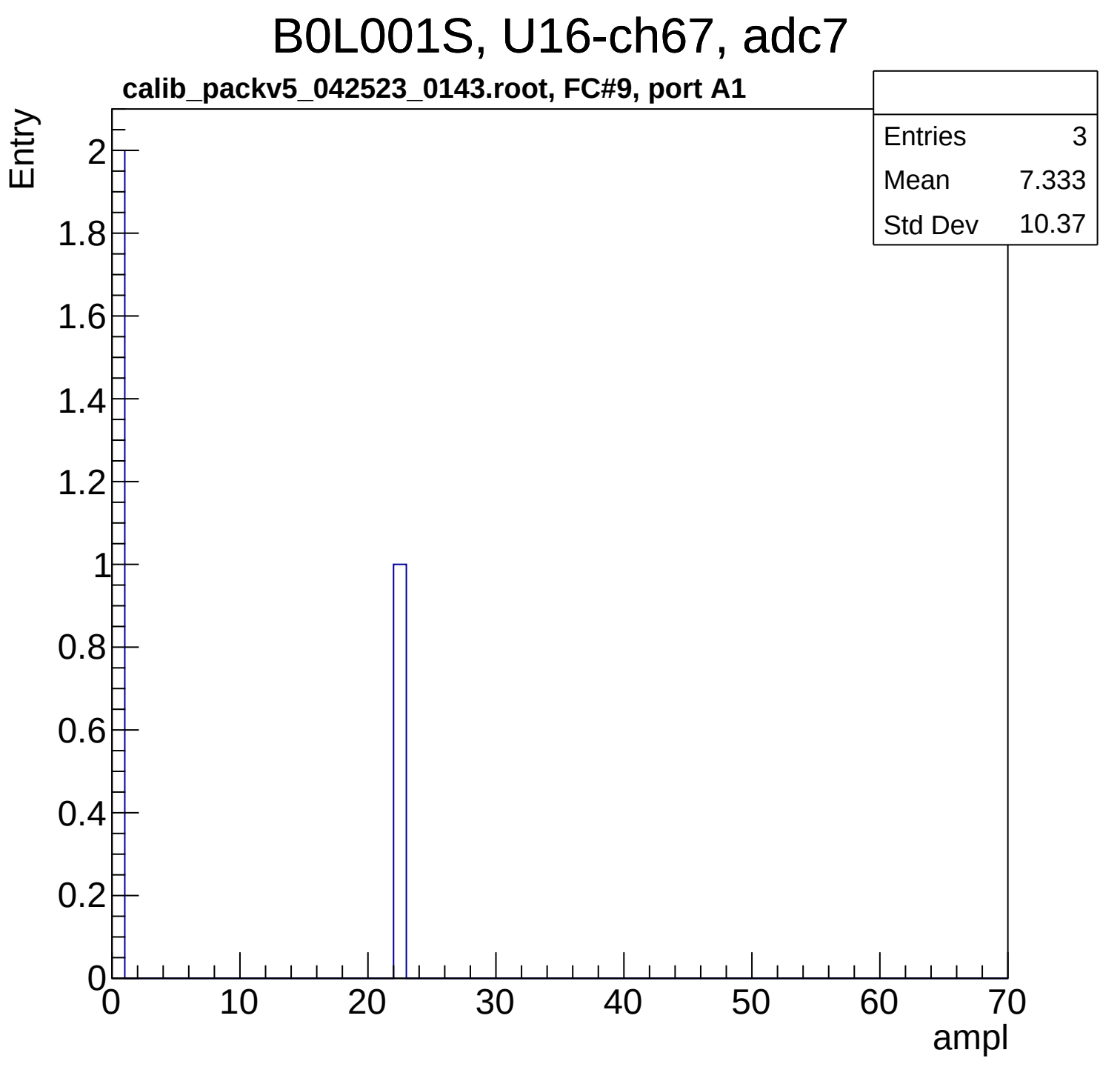

B0L001S, U16-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 51.89
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 57.8
Std Dev 3.533
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 57.21
Std Dev 13.66
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
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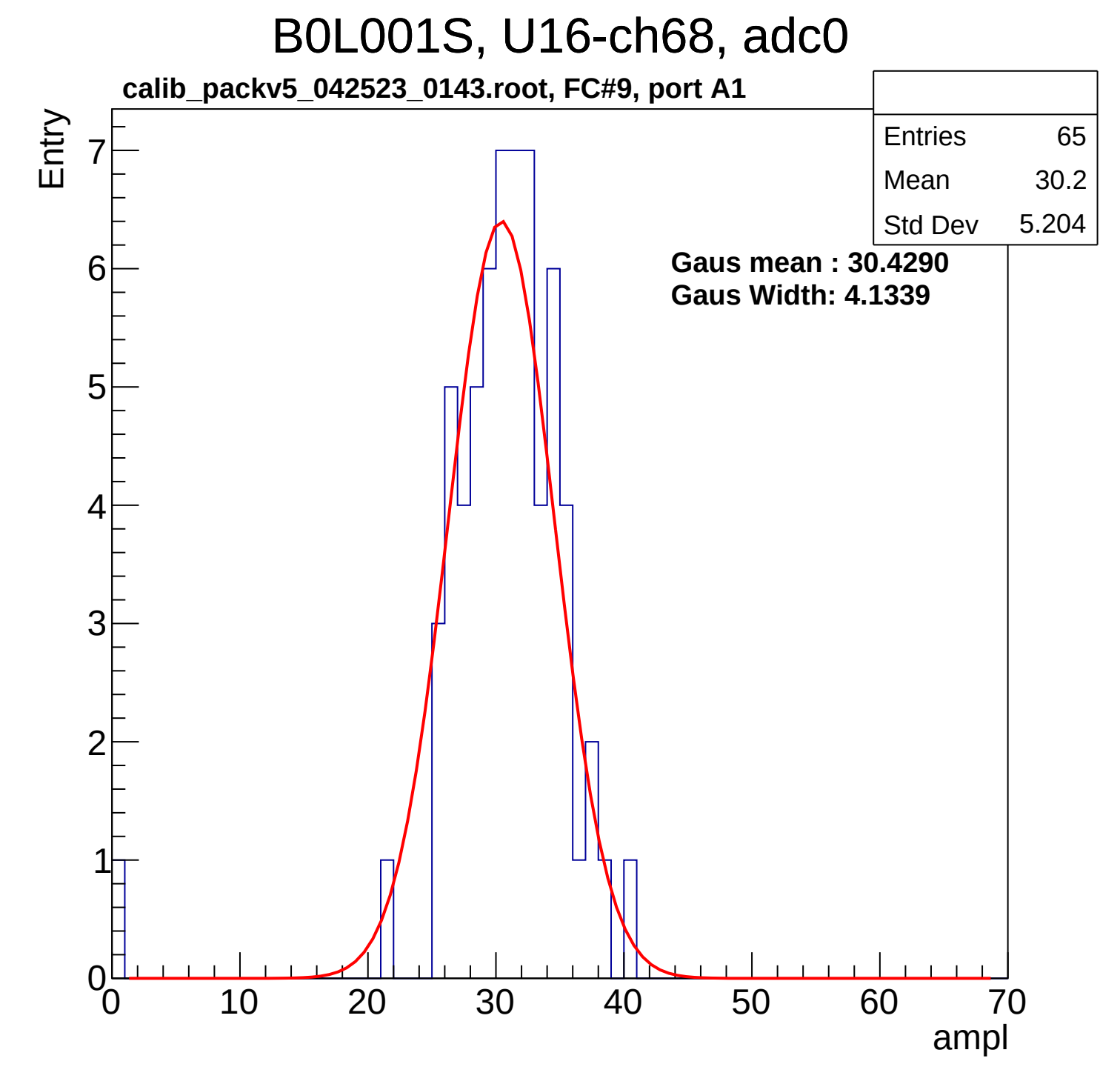
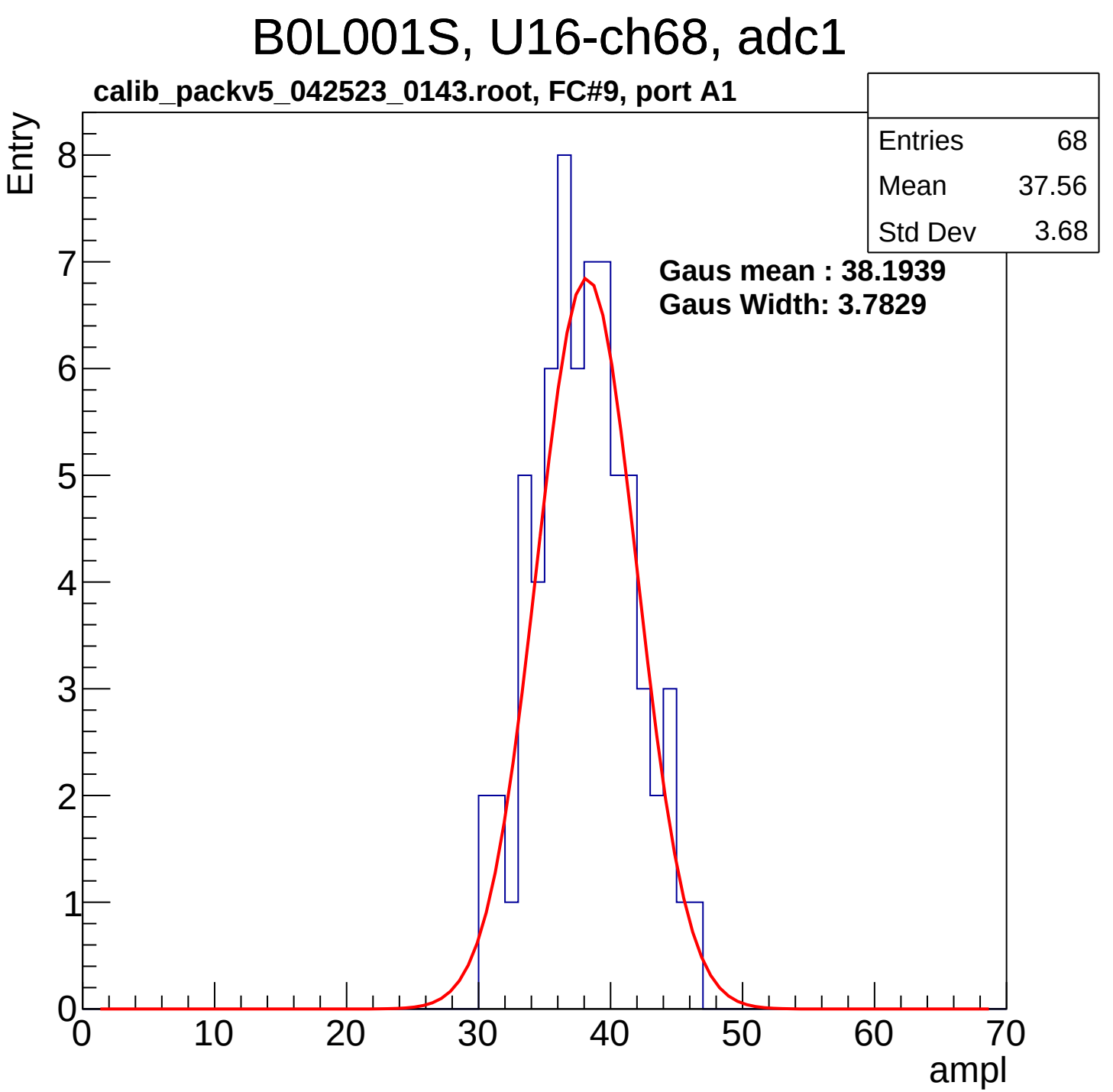
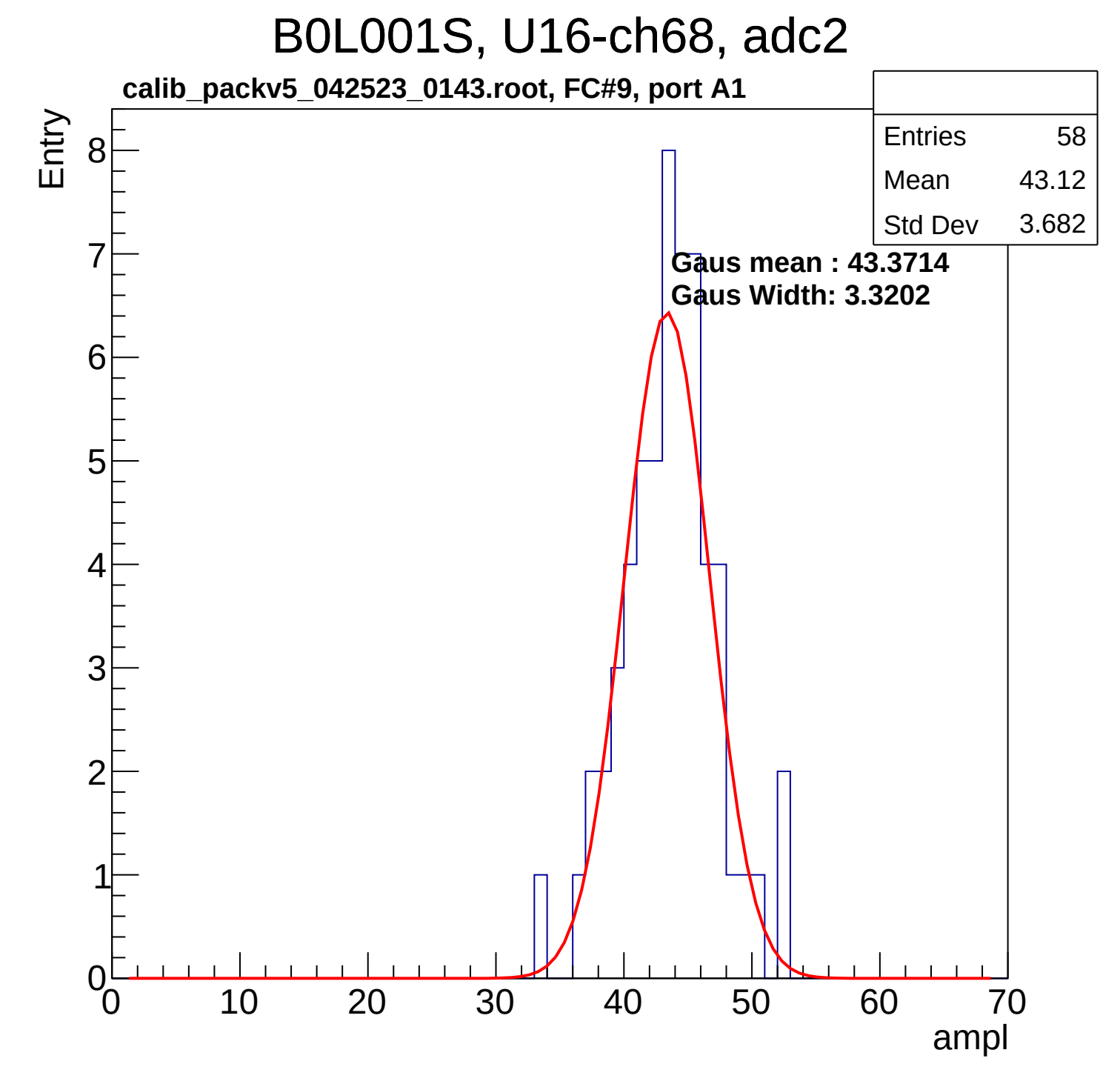
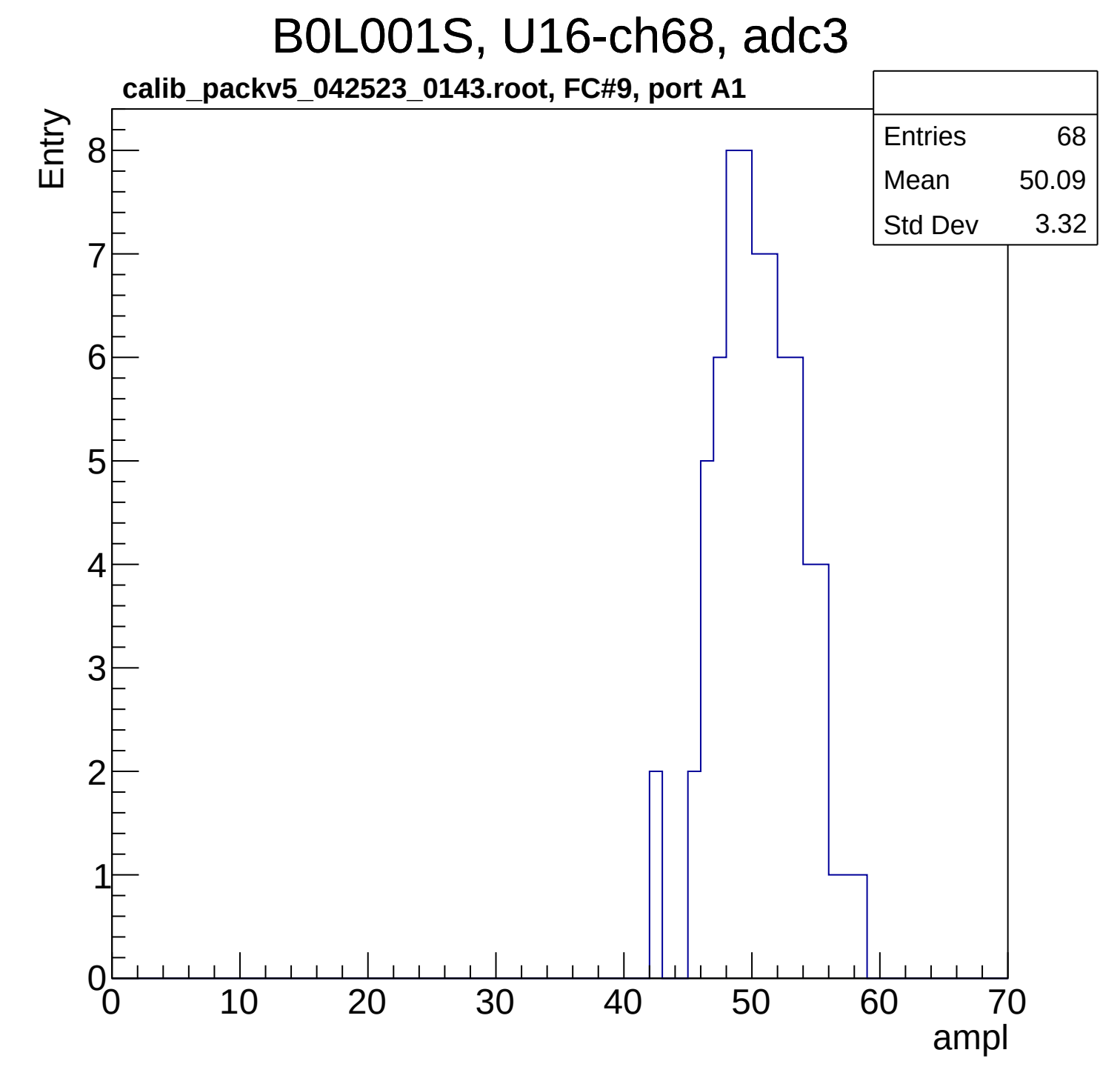
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.2
Std Dev 5.204
Gaus mean : 30.4290
Gaus Width: 4.1339
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.56
Std Dev 3.68
Gaus mean : 38.1939
Gaus Width: 3.7829
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.12
Std Dev 3.682
Gaus mean : 43.3714
Gaus Width: 3.3202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50.09
Std Dev 3.32
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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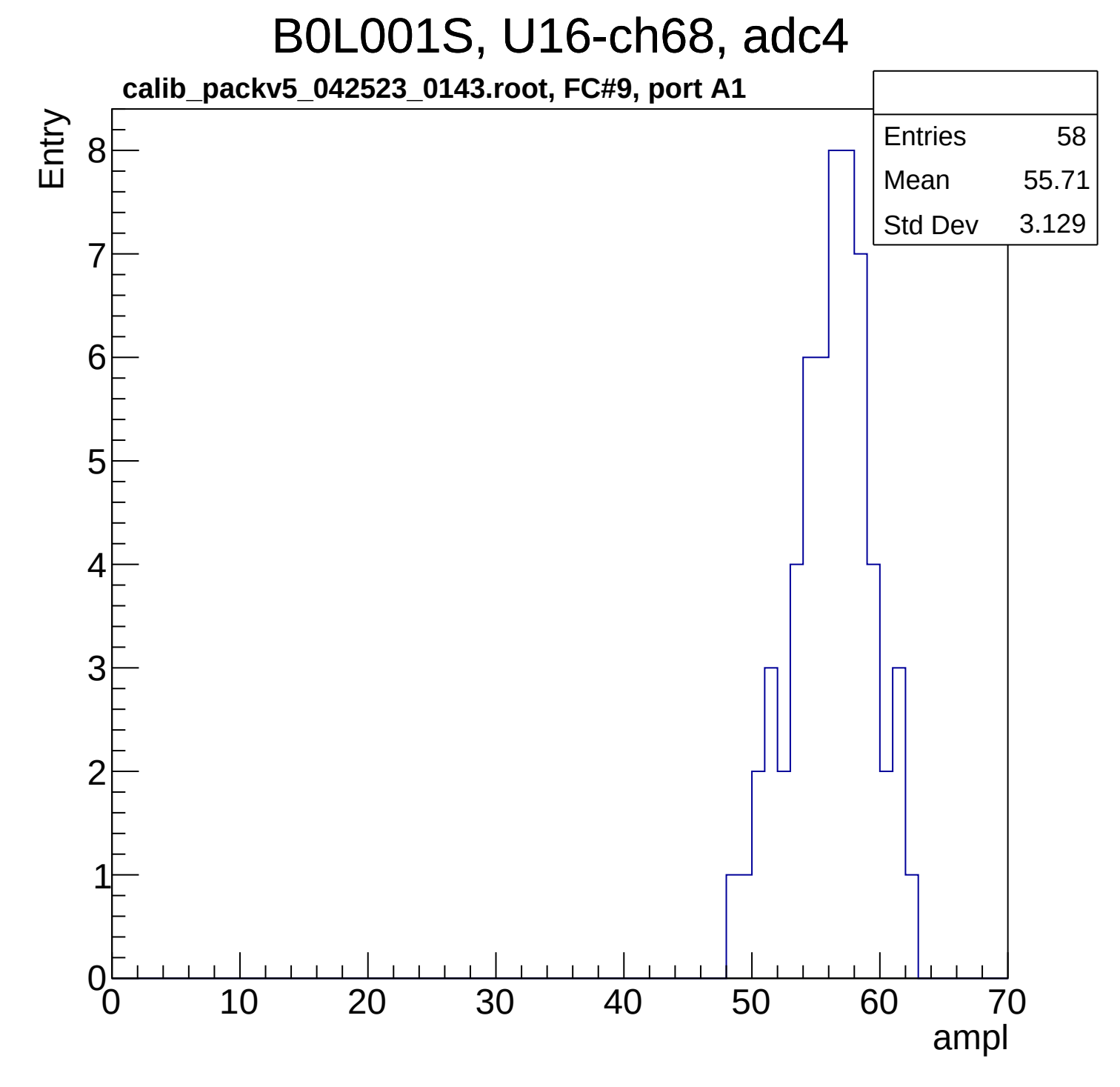
60
70


B0L001S, U16-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.71
Std Dev 3.129
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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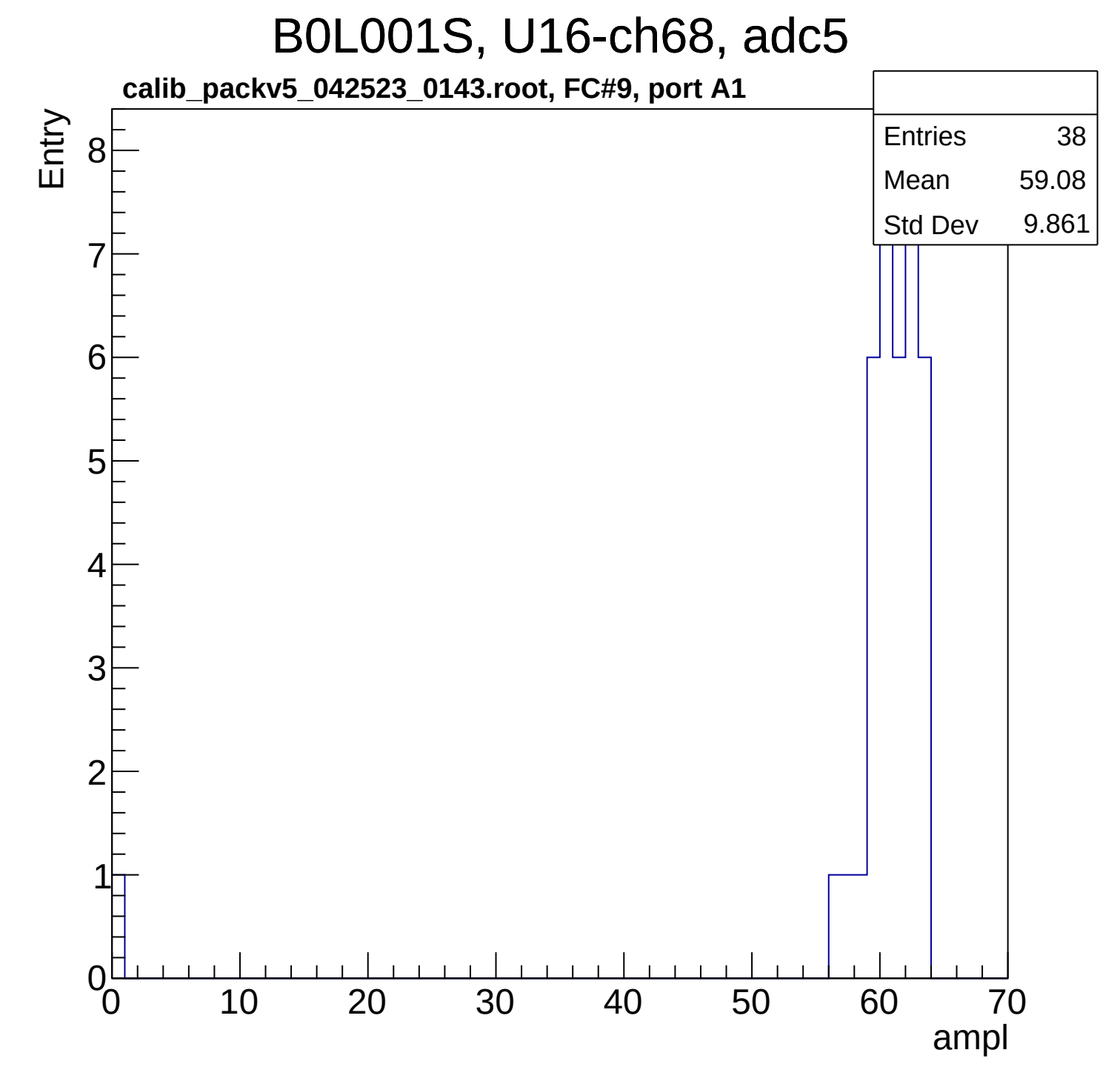
B0L001S, U16-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.08
Std Dev 9.861
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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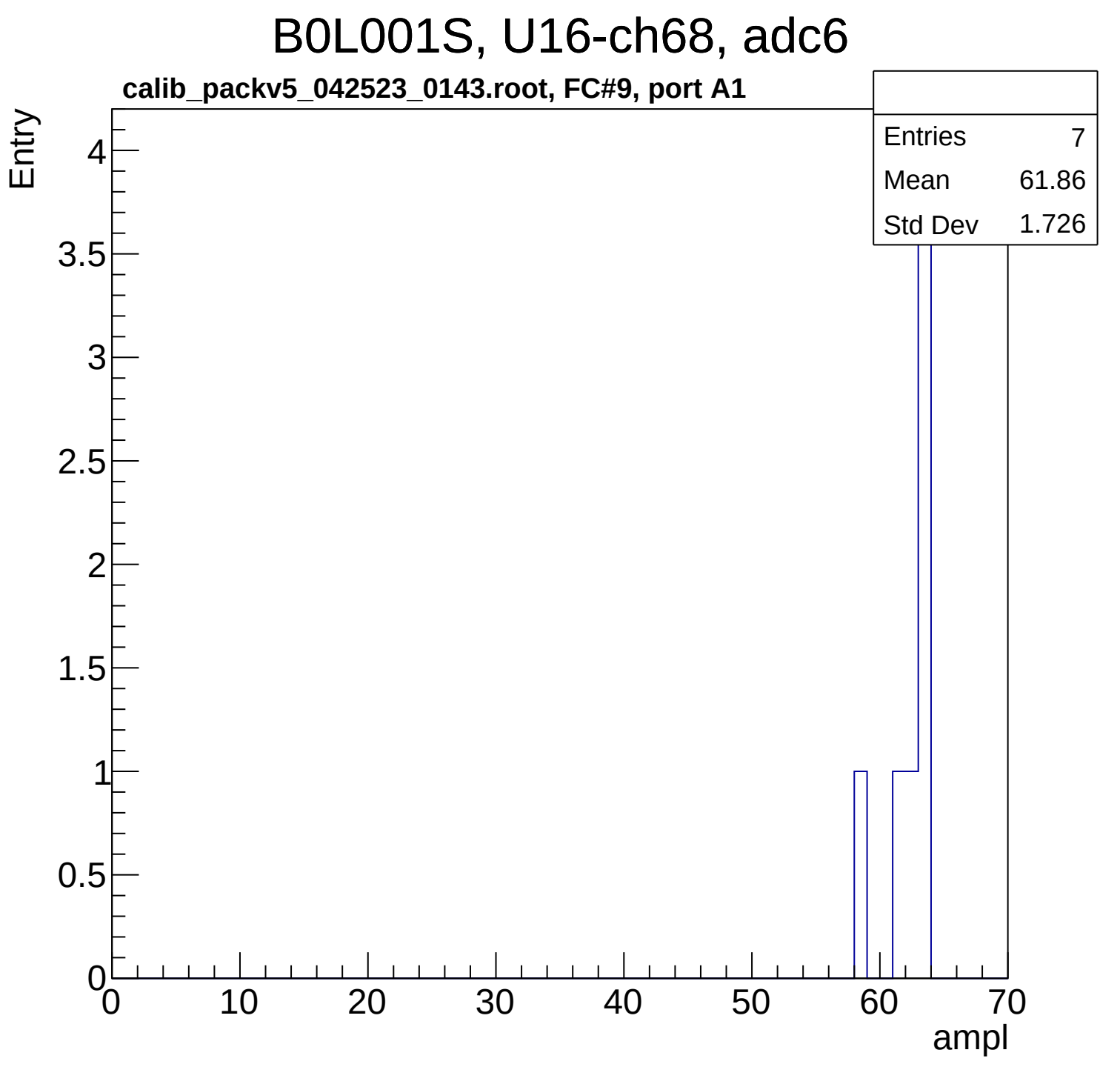
40
50
60
70


B0L001S, U16-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.86
Std Dev 1.726
Entry
ampl
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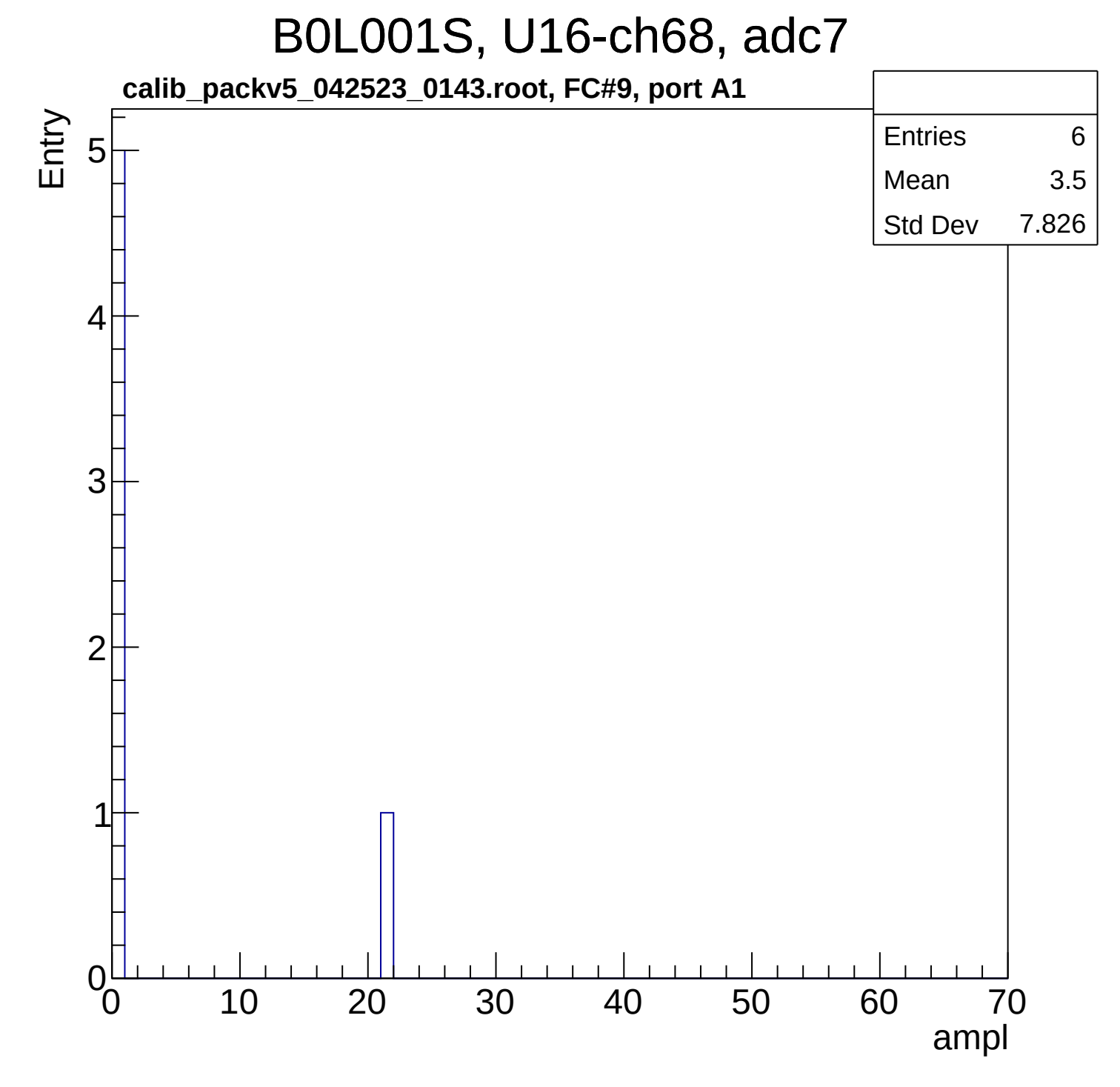
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 3.5
Std Dev 7.826
Entry
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1
2
3
4
5
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10
20
30
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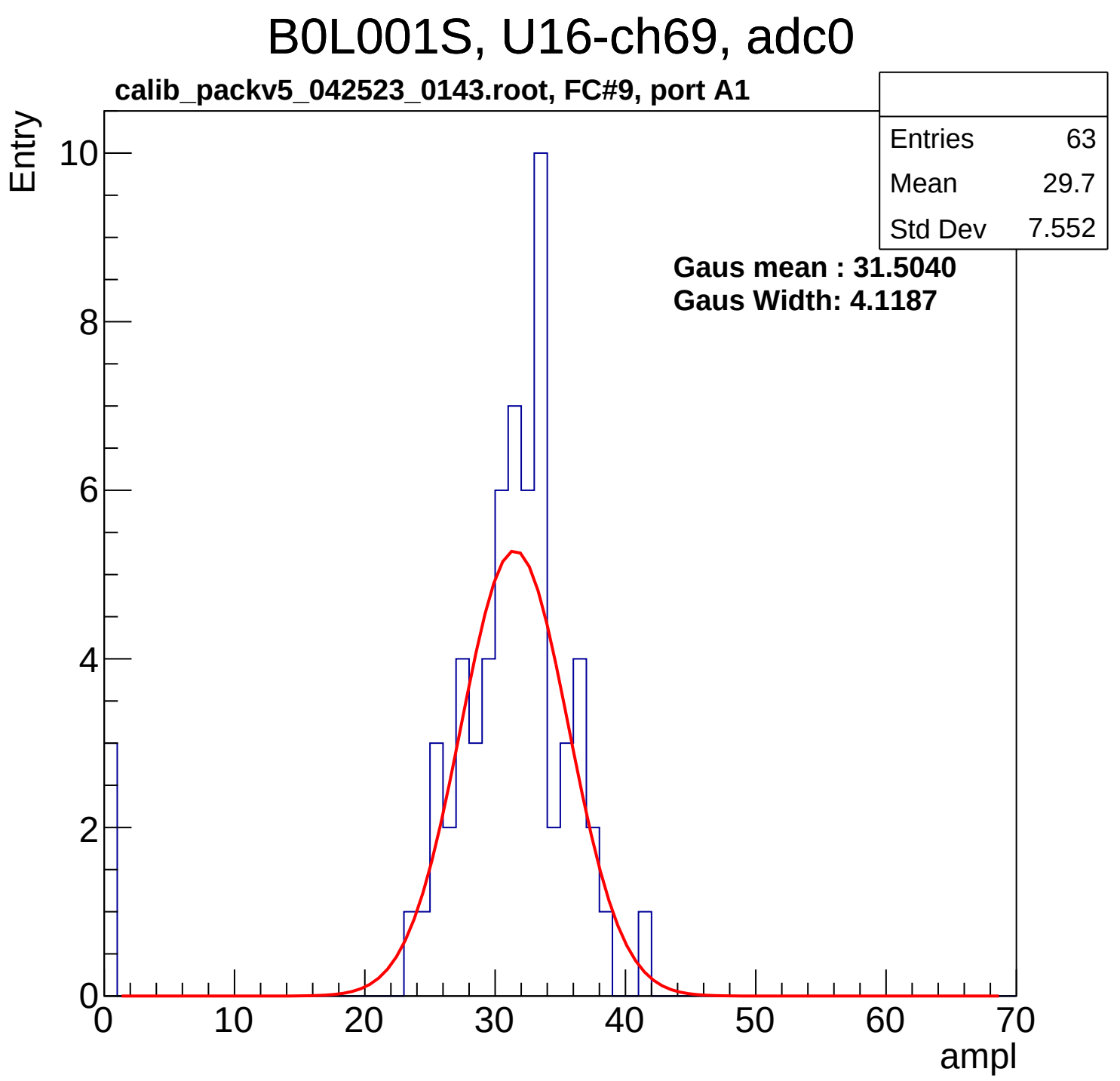
50
60
70
ampl


B0L001S, U16-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.7
Std Dev 7.552
Gaus mean : 31.5040
Gaus Width: 4.1187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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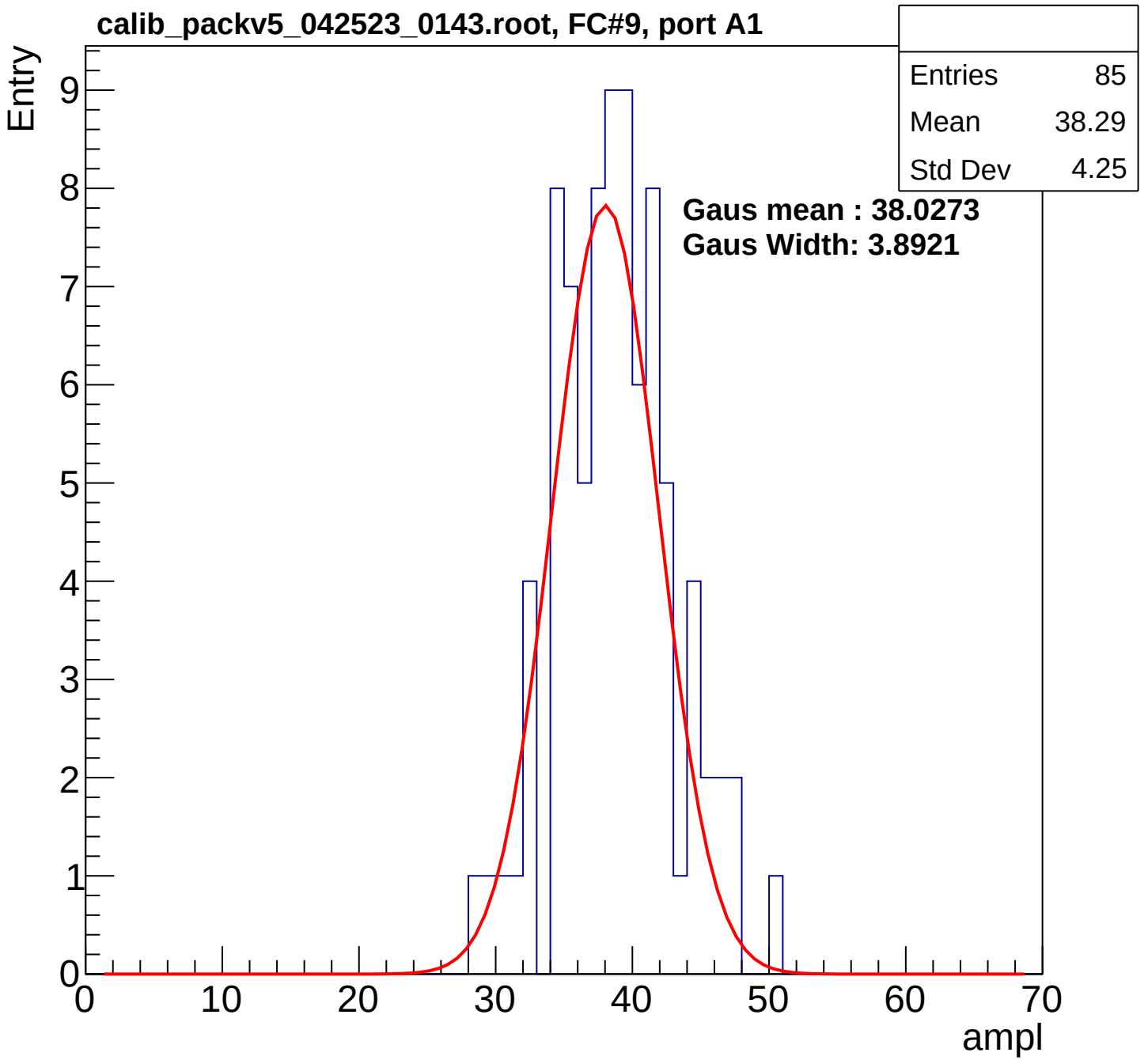
B0L001S, U16-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 38.29
Std Dev 4.25
Gaus mean : 38.0273
Gaus Width: 3.8921
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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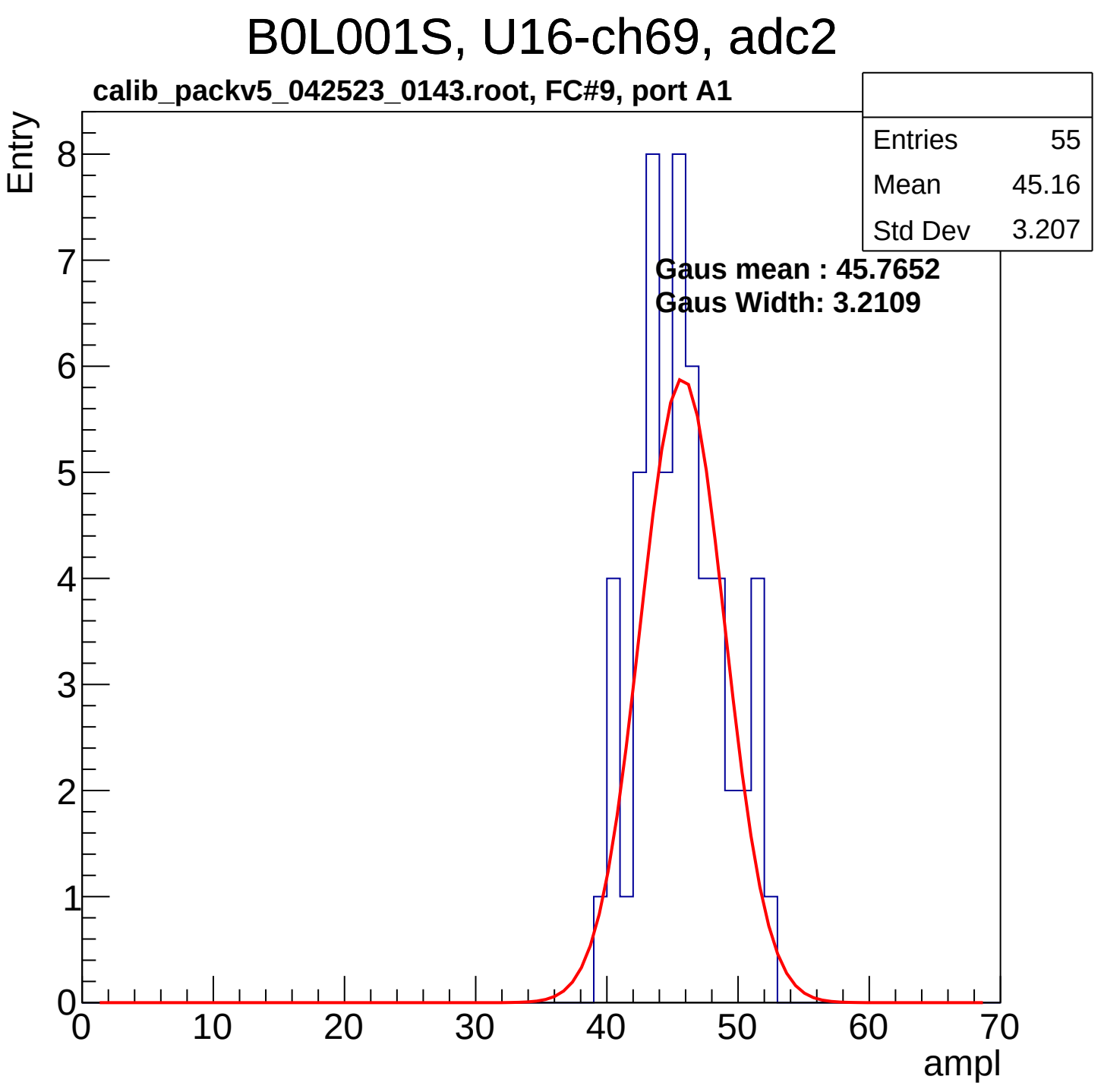
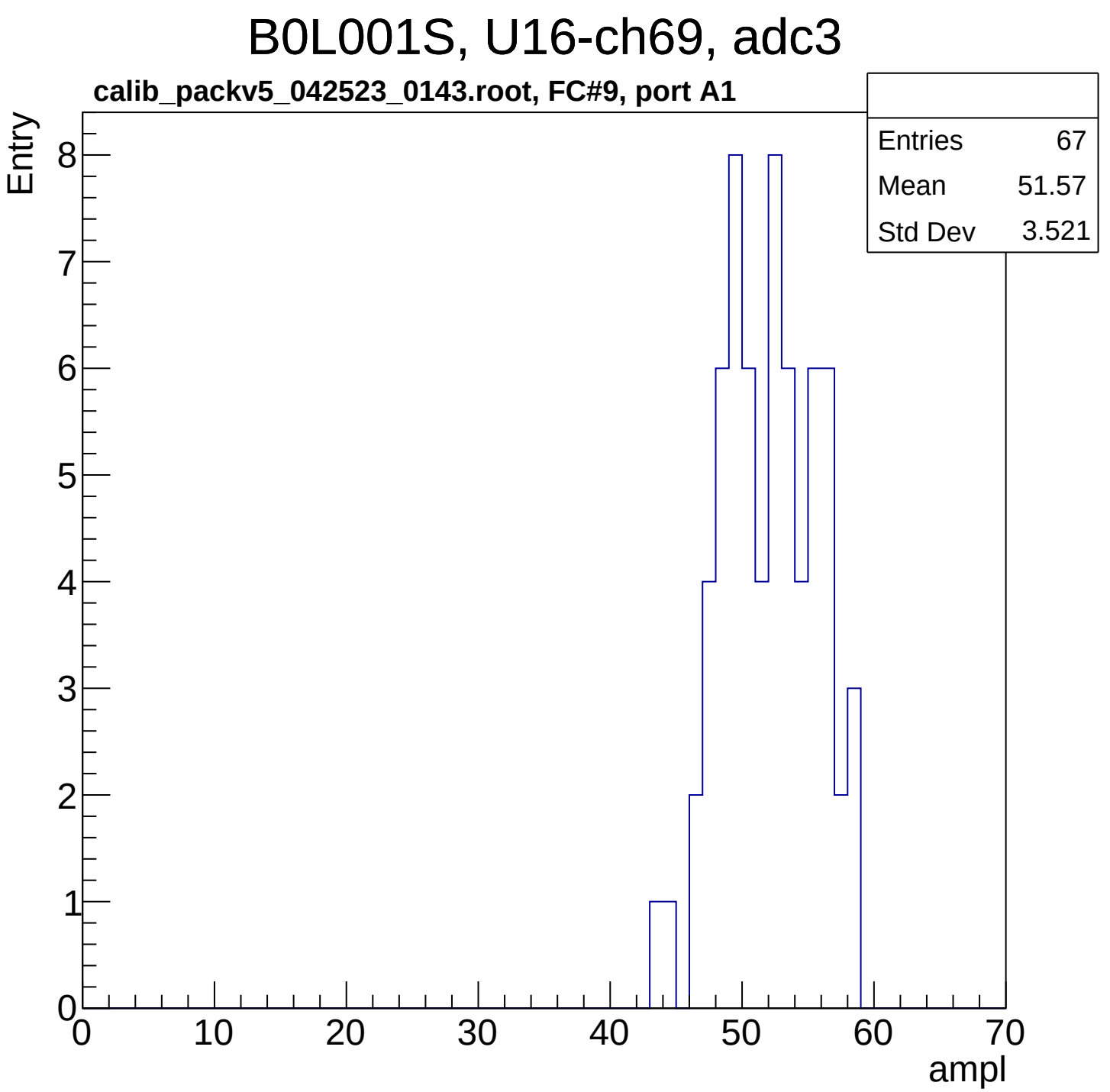
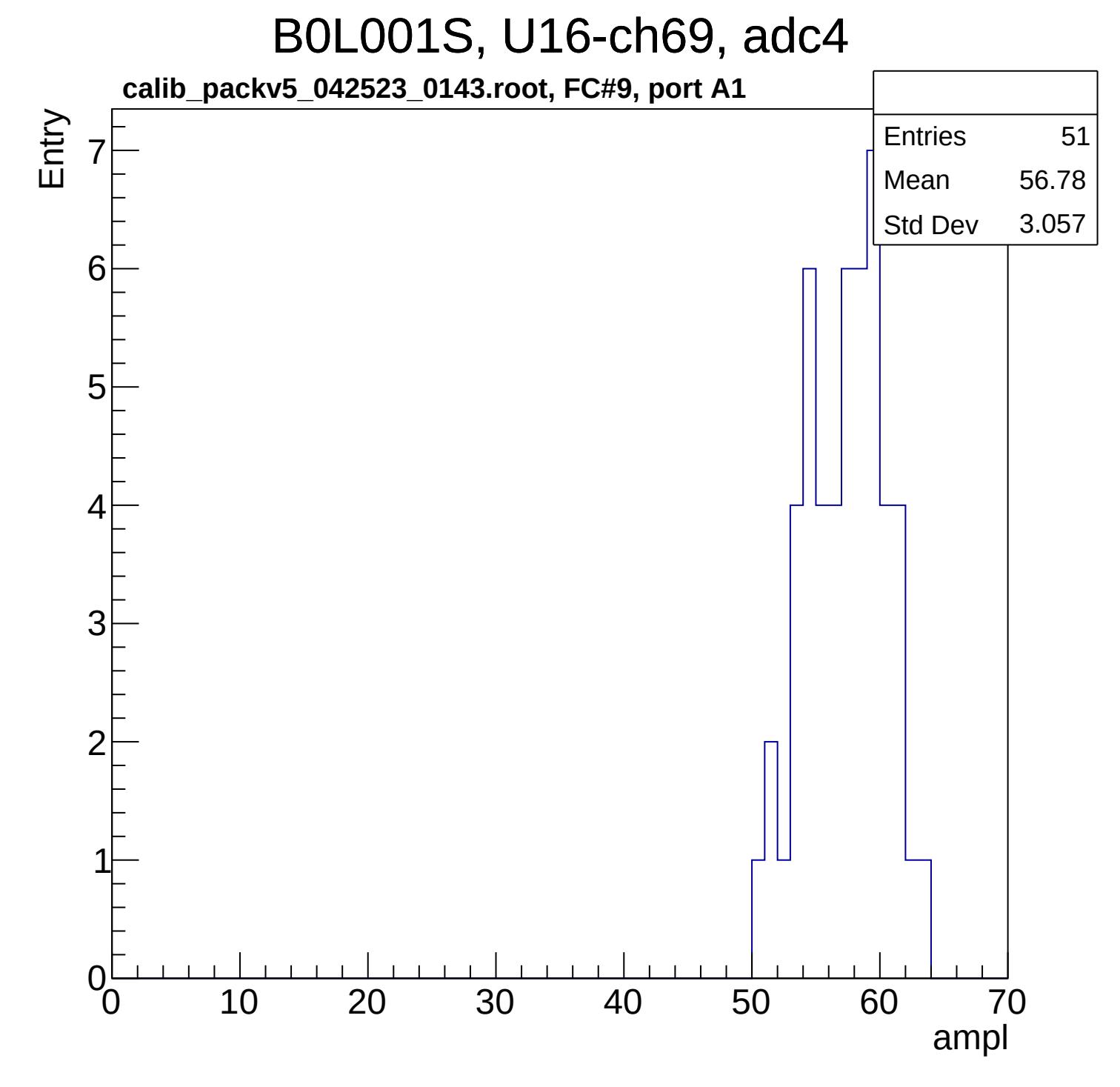
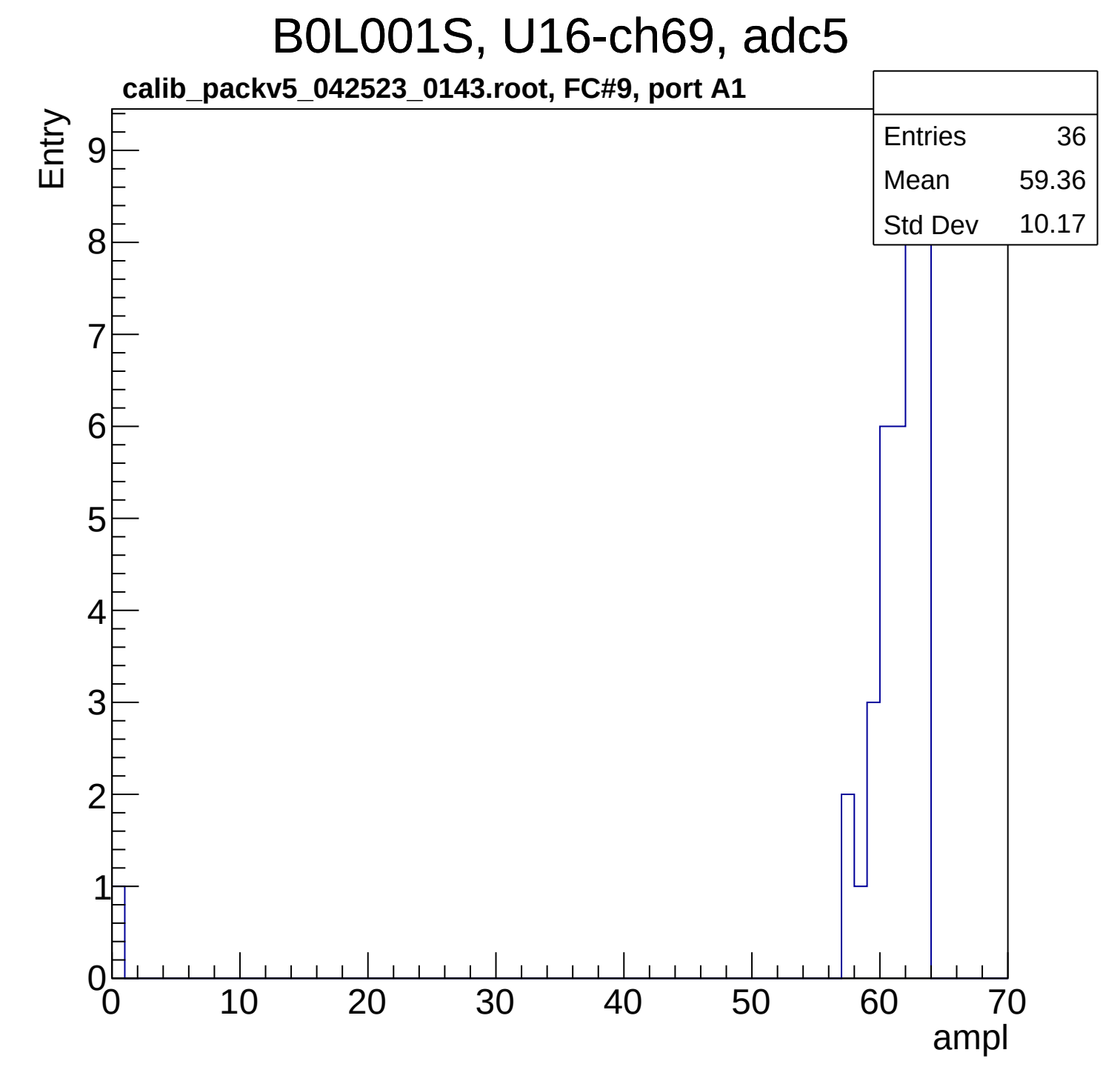
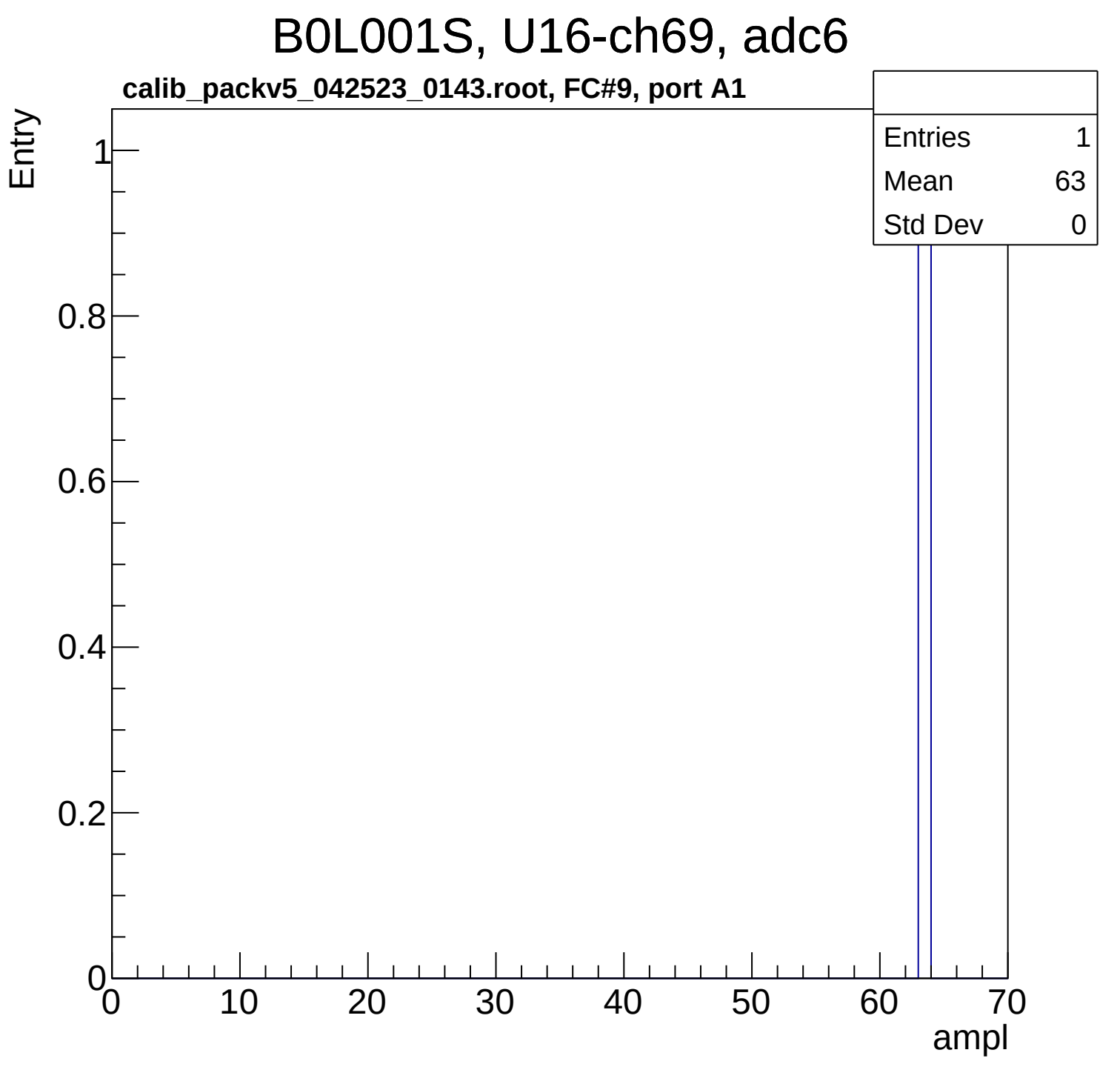
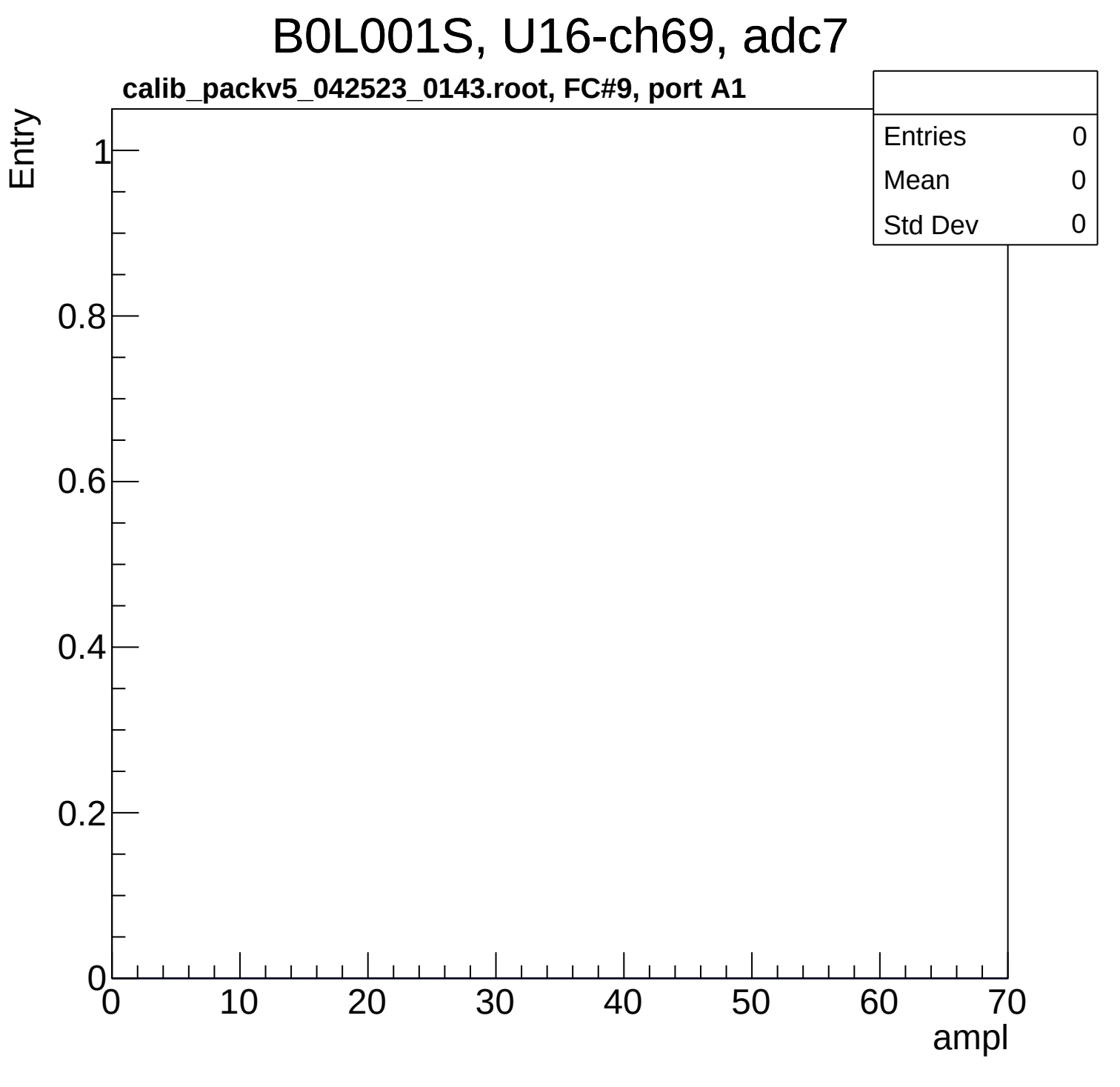

B0L001S, U16-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 45.16
Std Dev 3.207
Gaus mean : 45.7652
Gaus Width: 3.2109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

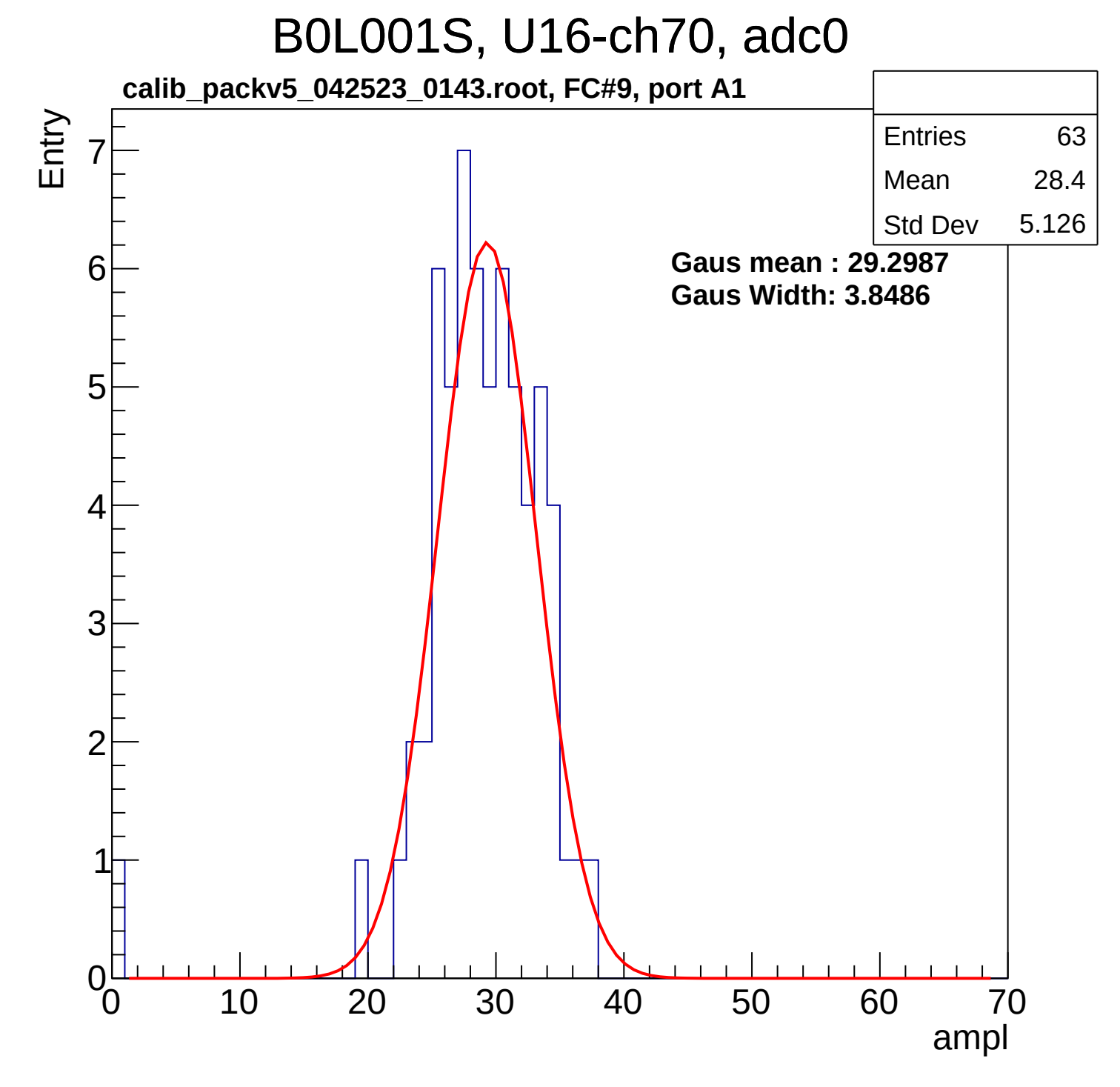

B0L001S, U16-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.57
Std Dev 3.521
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

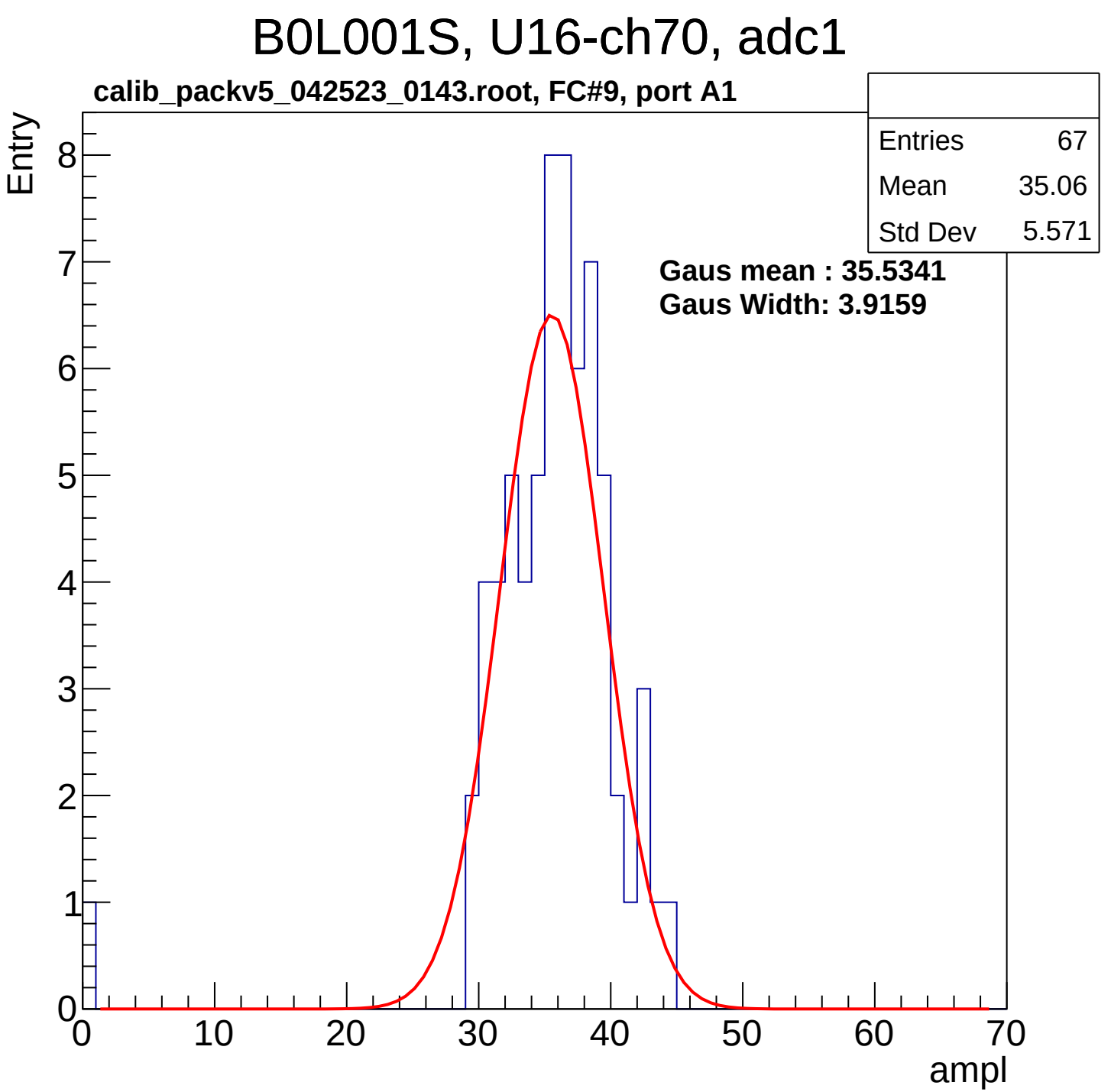

B0L001S, U16-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.78
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

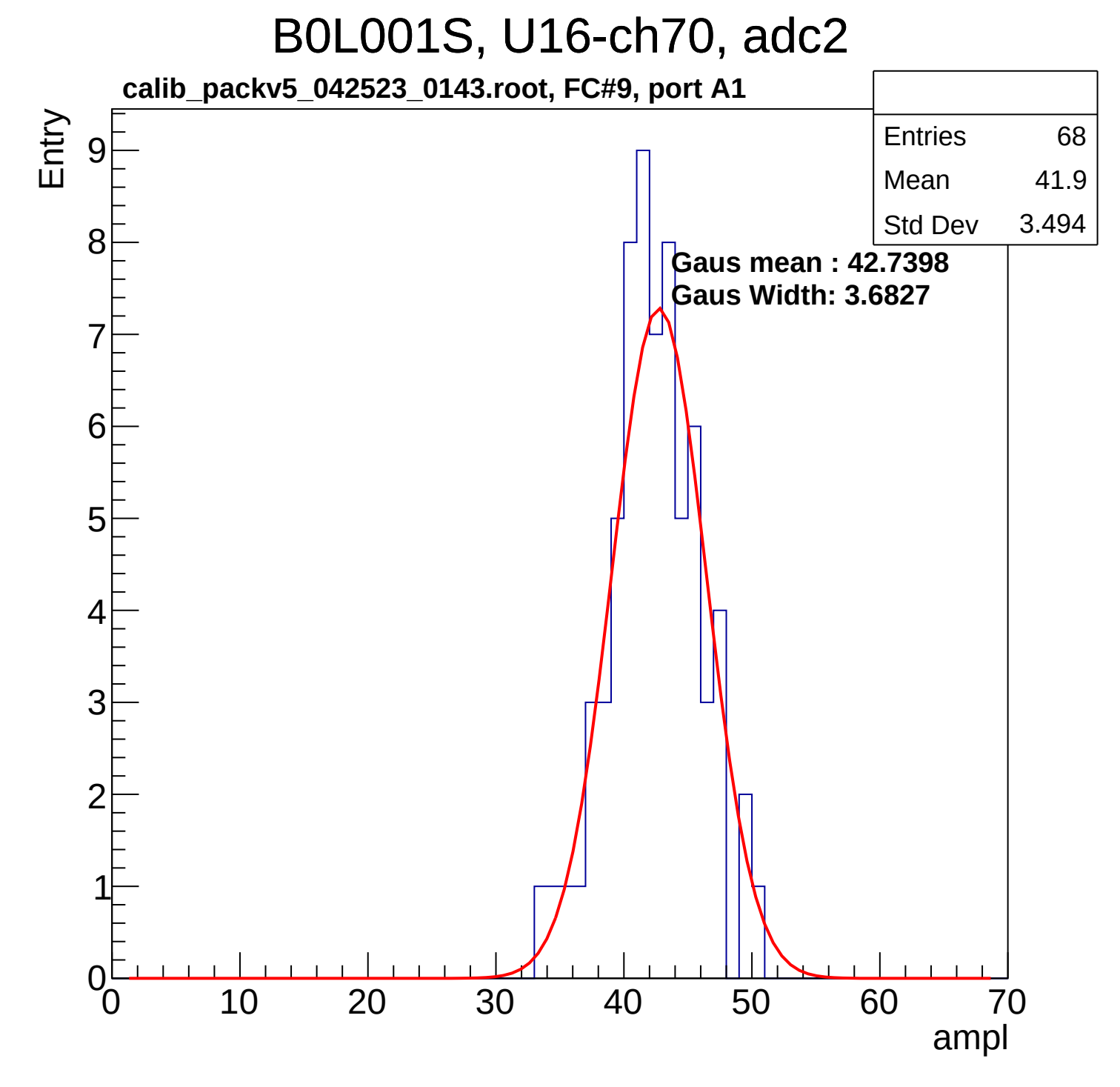

B0L001S, U16-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.36
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

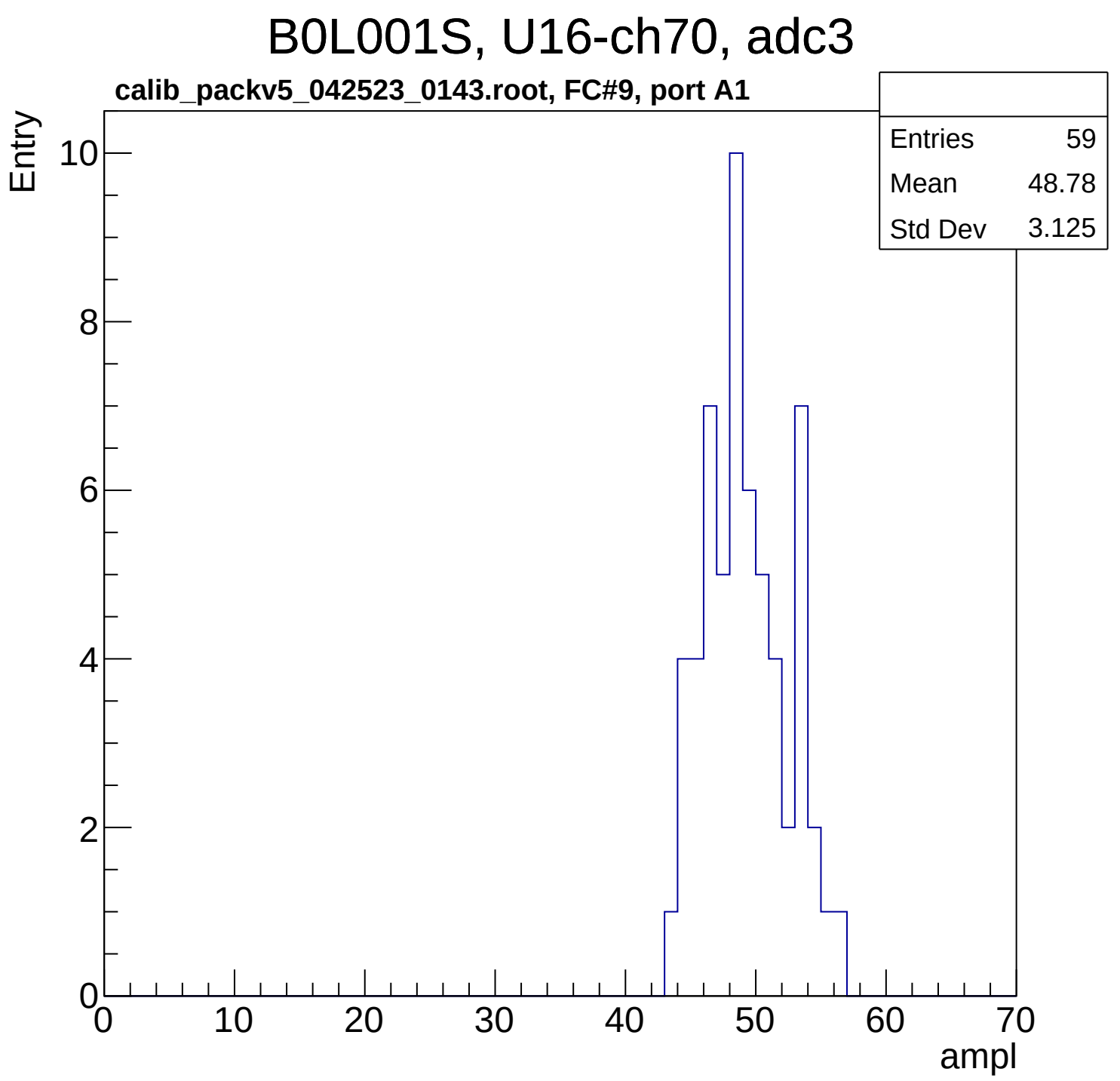

B0L001S, U16-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

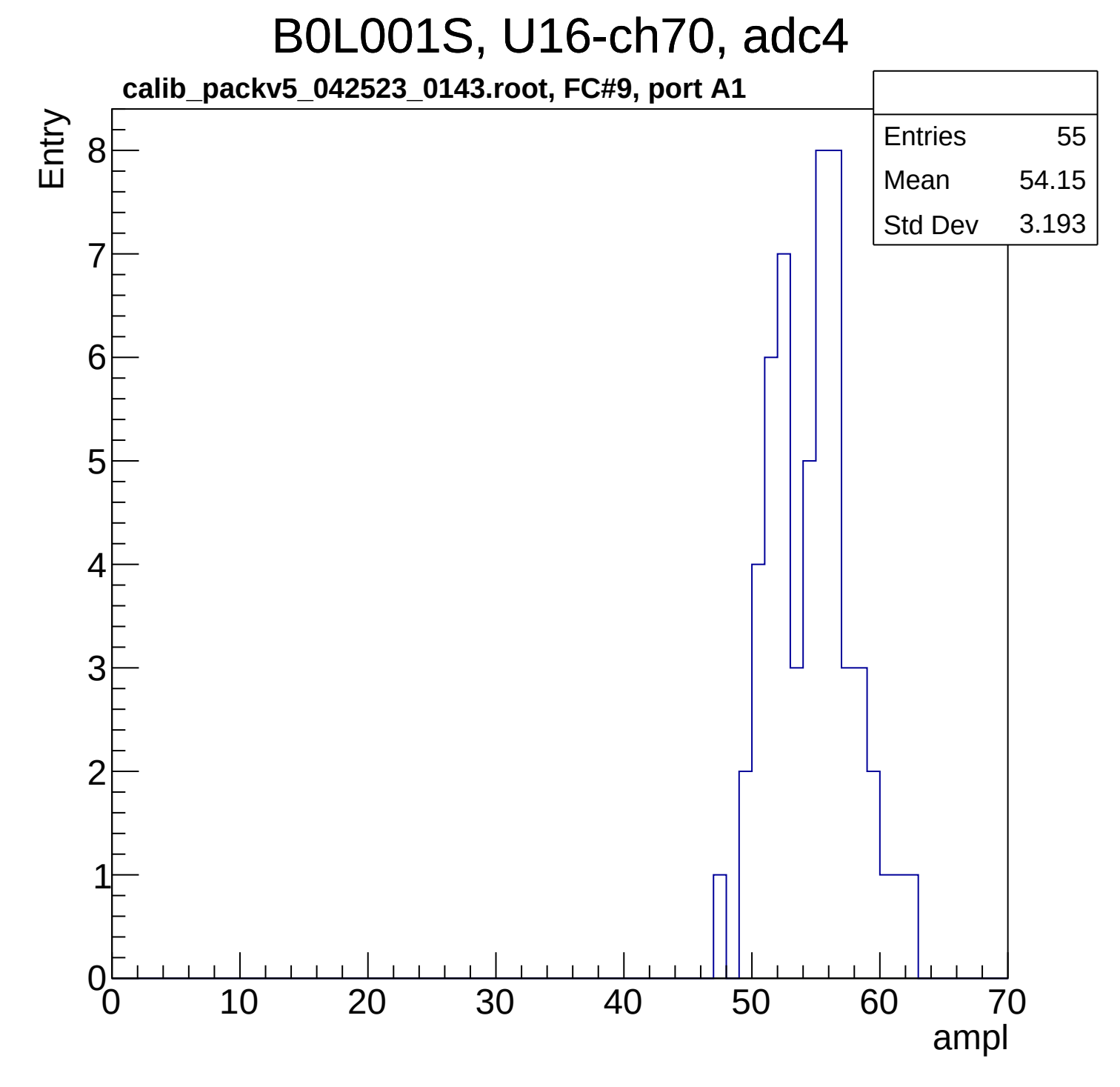

B0L001S, U16-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

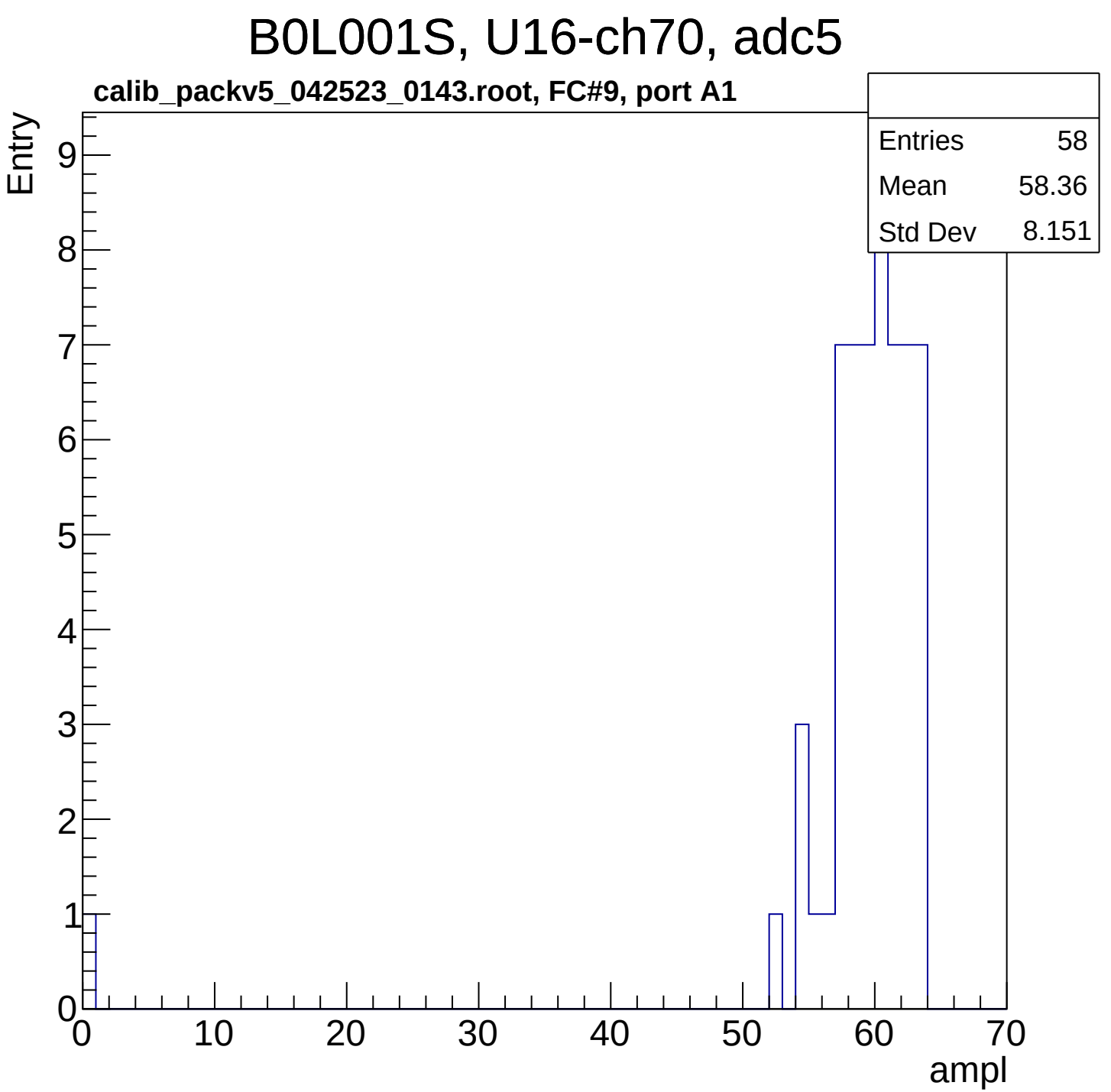

B0L001S, U16-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 28.4
Std Dev 5.126
Gaus mean : 29.2987
Gaus Width: 3.8486
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

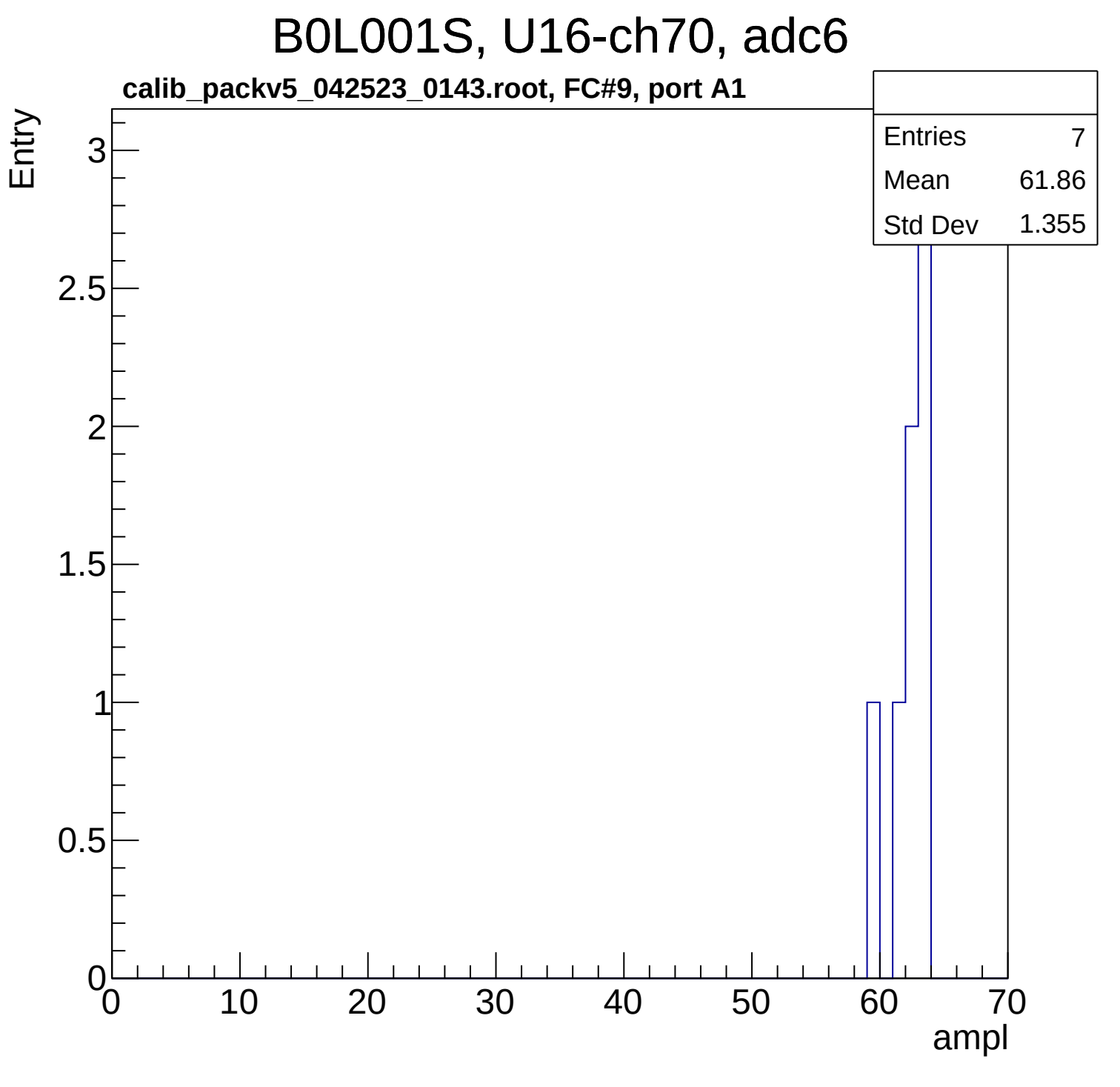

B0L001S, U16-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 35.06
Std Dev 5.571
Gaus mean : 35.5341
Gaus Width: 3.9159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

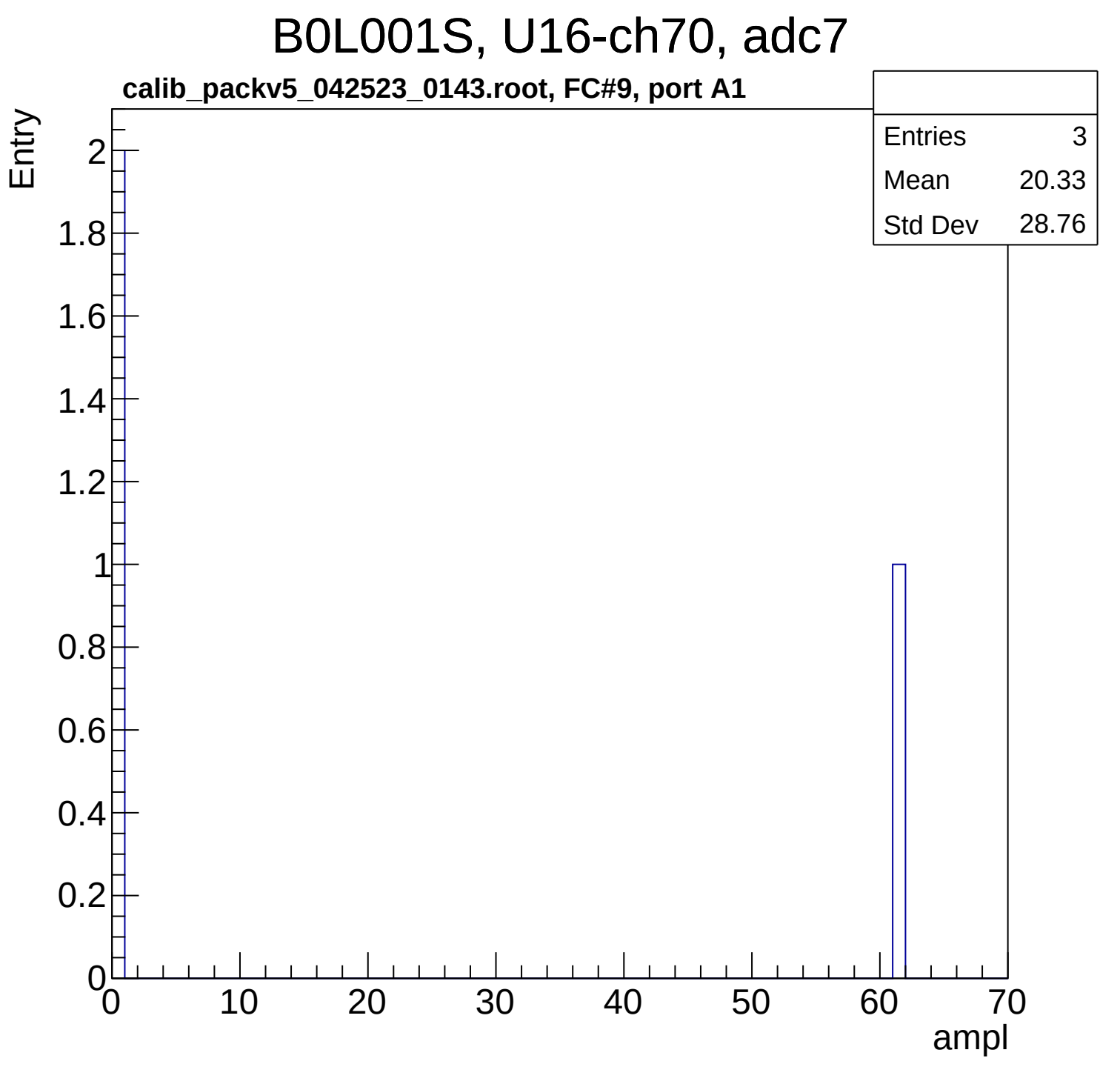

B0L001S, U16-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 41.9
Std Dev 3.494
Gaus mean : 42.7398
Gaus Width: 3.6827
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

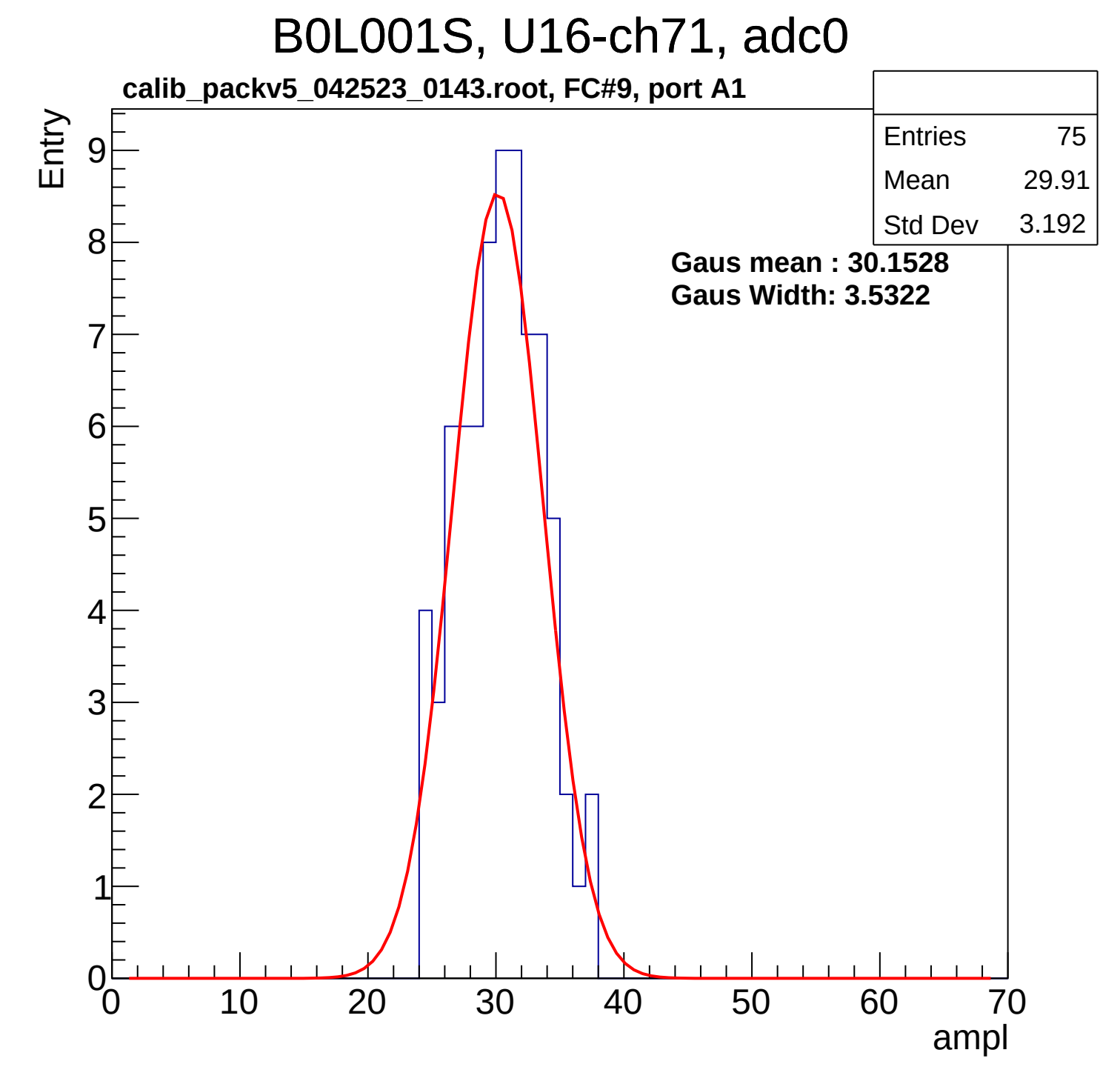

B0L001S, U16-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 48.78
Std Dev 3.125
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

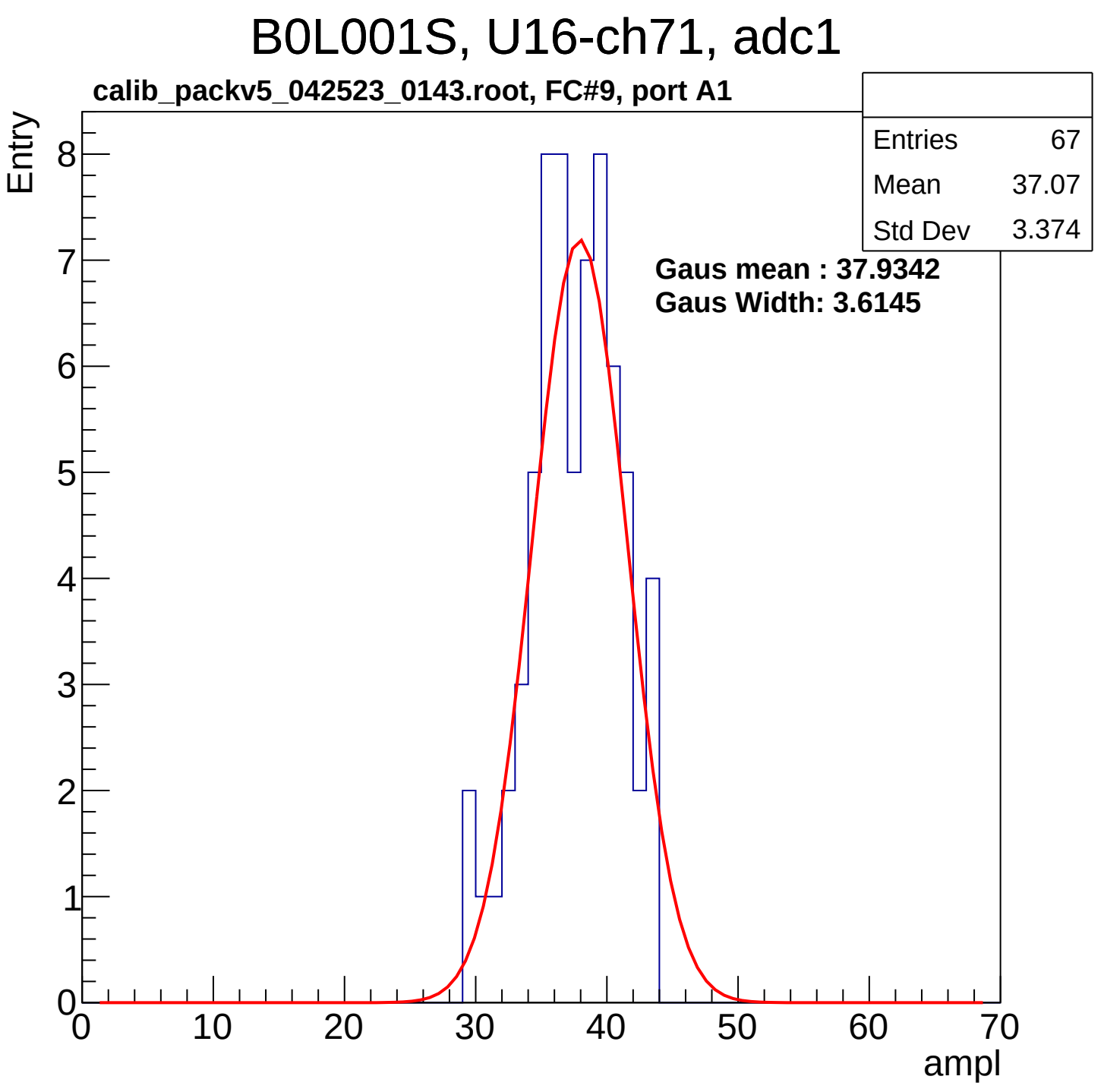

B0L001S, U16-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.15
Std Dev 3.193
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

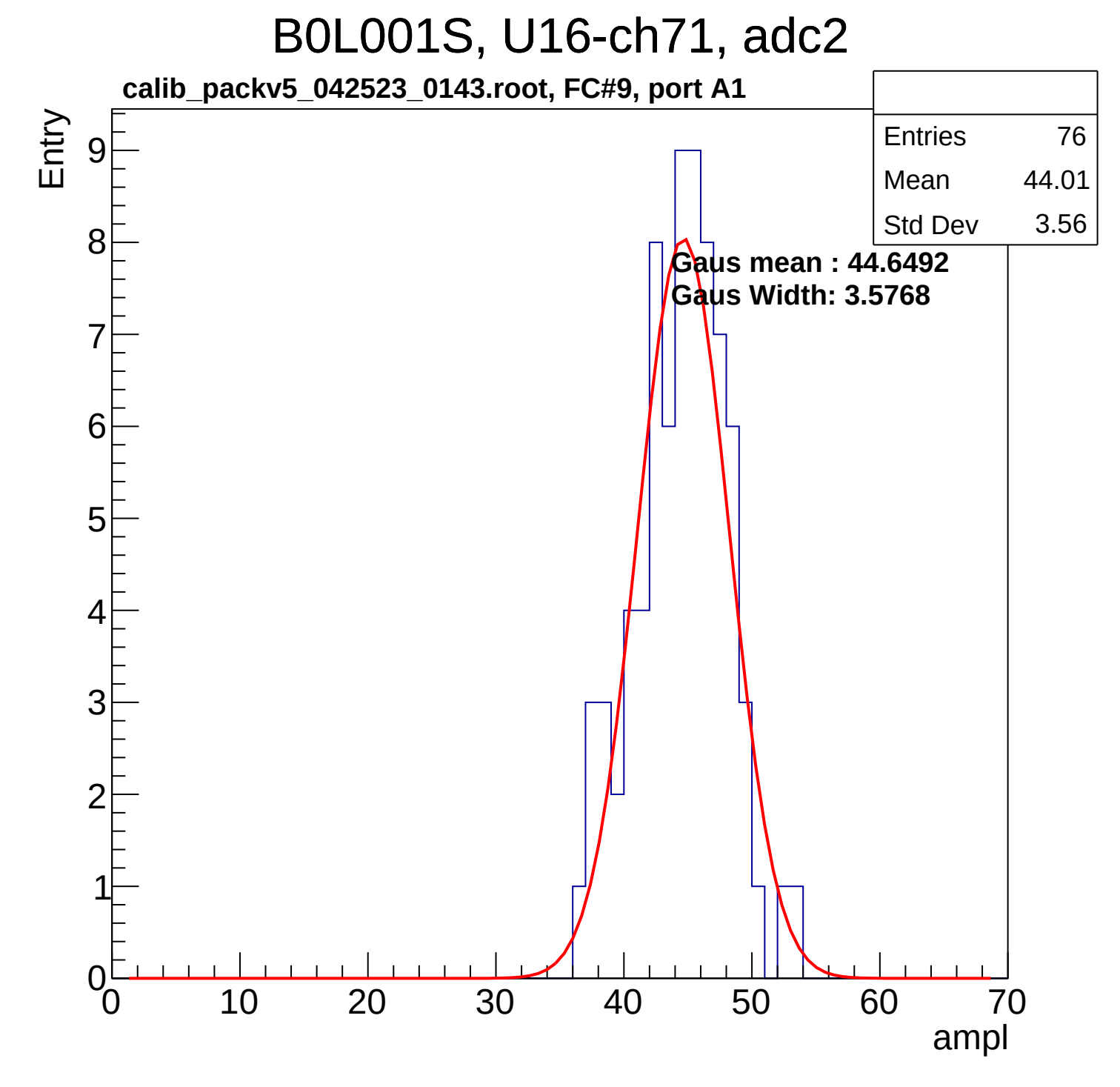

B0L001S, U16-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 58.36
Std Dev 8.151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

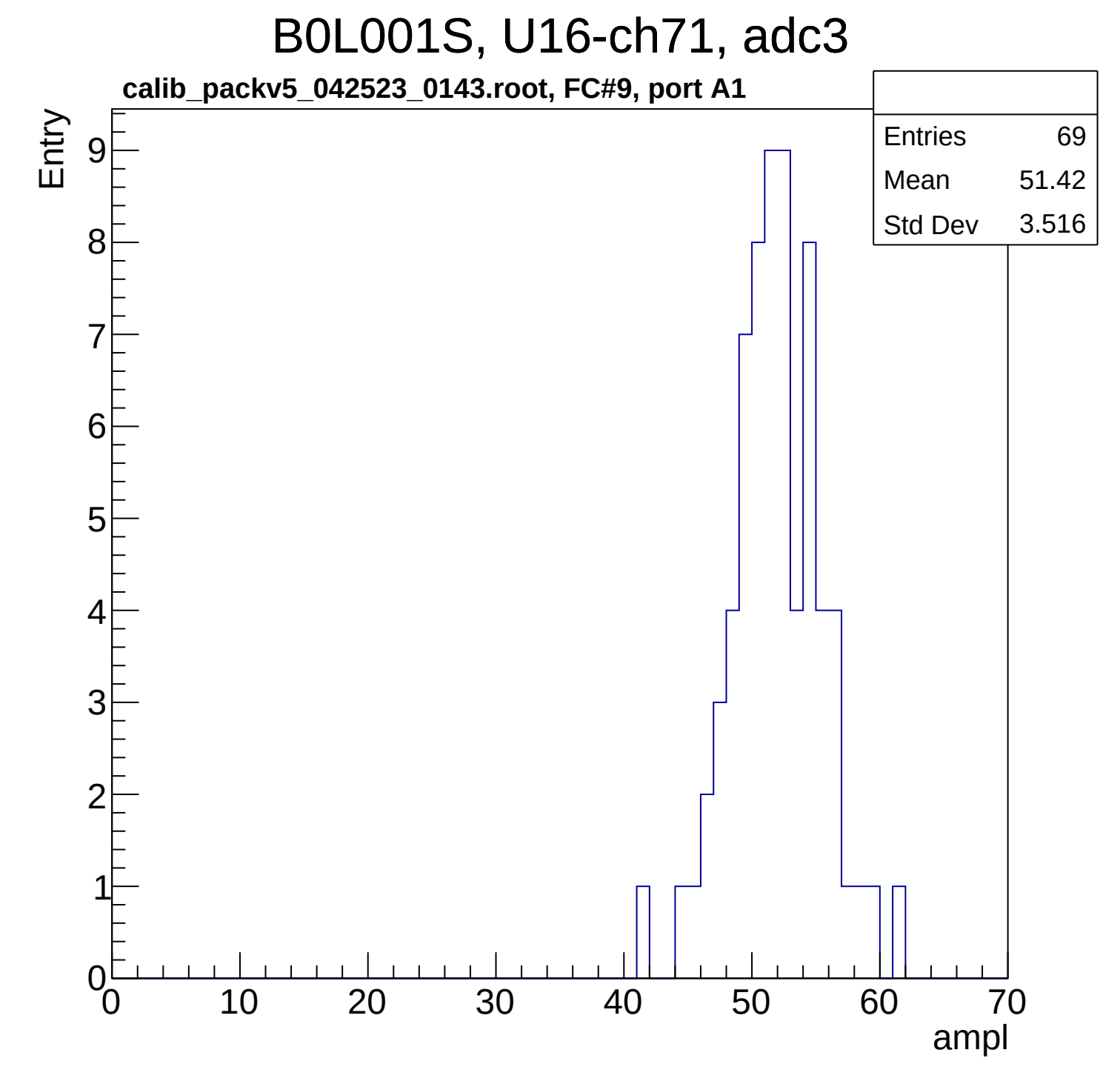

B0L001S, U16-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.86
Std Dev 1.355
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

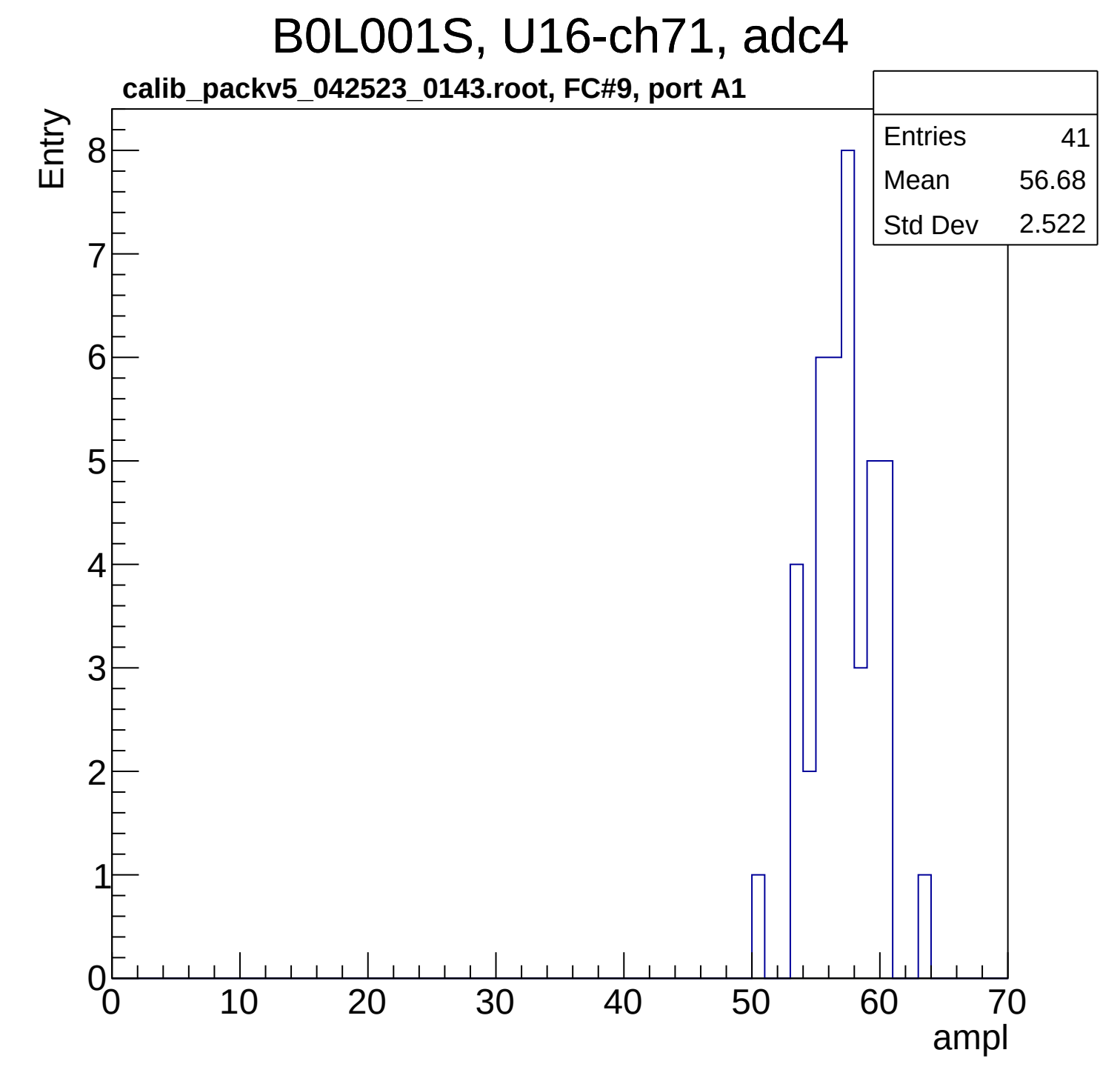

B0L001S, U16-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.91
Std Dev 3.192
Gaus mean : 30.1528
Gaus Width: 3.5322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.07
Std Dev 3.374
Gaus mean : 37.9342
Gaus Width: 3.6145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.01
Std Dev 3.56
Gaus mean : 44.6492
Gaus Width: 3.5768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.42
Std Dev 3.516
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 56.68
Std Dev 2.522
Entry
ampl
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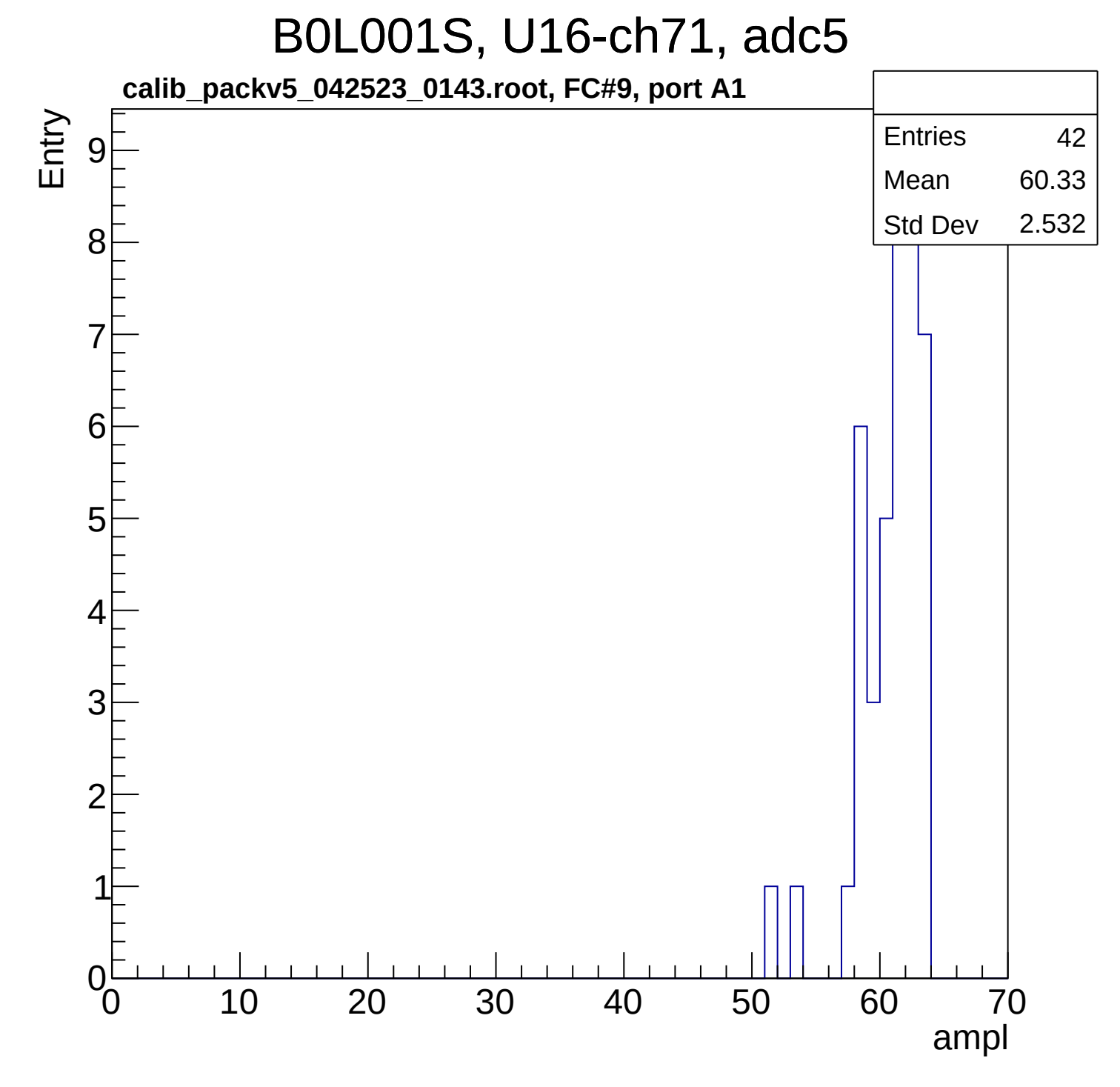
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.33
Std Dev 2.532
Entry
ampl
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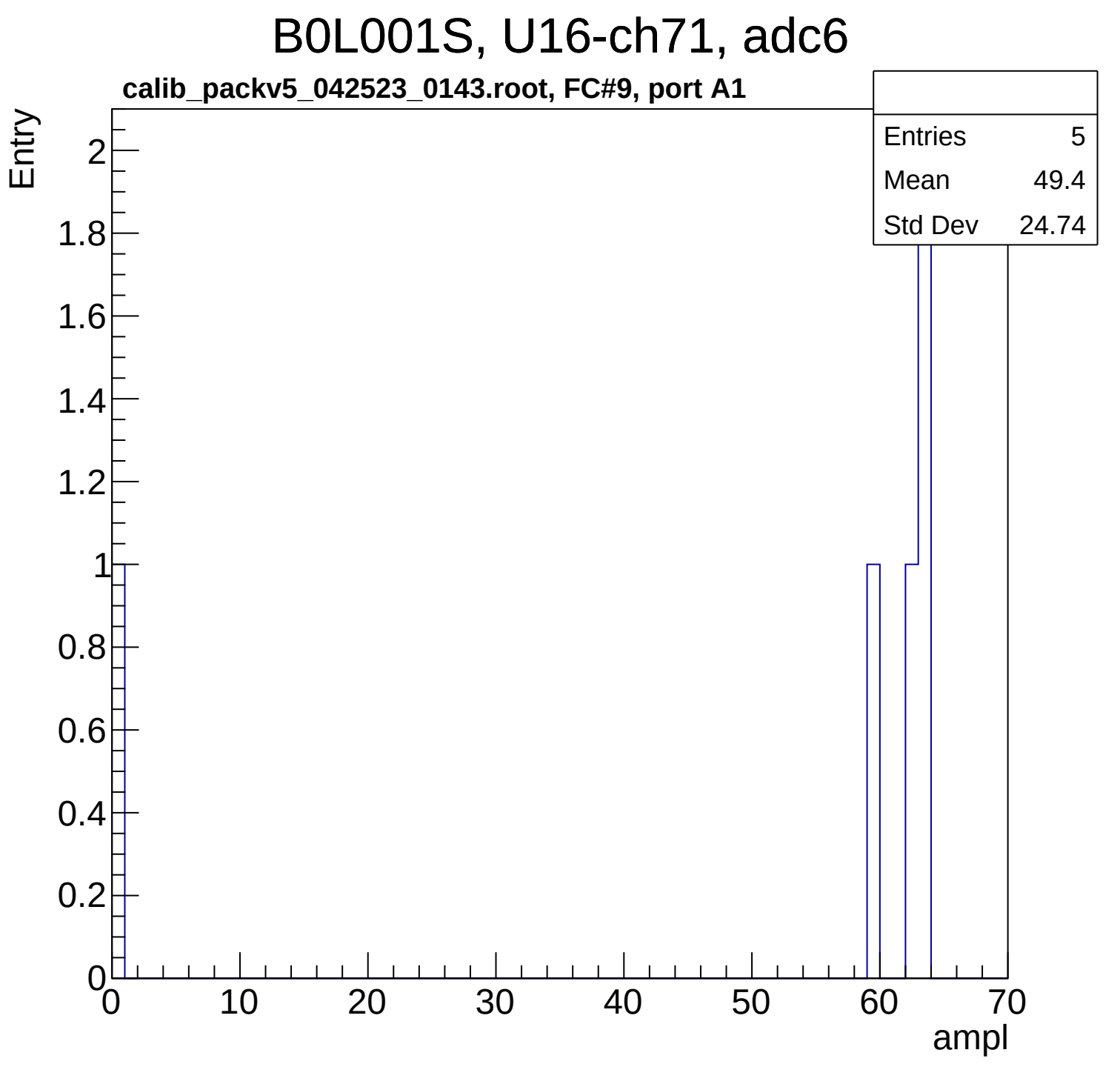
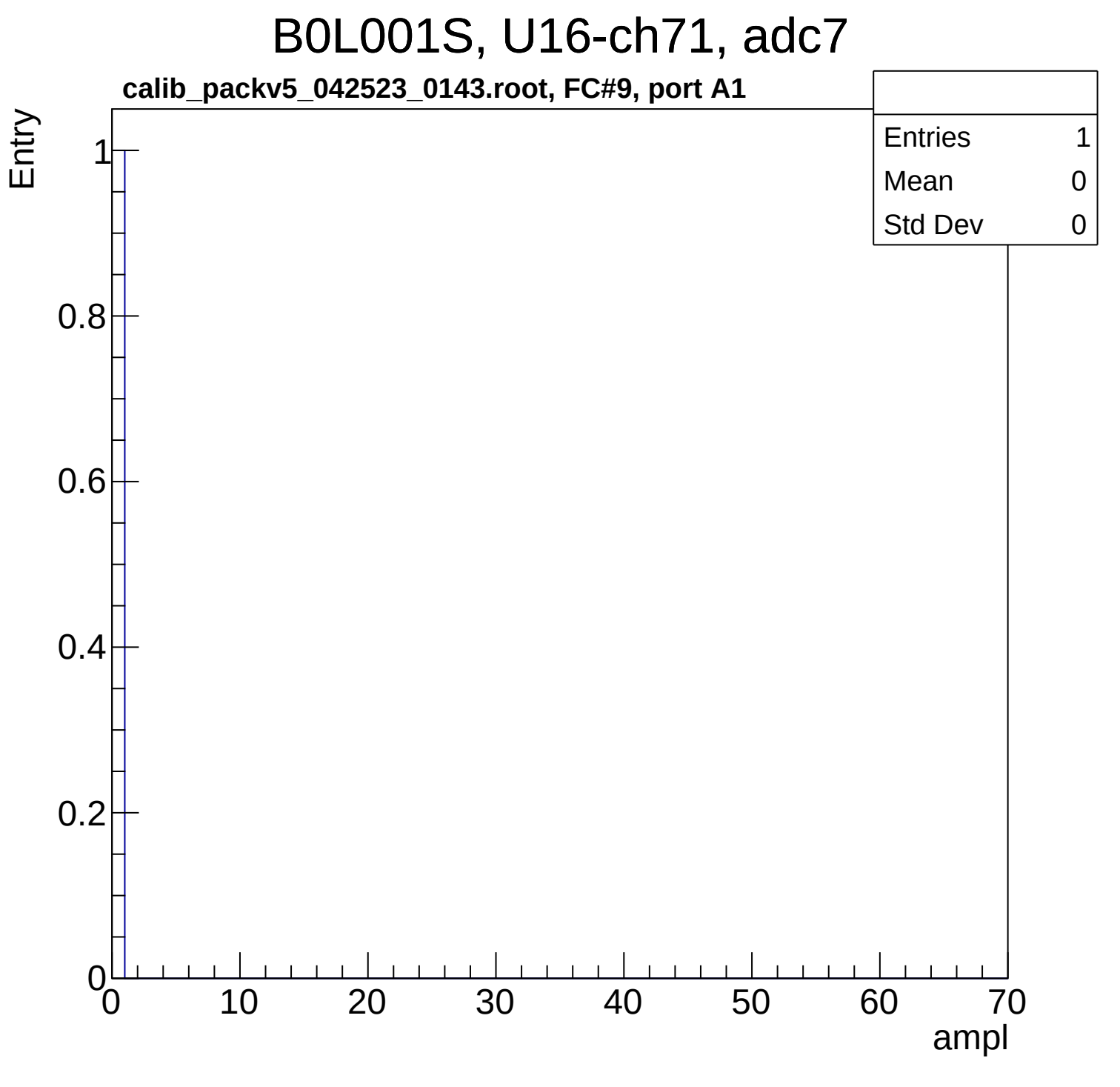
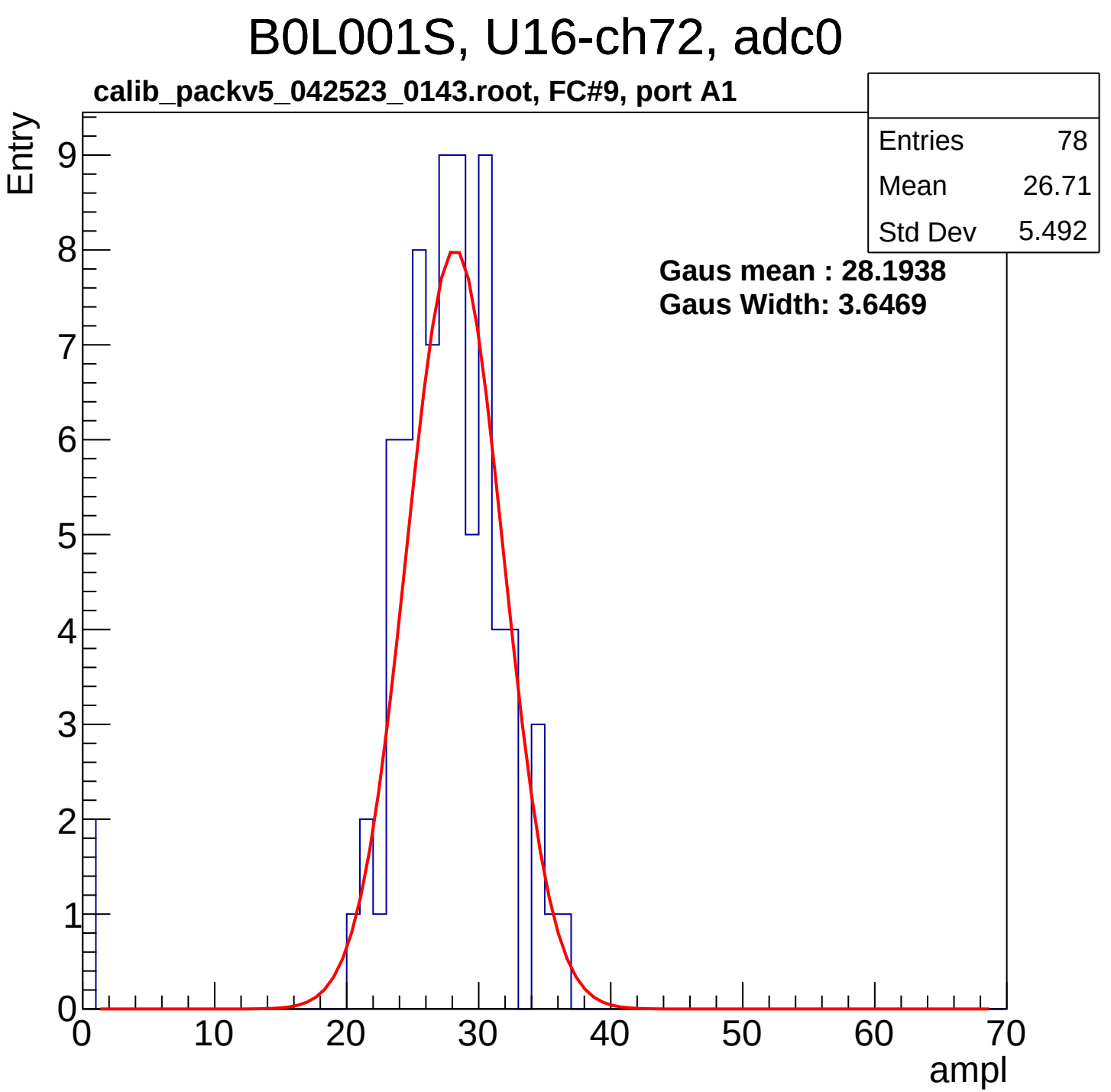
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.4
Std Dev 24.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 26.71
Std Dev 5.492
Gaus mean : 28.1938
Gaus Width: 3.6469
Entry
ampl
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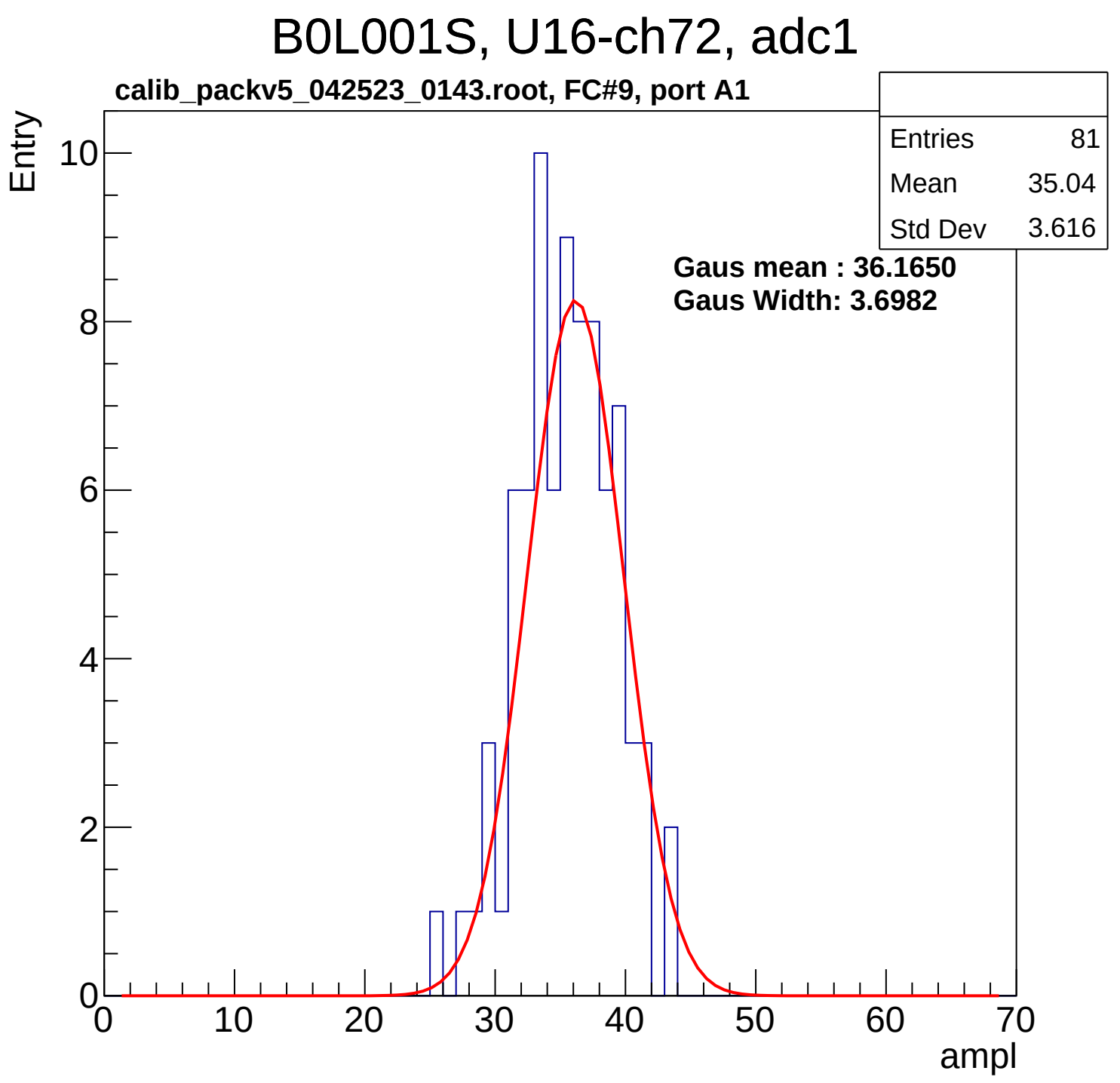
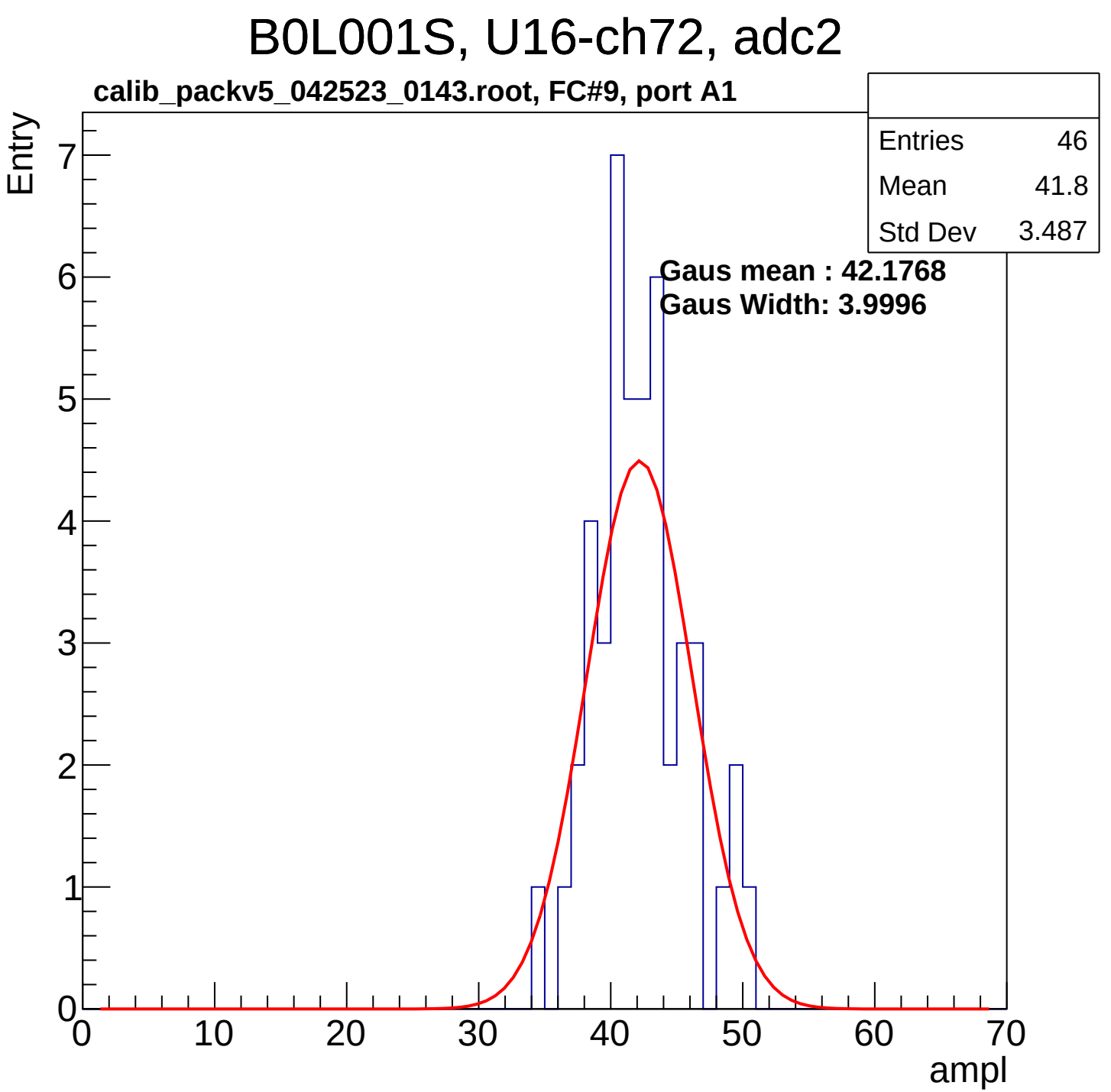
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 35.04
Std Dev 3.616
Gaus mean : 36.1650
Gaus Width: 3.6982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 41.8
Std Dev 3.487
Gaus mean : 42.1768
Gaus Width: 3.9996
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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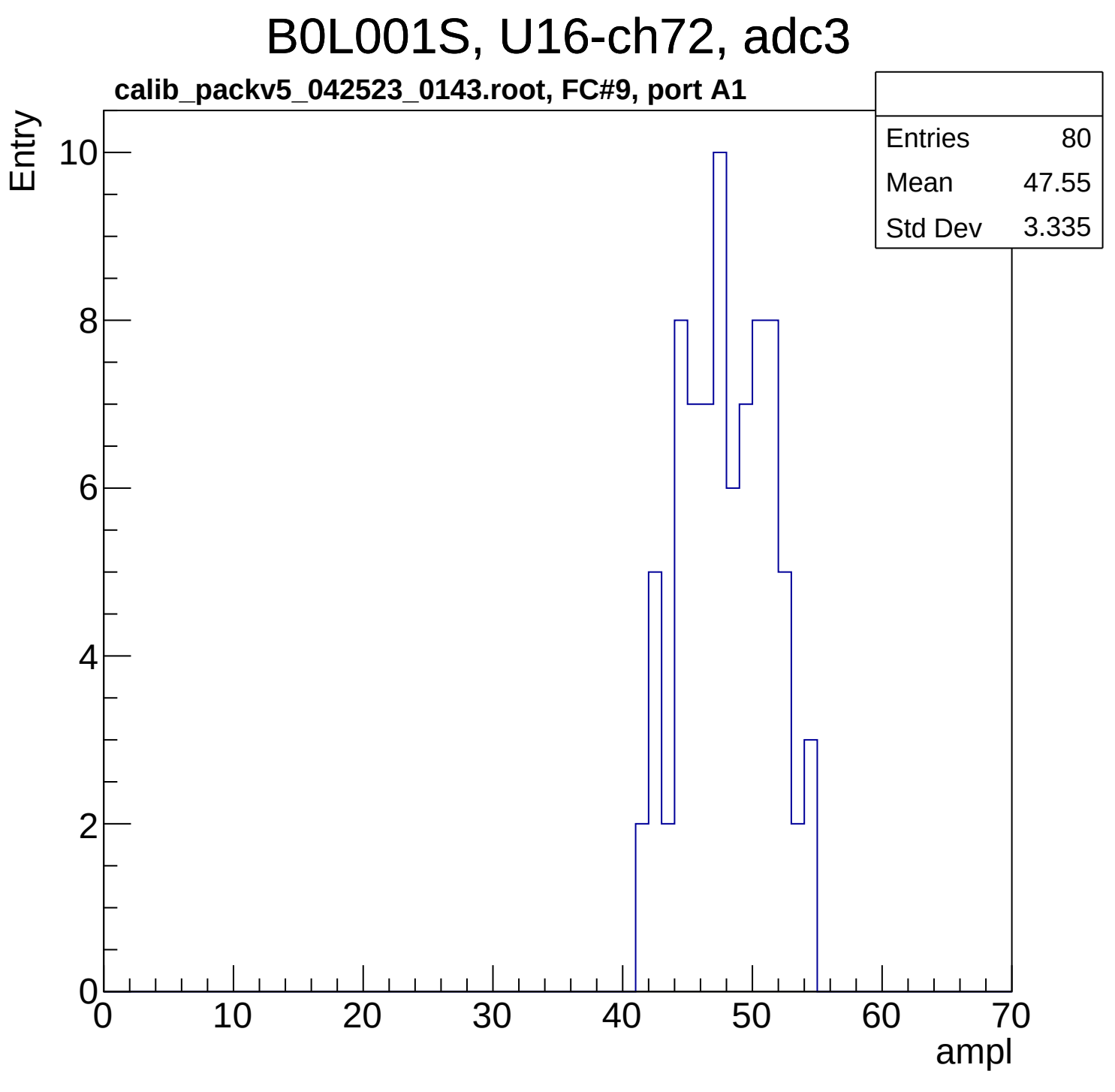
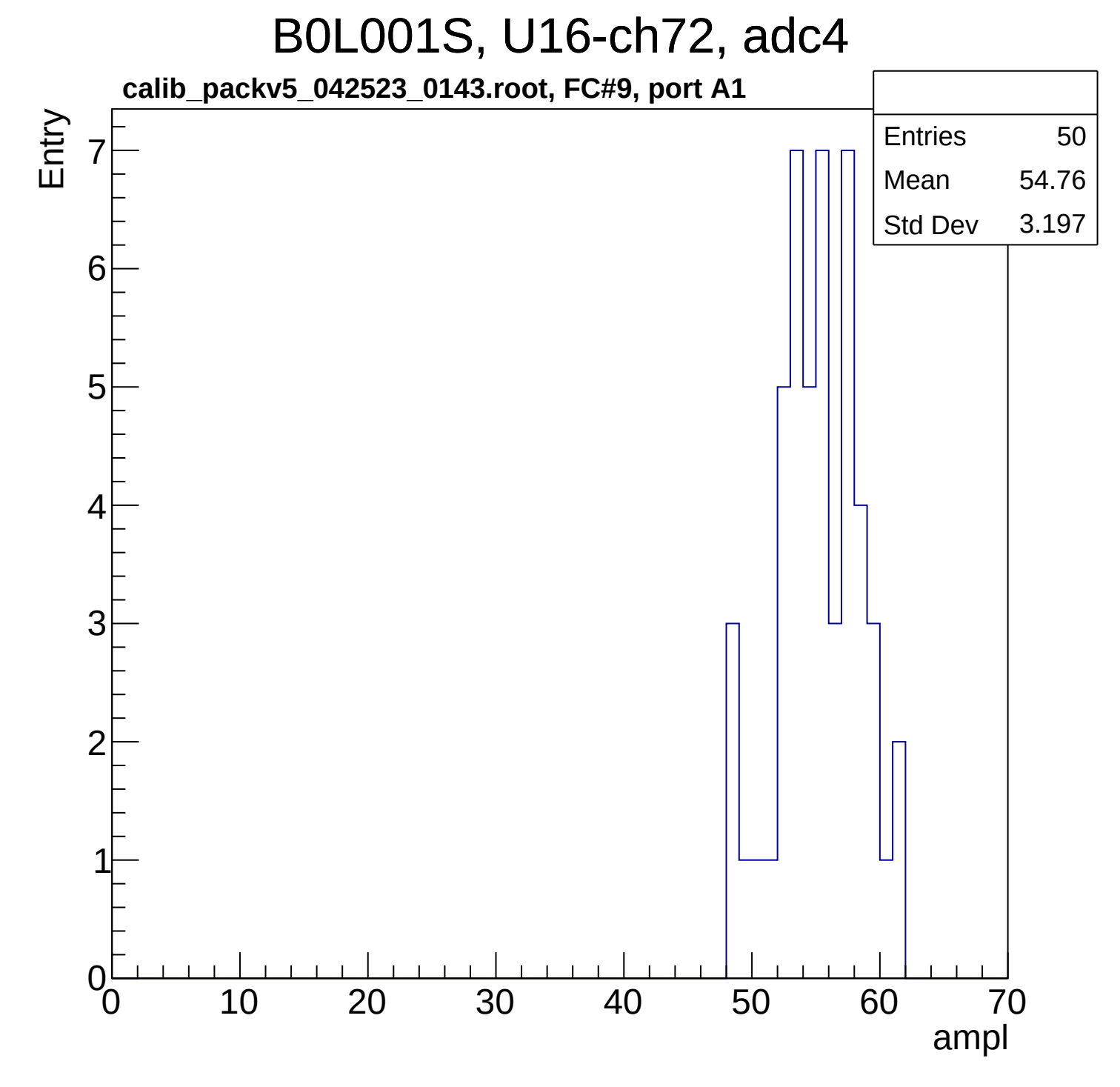
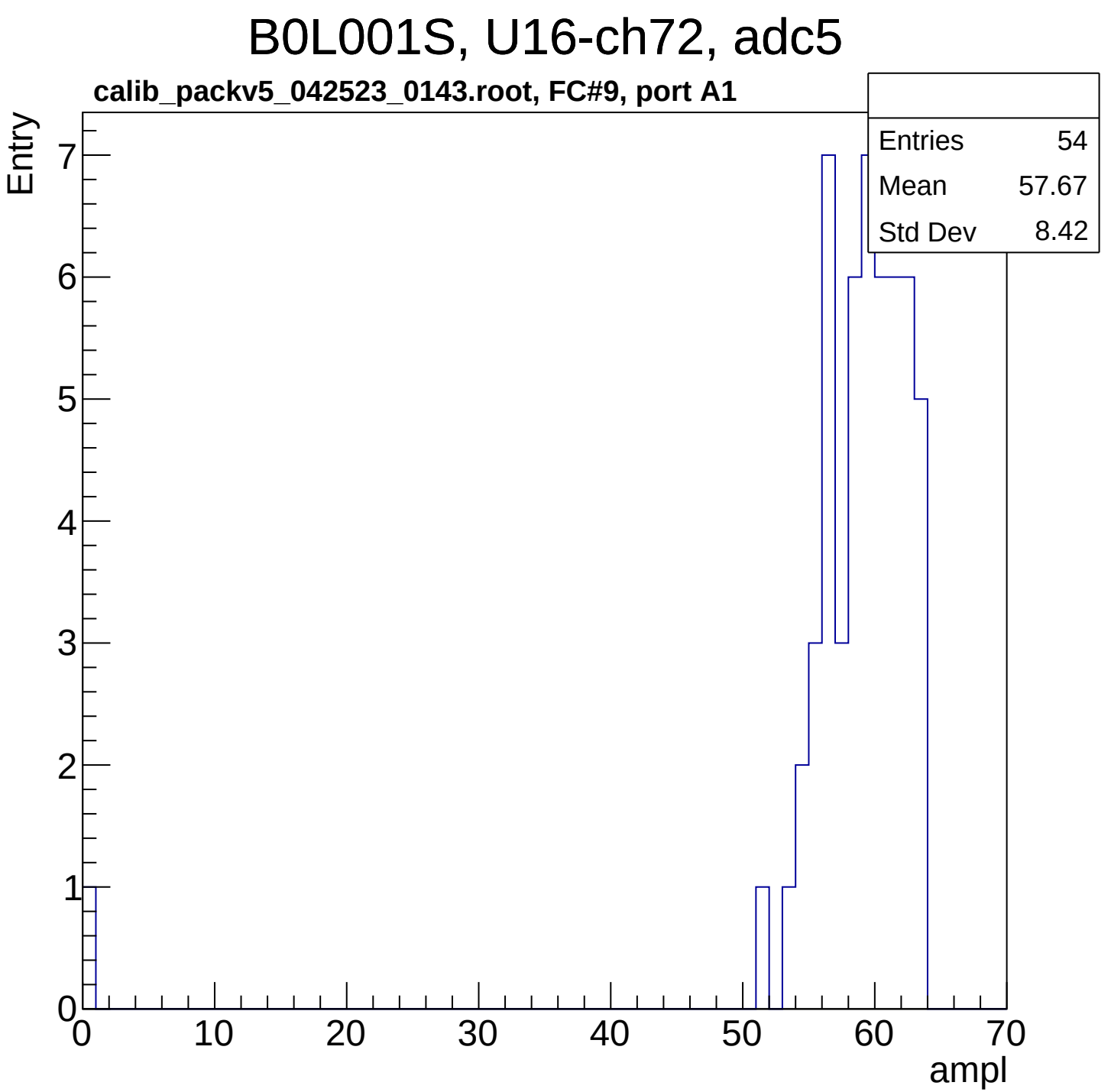
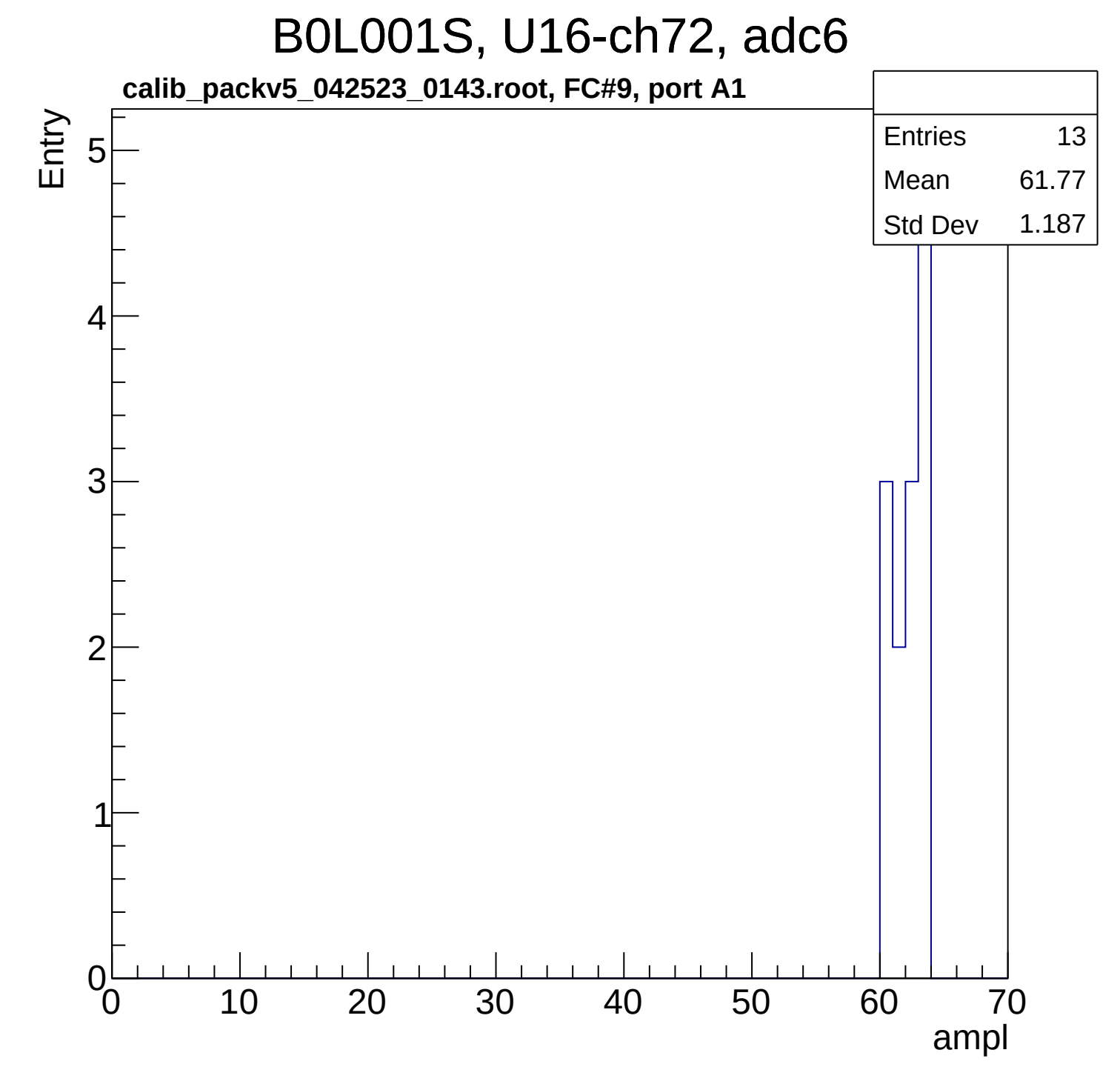
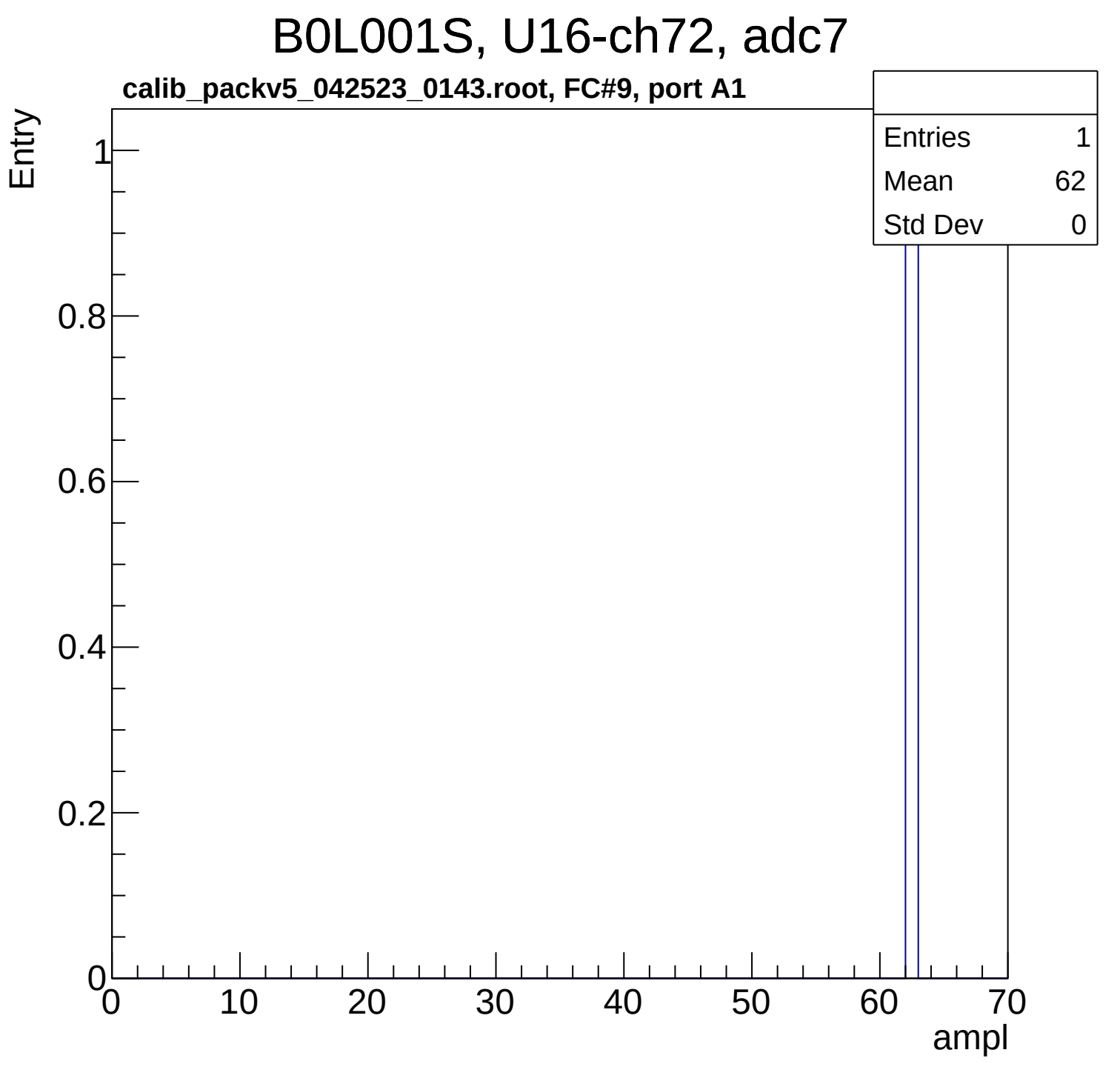

B0L001S, U16-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 47.55
Std Dev 3.335
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 54.76
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.67
Std Dev 8.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.77
Std Dev 1.187
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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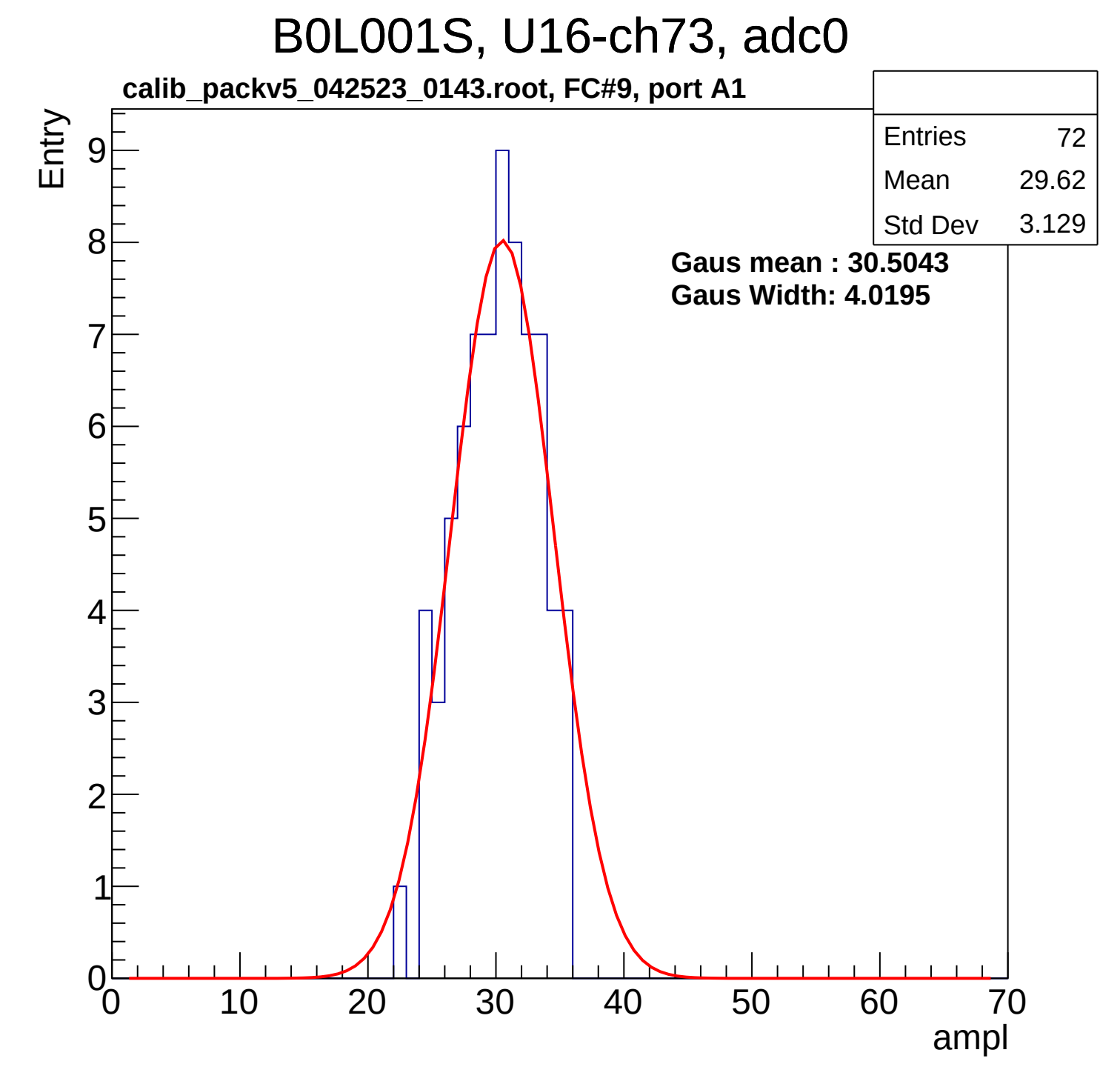
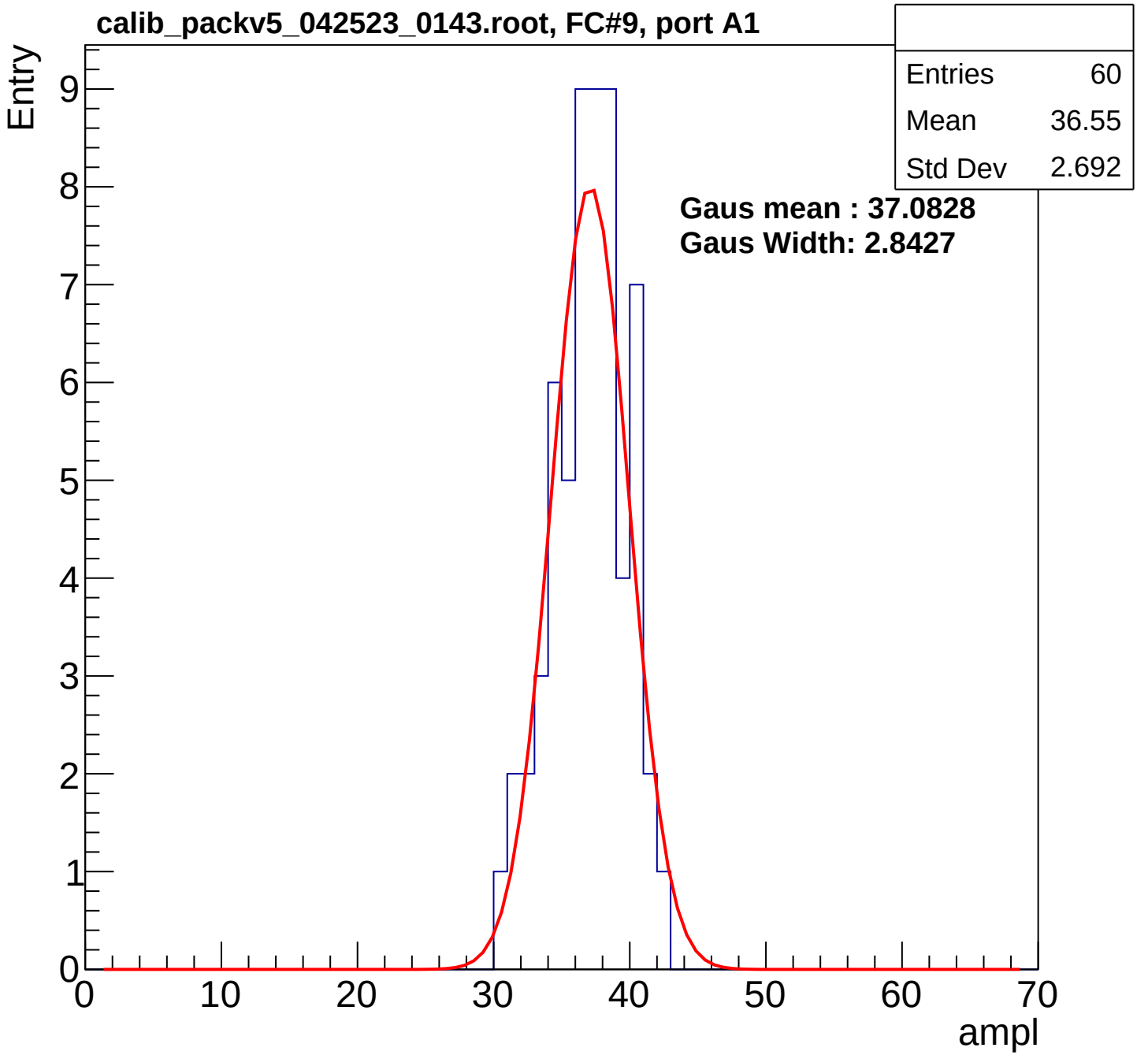
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.62
Std Dev 3.129
Gaus mean : 30.5043
Gaus Width: 4.0195
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.55
Std Dev 2.692
Gaus mean : 37.0828
Gaus Width: 2.8427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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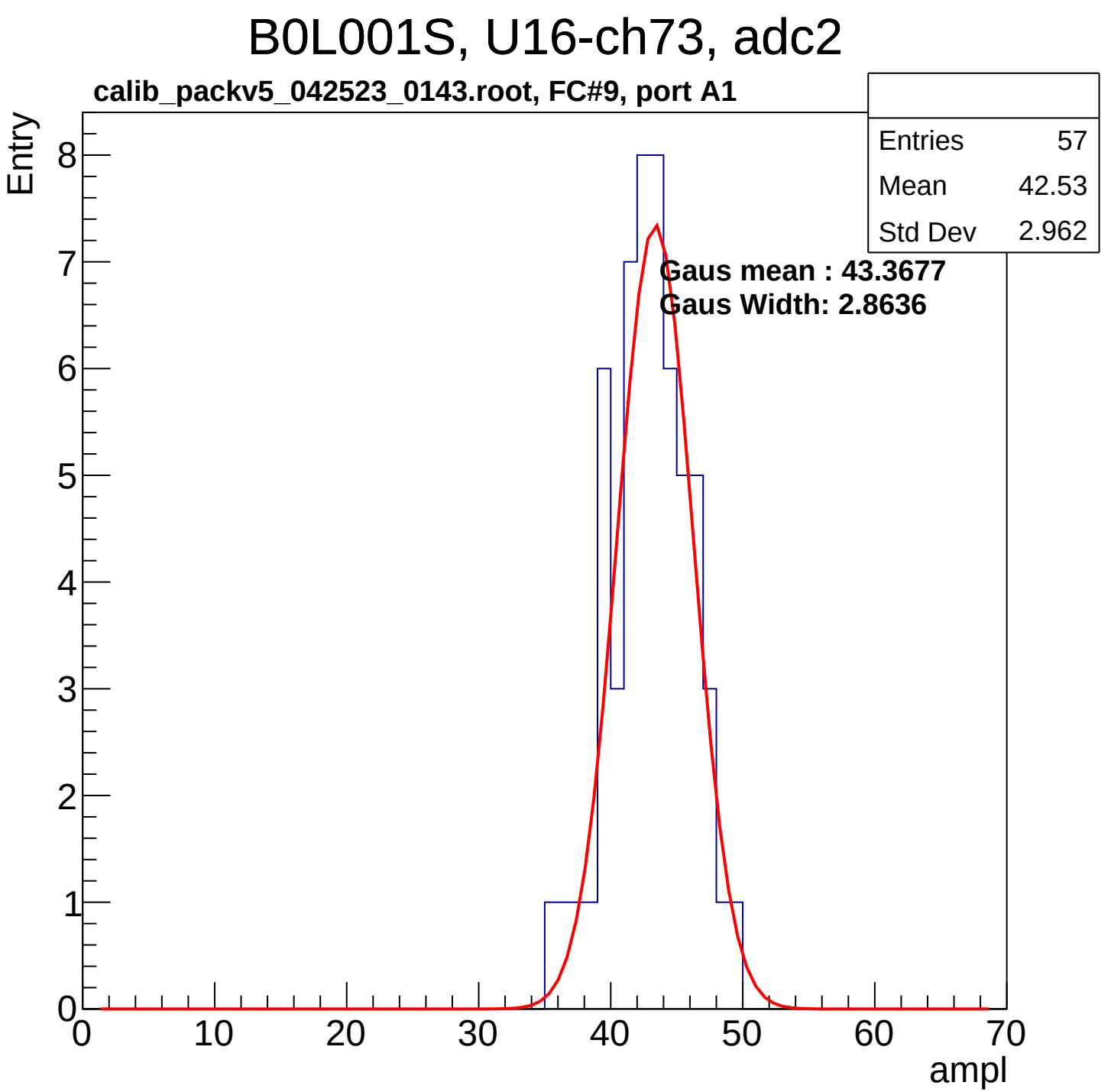
B0L001S, U16-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.53
Std Dev 2.962
Gaus mean : 43.3677
Gaus Width: 2.8636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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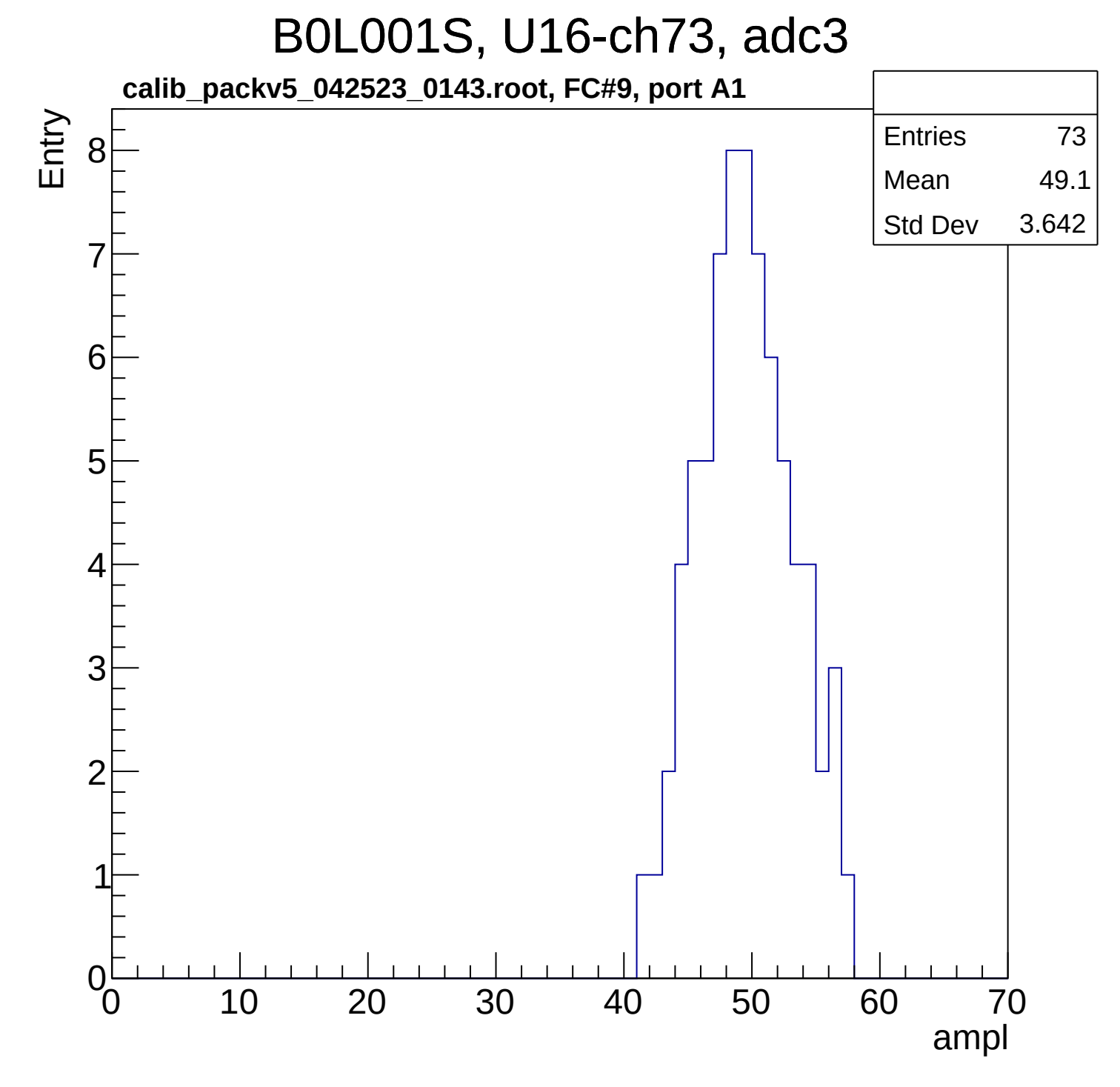
B0L001S, U16-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 49.1
Std Dev 3.642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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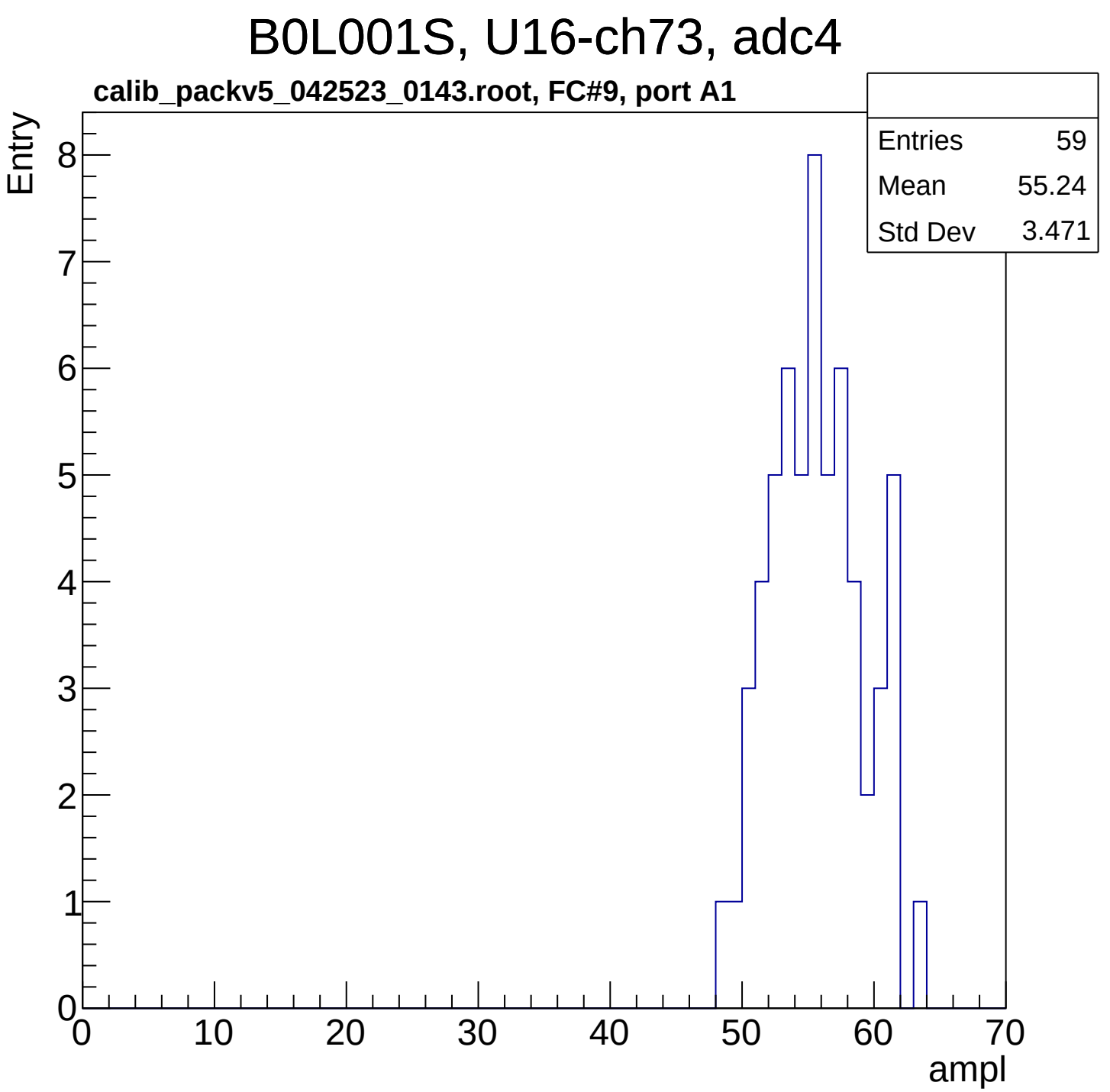
B0L001S, U16-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.24
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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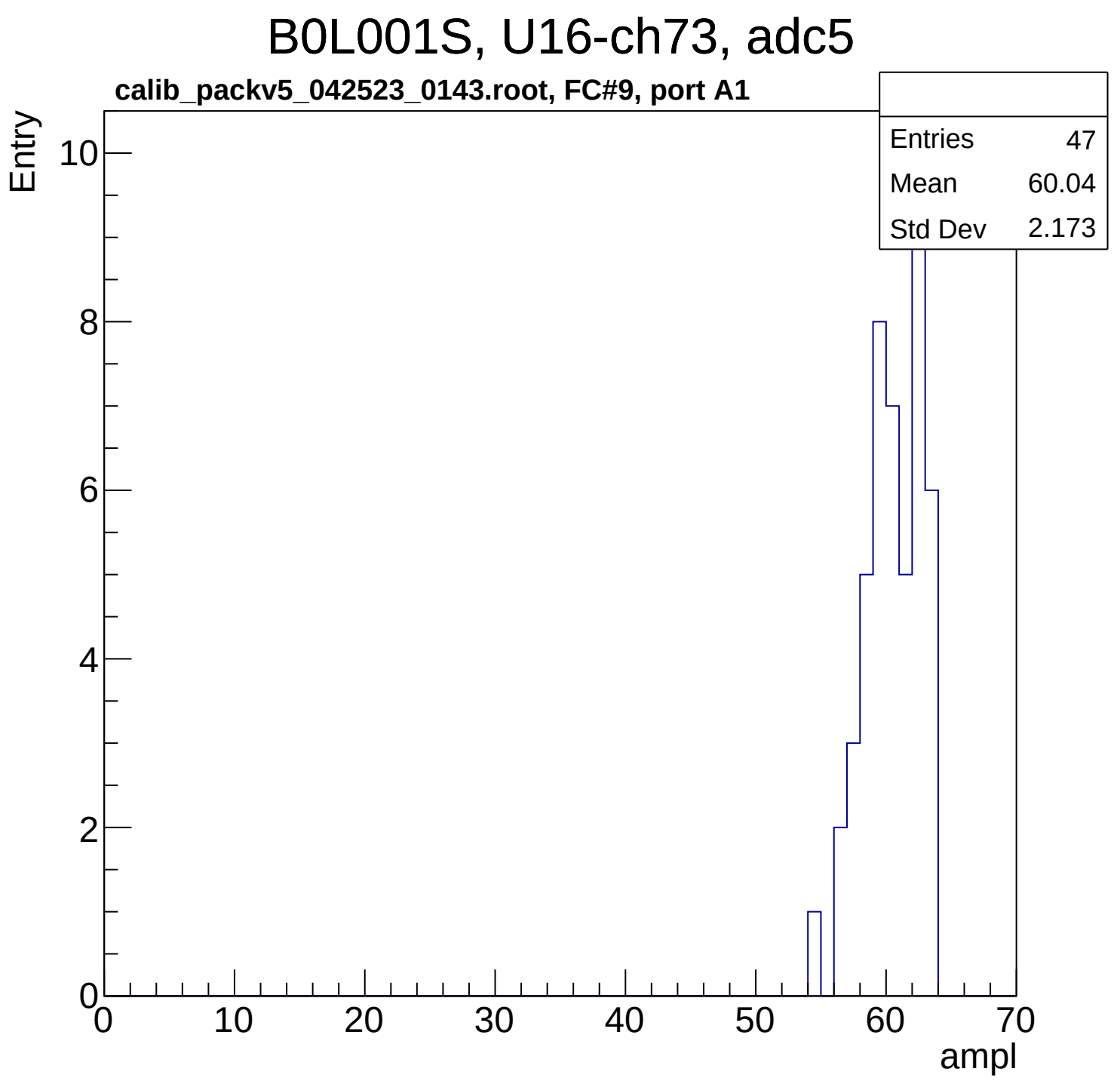
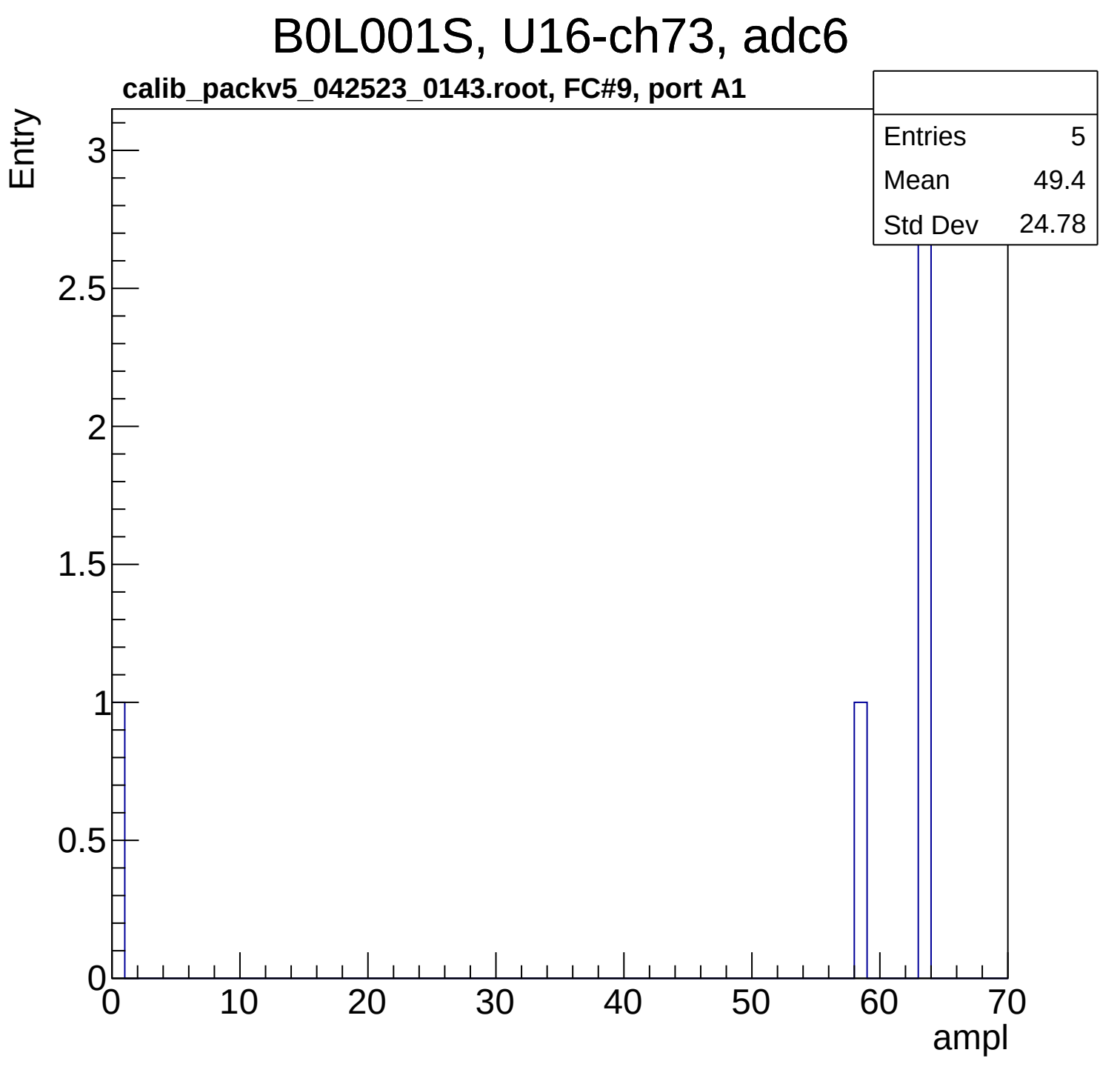
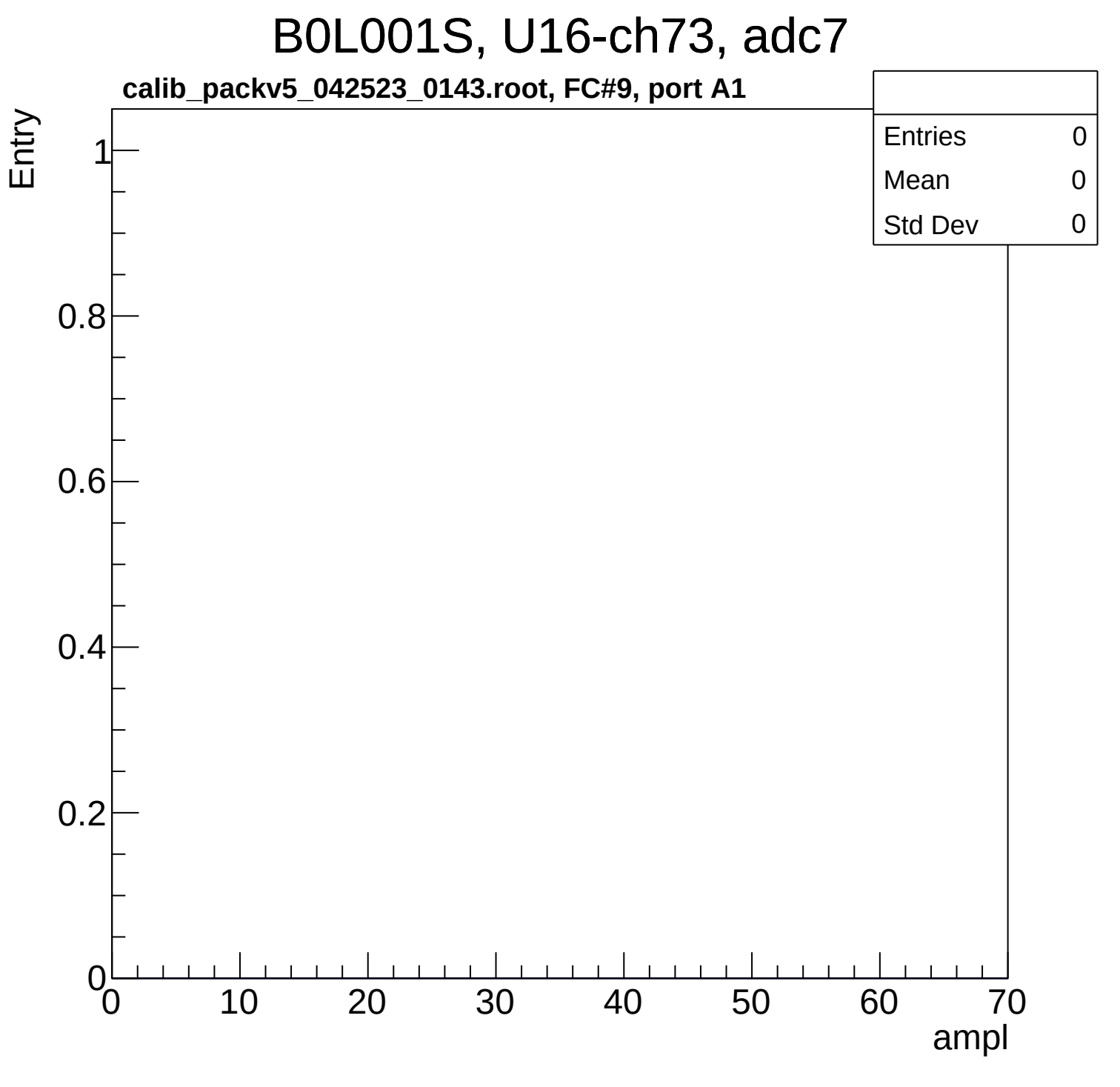
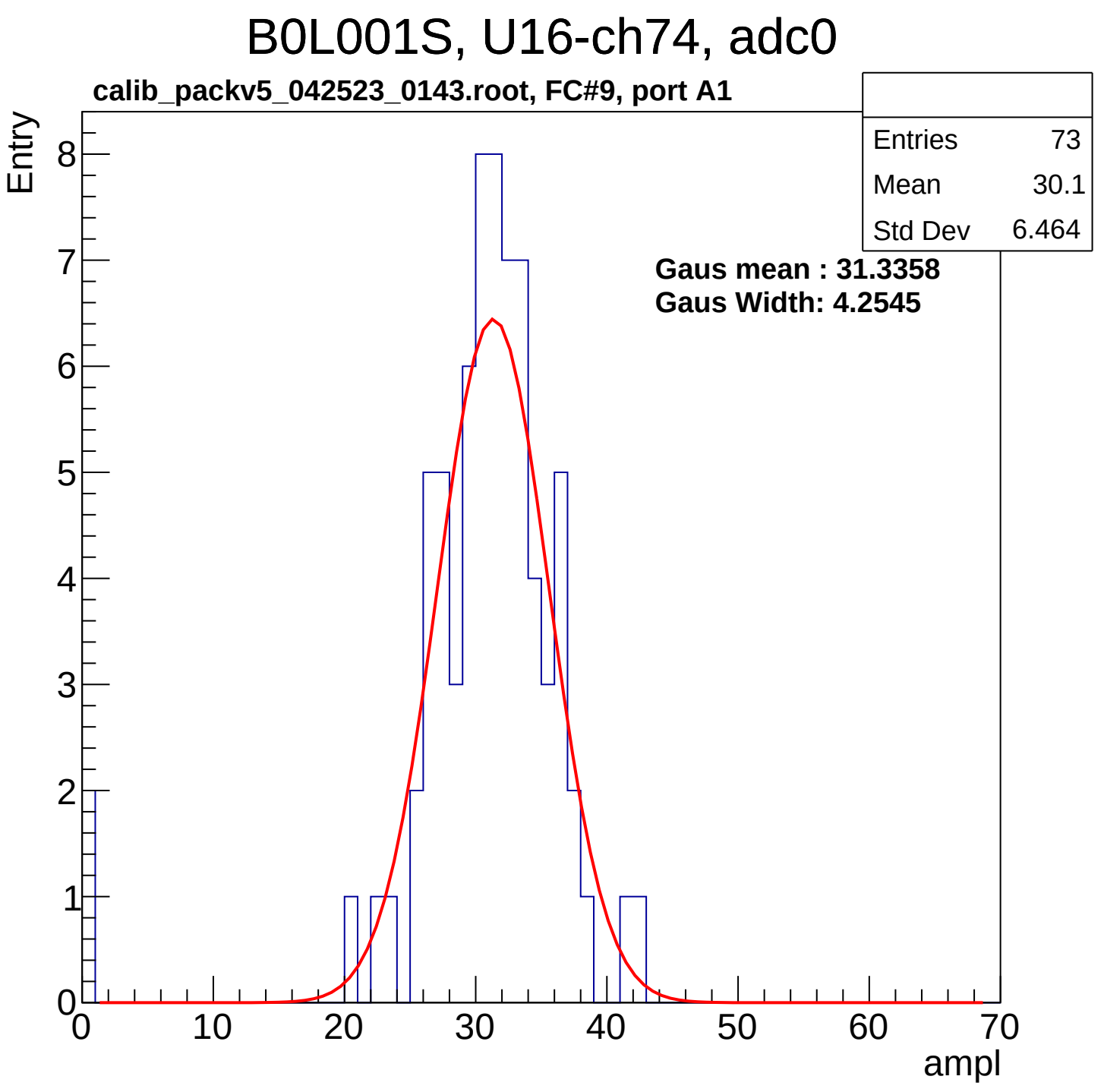
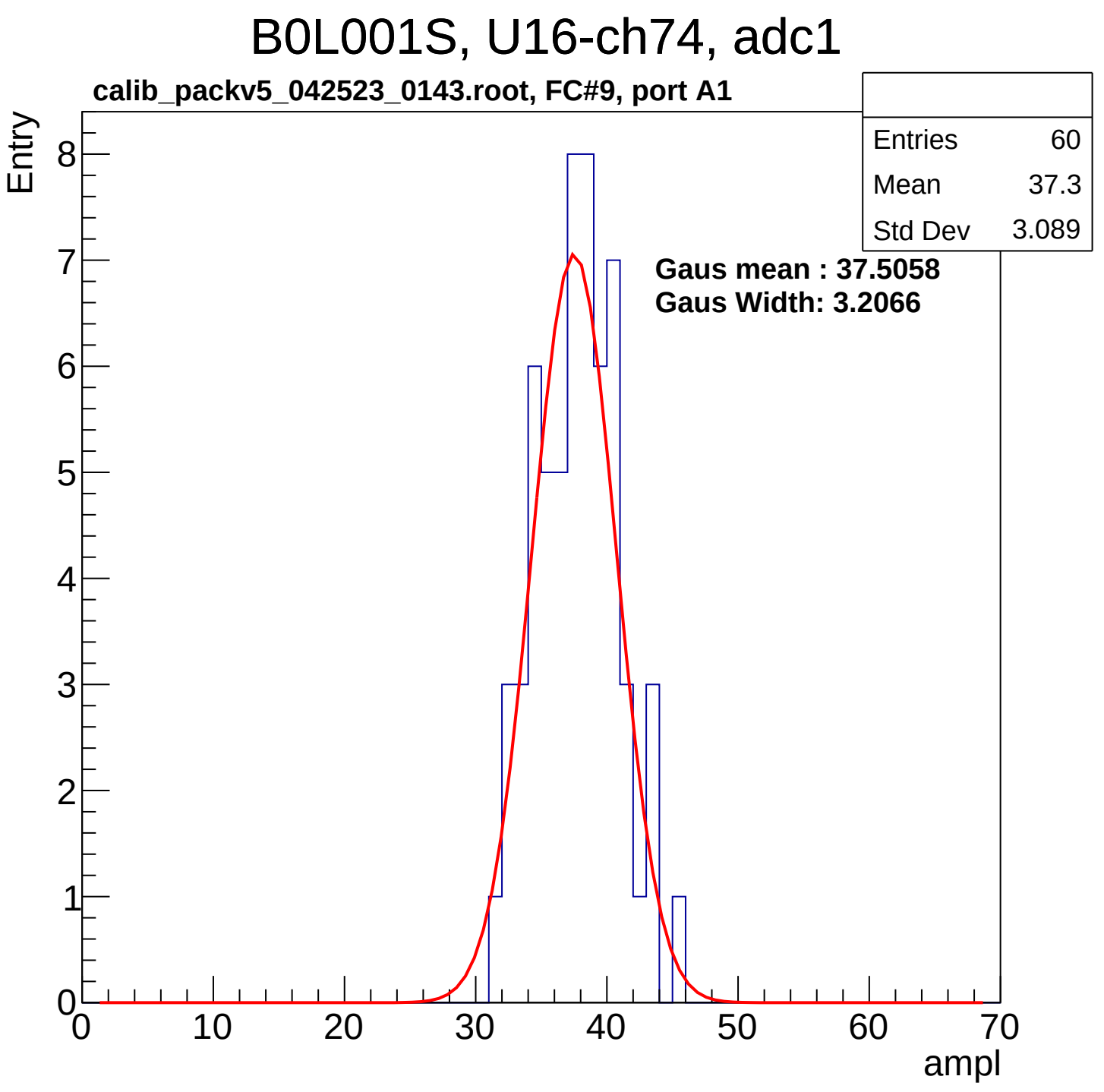
60
70

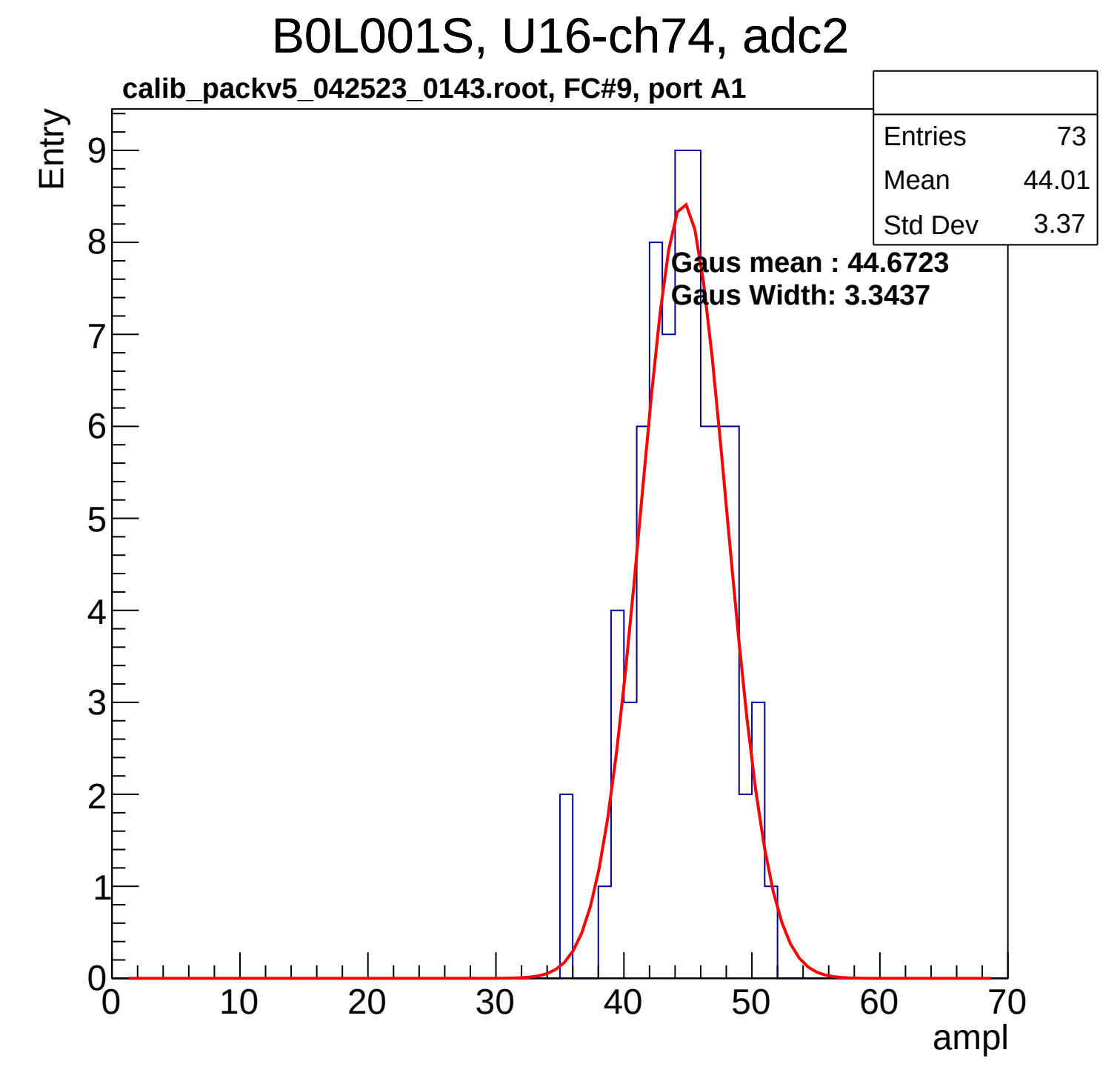

B0L001S, U16-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 60.04
Std Dev 2.173
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.4
Std Dev 24.78
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.1
Std Dev 6.464
Gaus mean : 31.3358
Gaus Width: 4.2545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.3
Std Dev 3.089
Gaus mean : 37.5058
Gaus Width: 3.2066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.01
Std Dev 3.37
Gaus mean : 44.6723
Gaus Width: 3.3437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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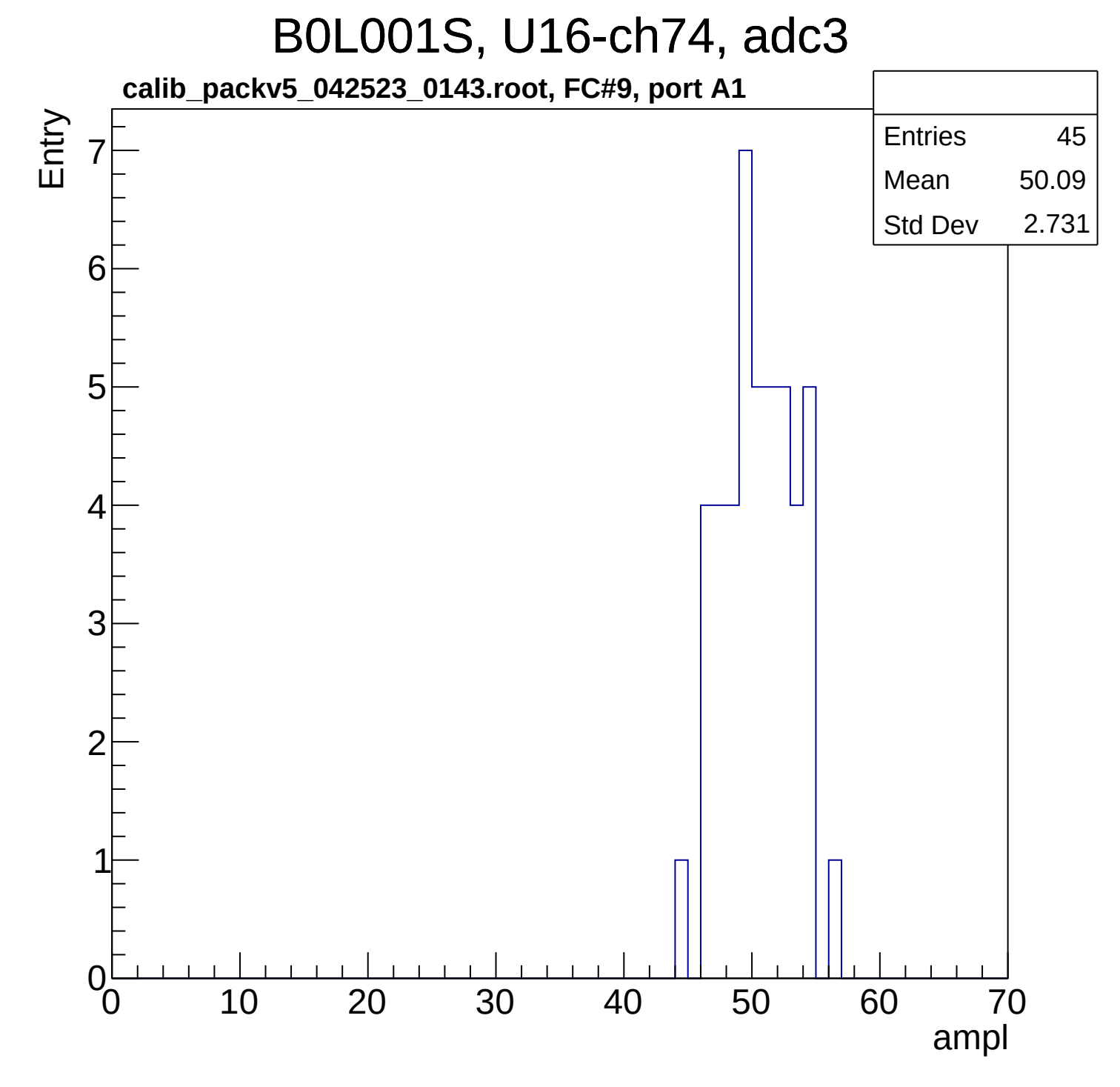
B0L001S, U16-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 50.09
Std Dev 2.731
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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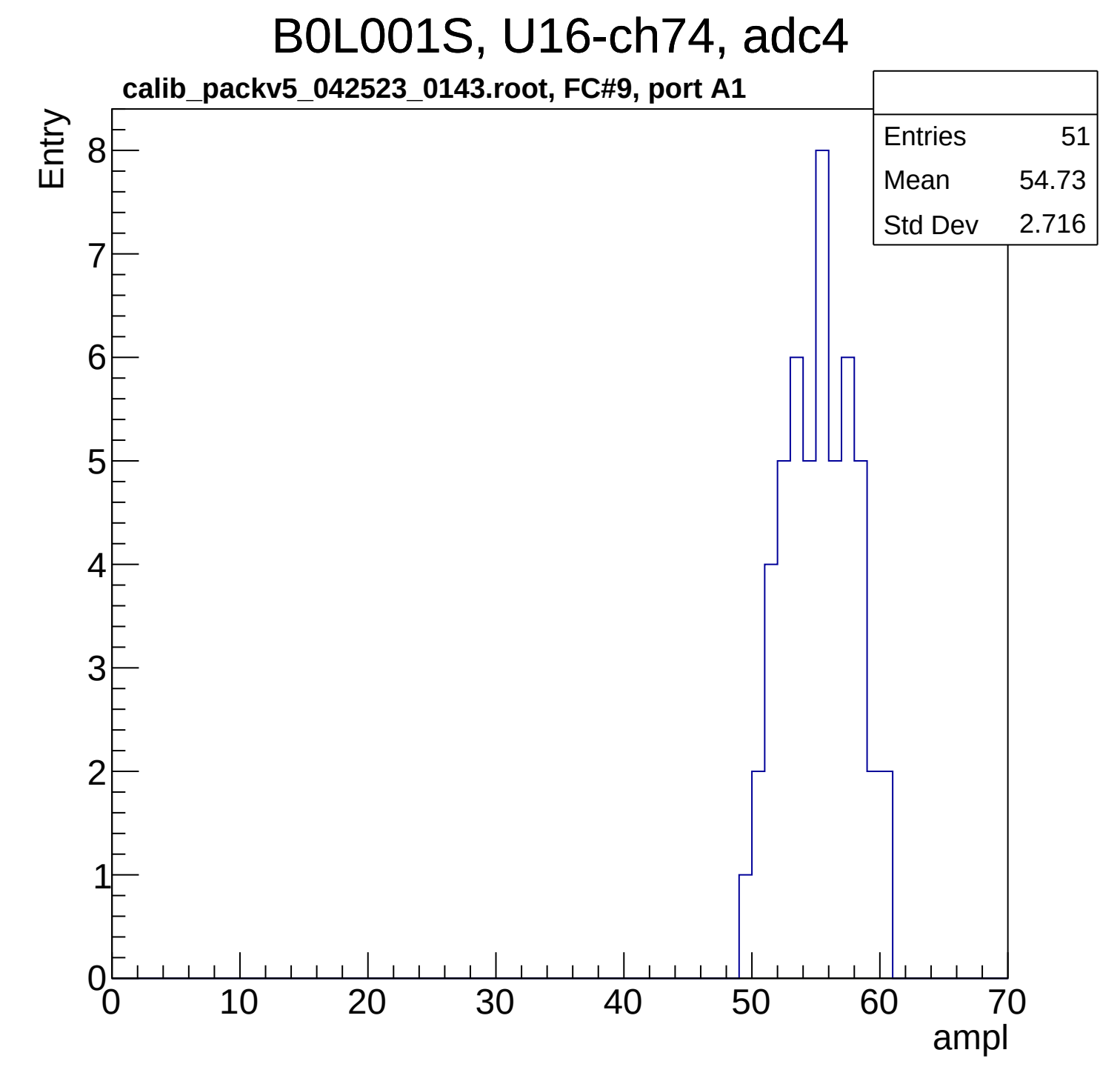
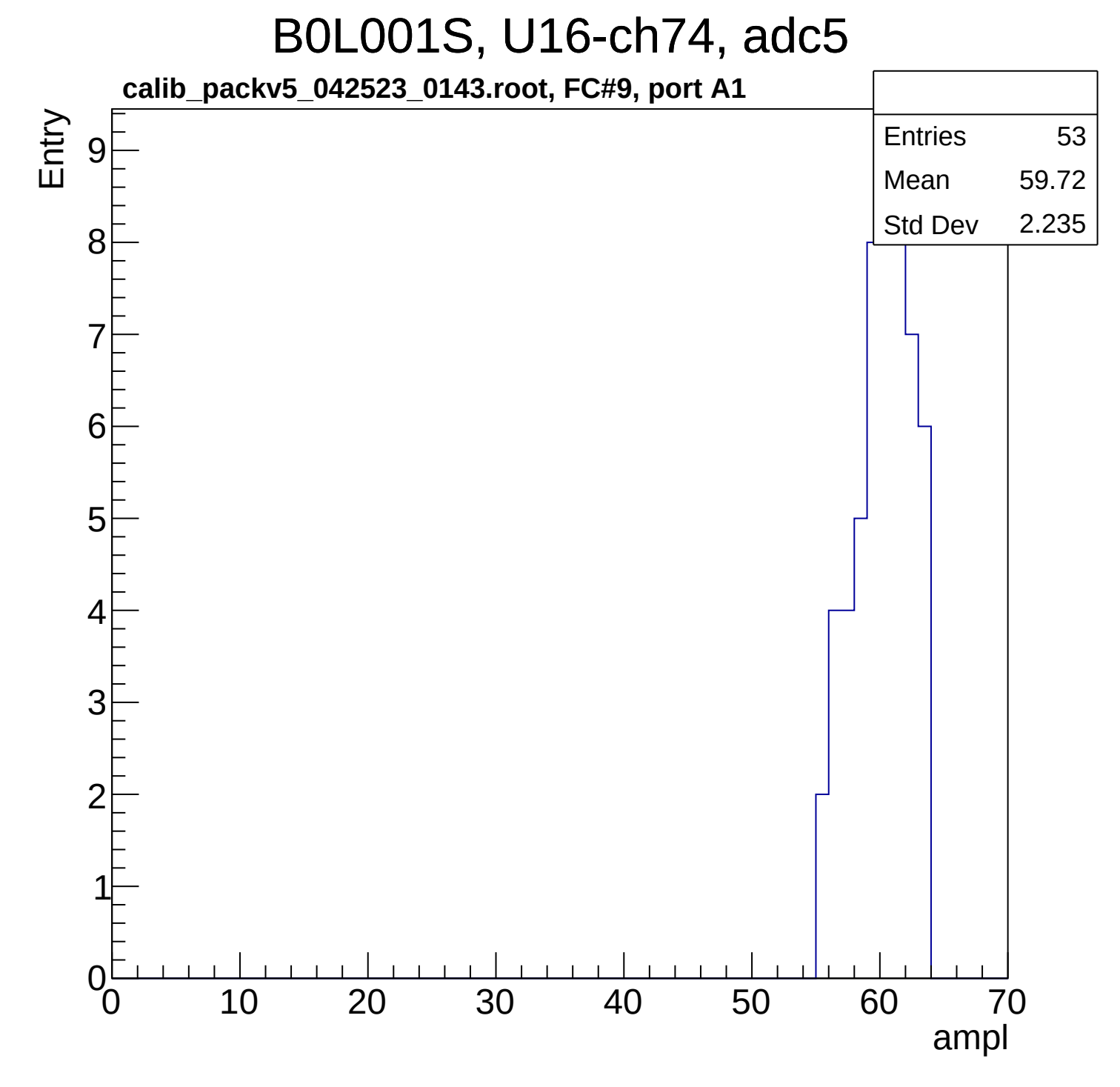
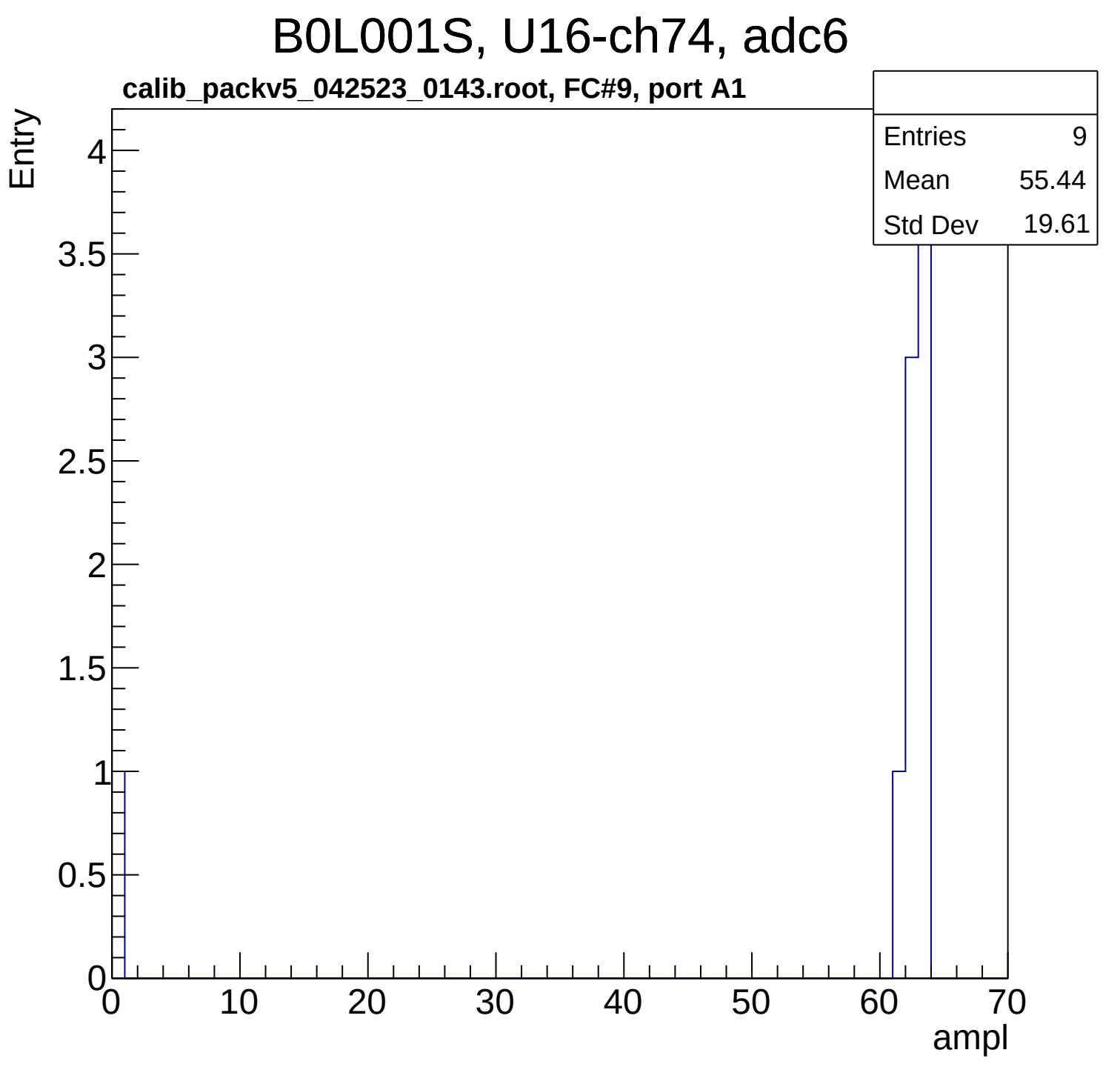
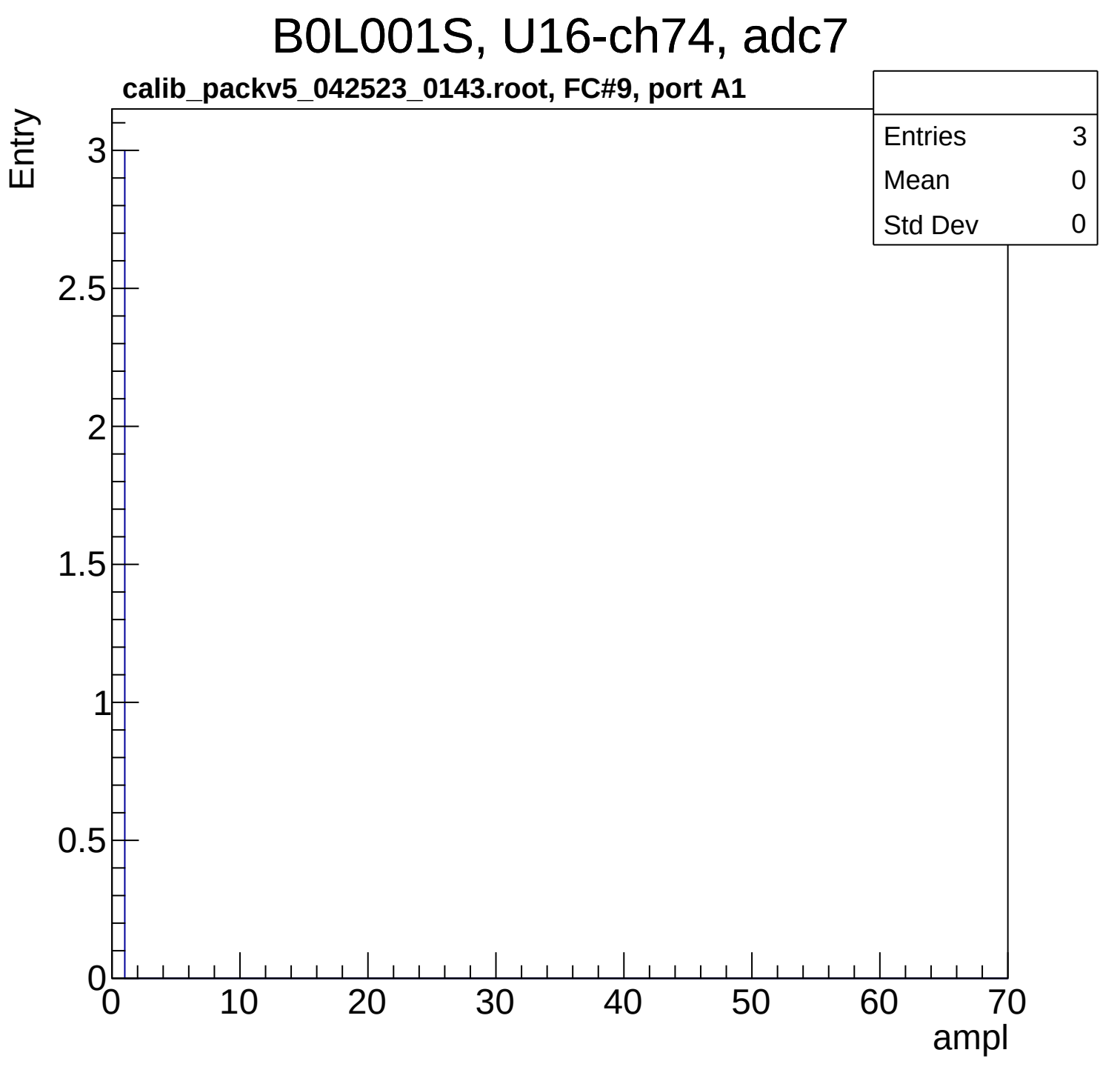
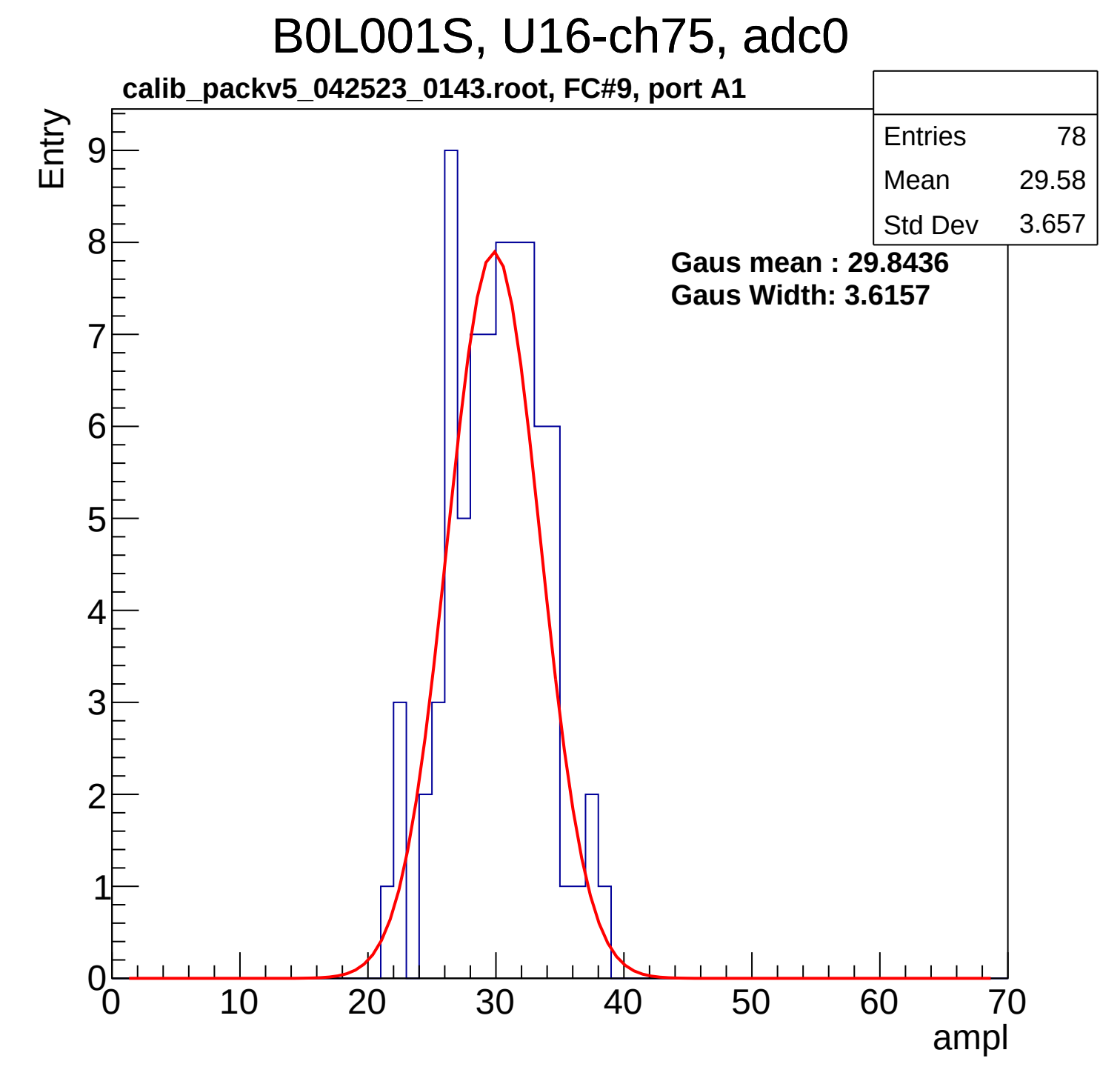
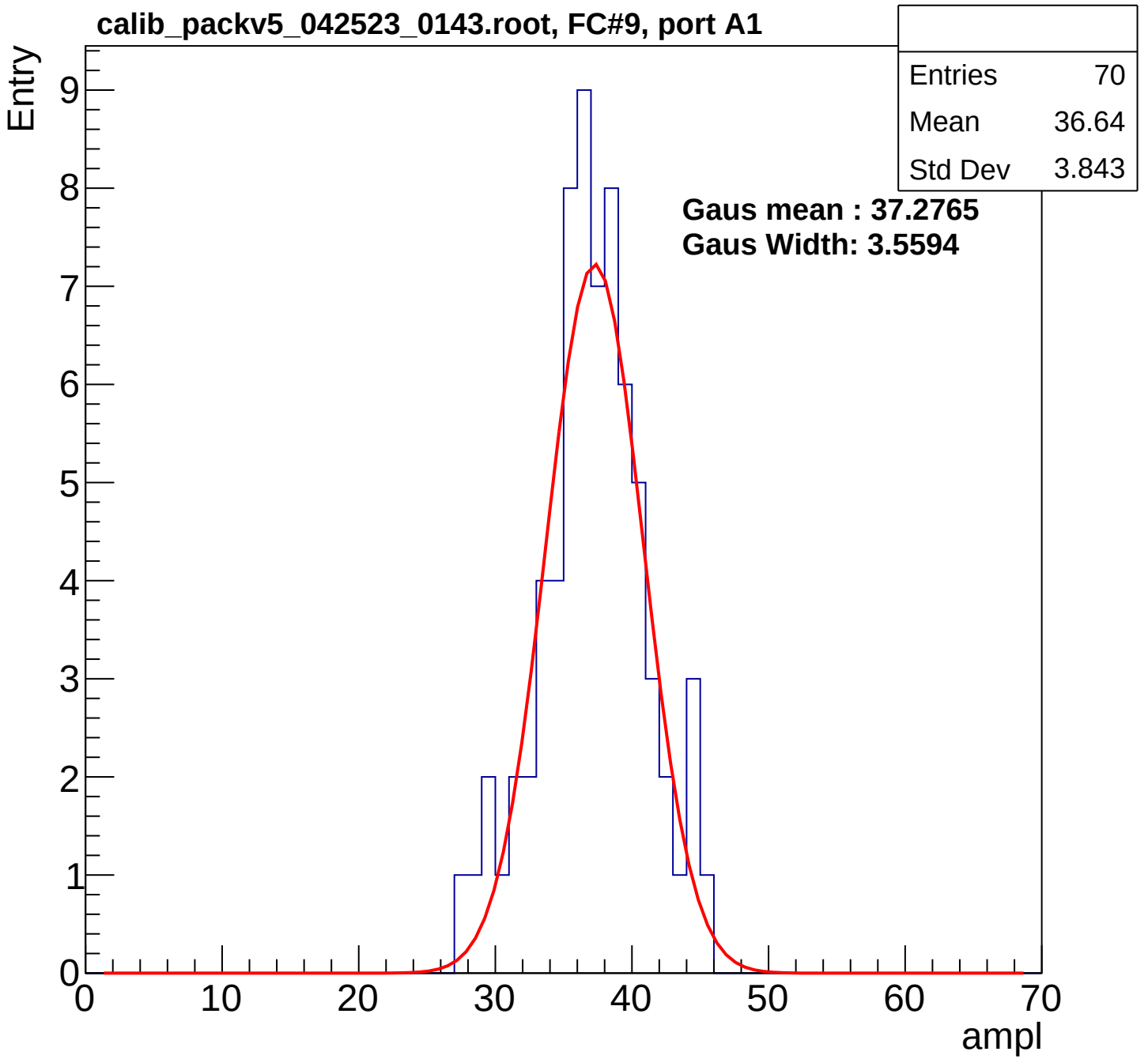

B0L001S, U16-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 54.73
Std Dev 2.716
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

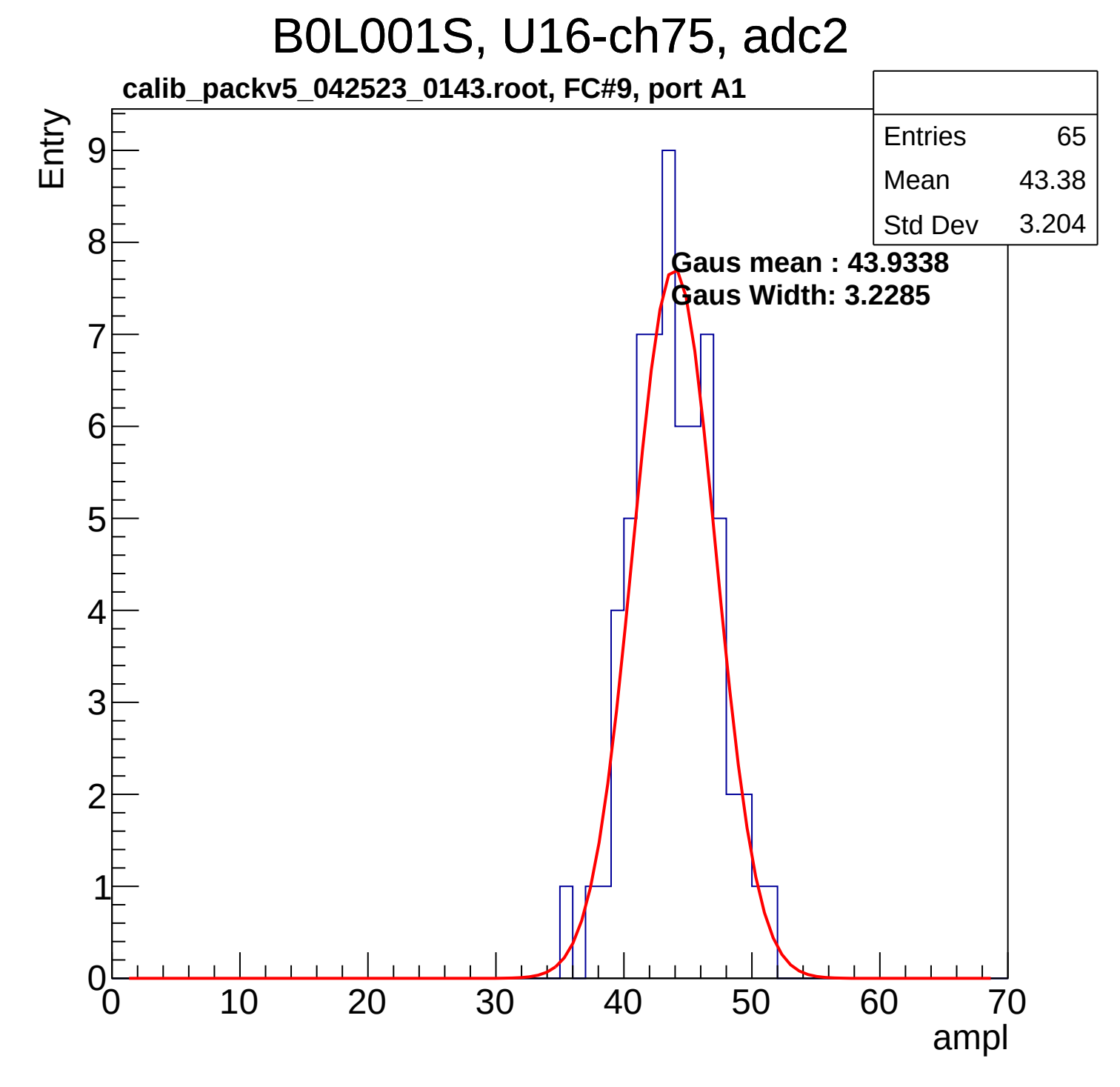

B0L001S, U16-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 59.72
Std Dev 2.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

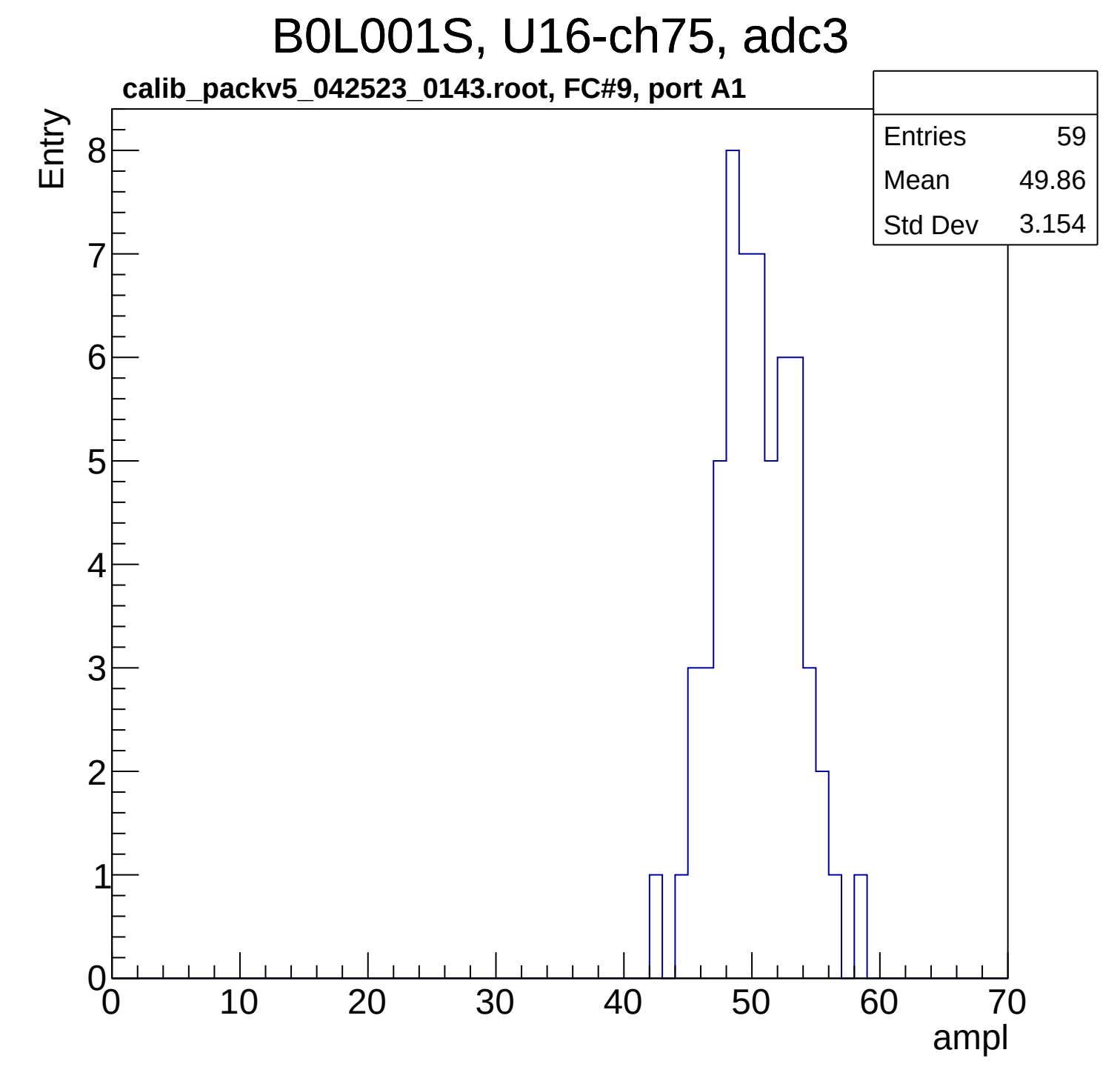

B0L001S, U16-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 55.44
Std Dev 19.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

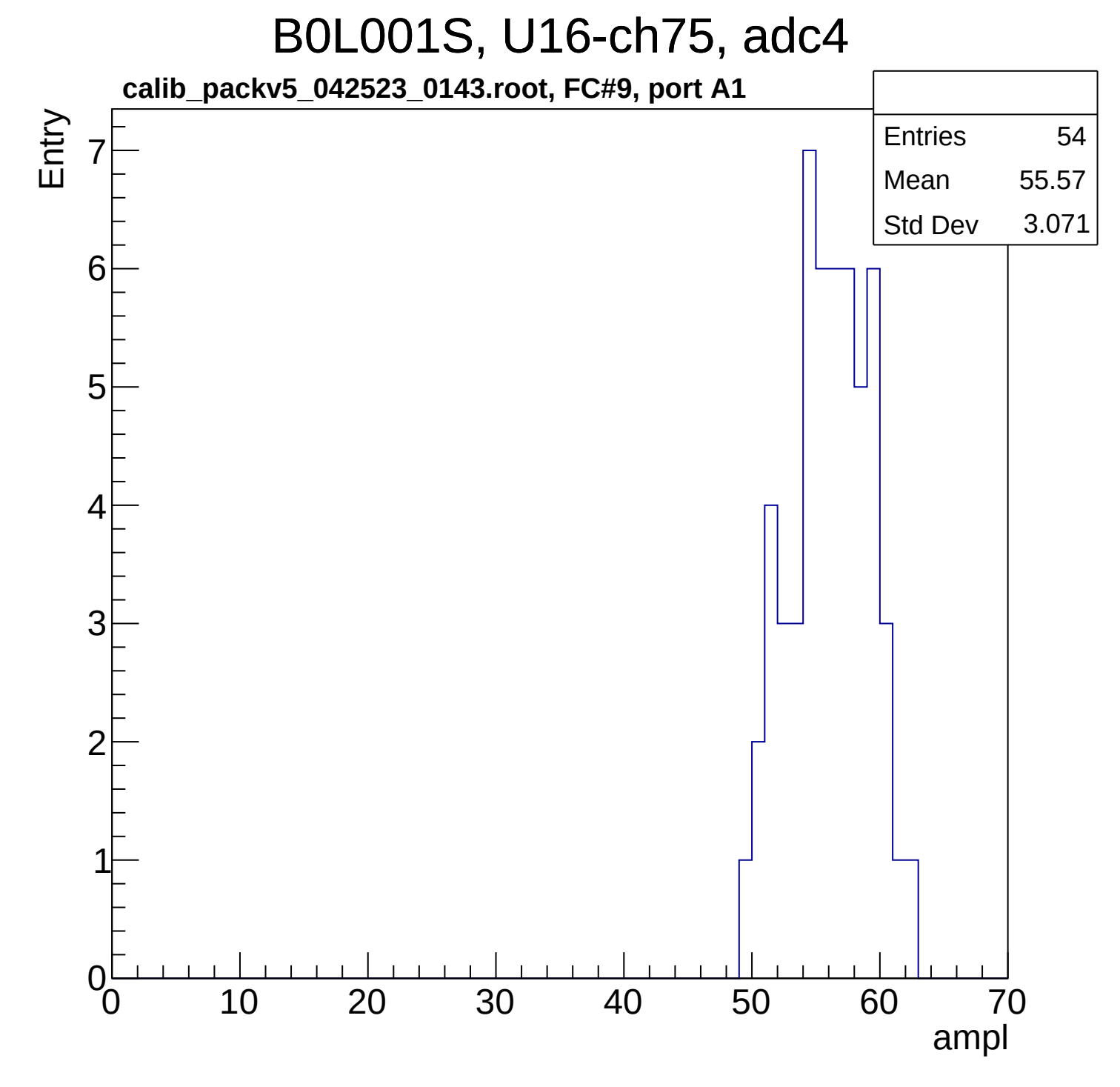

B0L001S, U16-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

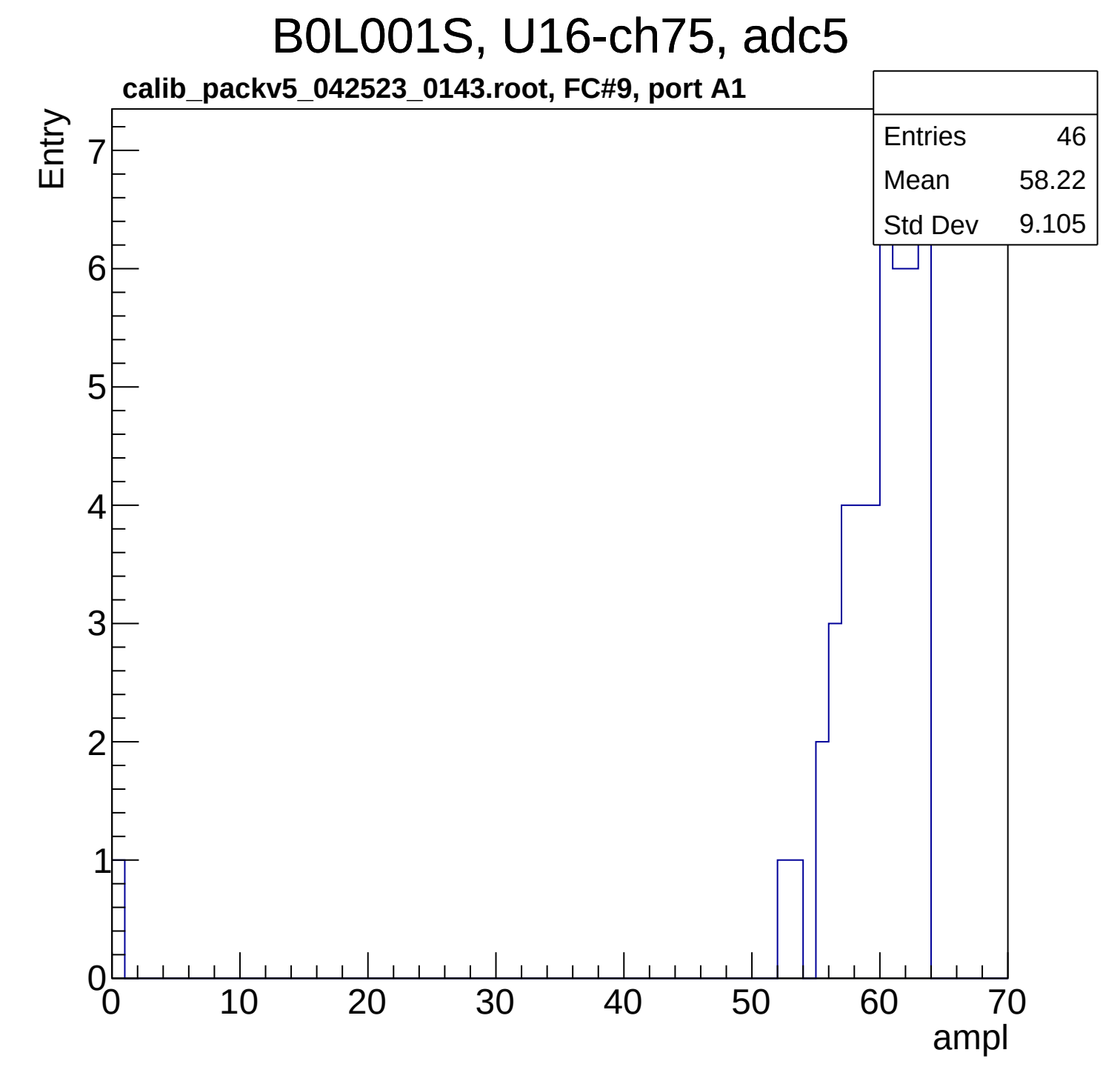

B0L001S, U16-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.58
Std Dev 3.657
Gaus mean : 29.8436
Gaus Width: 3.6157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

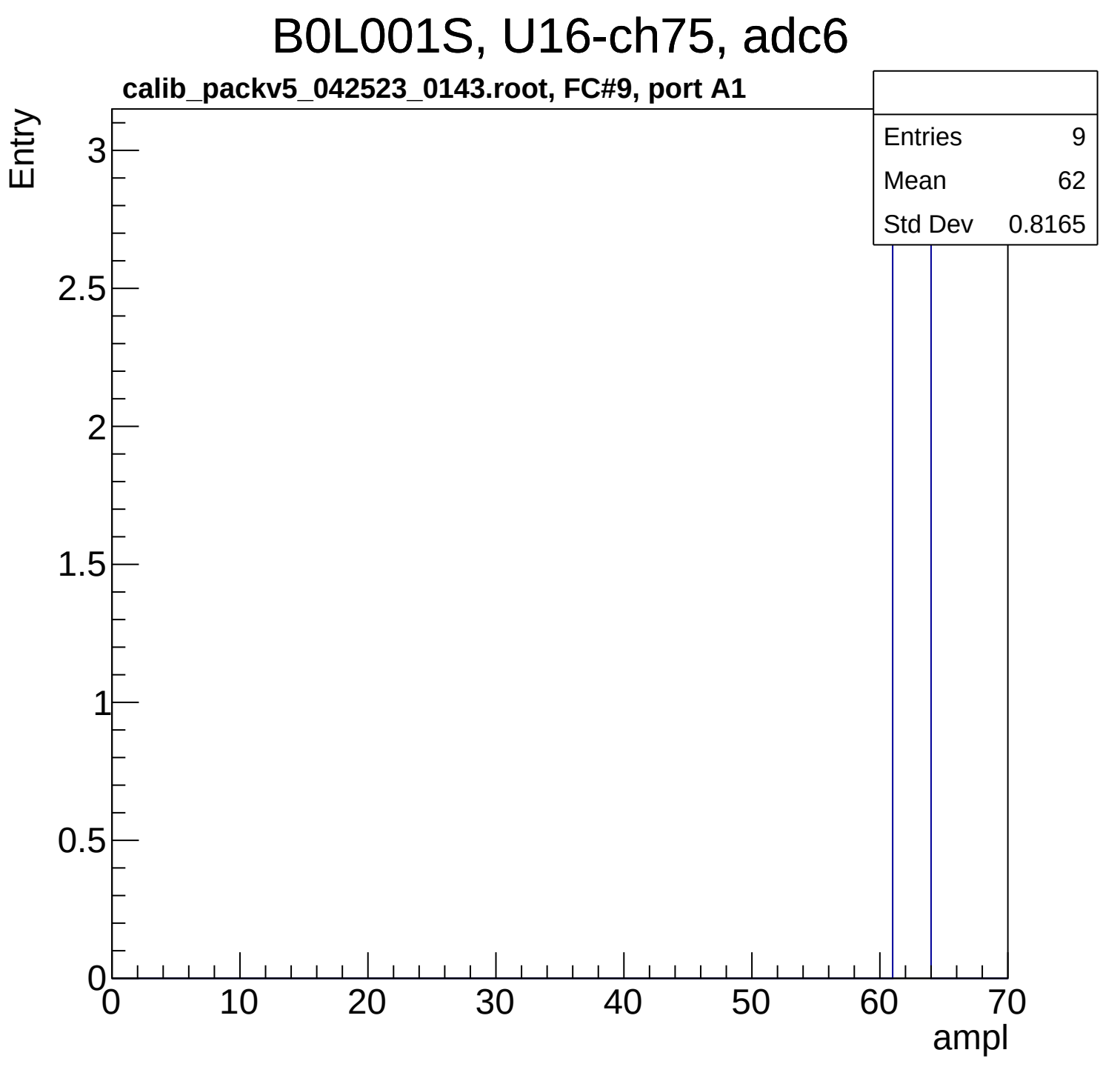

B0L001S, U16-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.64
Std Dev 3.843
Gaus mean : 37.2765
Gaus Width: 3.5594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.38
Std Dev 3.204
Gaus mean : 43.9338
Gaus Width: 3.2285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 49.86
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.57
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.22
Std Dev 9.105
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 62
Std Dev 0.8165
Entry
ampl
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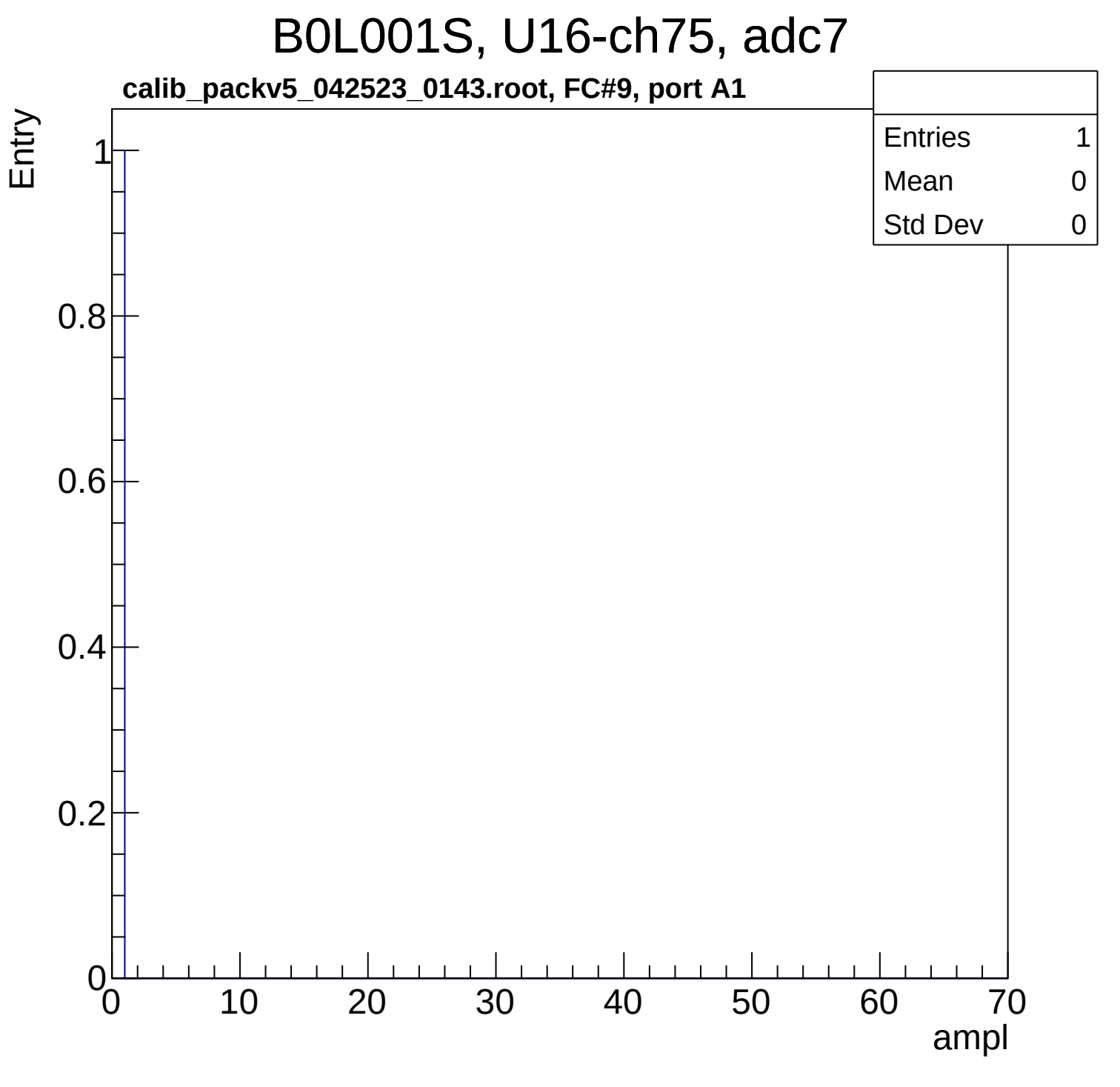
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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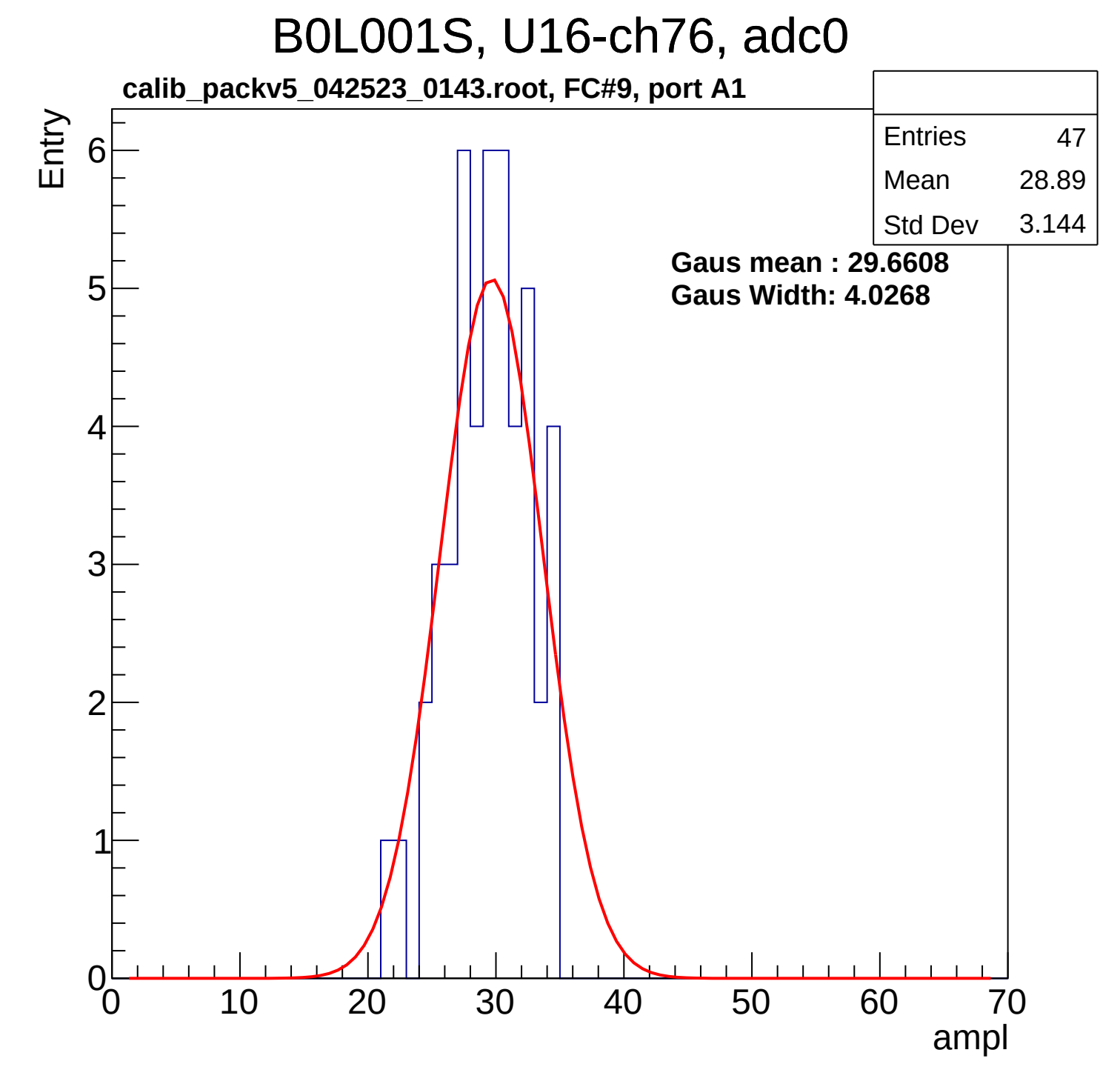
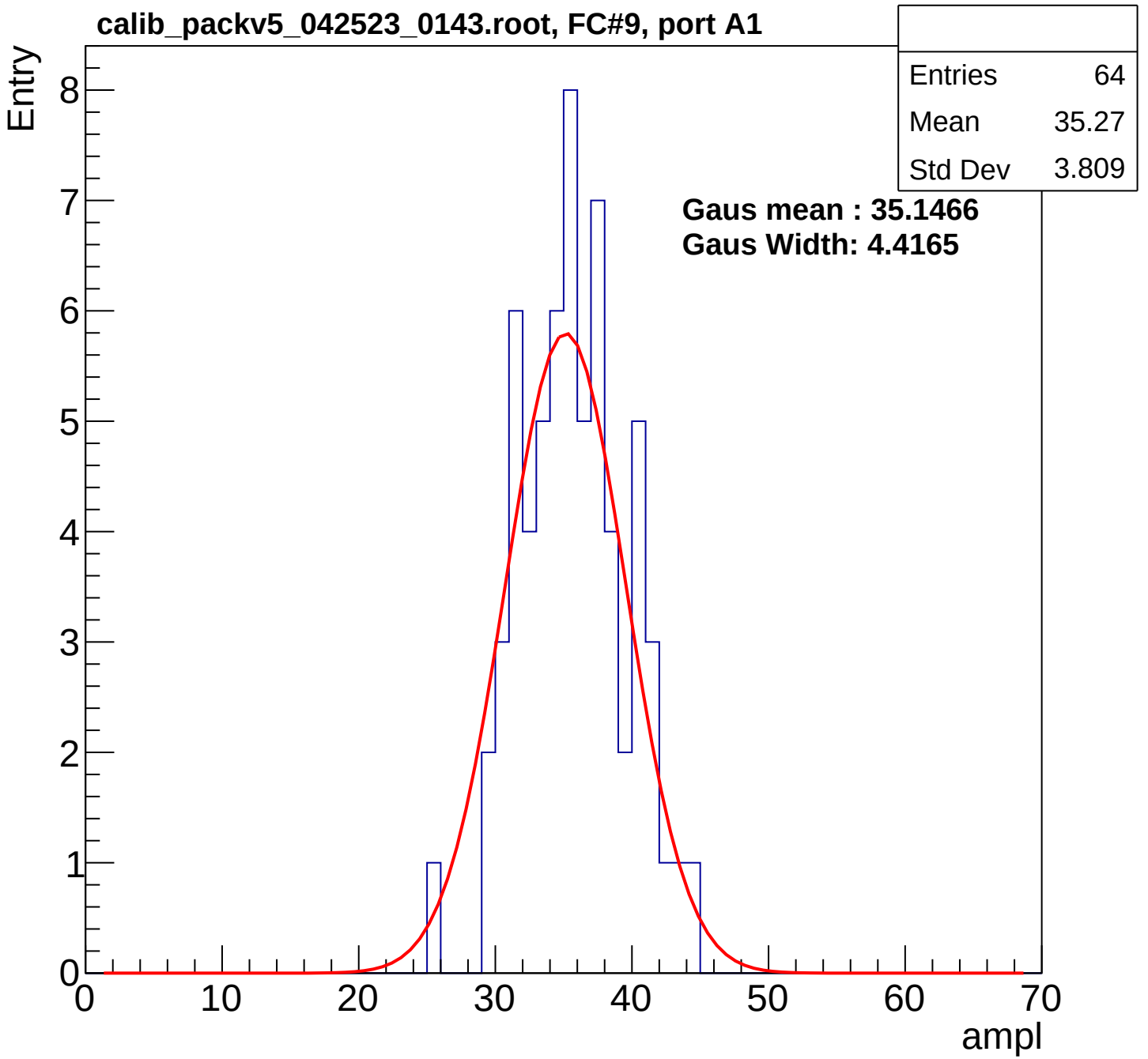
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 28.89
Std Dev 3.144
Gaus mean : 29.6608
Gaus Width: 4.0268
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 35.27
Std Dev 3.809
Gaus mean : 35.1466
Gaus Width: 4.4165
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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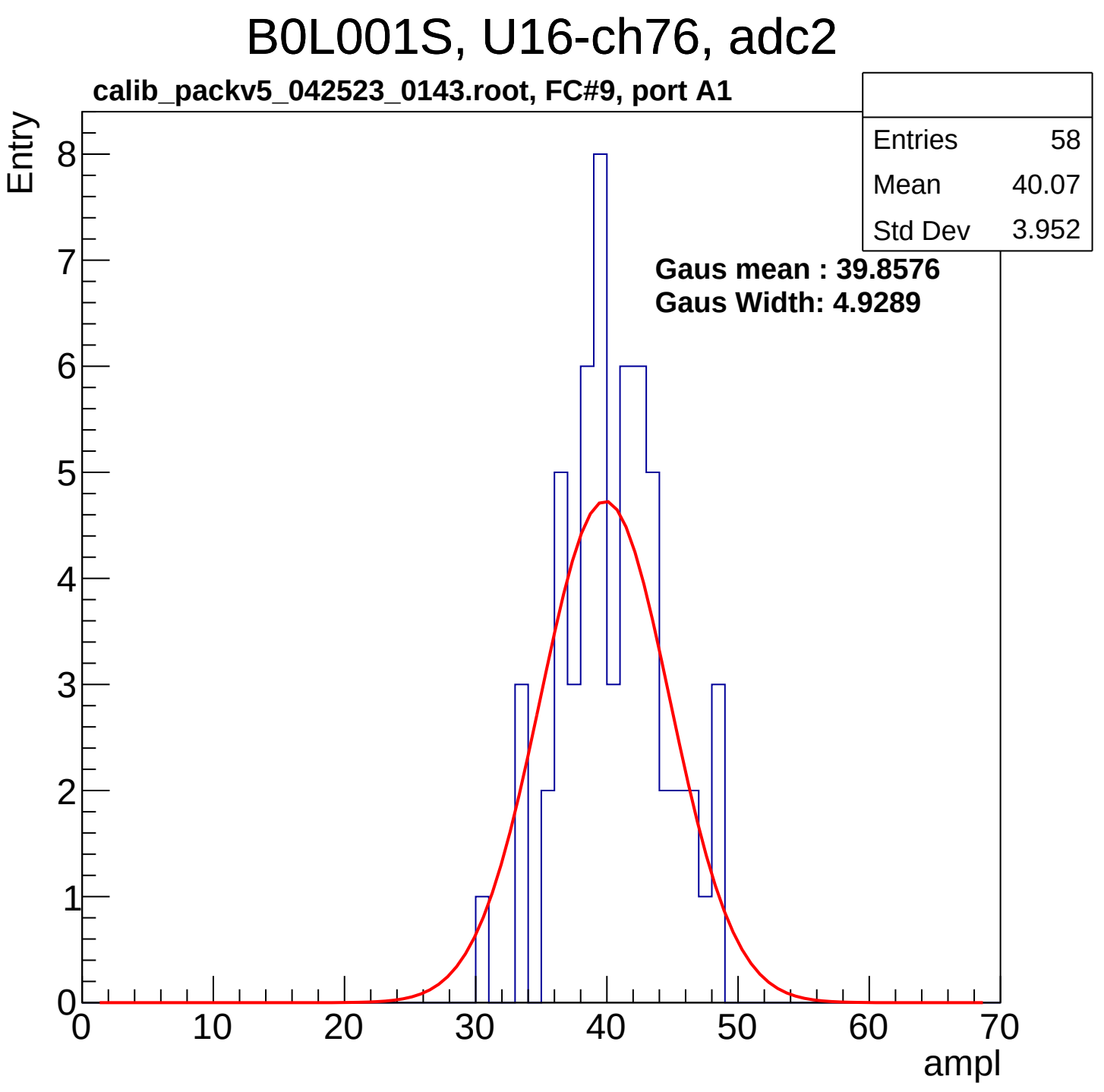
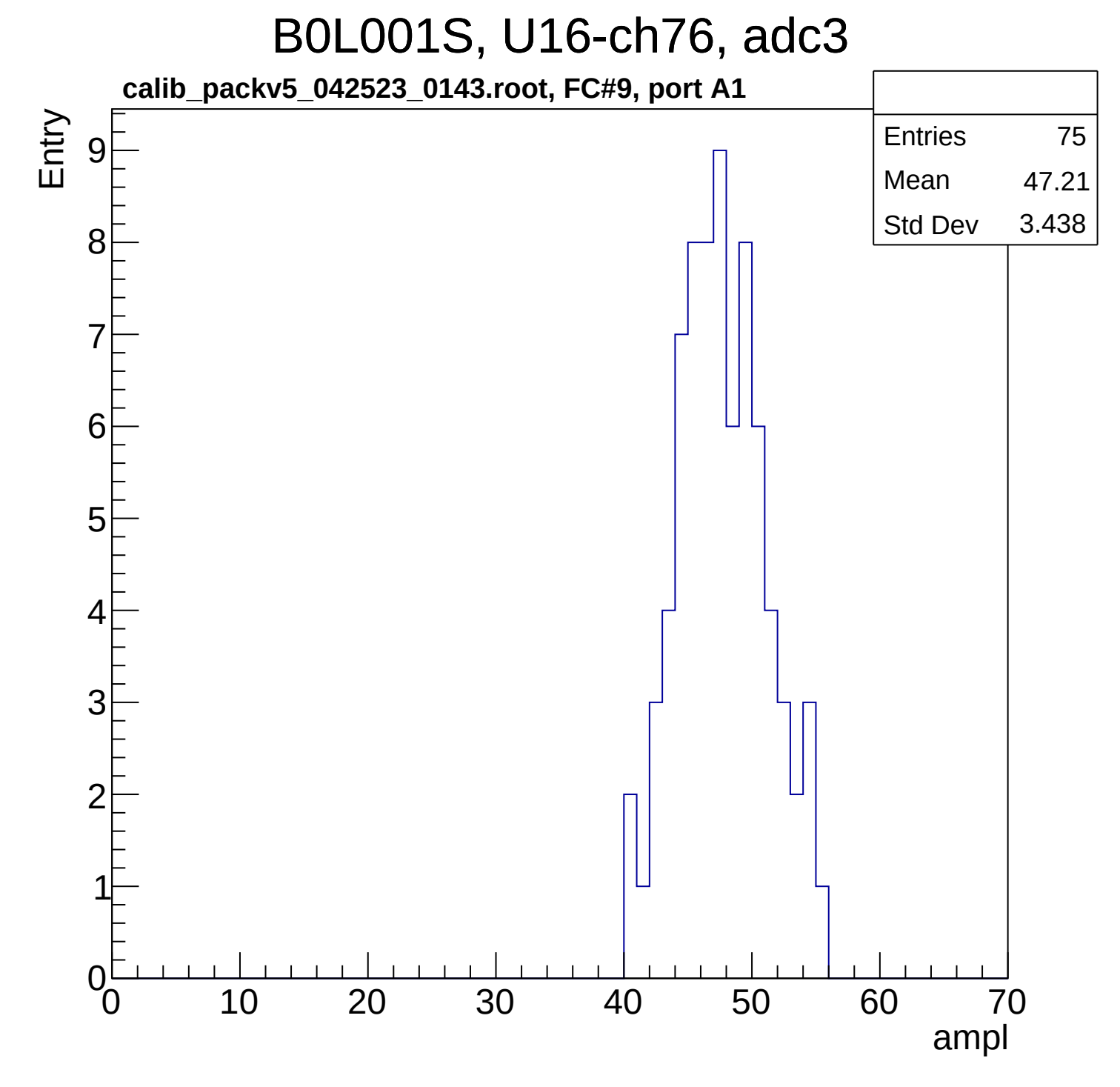

B0L001S, U16-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 40.07
Std Dev 3.952
Gaus mean : 39.8576
Gaus Width: 4.9289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 47.21
Std Dev 3.438
Entry
ampl
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2
3
4
5
6
7
8
9
0
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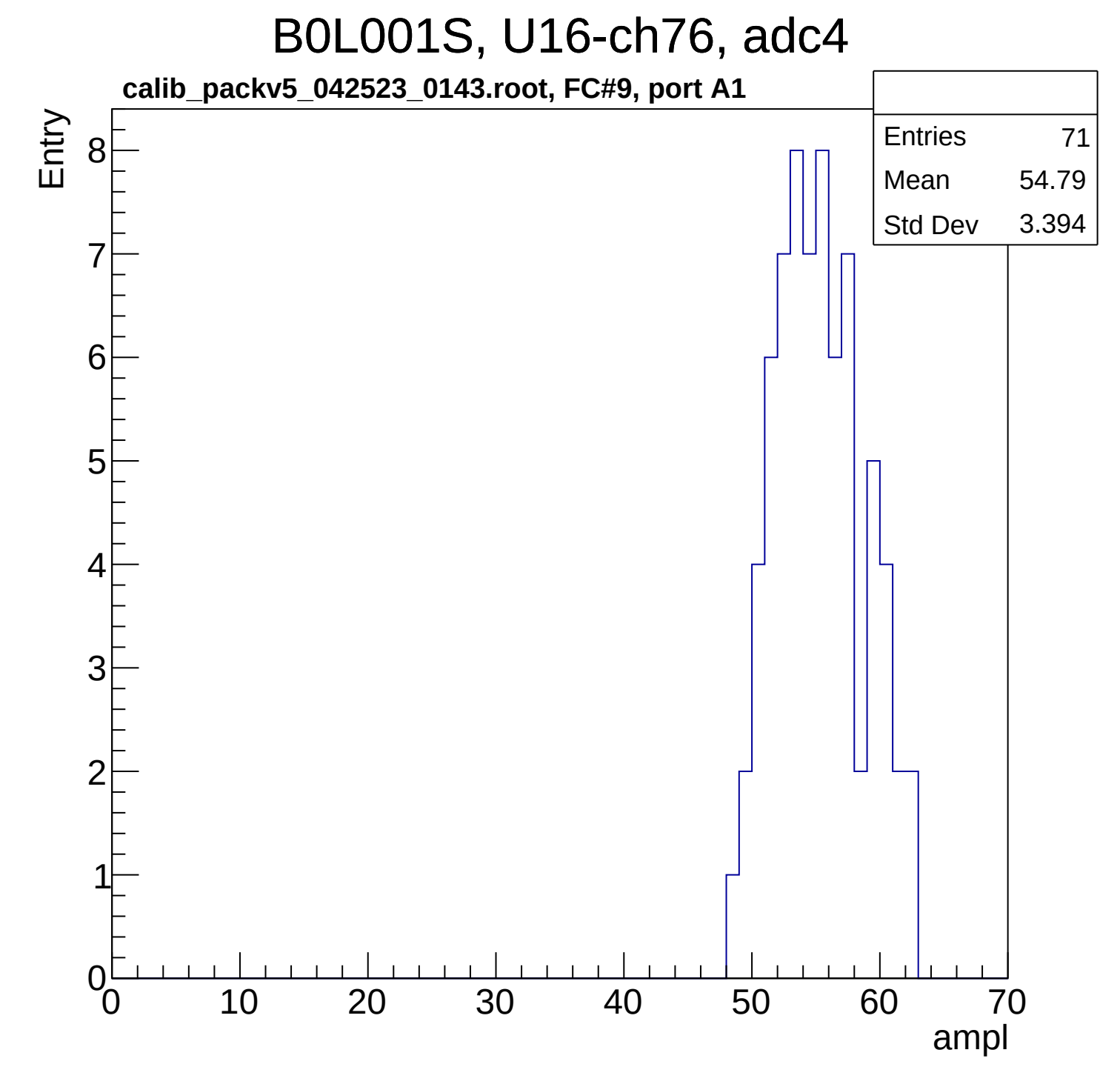
B0L001S, U16-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 54.79
Std Dev 3.394
Entry
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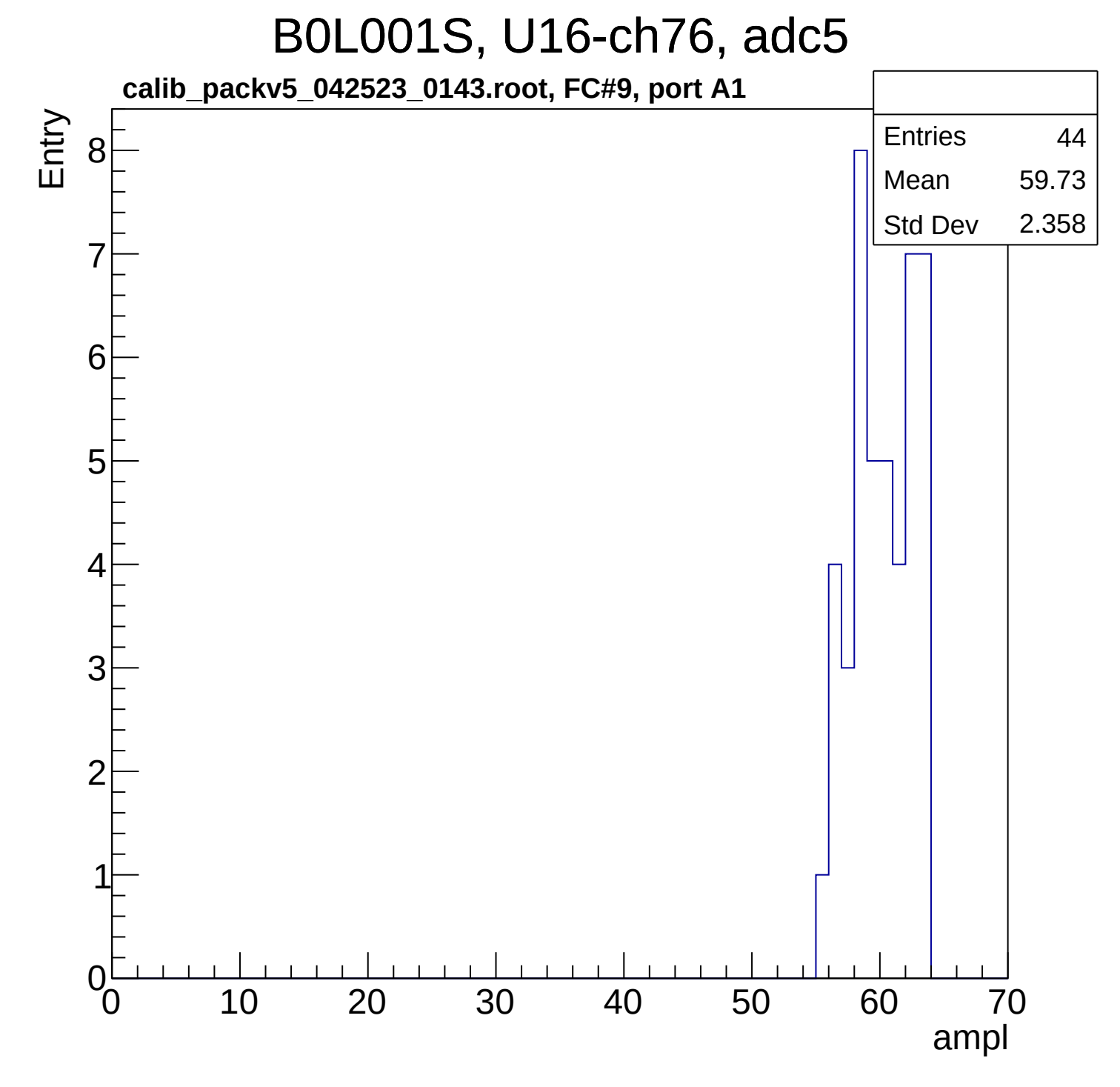
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.73
Std Dev 2.358
Entry
ampl
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1
2
3
4
5
6
7
8
0
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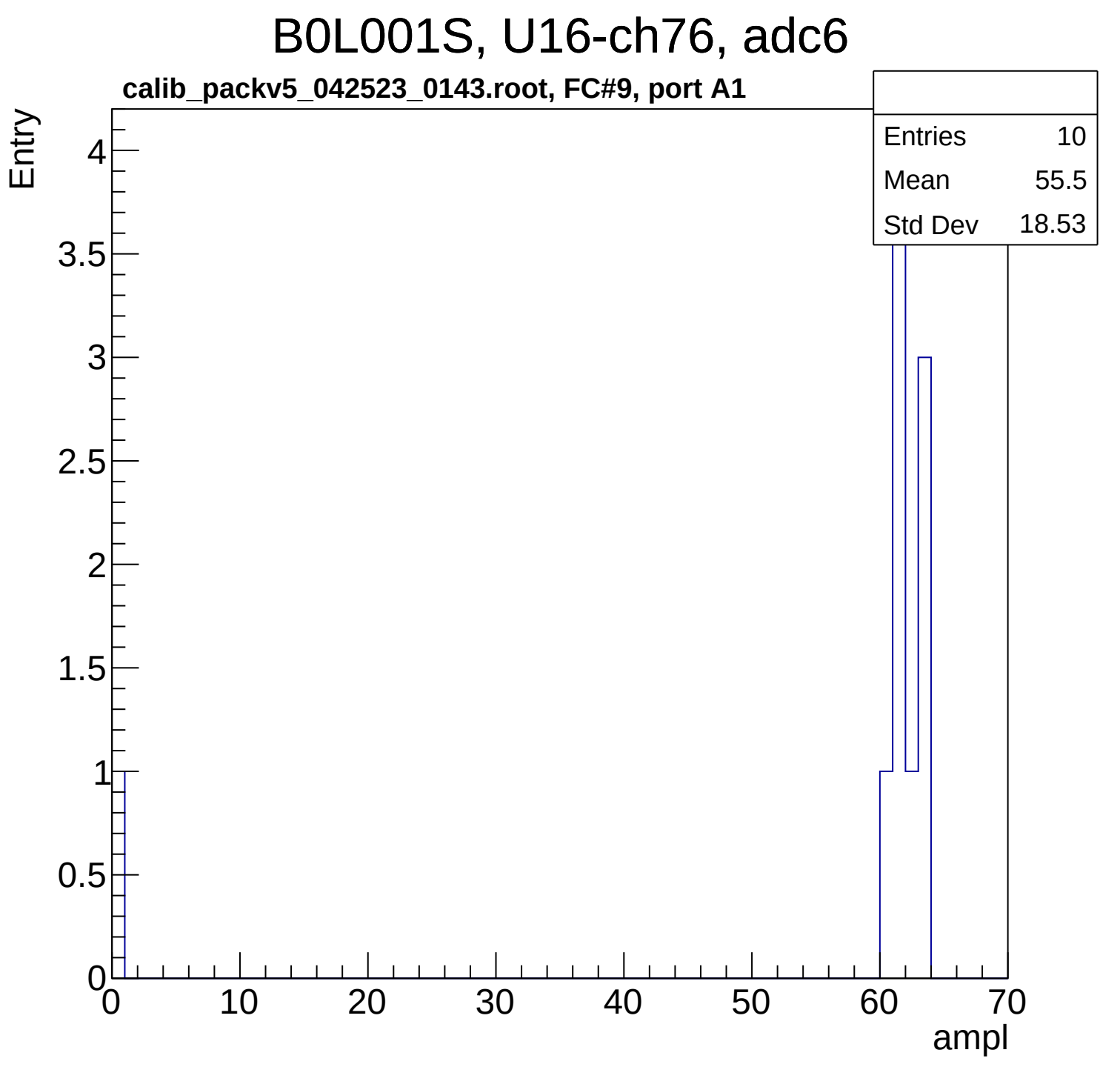
B0L001S, U16-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 55.5
Std Dev 18.53
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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40
50
60
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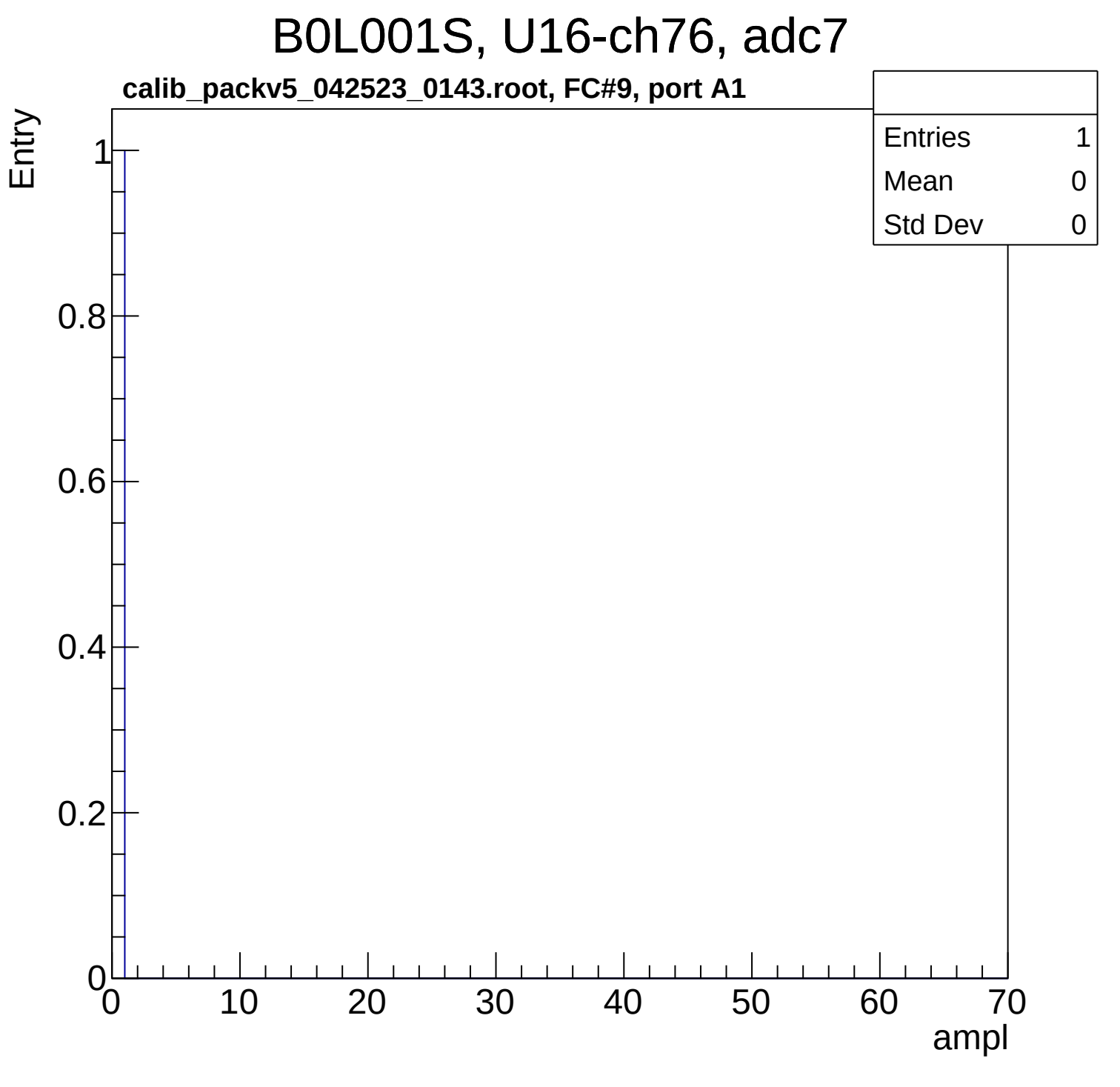
B0L001S, U16-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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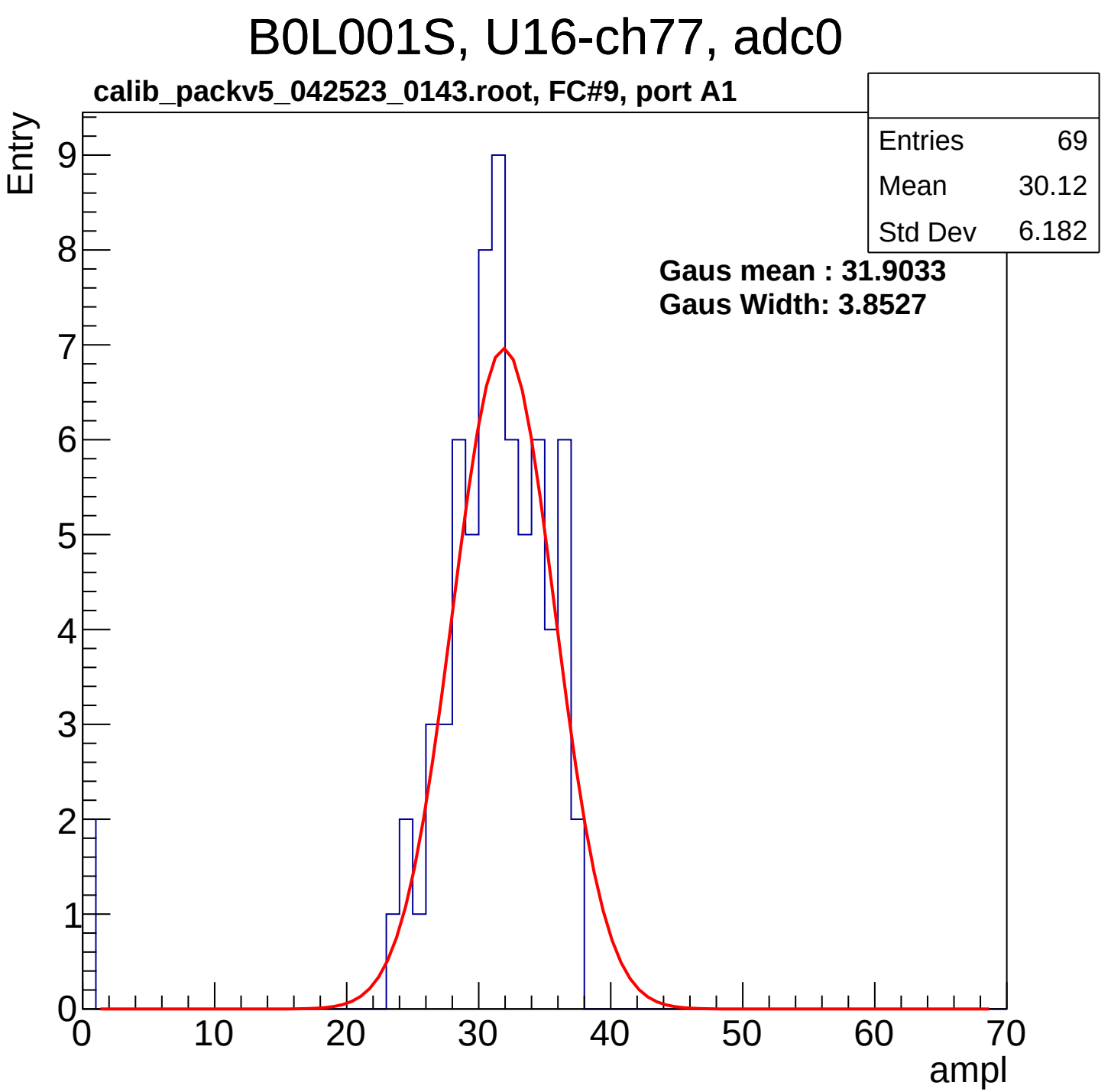
50
60
70
ampl


B0L001S, U16-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.12
Std Dev 6.182
Gaus mean : 31.9033
Gaus Width: 3.8527
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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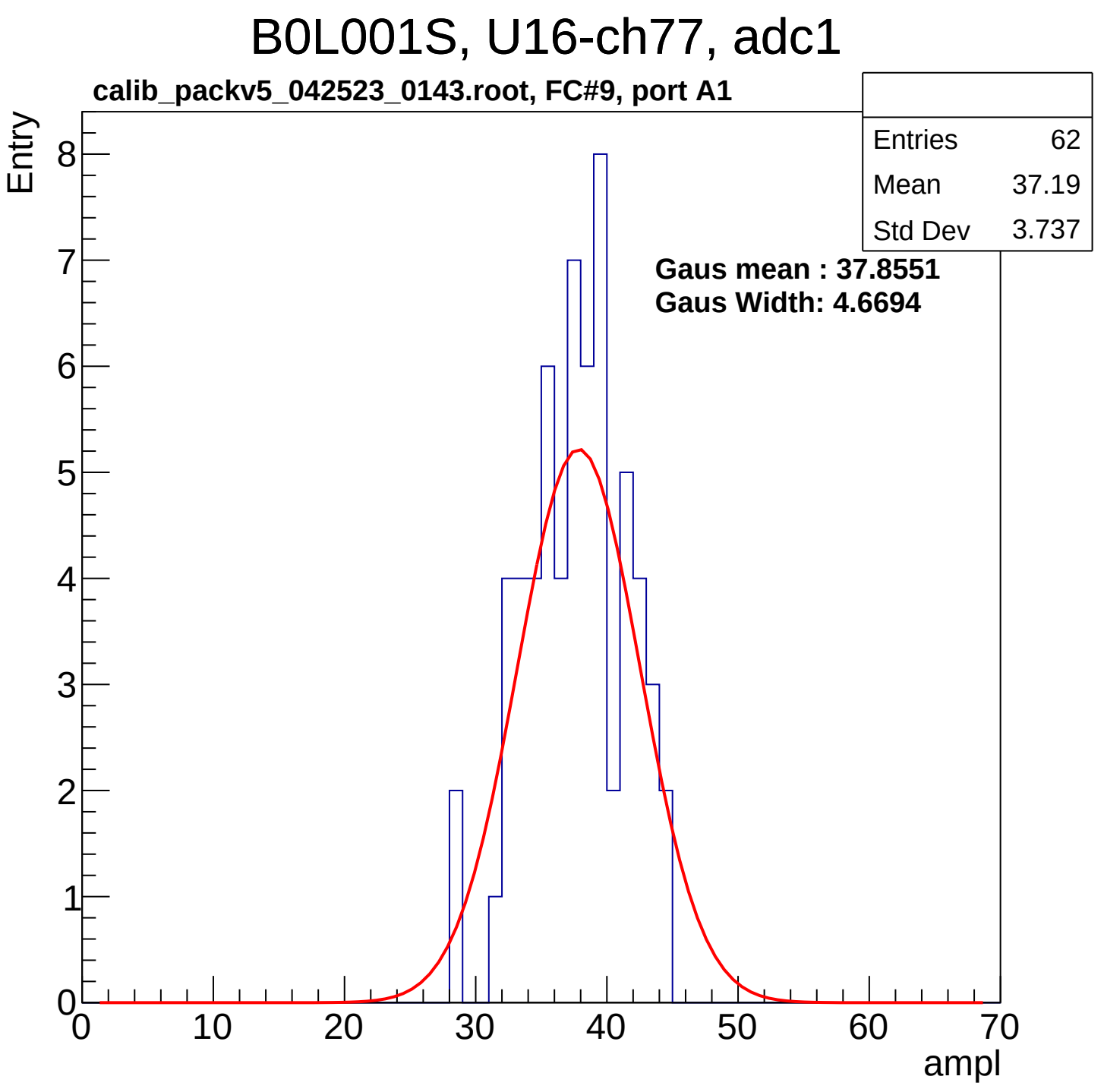
B0L001S, U16-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.19
Std Dev 3.737
Gaus mean : 37.8551
Gaus Width: 4.6694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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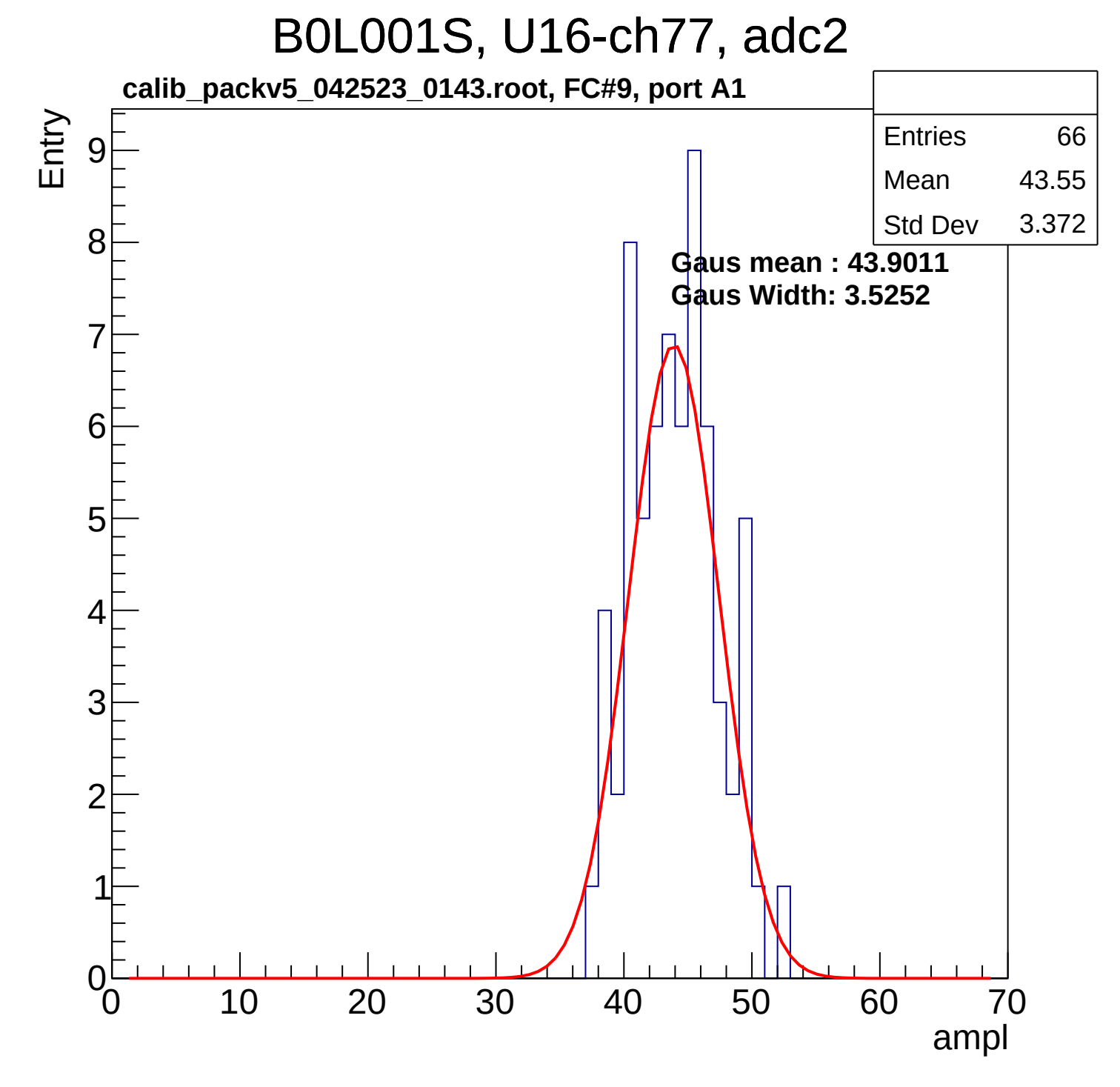
B0L001S, U16-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.55
Std Dev 3.372
Gaus mean : 43.9011
Gaus Width: 3.5252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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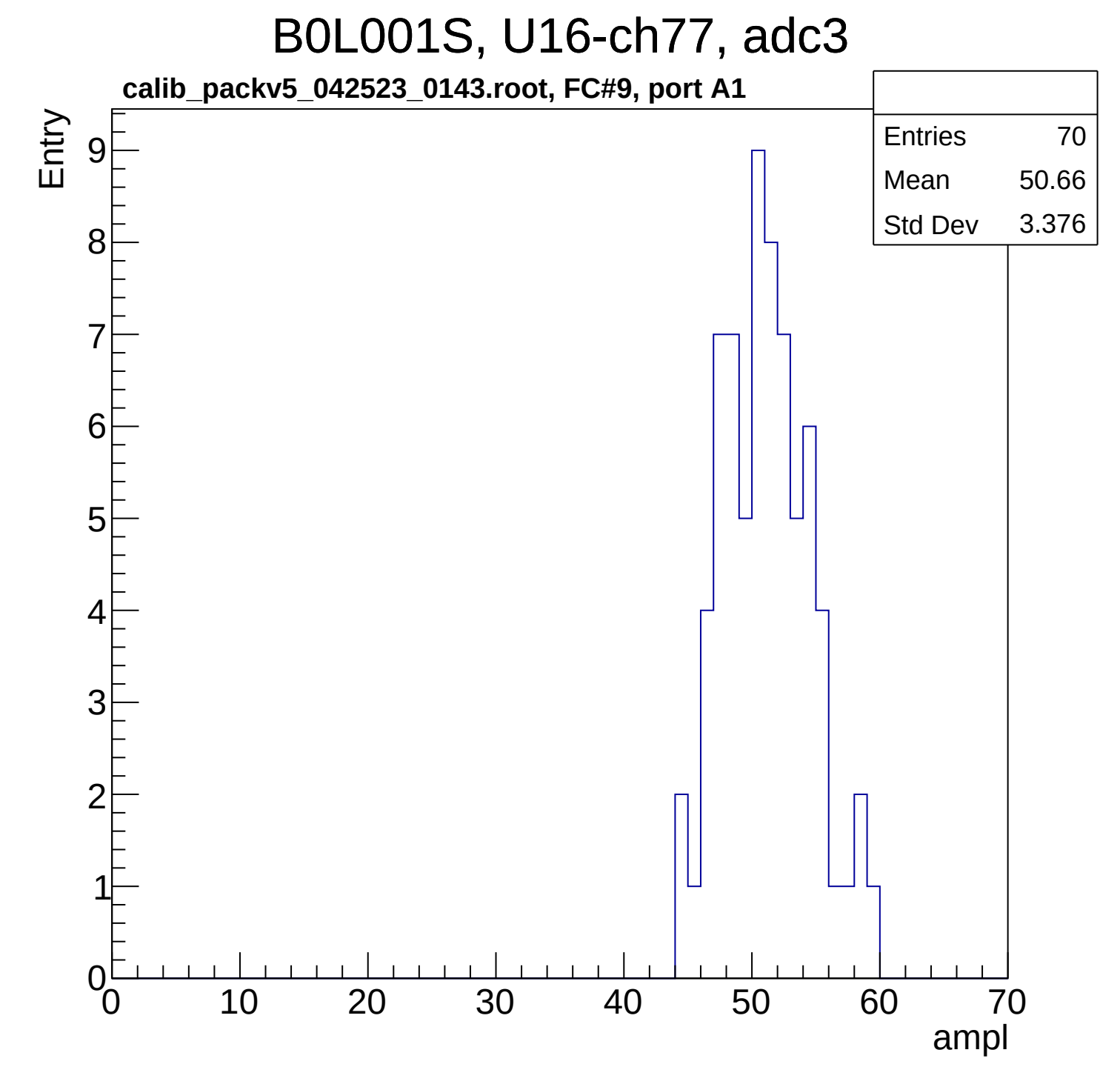
B0L001S, U16-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.66
Std Dev 3.376
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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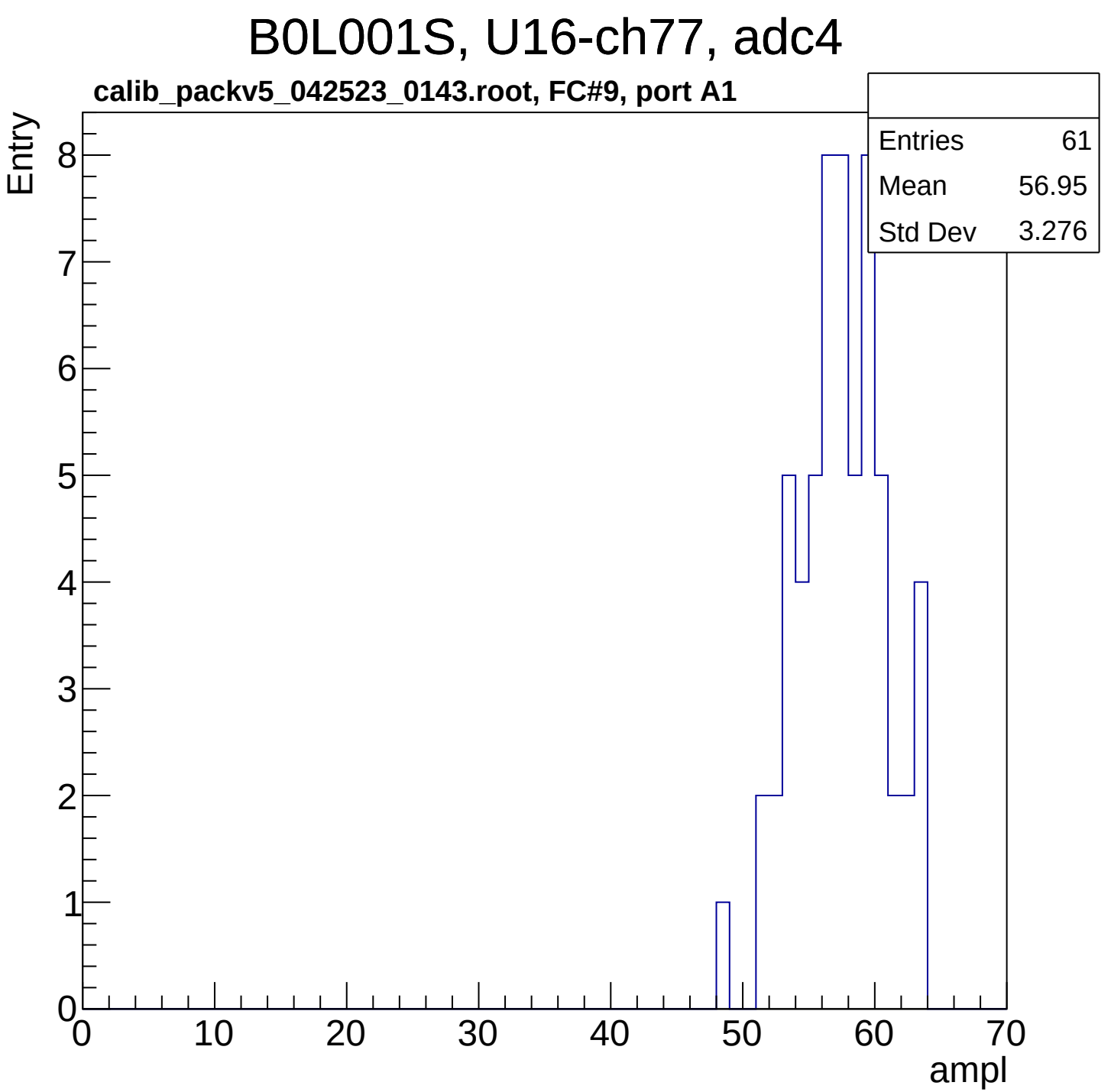
B0L001S, U16-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.95
Std Dev 3.276
Entry
ampl
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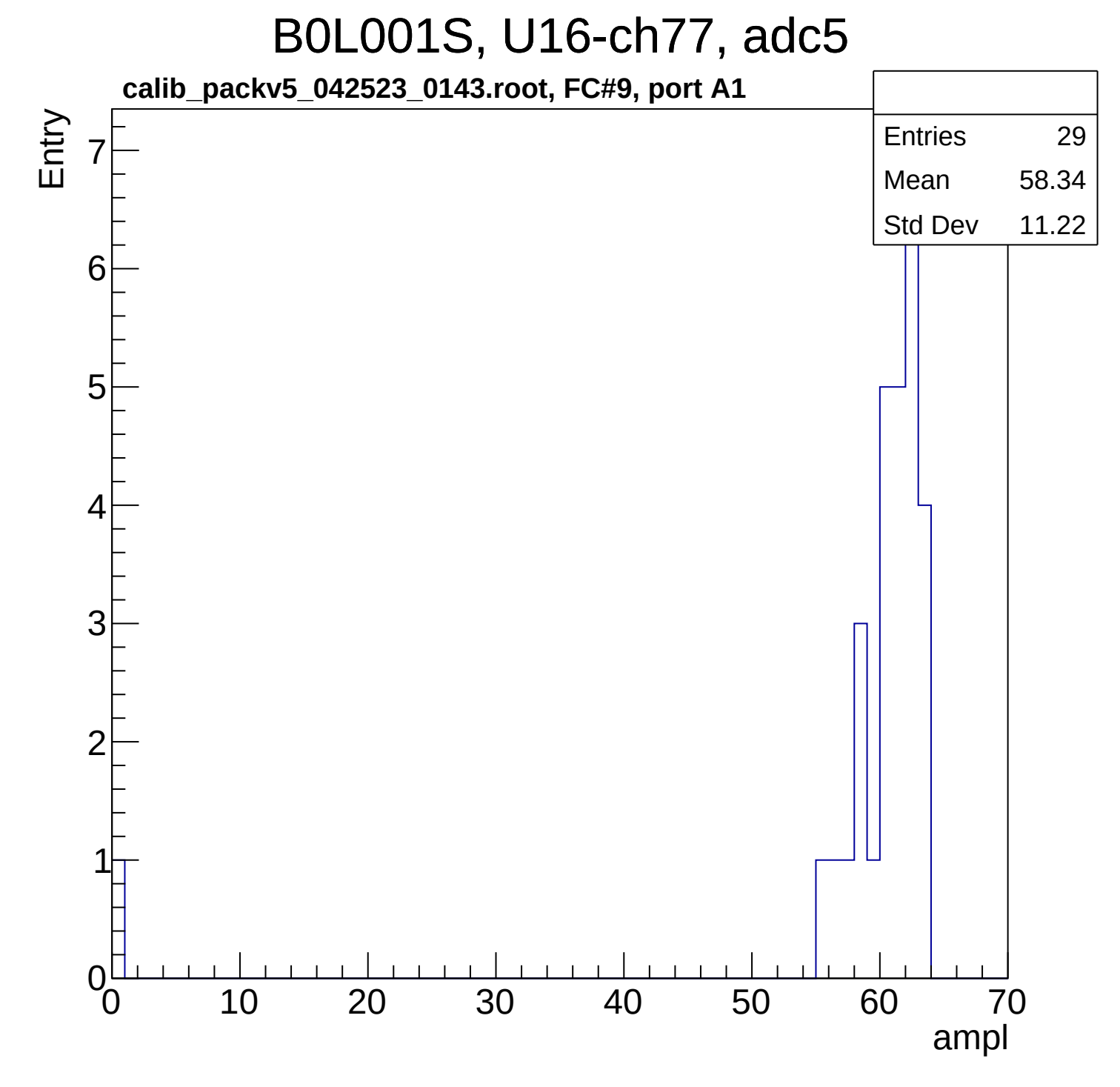
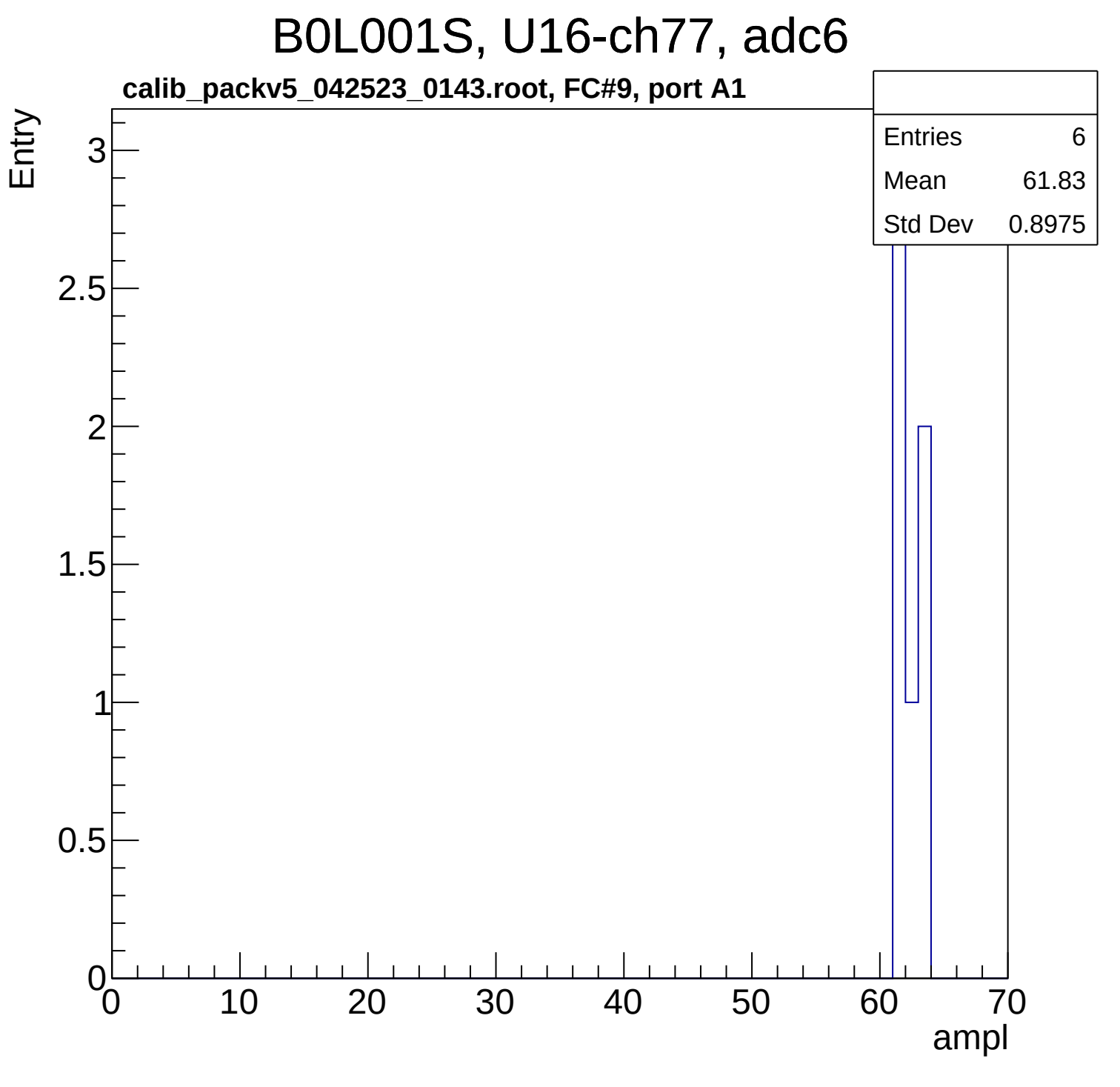
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.34
Std Dev 11.22
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.83
Std Dev 0.8975
Entry
ampl
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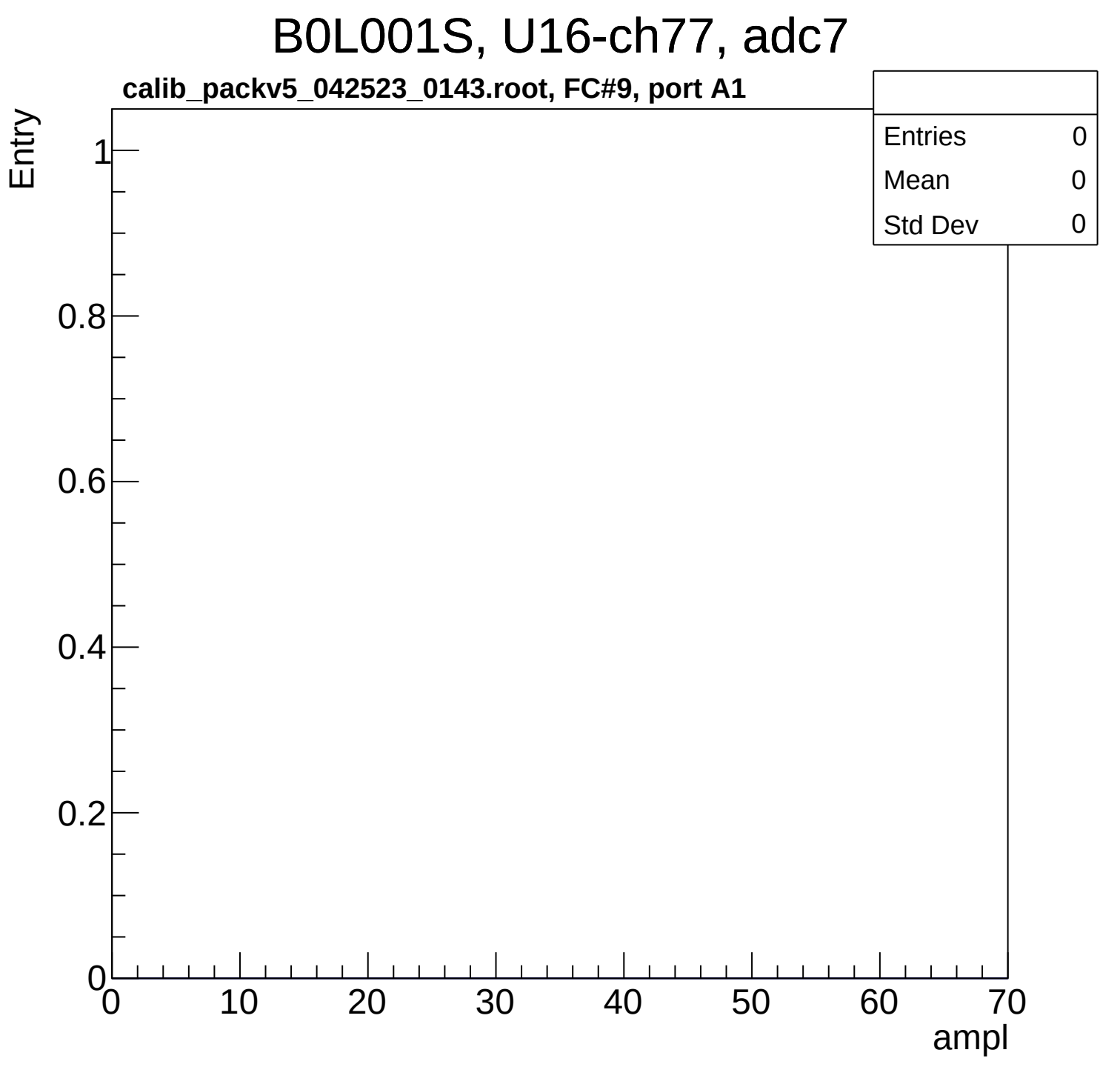
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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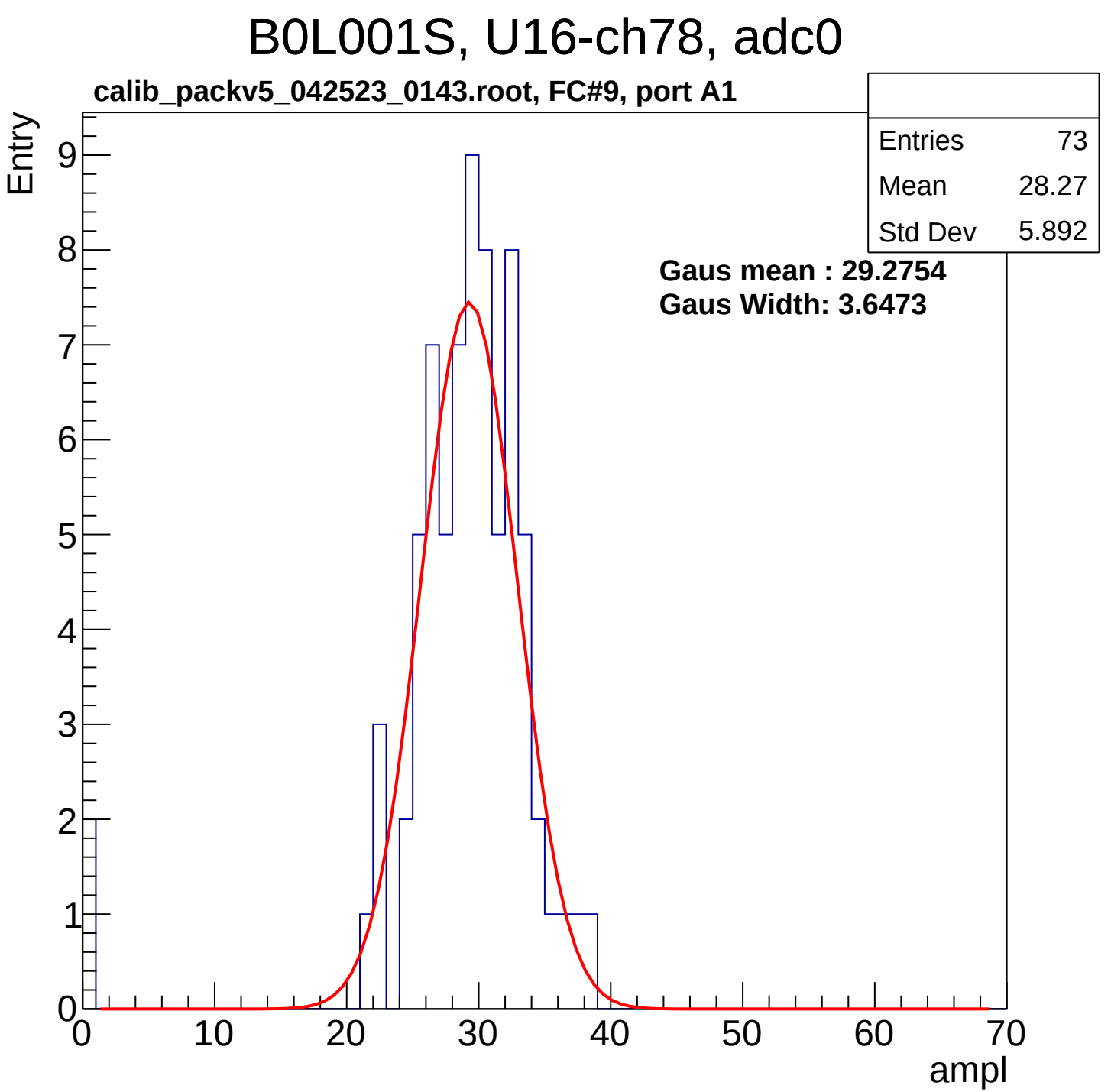
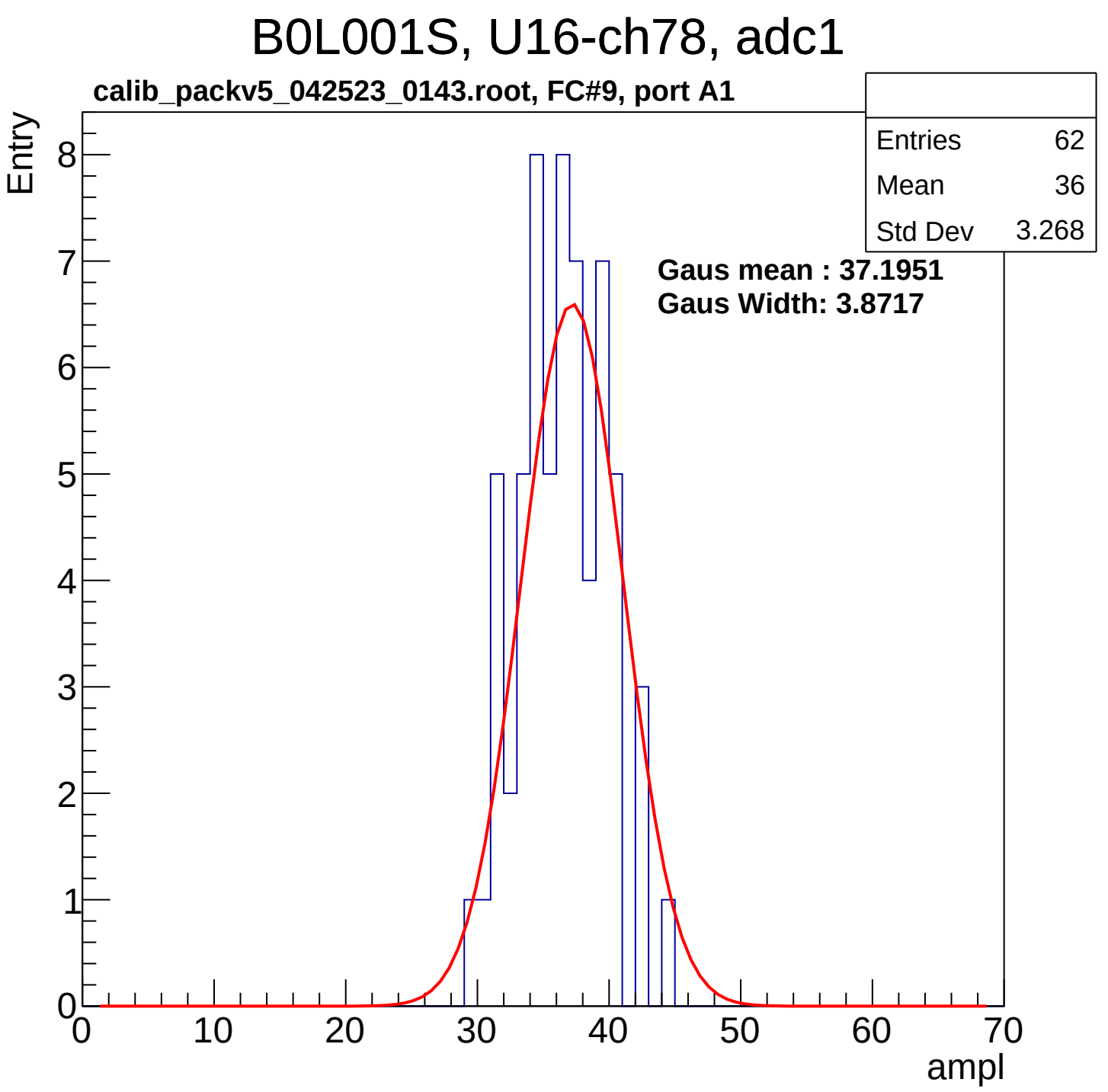
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 28.27
Std Dev 5.892
Gaus mean : 29.2754
Gaus Width: 3.6473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36
Std Dev 3.268
Gaus mean : 37.1951
Gaus Width: 3.8717
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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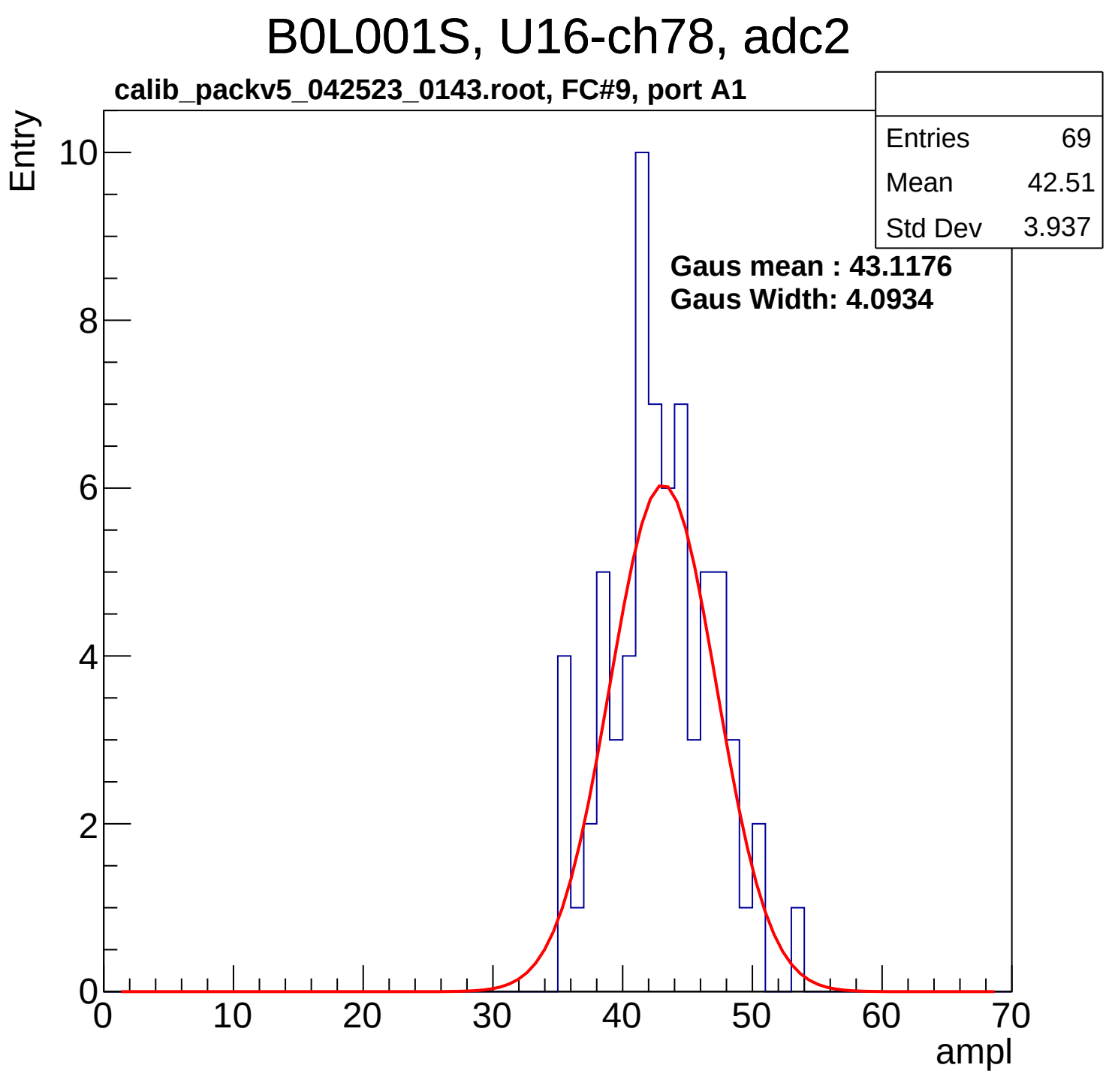
B0L001S, U16-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 42.51
Std Dev 3.937
Gaus mean : 43.1176
Gaus Width: 4.0934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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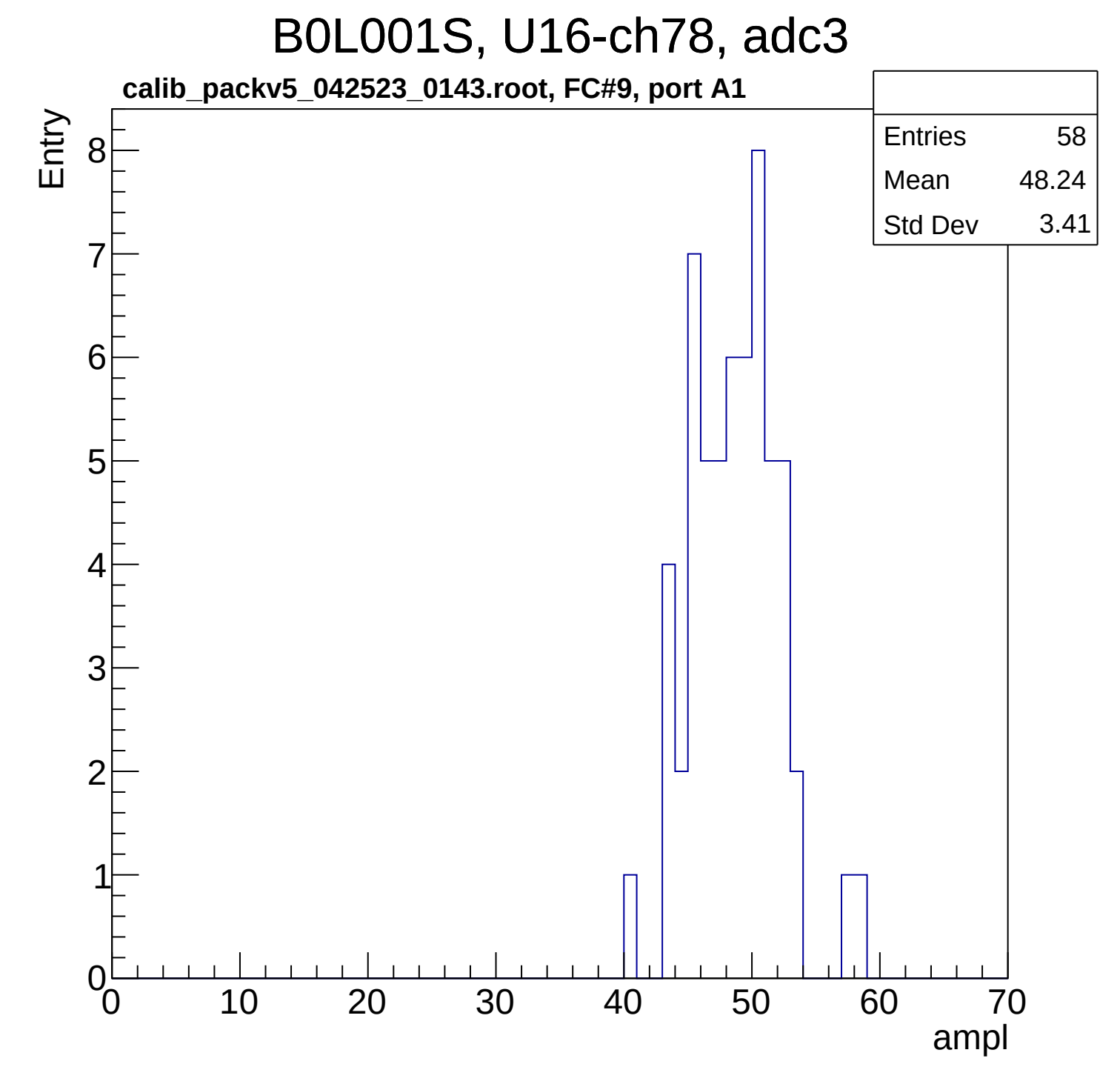
B0L001S, U16-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 48.24
Std Dev 3.41
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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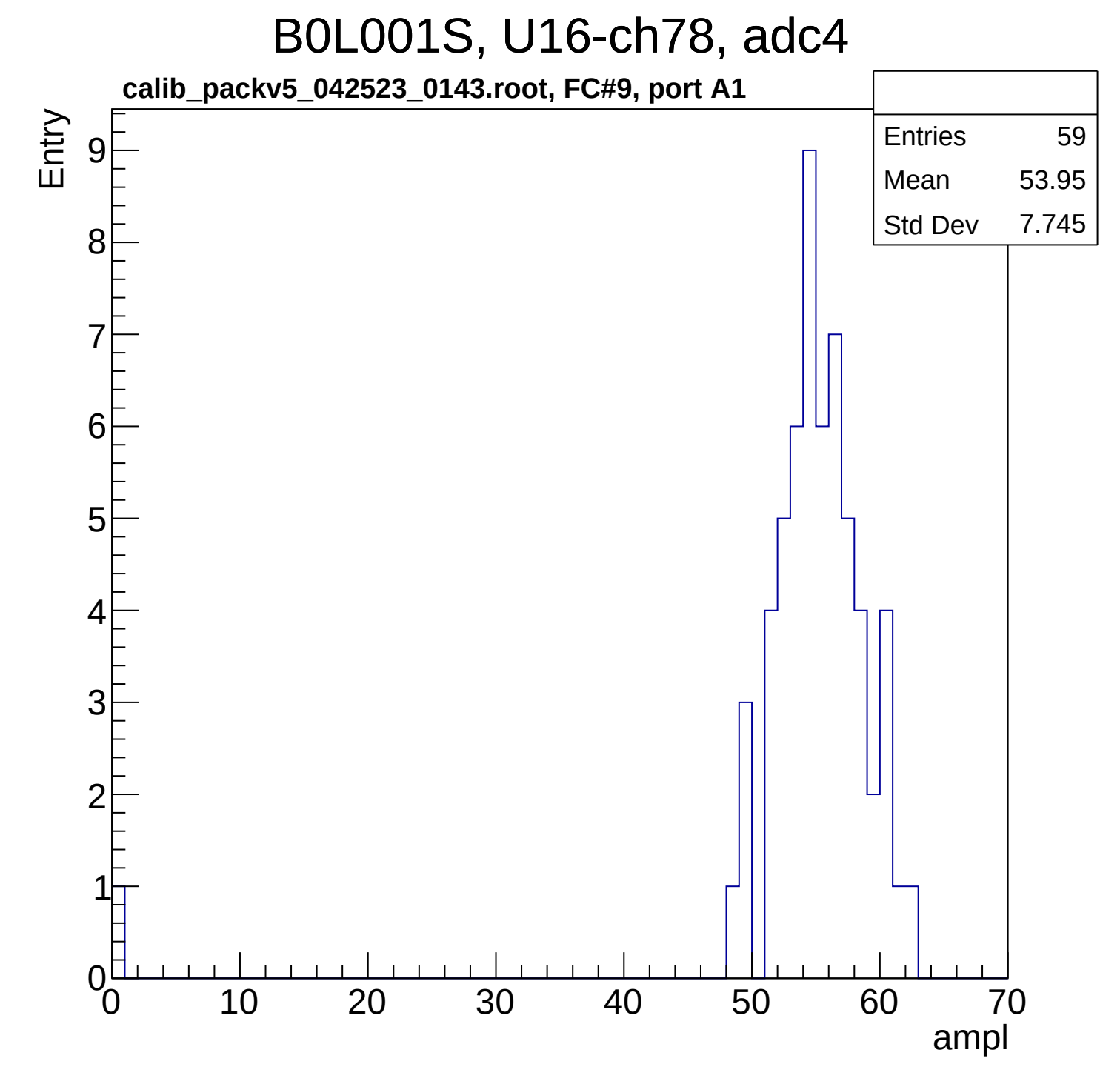
B0L001S, U16-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 53.95
Std Dev 7.745
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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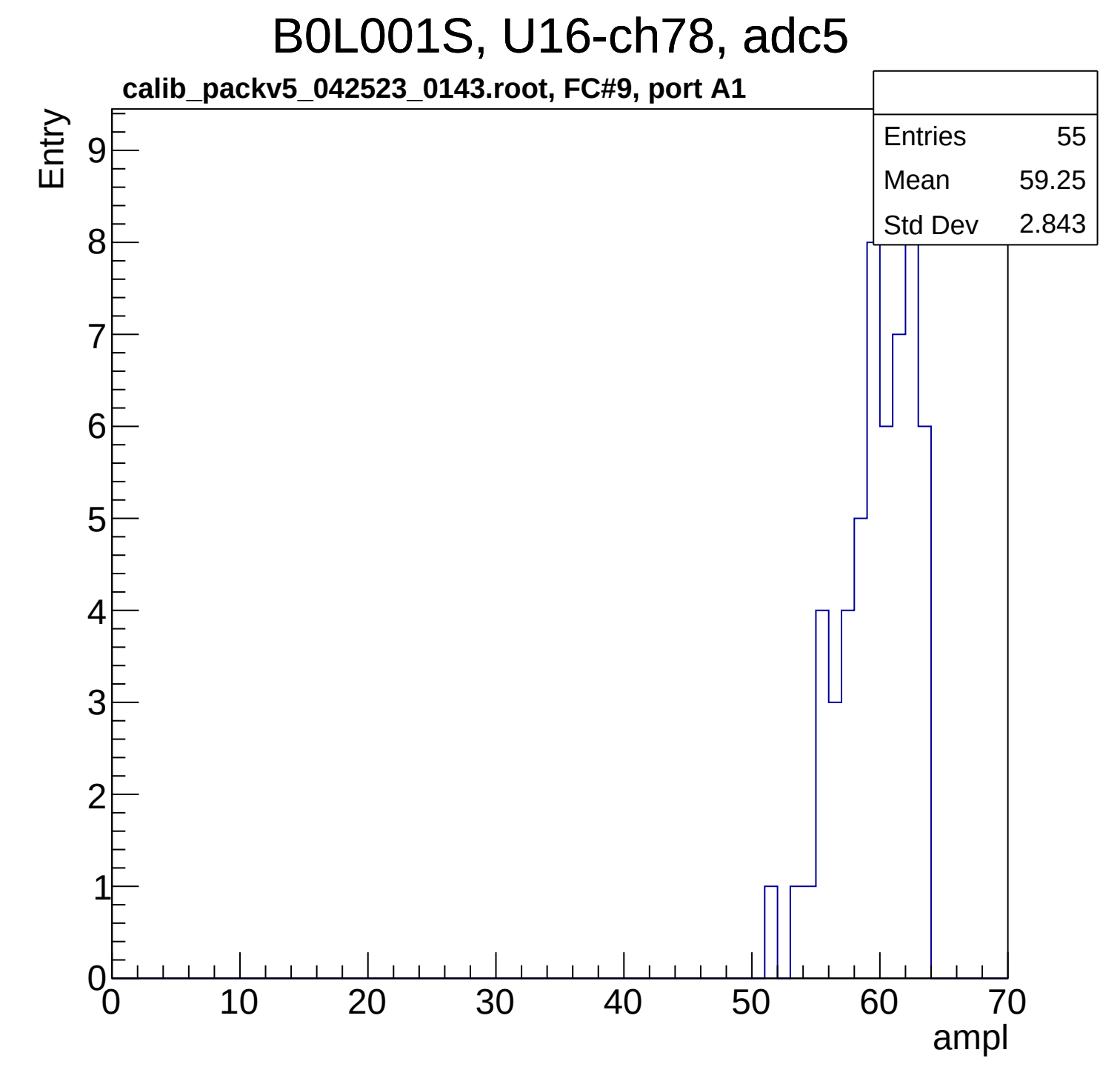
B0L001S, U16-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 59.25
Std Dev 2.843
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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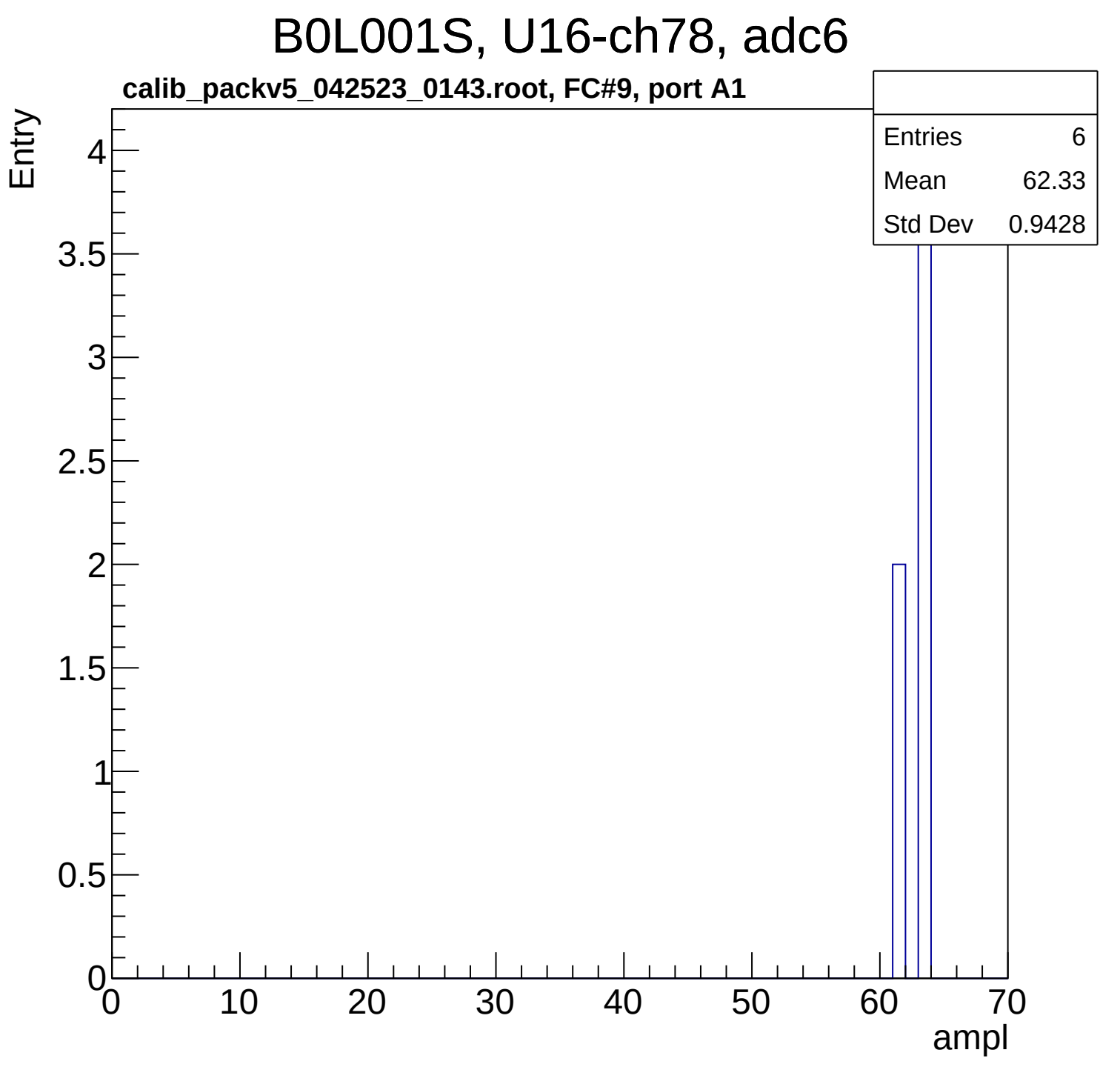
B0L001S, U16-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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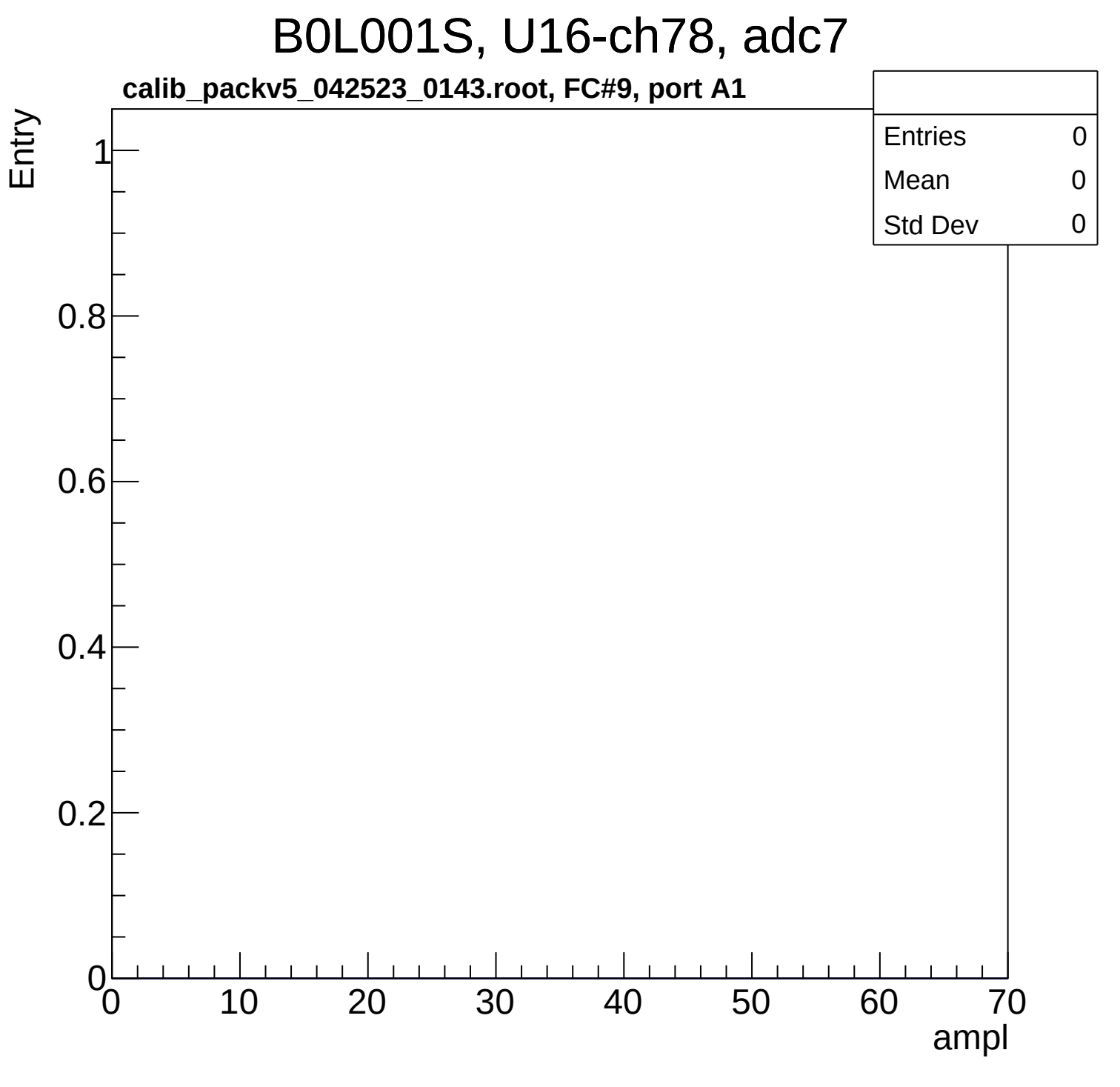
60
70


B0L001S, U16-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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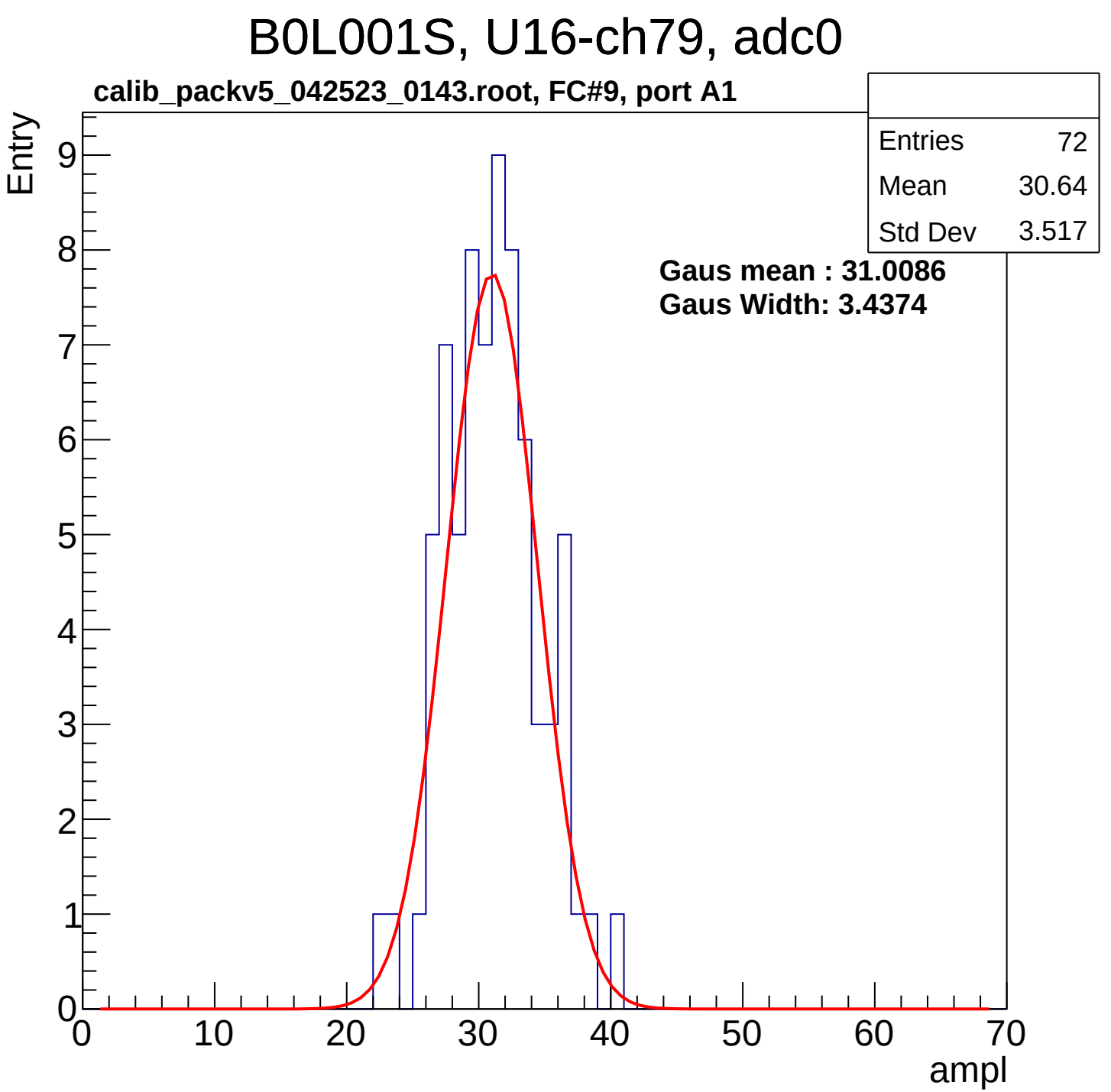
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.64
Std Dev 3.517
Gaus mean : 31.0086
Gaus Width: 3.4374
Entry
ampl
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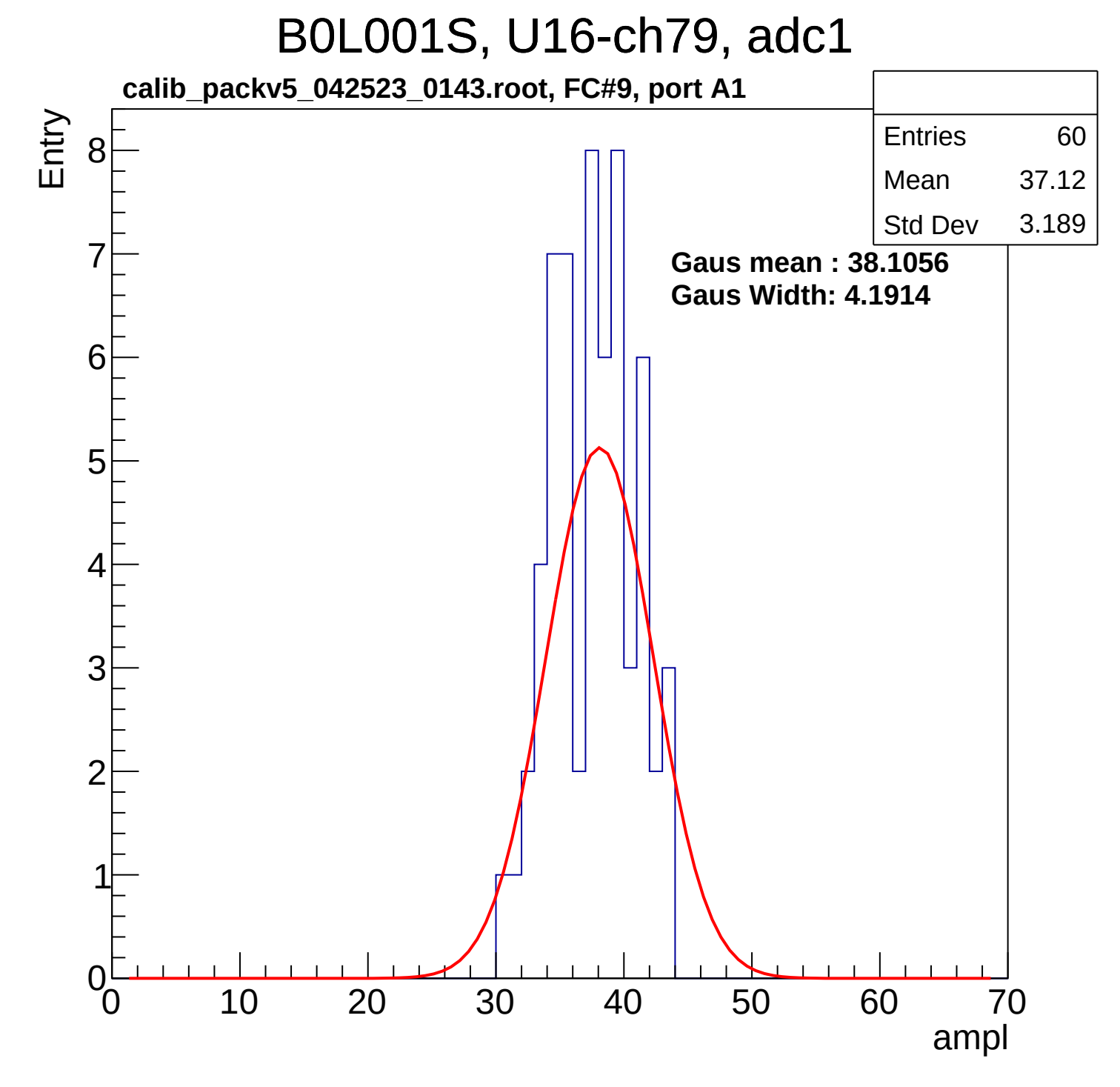
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.12
Std Dev 3.189
Gaus mean : 38.1056
Gaus Width: 4.1914
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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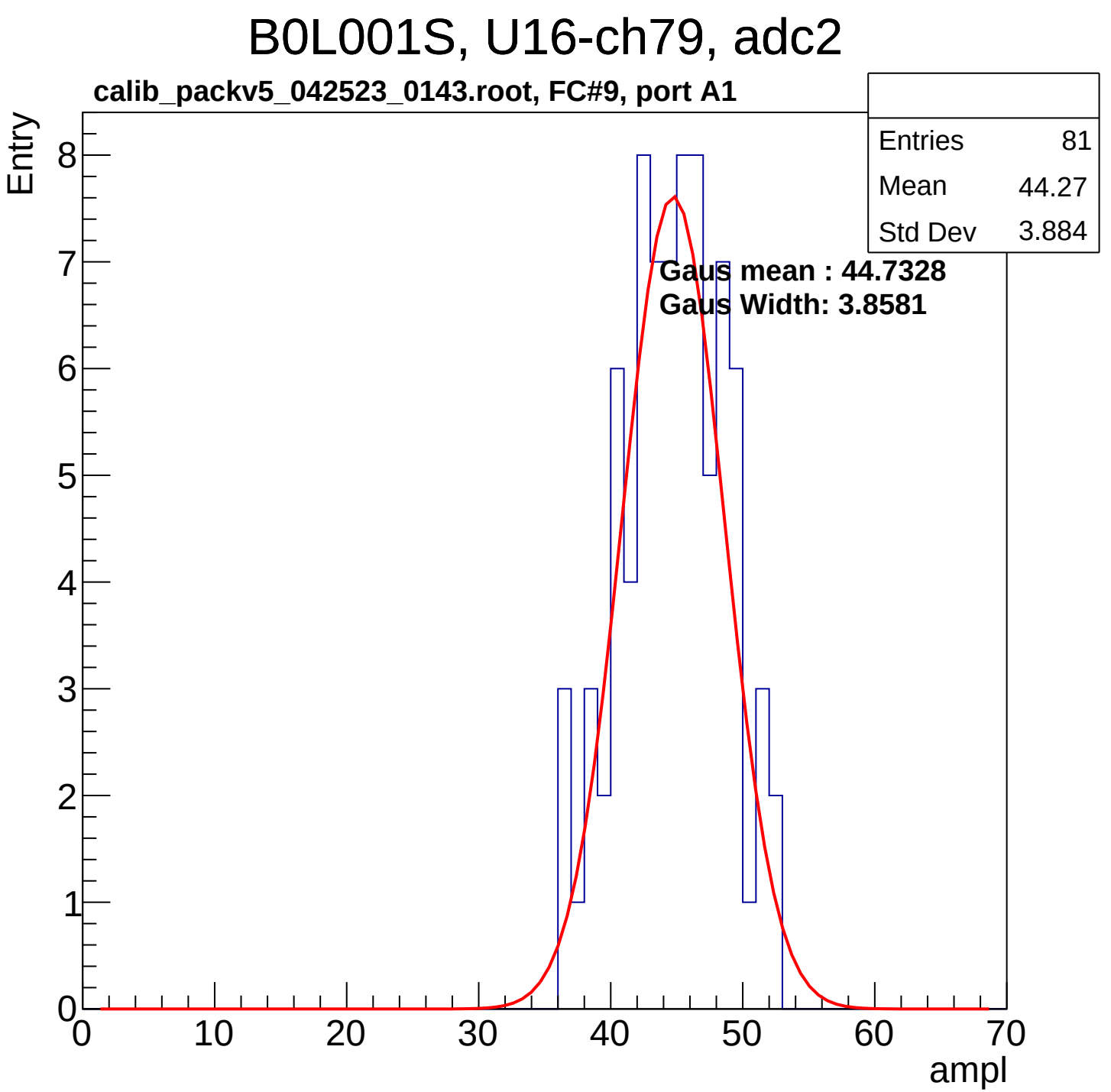
40
50
60
70


B0L001S, U16-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 44.27
Std Dev 3.884
Gaus mean : 44.7328
Gaus Width: 3.8581
Entry
ampl
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1
2
3
4
5
6
7
8
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10
20
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40
50
60
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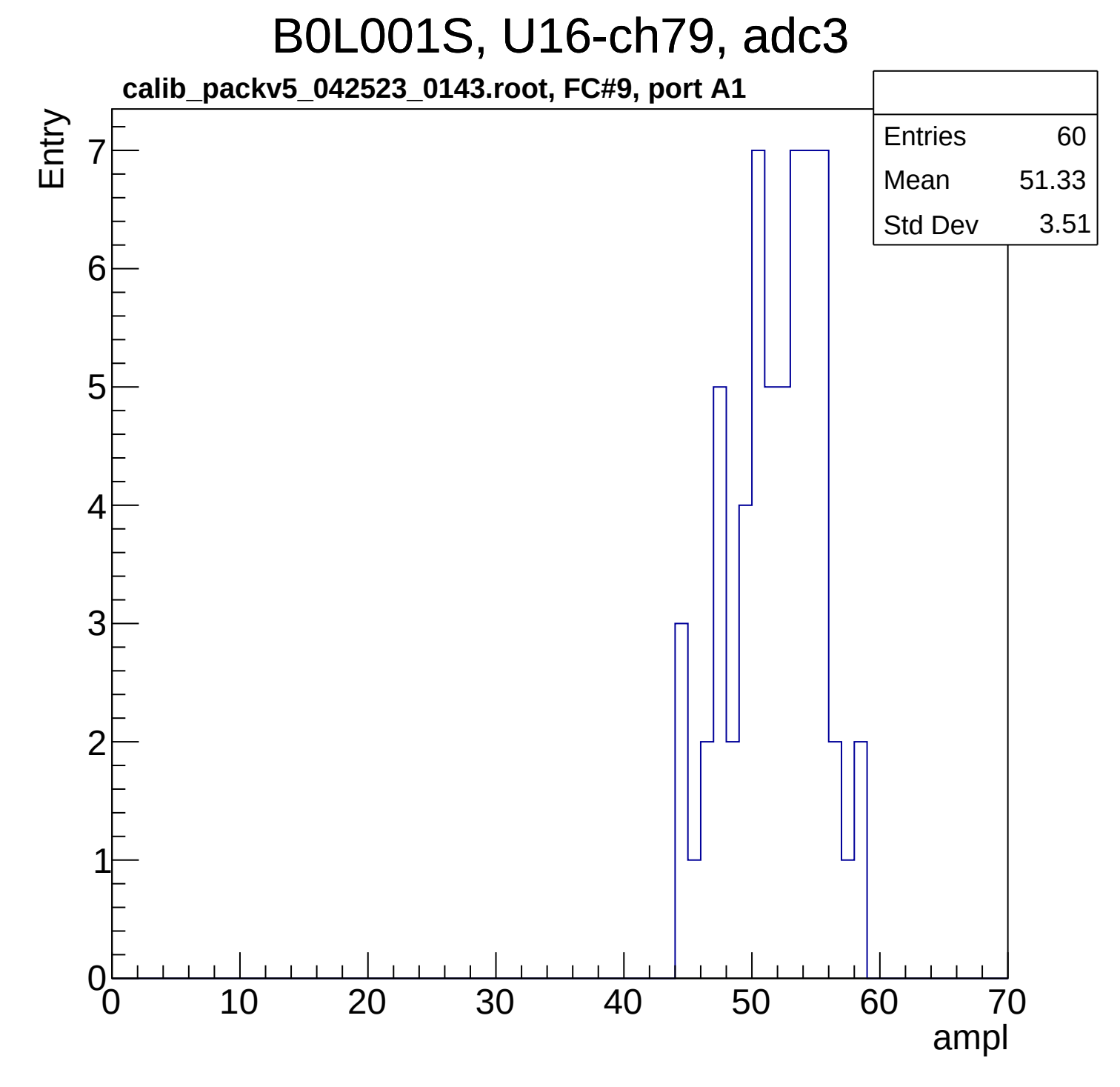
B0L001S, U16-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.33
Std Dev 3.51
Entry
ampl
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20
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40
50
60
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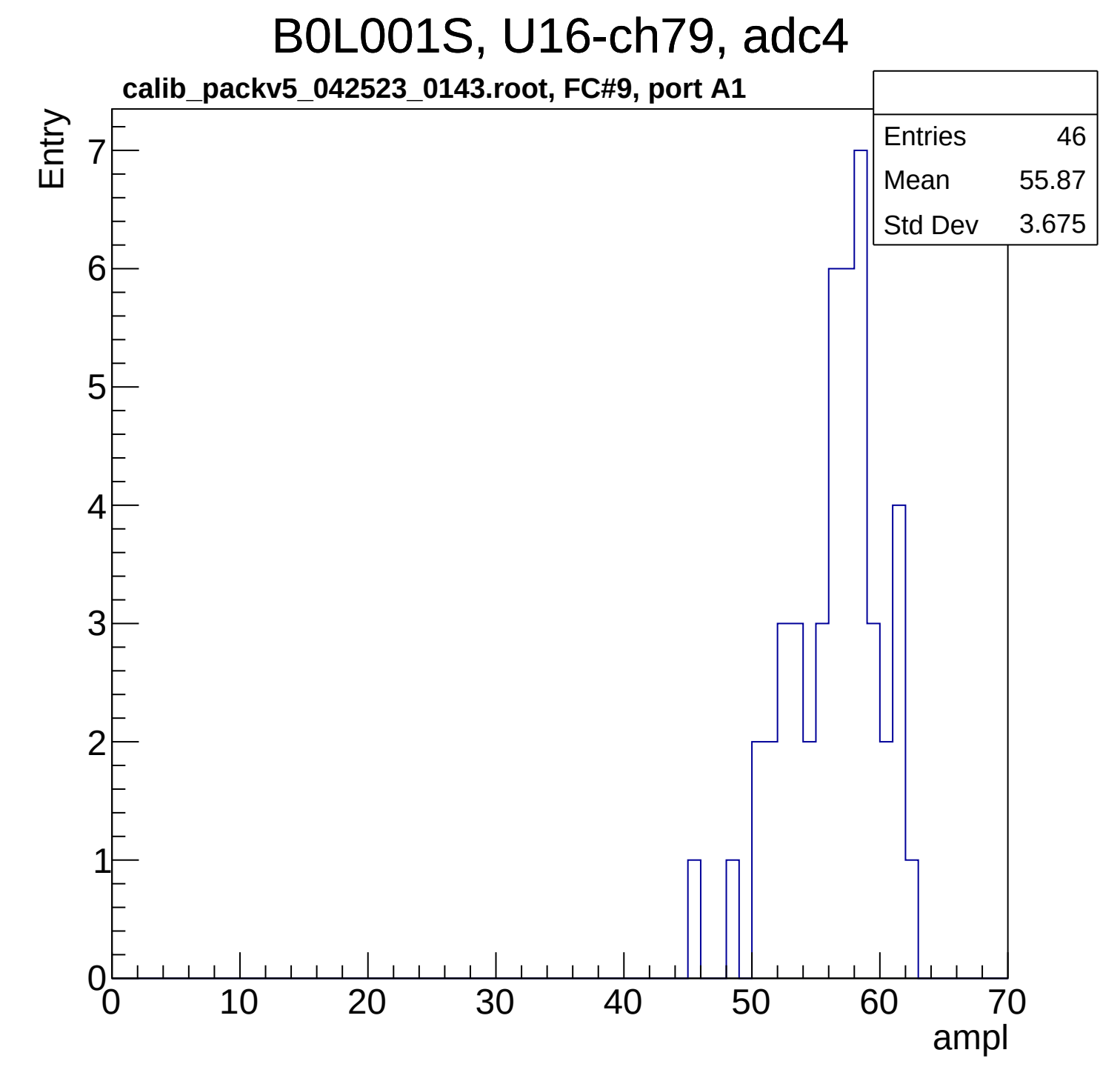
B0L001S, U16-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.87
Std Dev 3.675
Entry
ampl
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3
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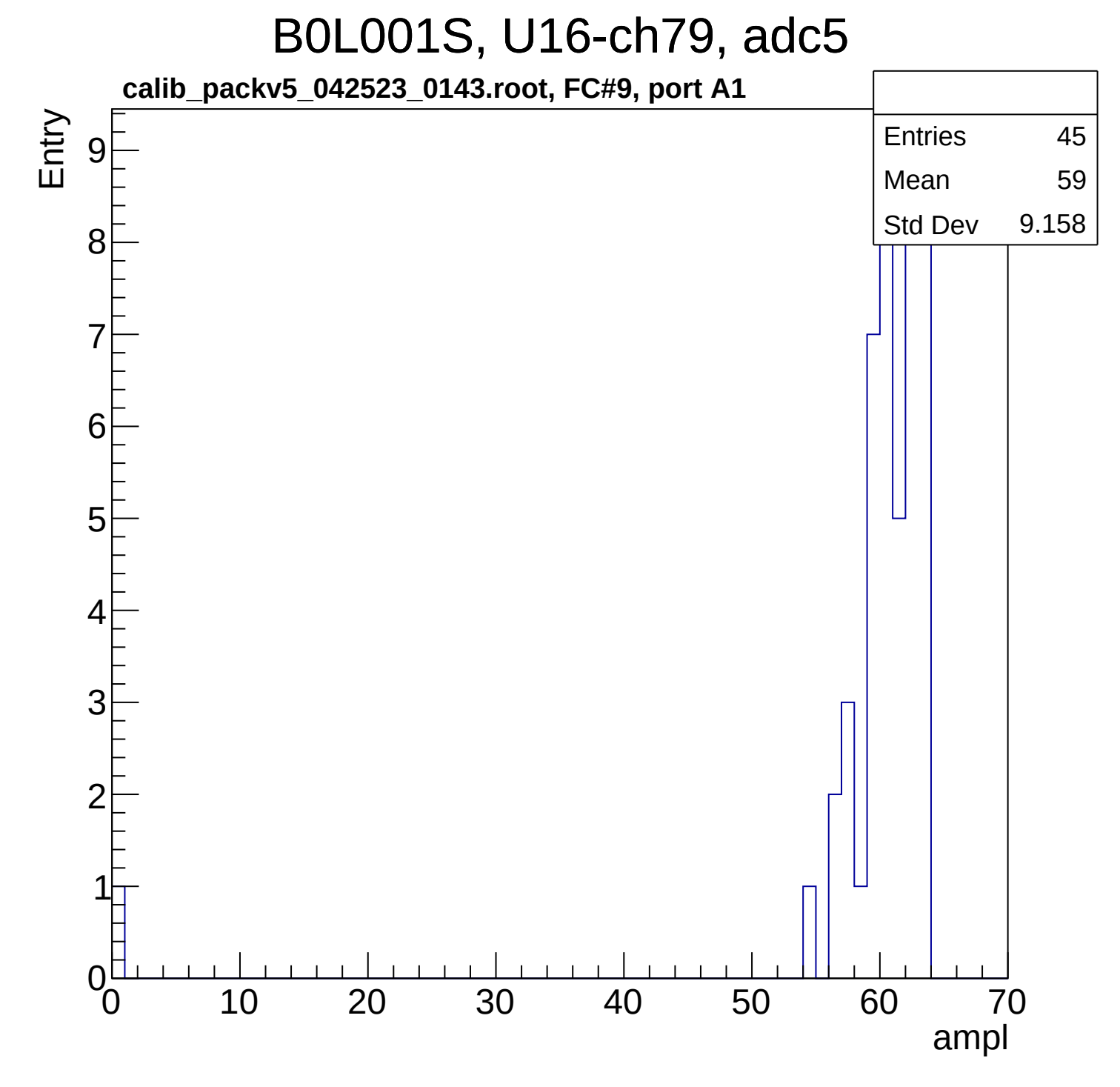
B0L001S, U16-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59
Std Dev 9.158
Entry
ampl
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2
3
4
5
6
7
8
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0
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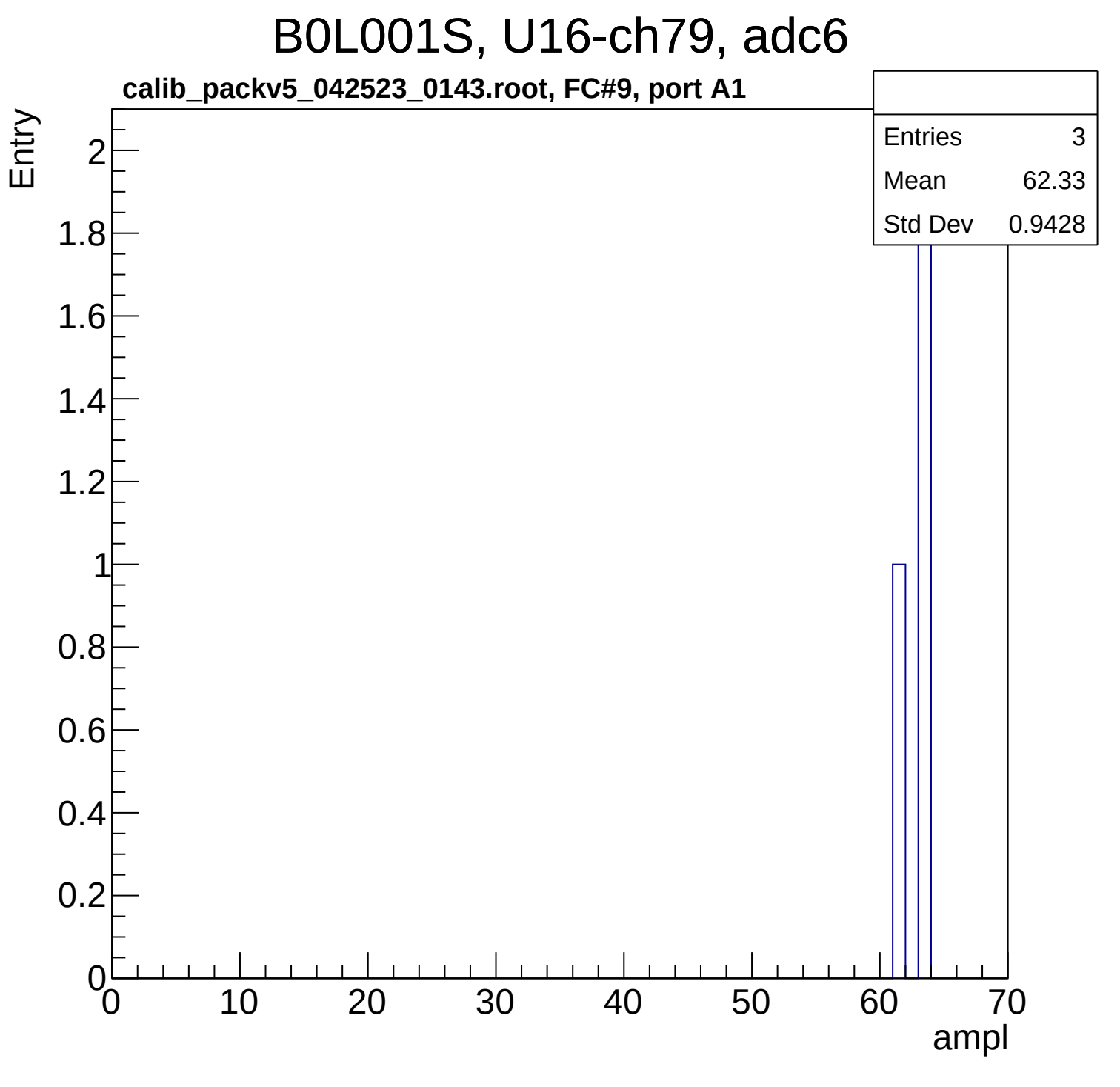
B0L001S, U16-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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40
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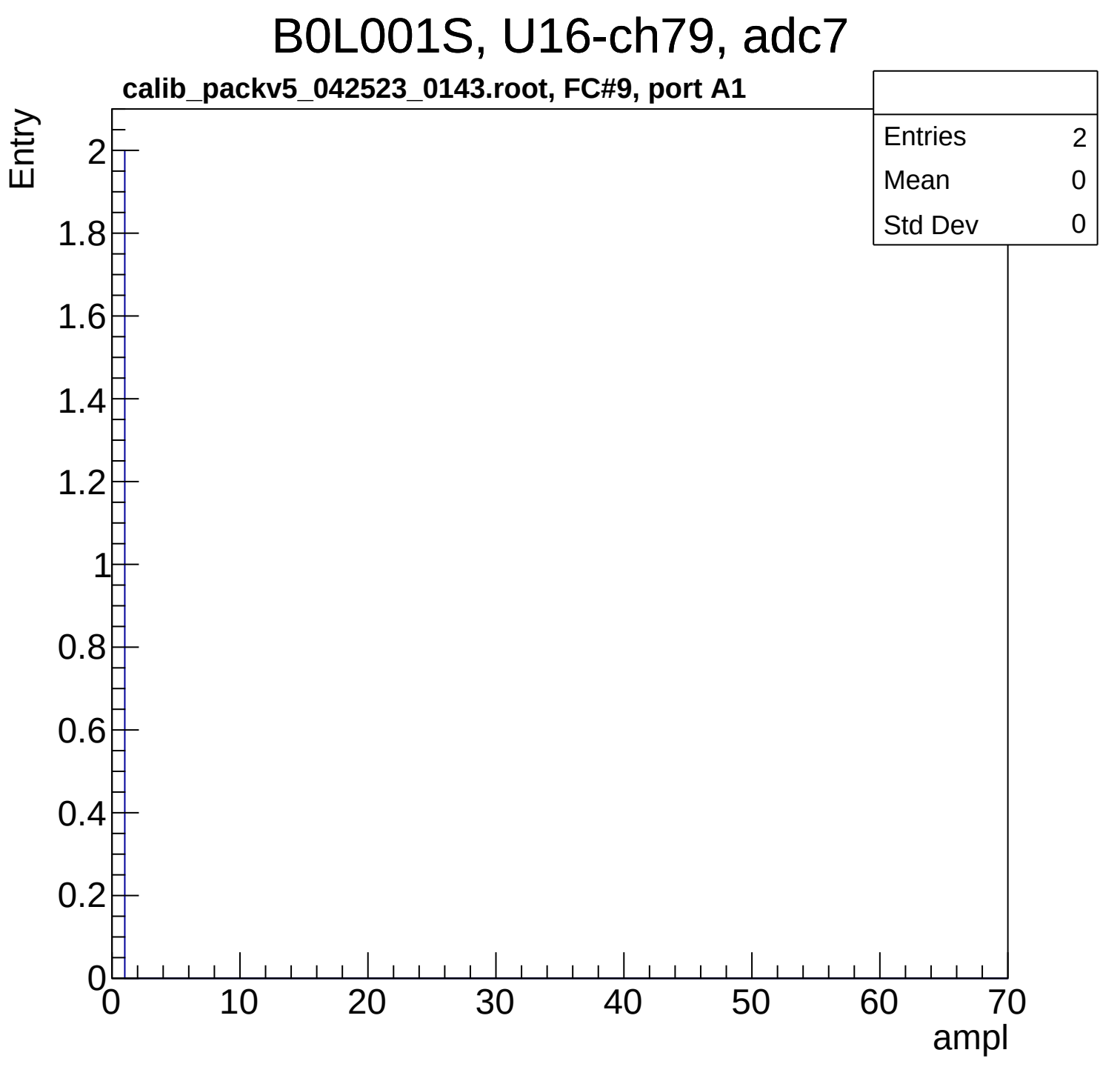
60
70


B0L001S, U16-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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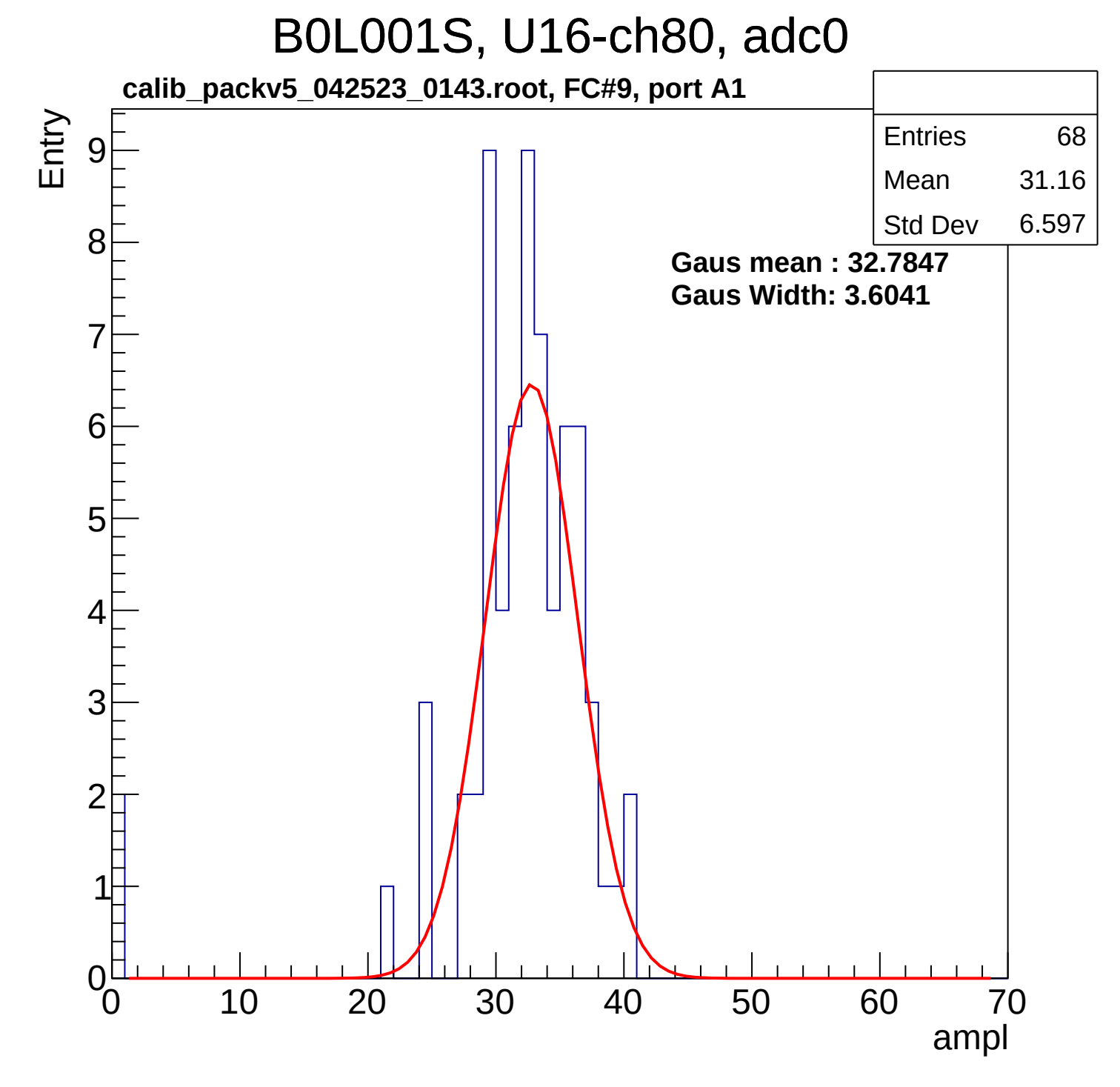
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.16
Std Dev 6.597
Gaus mean : 32.7847
Gaus Width: 3.6041
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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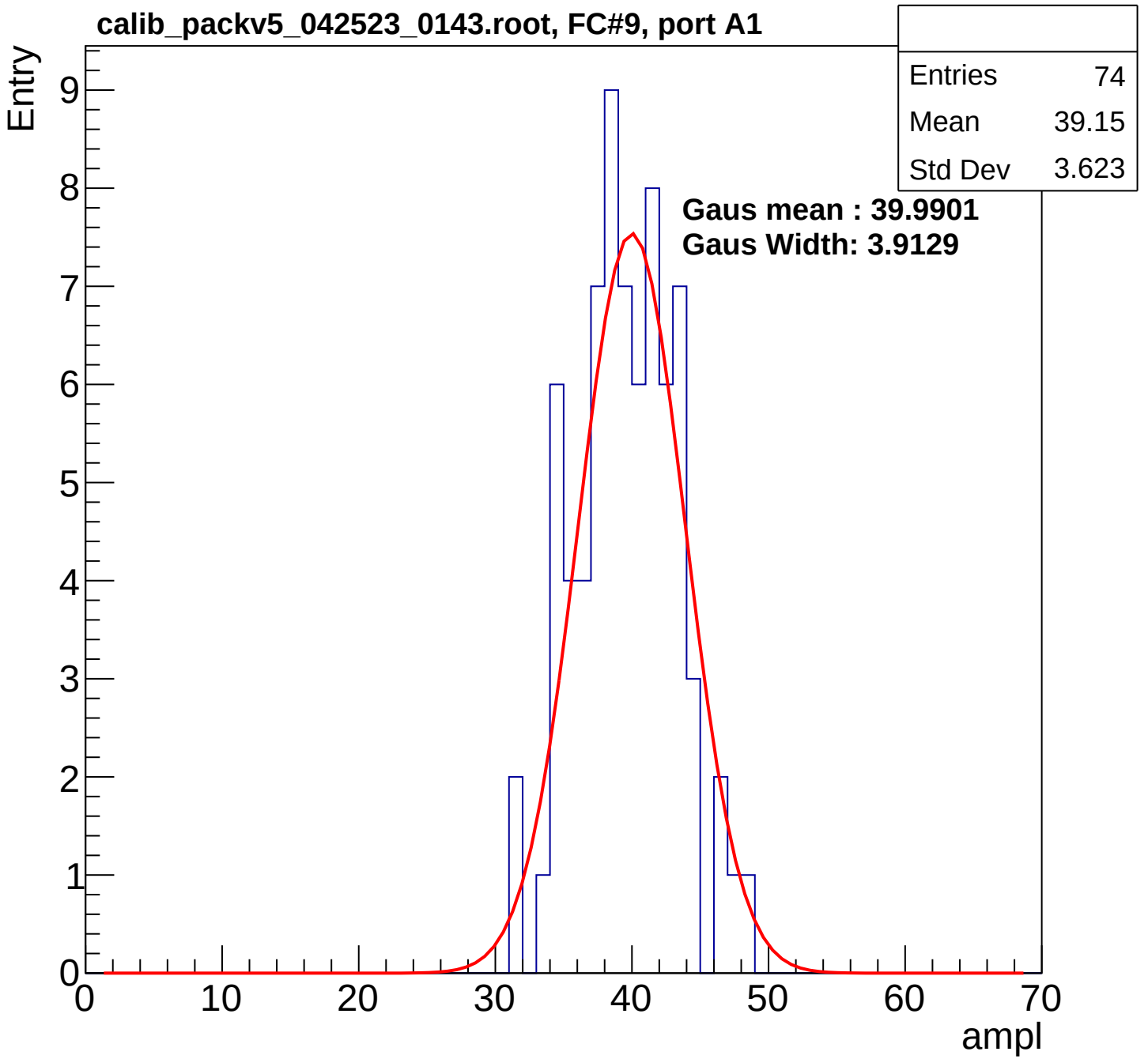
B0L001S, U16-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 39.15
Std Dev 3.623
Gaus mean : 39.9901
Gaus Width: 3.9129
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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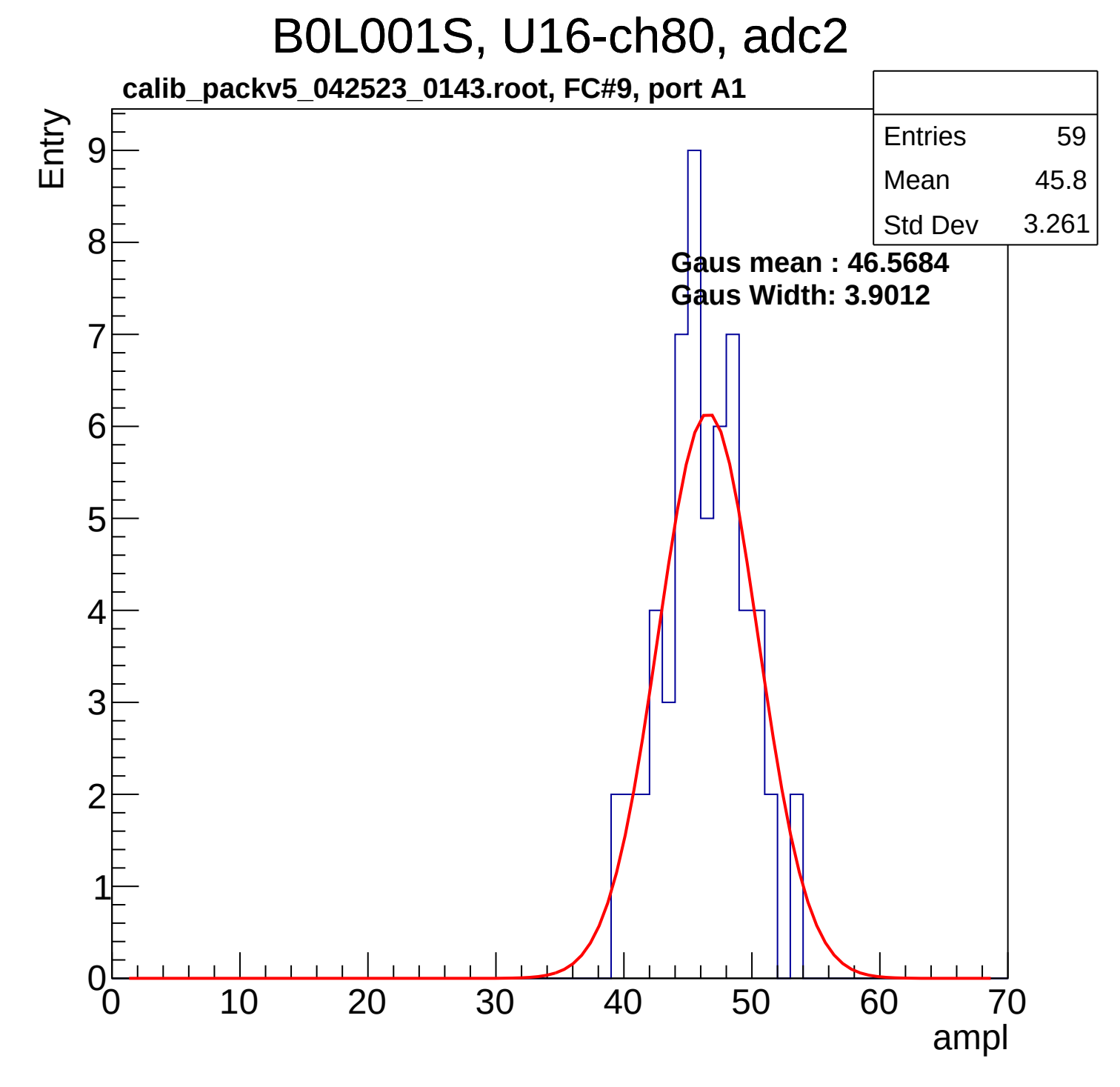
60
70


B0L001S, U16-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.8
Std Dev 3.261
Gaus mean : 46.5684
Gaus Width: 3.9012
Entry
ampl
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2
3
4
5
6
7
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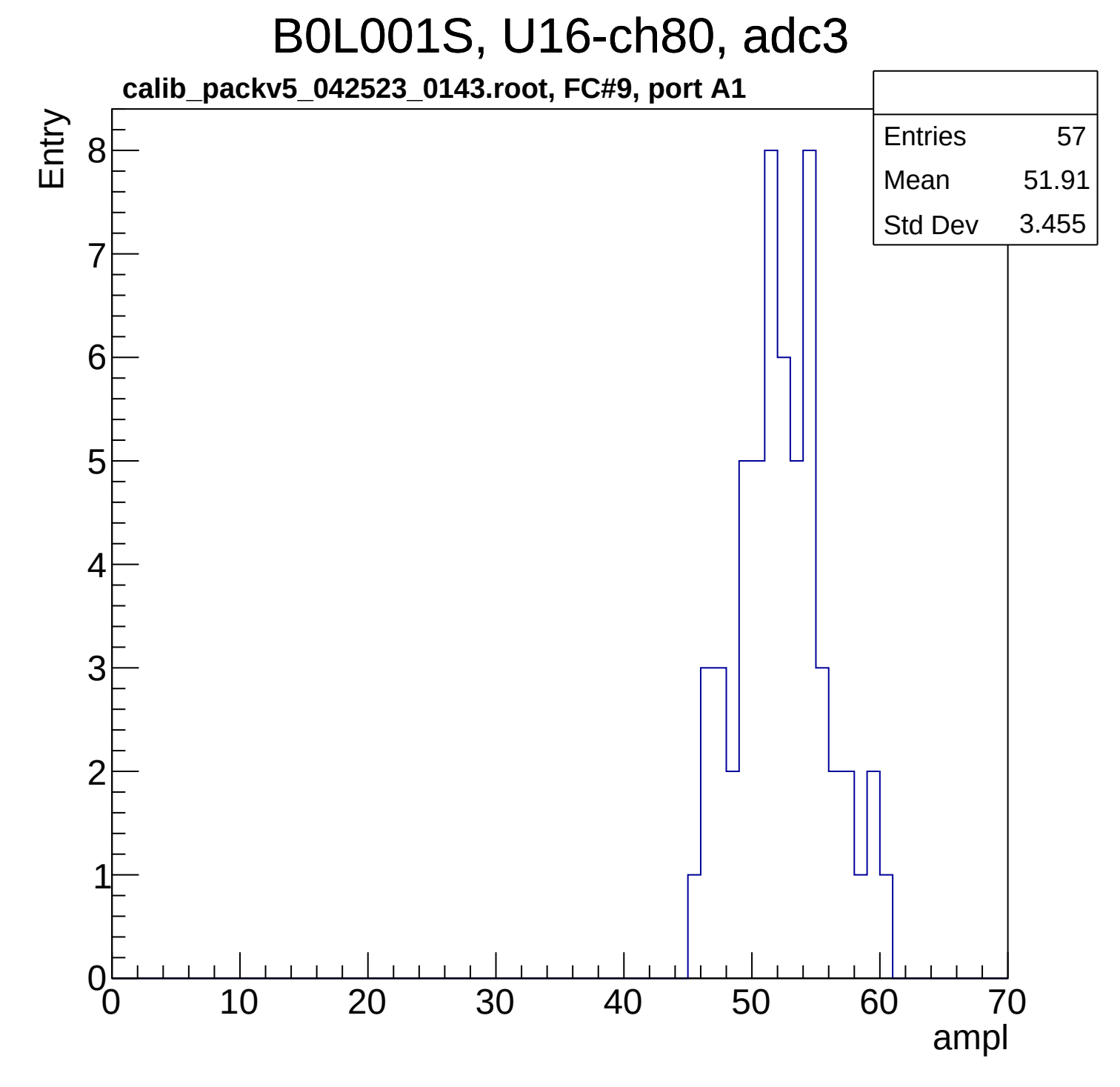
B0L001S, U16-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.91
Std Dev 3.455
Entry
ampl
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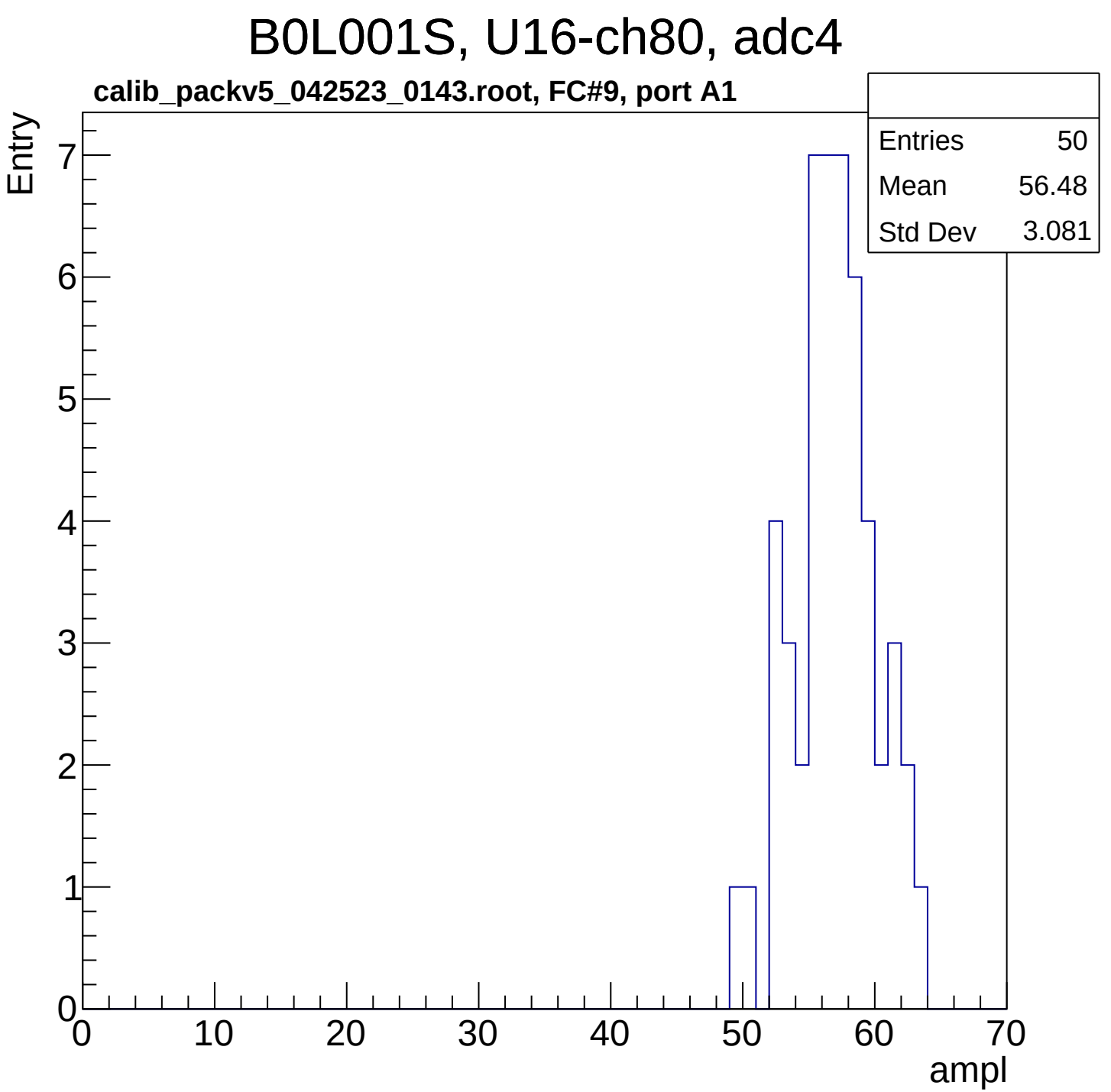
B0L001S, U16-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.48
Std Dev 3.081
Entry
ampl
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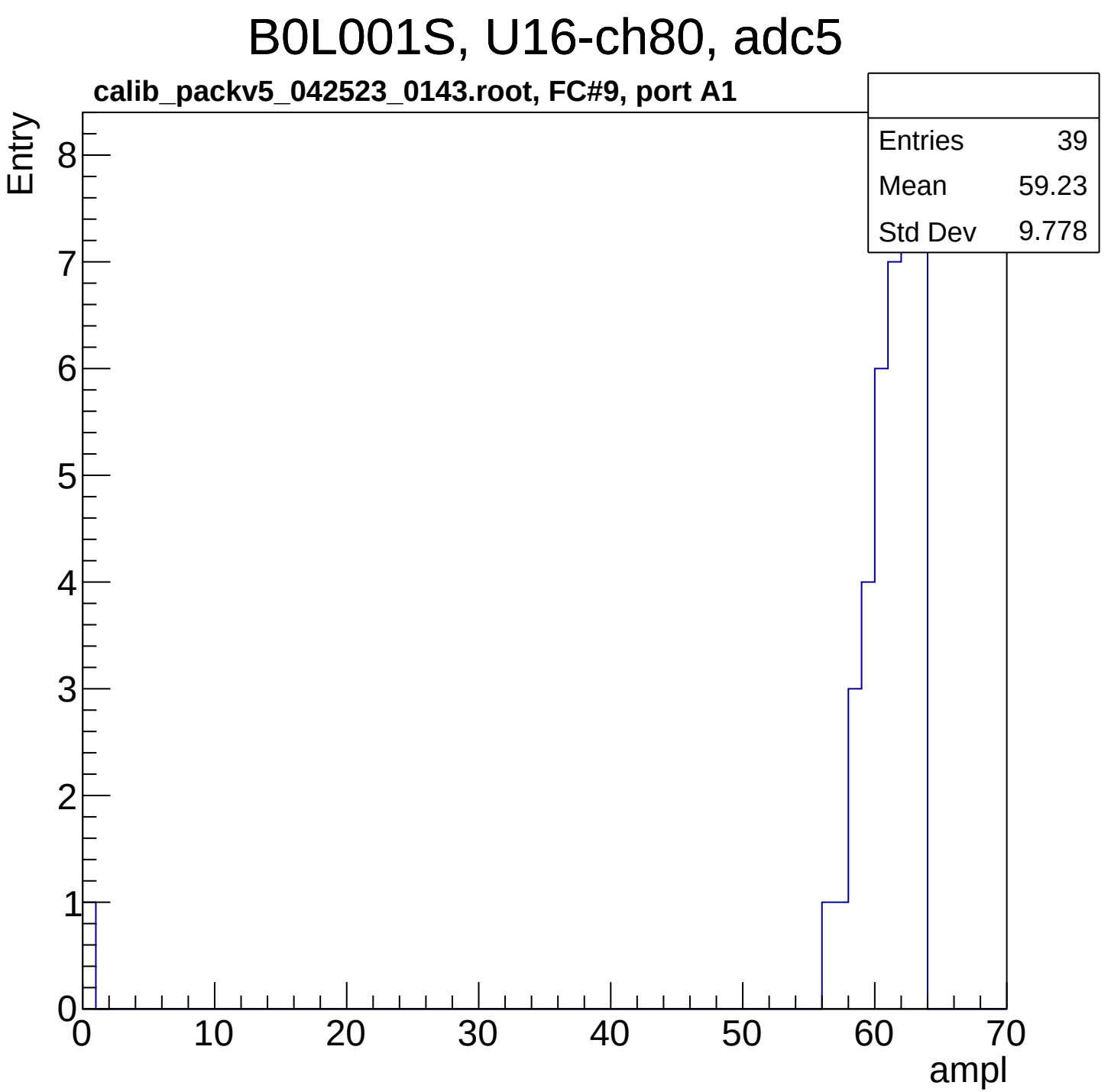
B0L001S, U16-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.23
Std Dev 9.778
Entry
ampl
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2
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4
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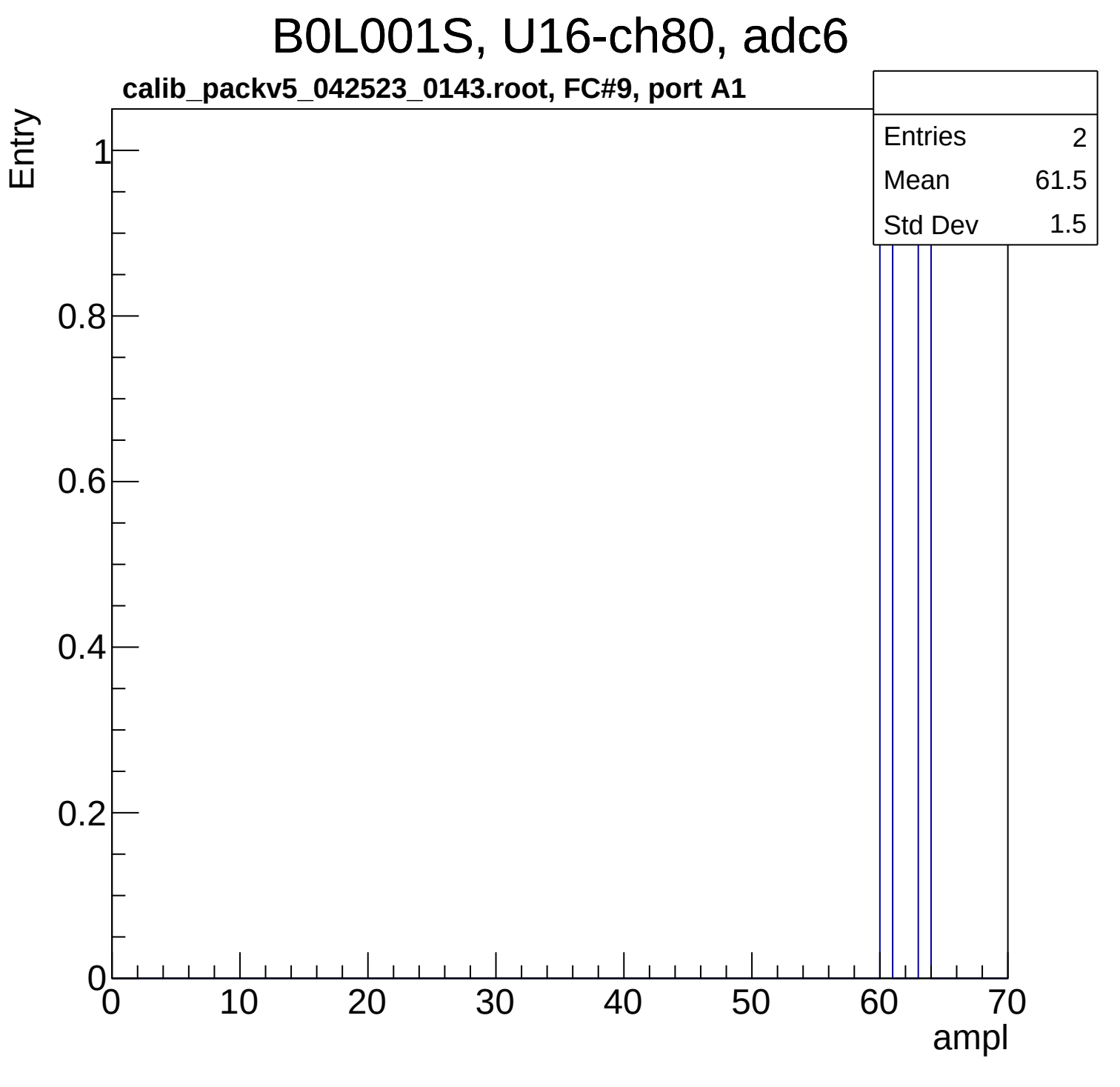
B0L001S, U16-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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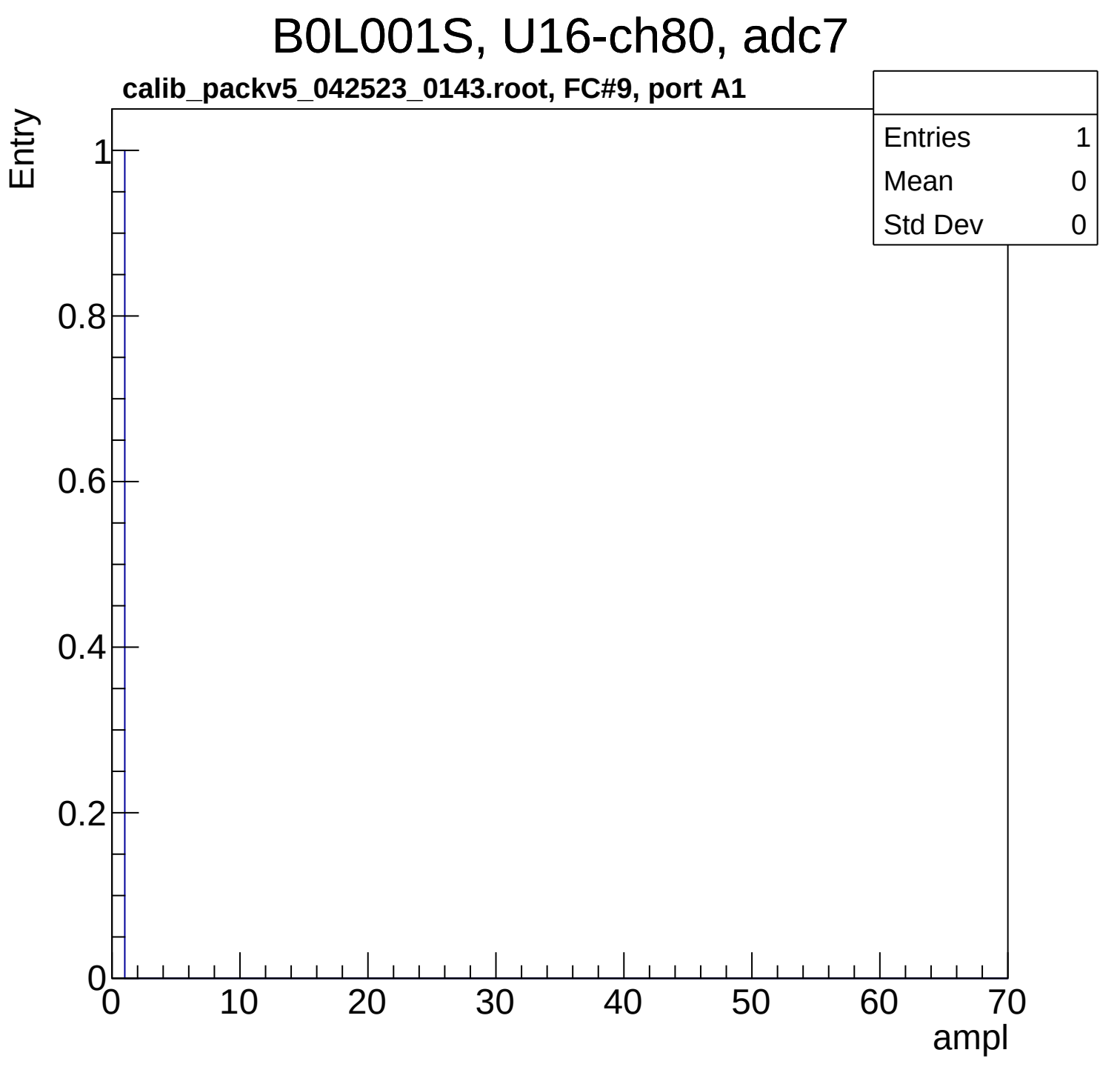
50
60
70
ampl


B0L001S, U16-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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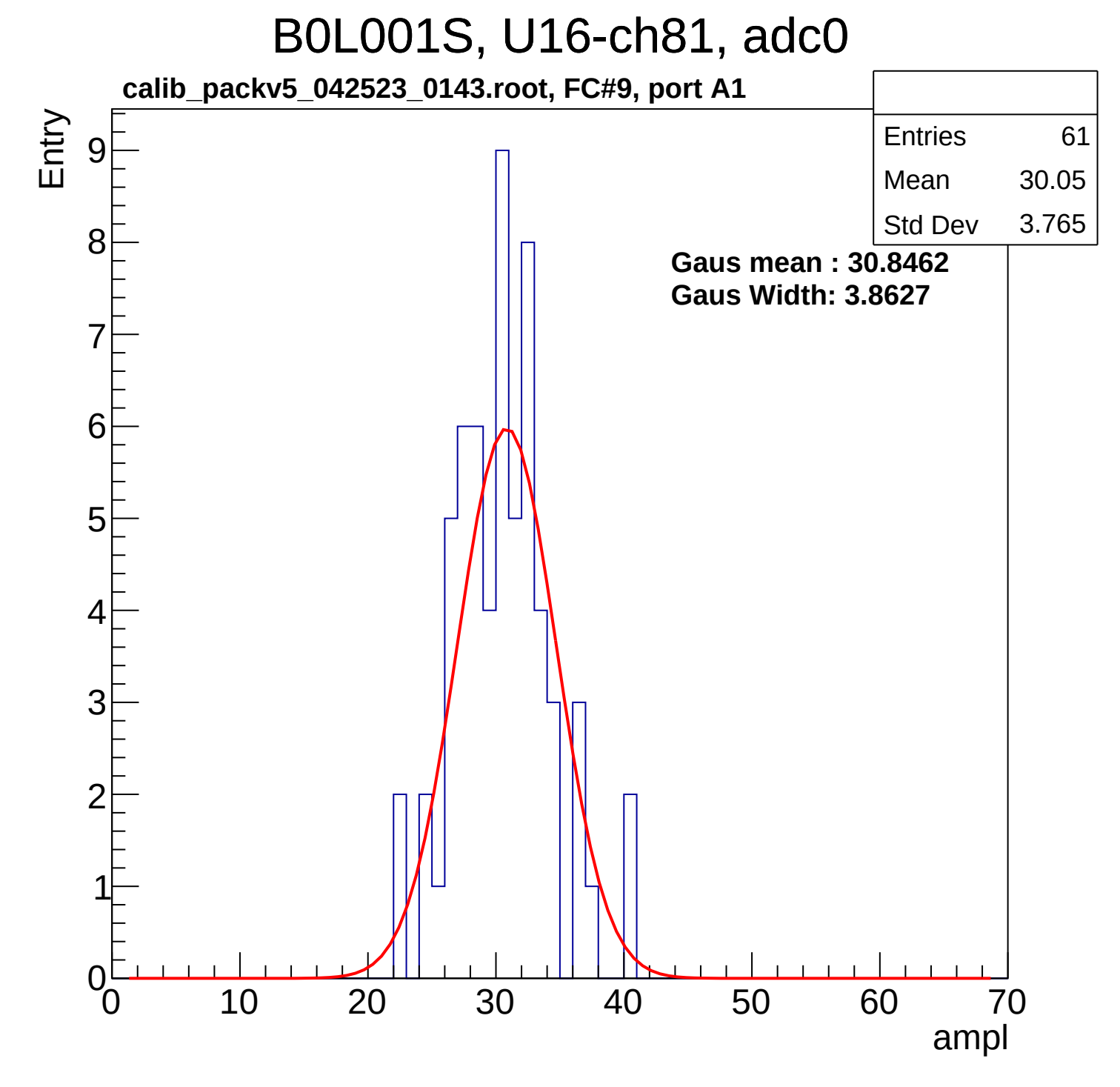
40
50
60
70
ampl


B0L001S, U16-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 30.05
Std Dev 3.765
Gaus mean : 30.8462
Gaus Width: 3.8627
Entry
ampl
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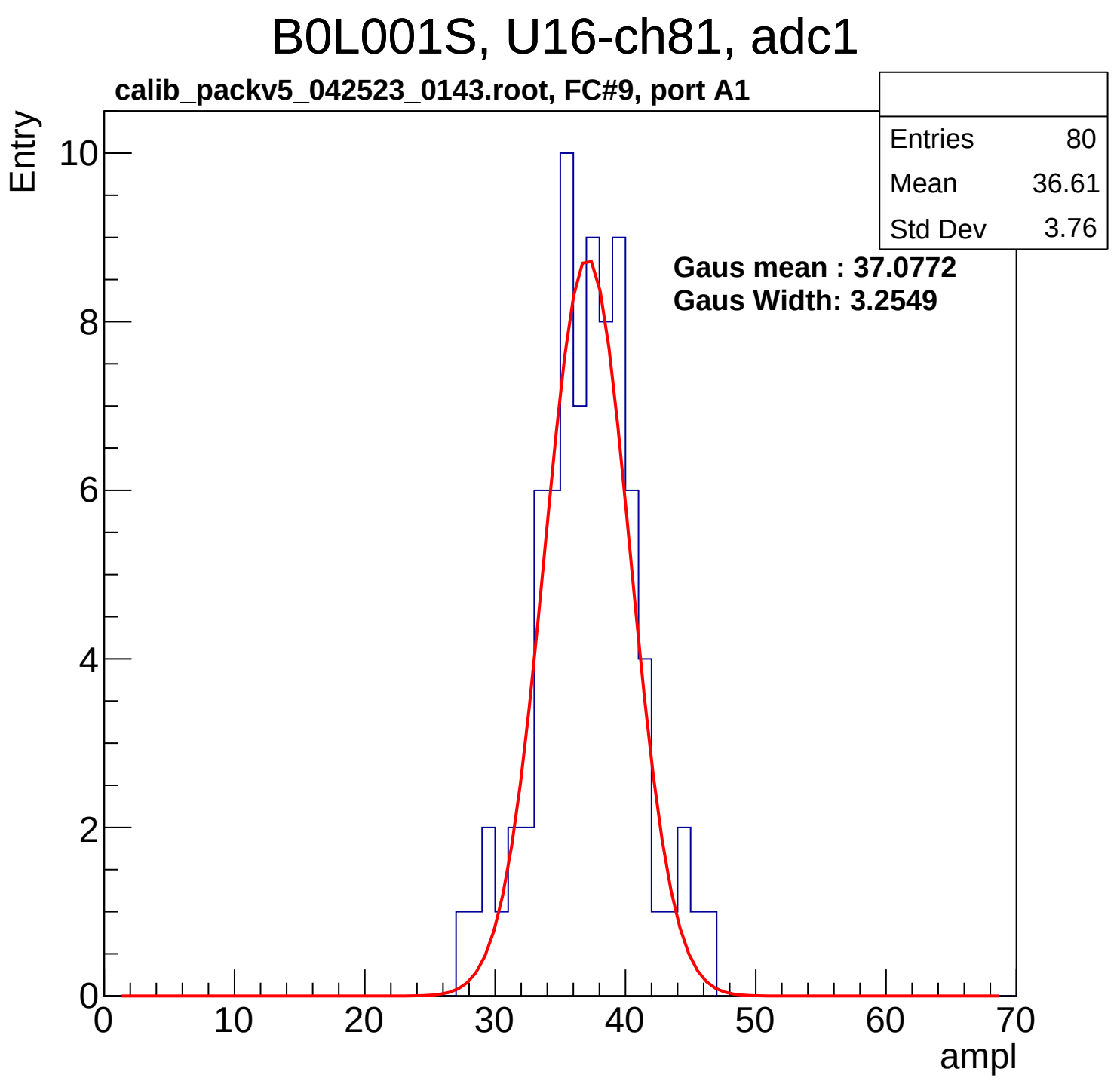
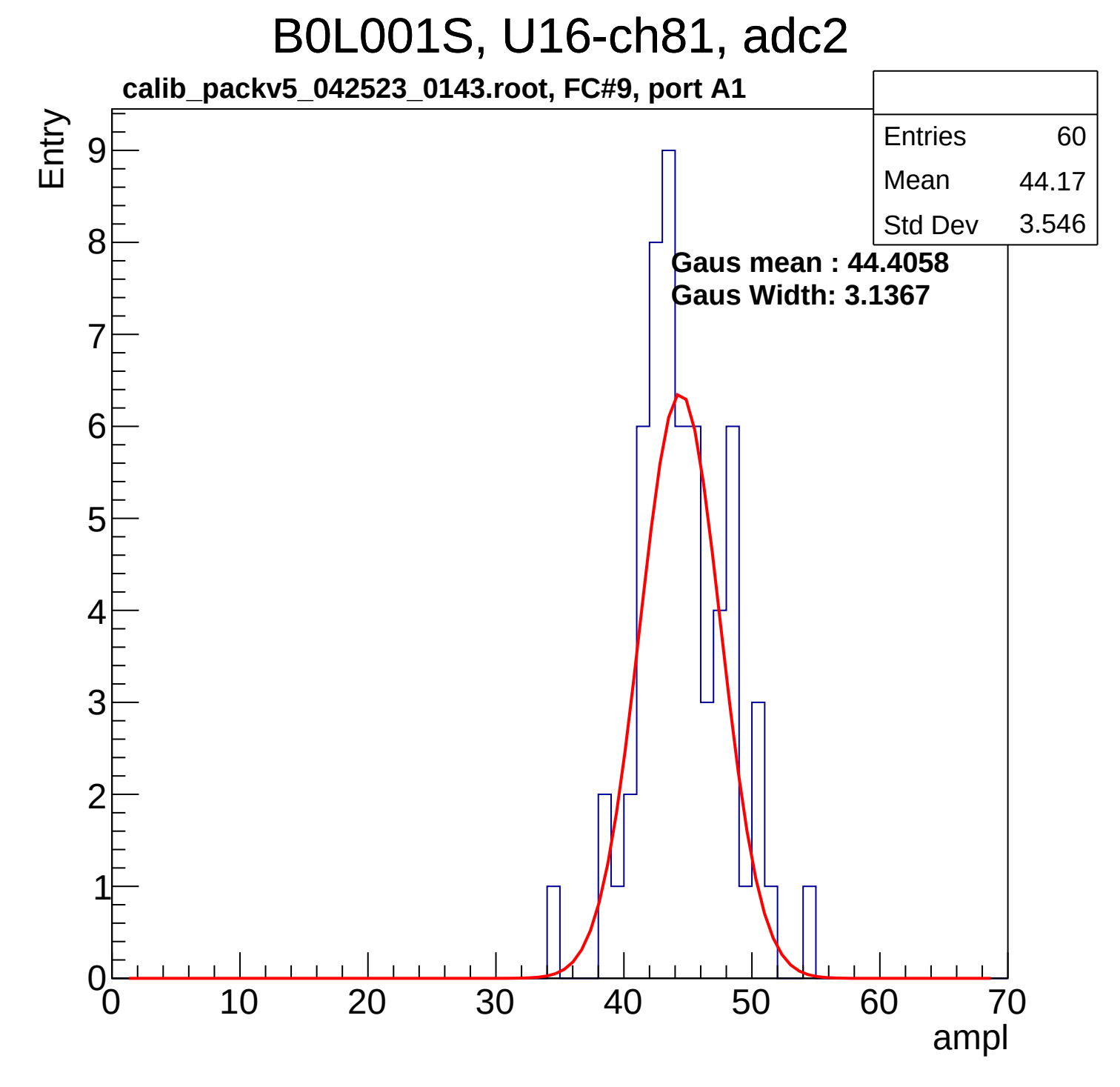
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 36.61
Std Dev 3.76
Gaus mean : 37.0772
Gaus Width: 3.2549
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.17
Std Dev 3.546
Gaus mean : 44.4058
Gaus Width: 3.1367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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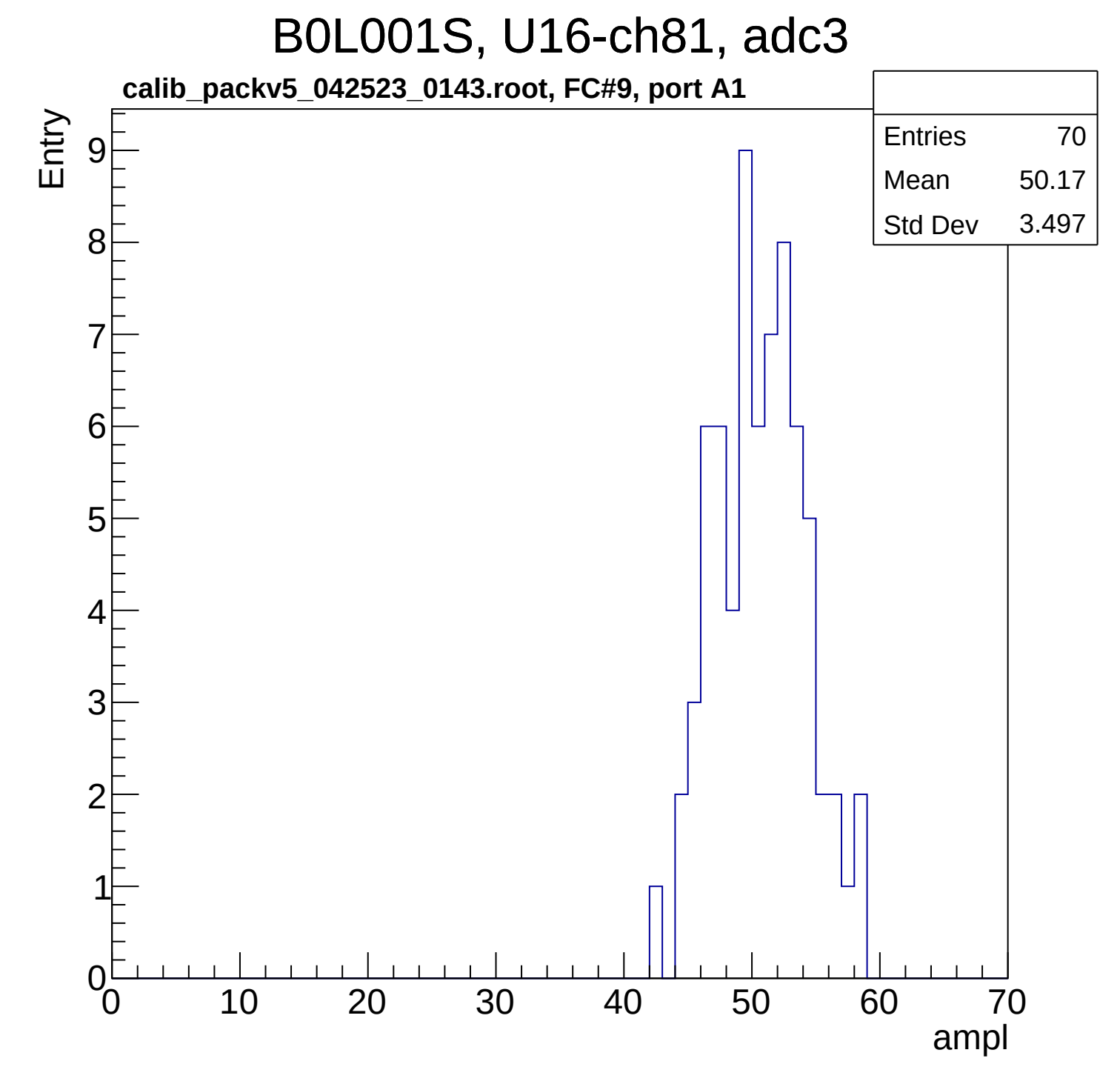
B0L001S, U16-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.17
Std Dev 3.497
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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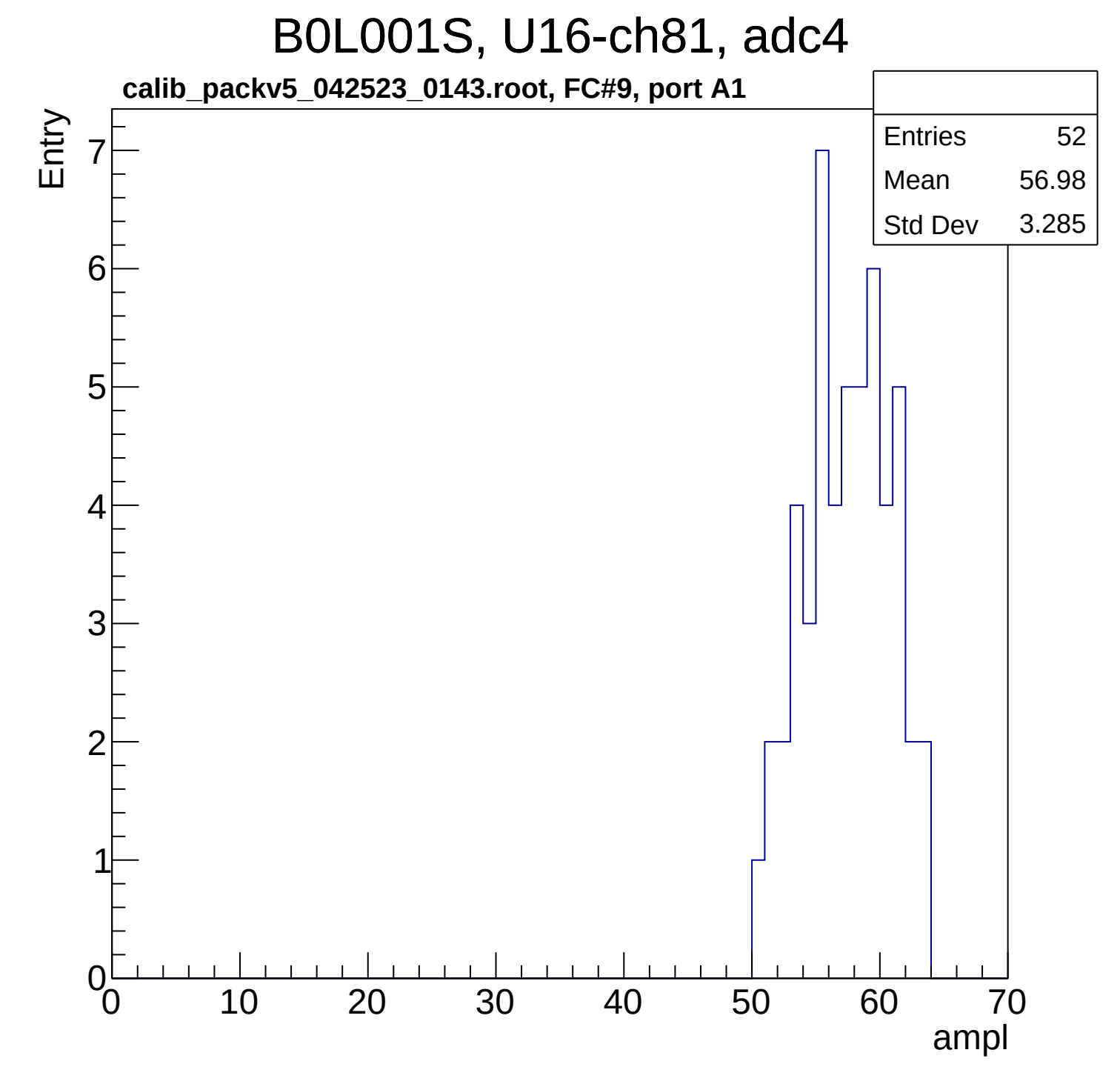
B0L001S, U16-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.98
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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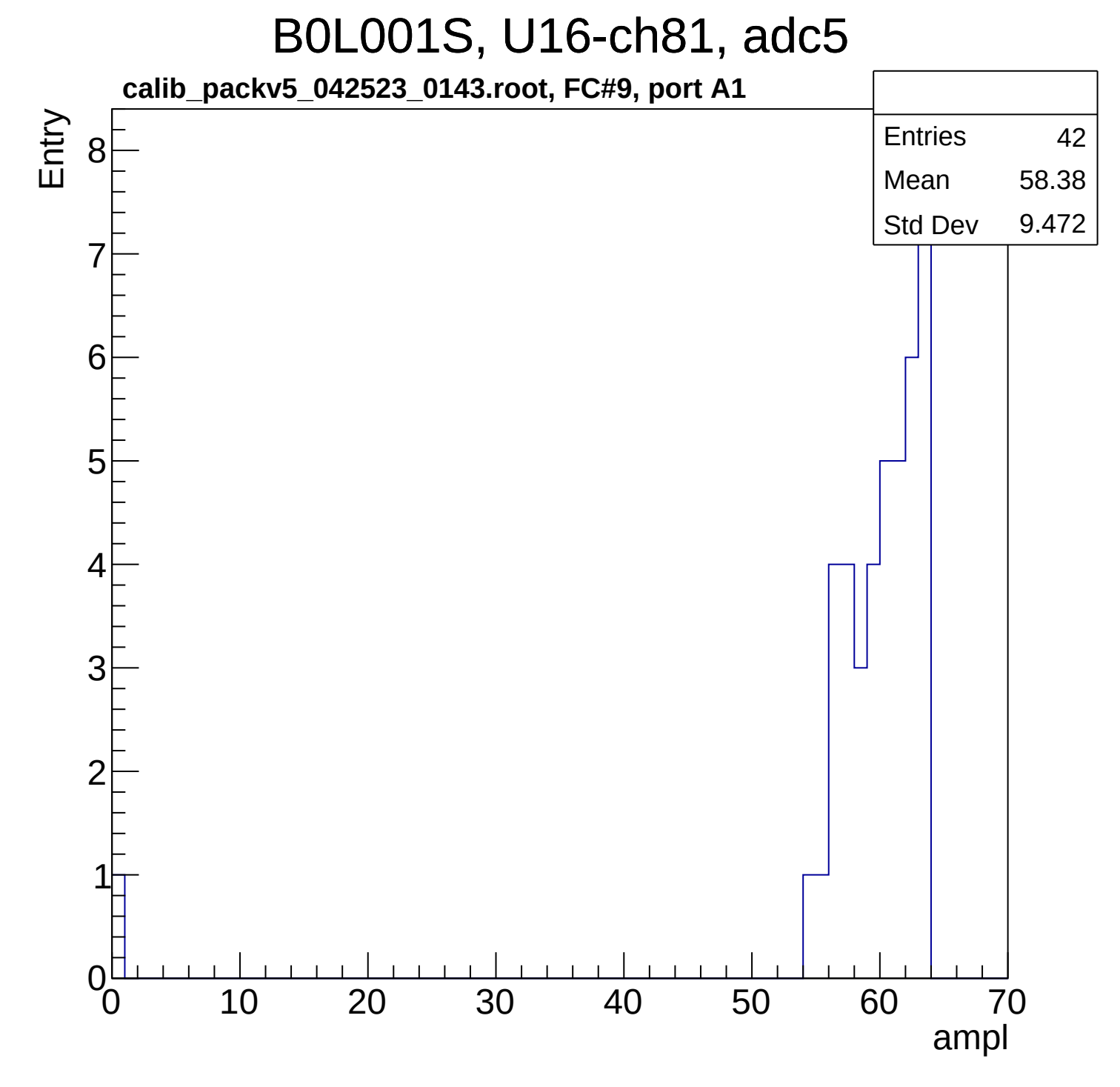
B0L001S, U16-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.38
Std Dev 9.472
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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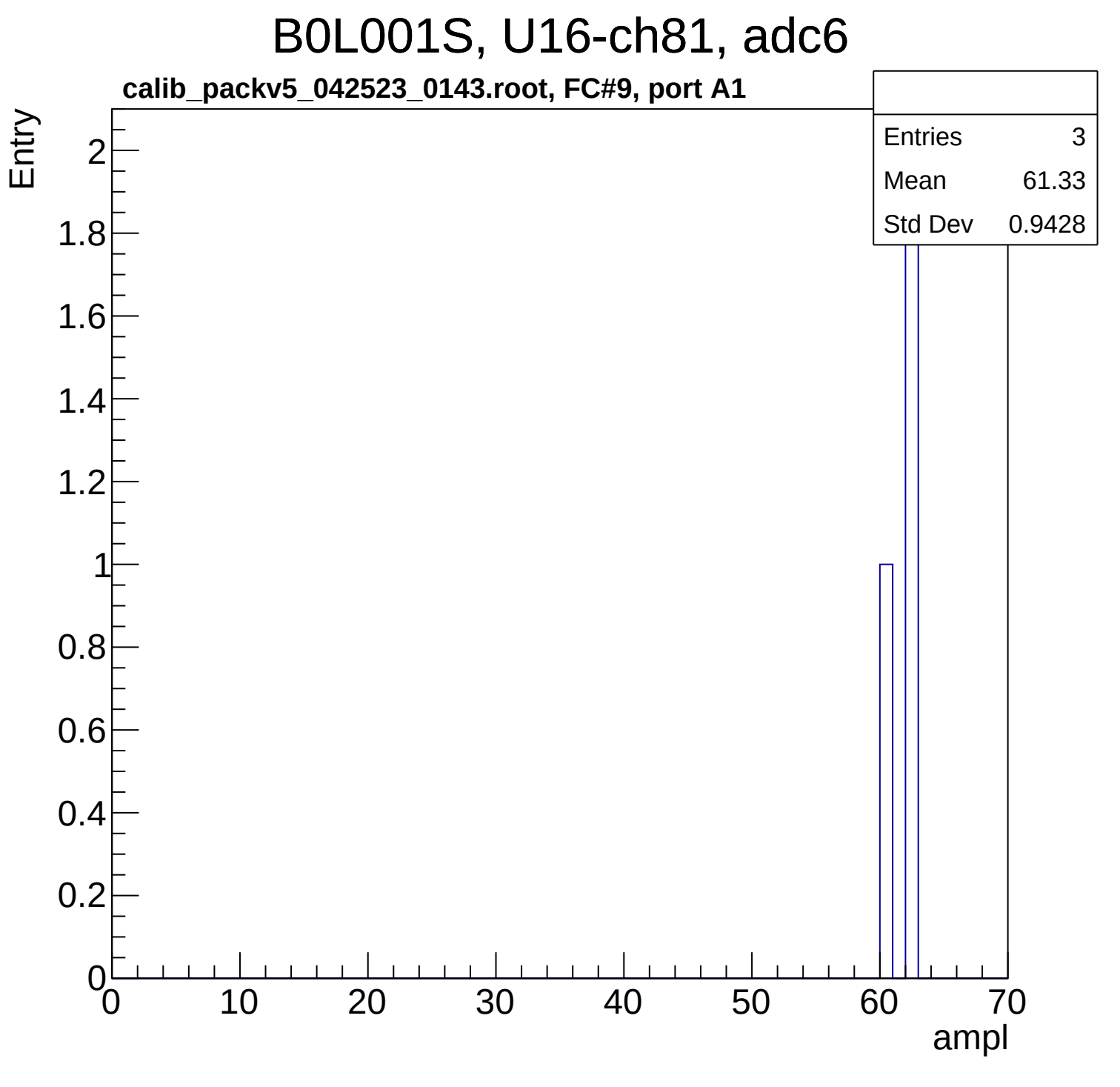
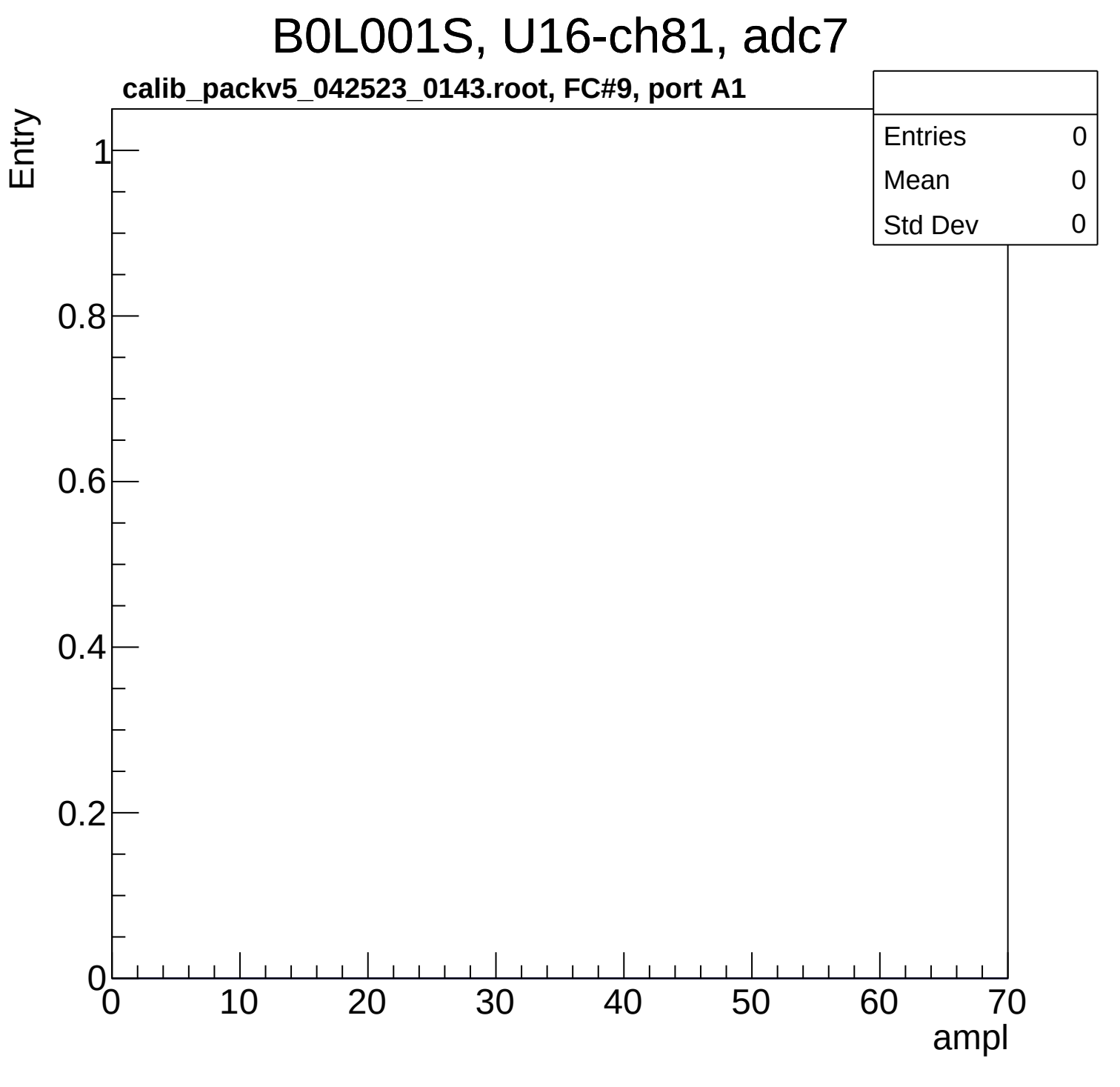
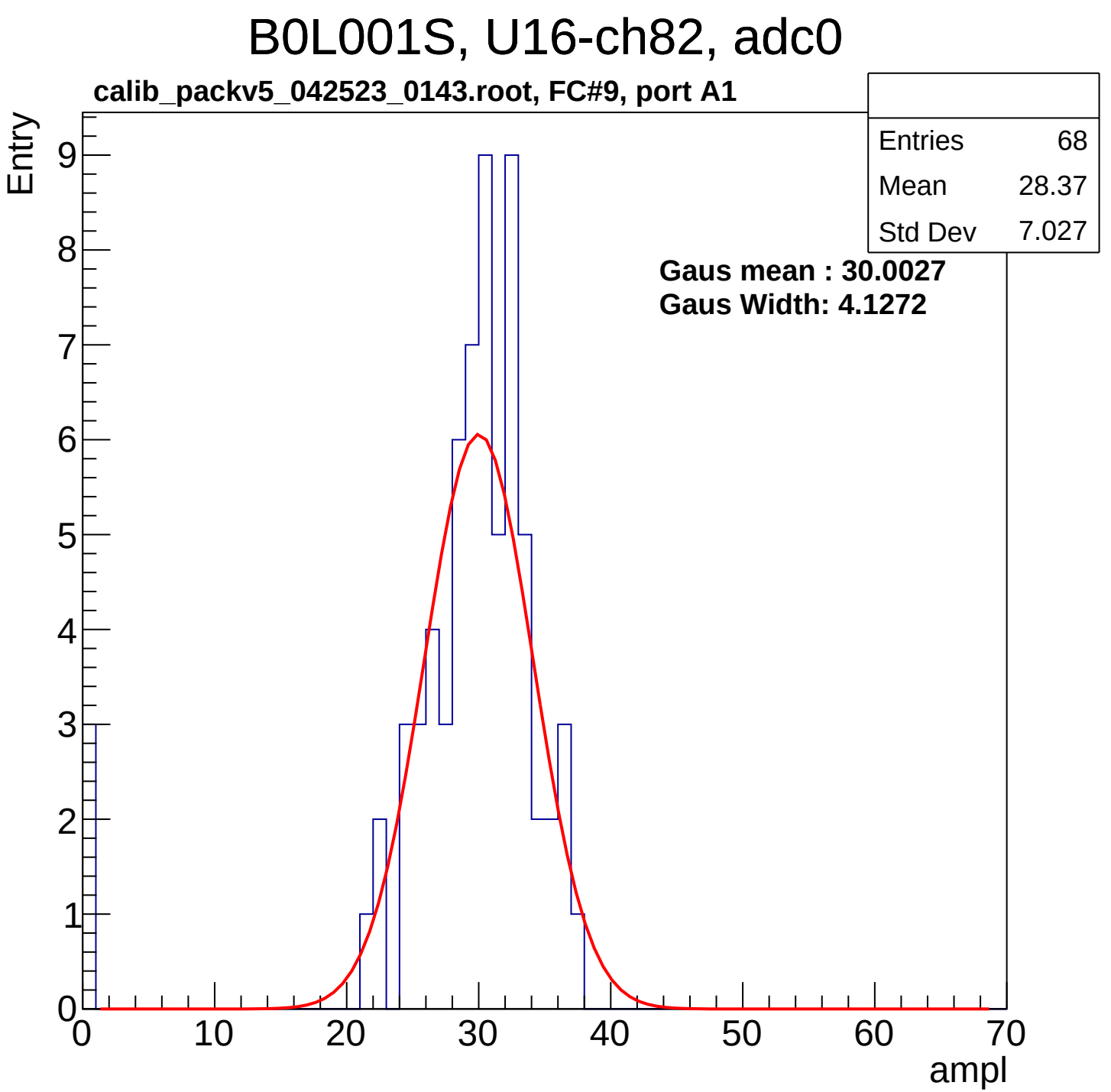

B0L001S, U16-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 28.37
Std Dev 7.027
Gaus mean : 30.0027
Gaus Width: 4.1272
Entry
ampl
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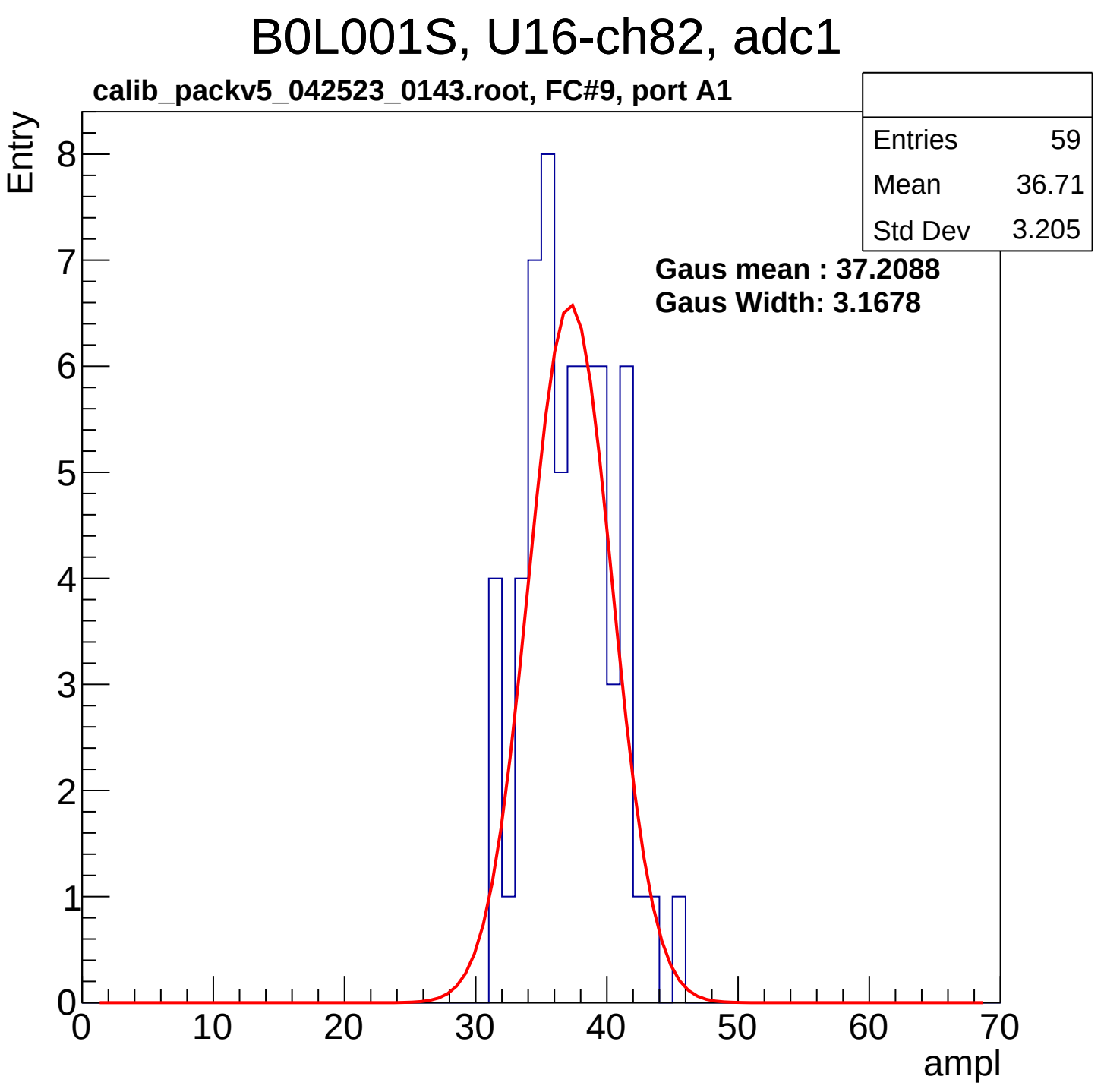
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 36.71
Std Dev 3.205
Gaus mean : 37.2088
Gaus Width: 3.1678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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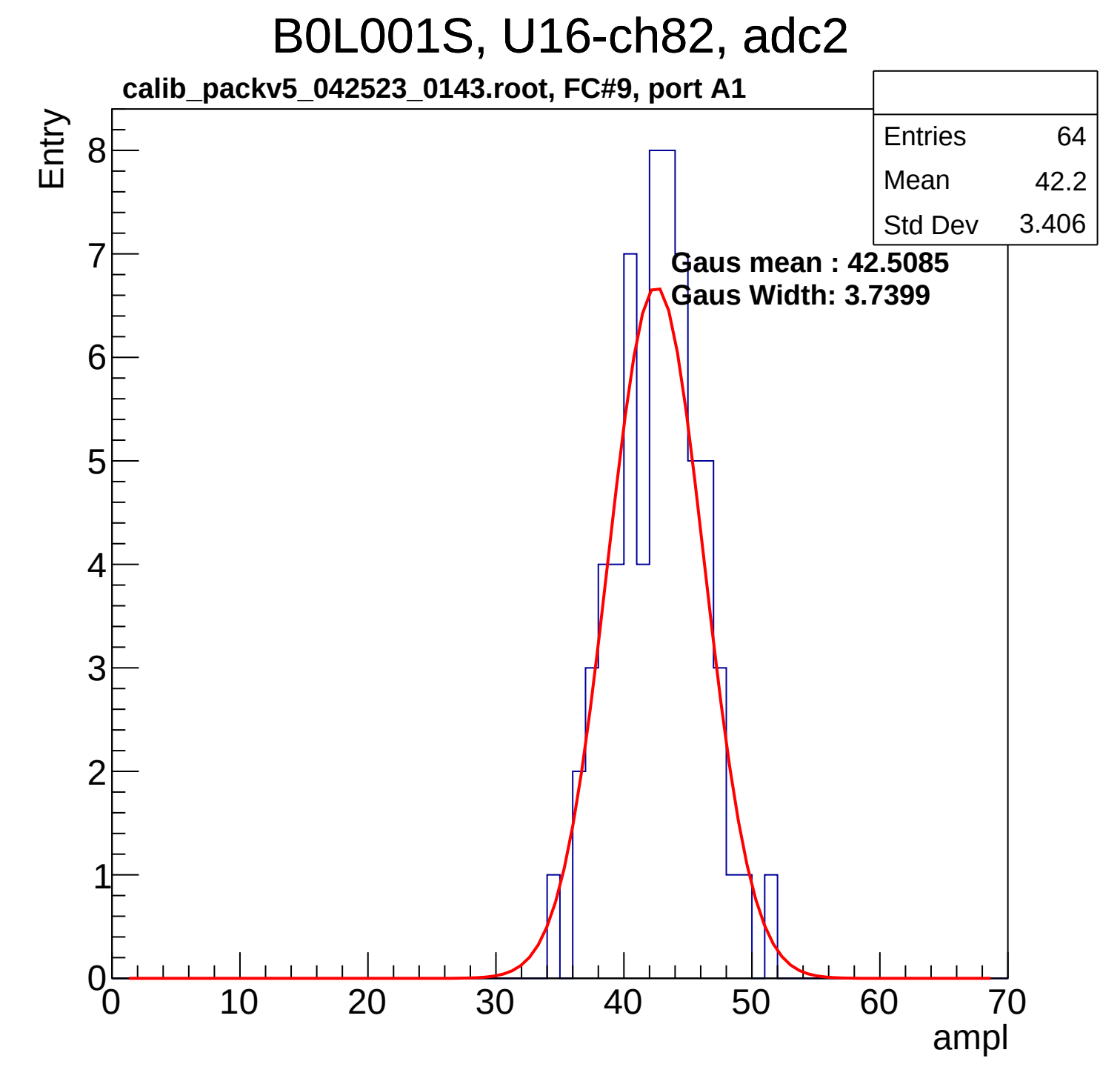
B0L001S, U16-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 42.2
Std Dev 3.406
Gaus mean : 42.5085
Gaus Width: 3.7399
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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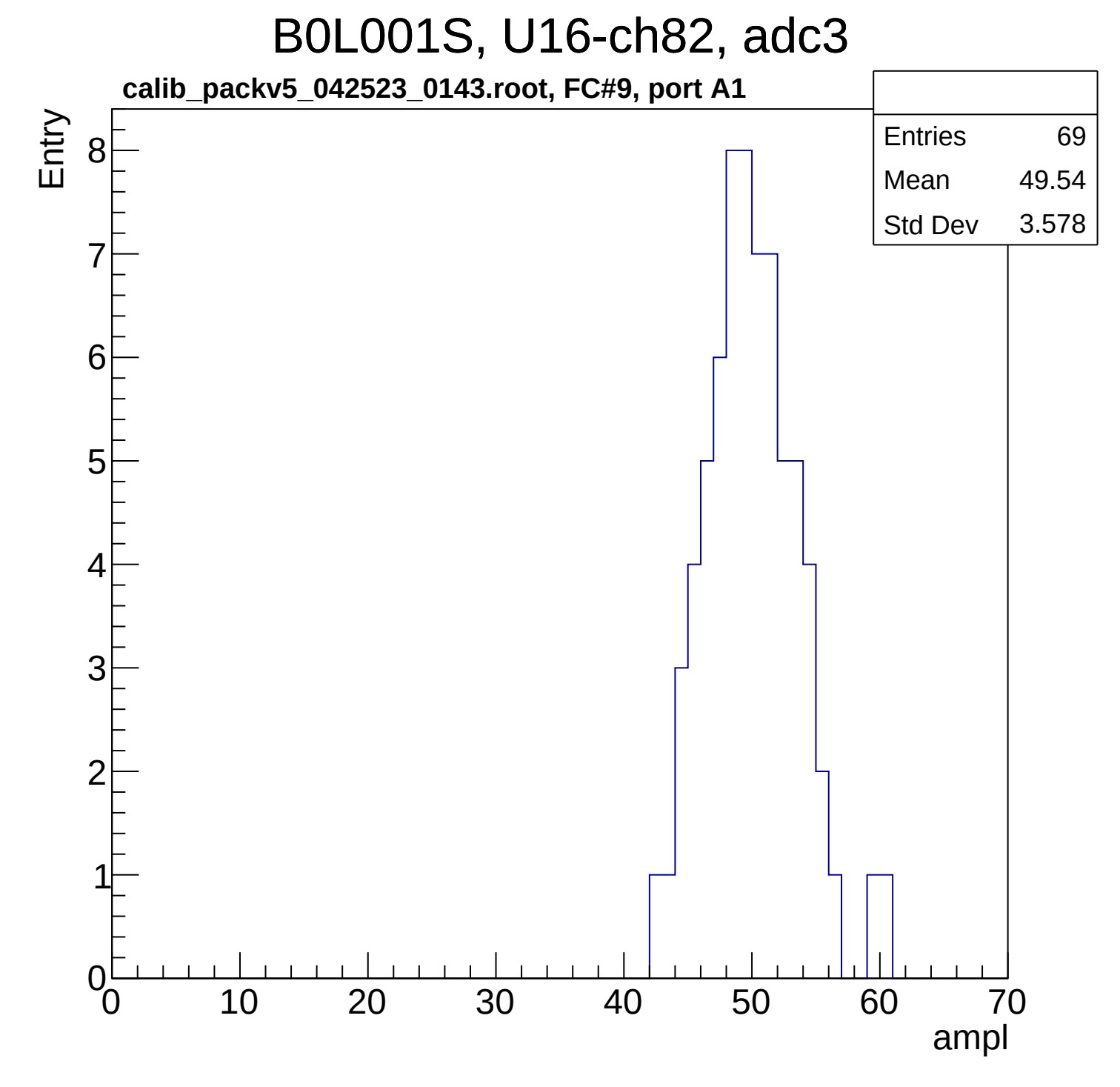
B0L001S, U16-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 49.54
Std Dev 3.578
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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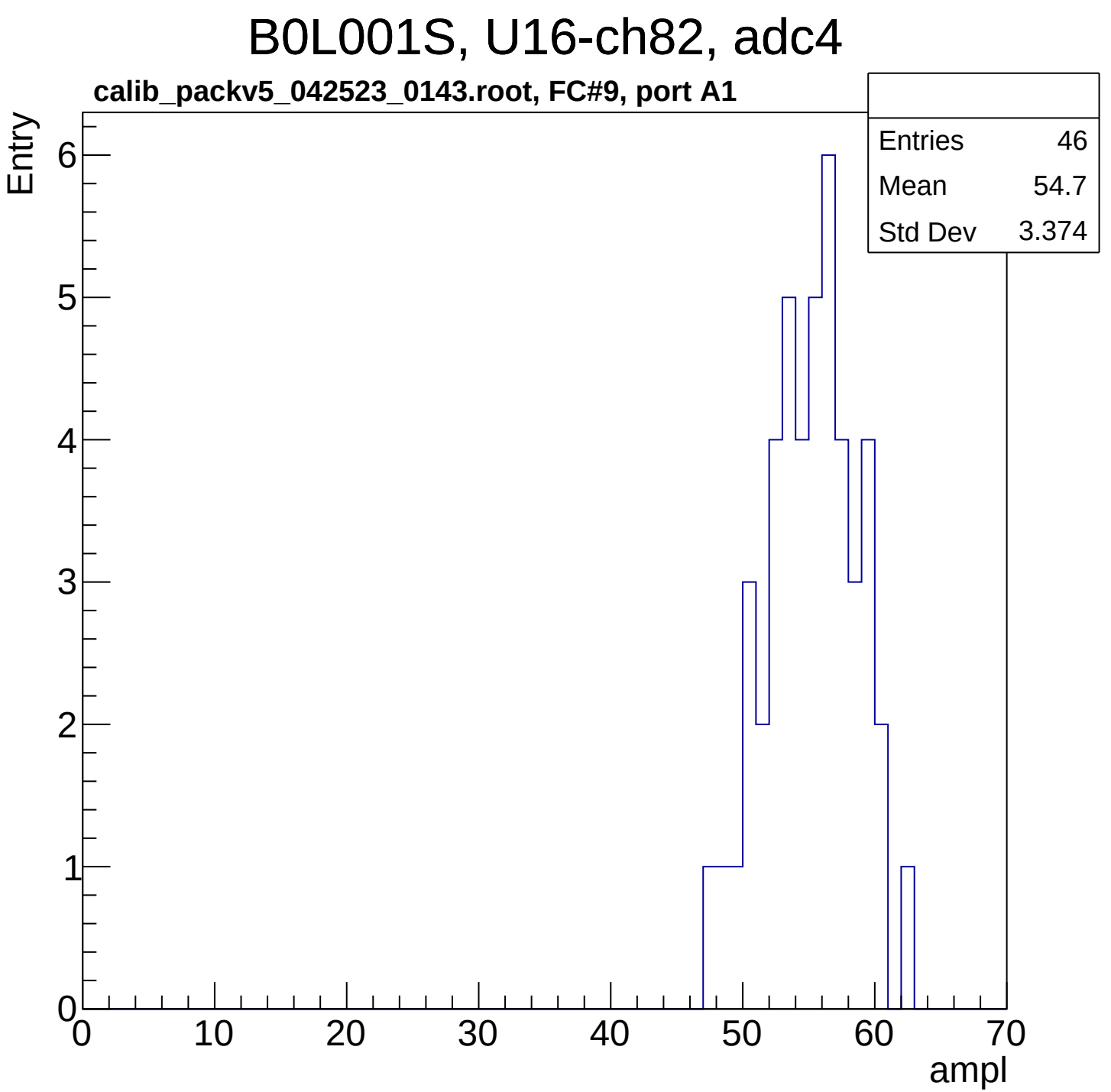
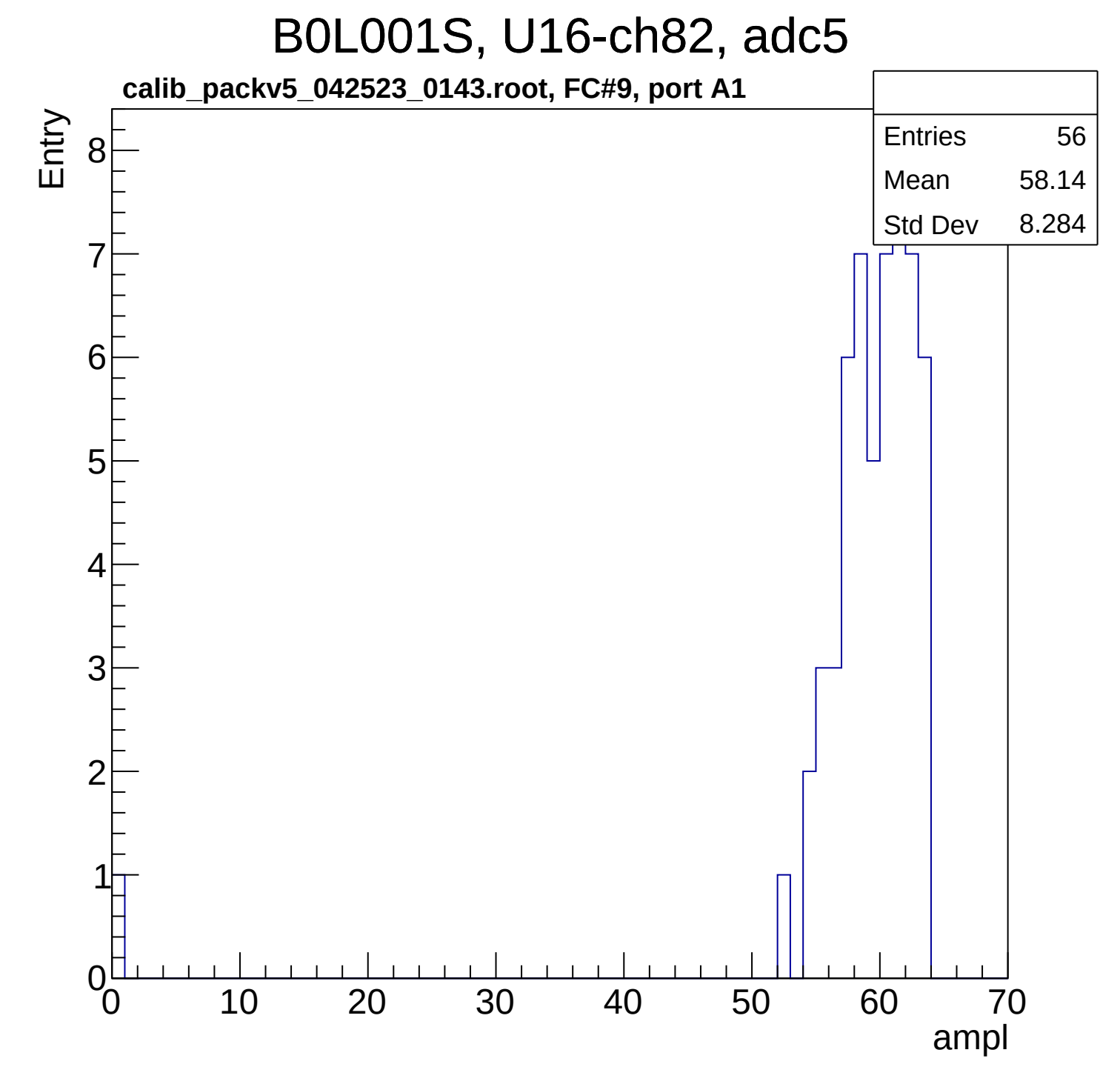

B0L001S, U16-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 54.7
Std Dev 3.374
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 58.14
Std Dev 8.284
Entry
ampl
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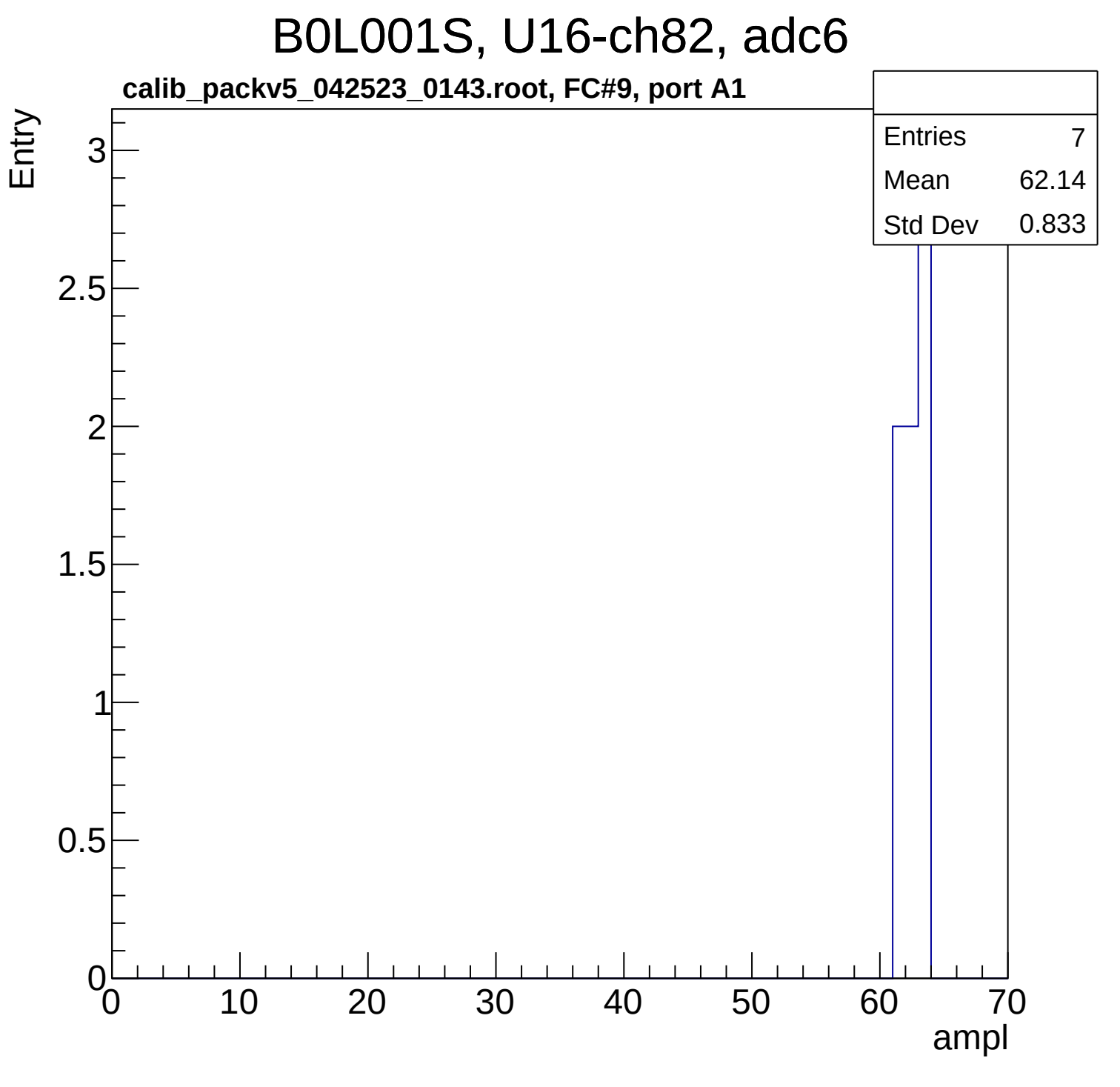
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
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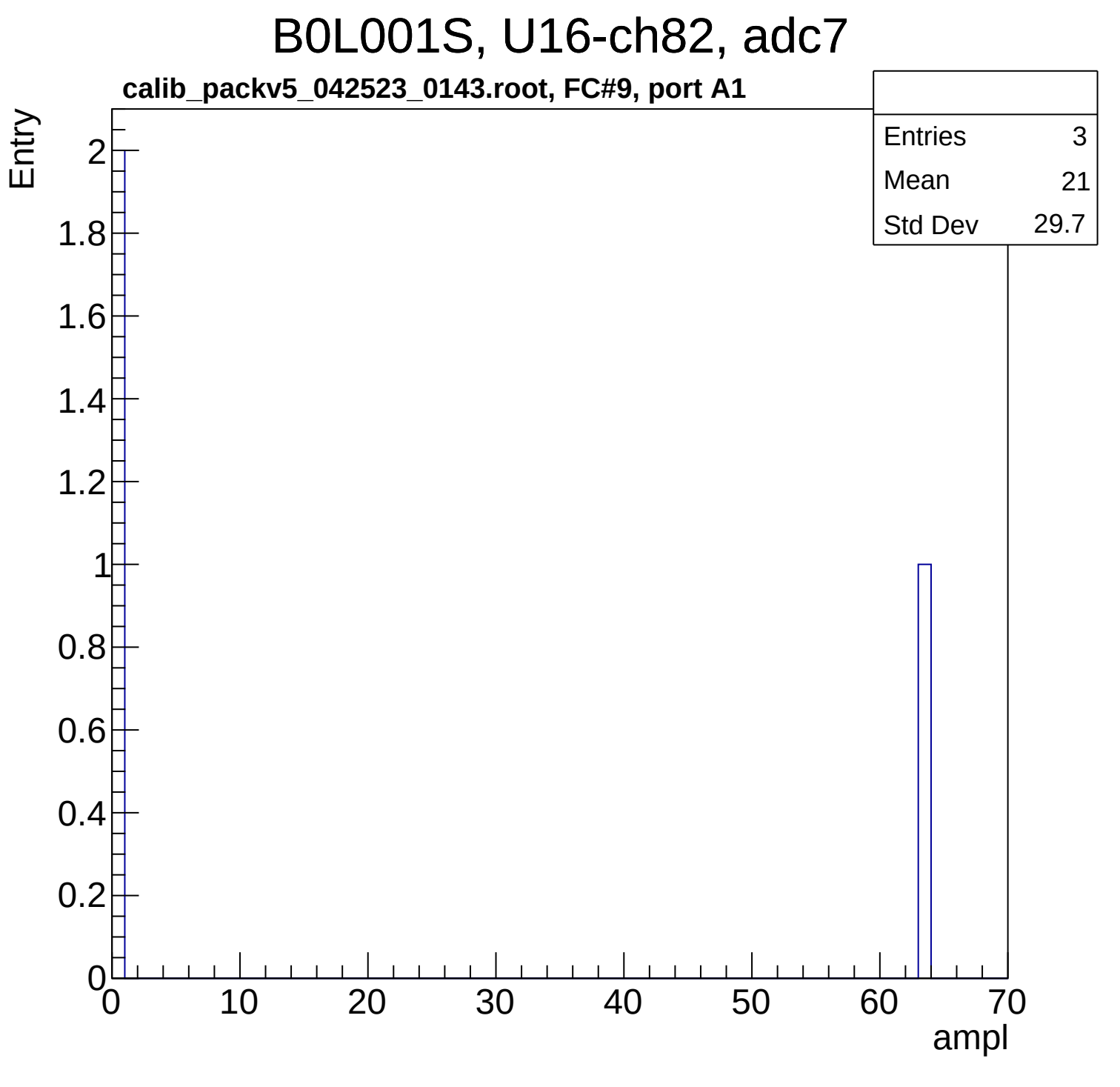
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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30
40
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60
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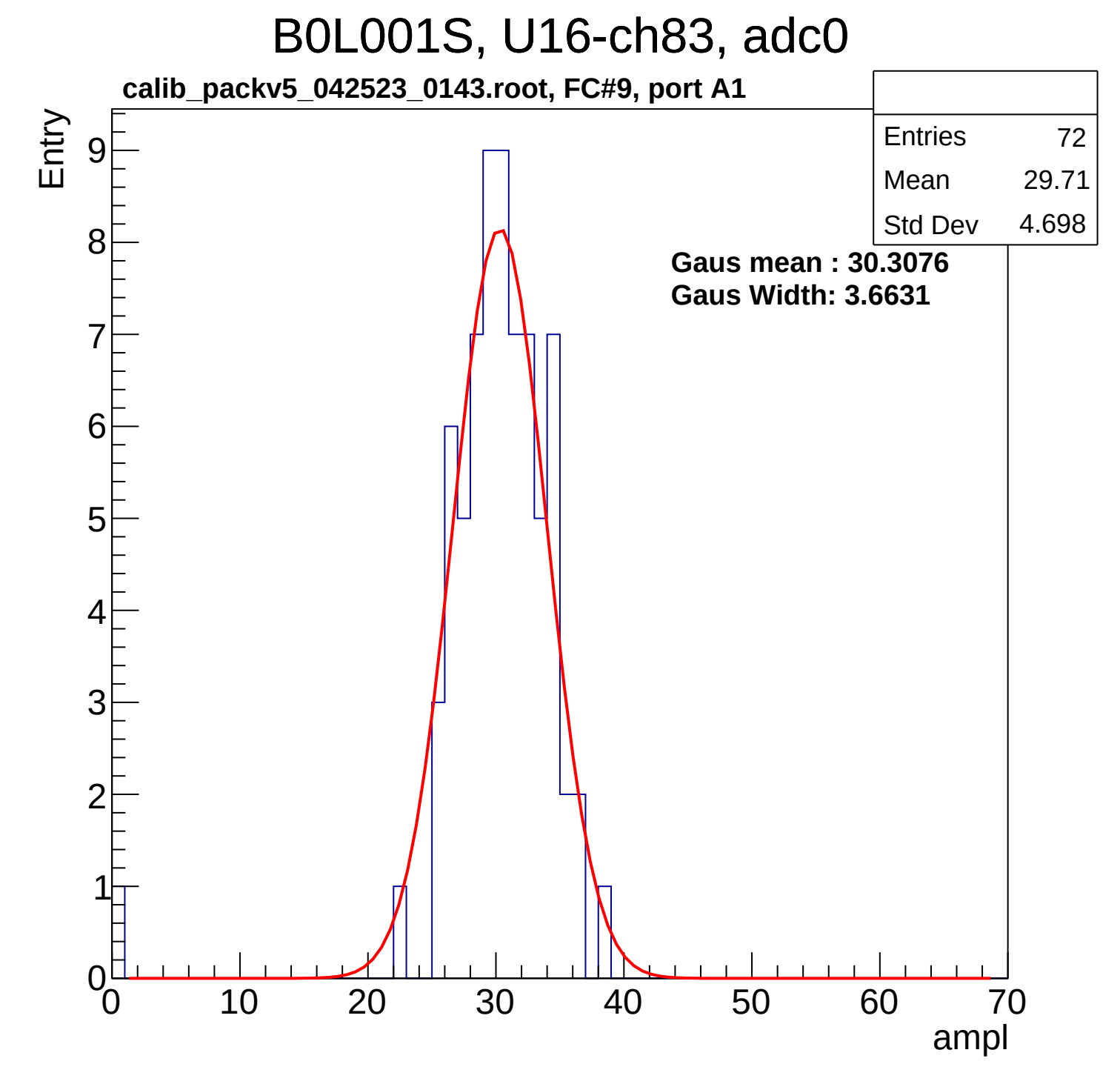
B0L001S, U16-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.71
Std Dev 4.698
Gaus mean : 30.3076
Gaus Width: 3.6631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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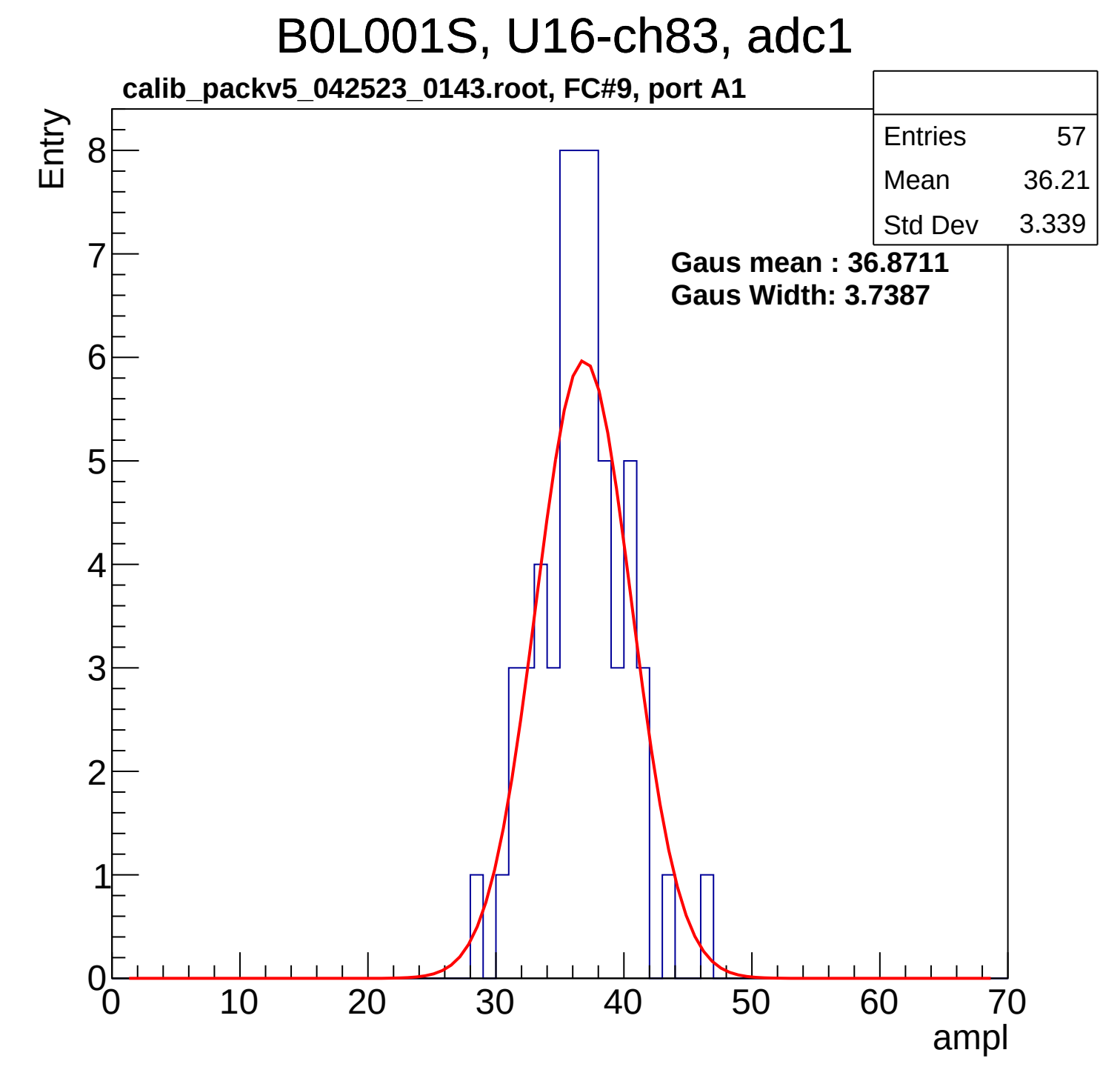
40
50
60
70


B0L001S, U16-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.21
Std Dev 3.339
Gaus mean : 36.8711
Gaus Width: 3.7387
Entry
ampl
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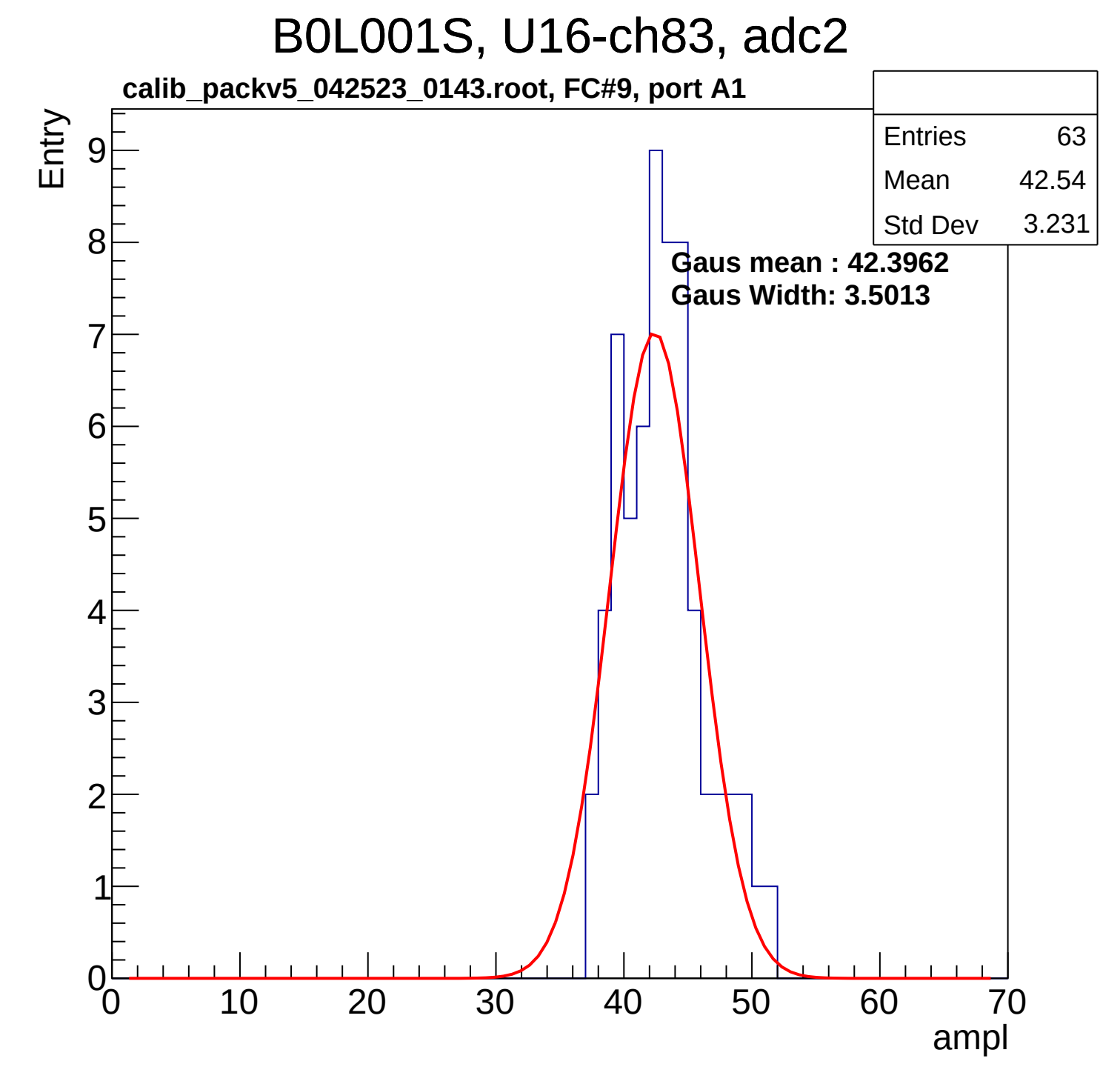
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.54
Std Dev 3.231
Gaus mean : 42.3962
Gaus Width: 3.5013
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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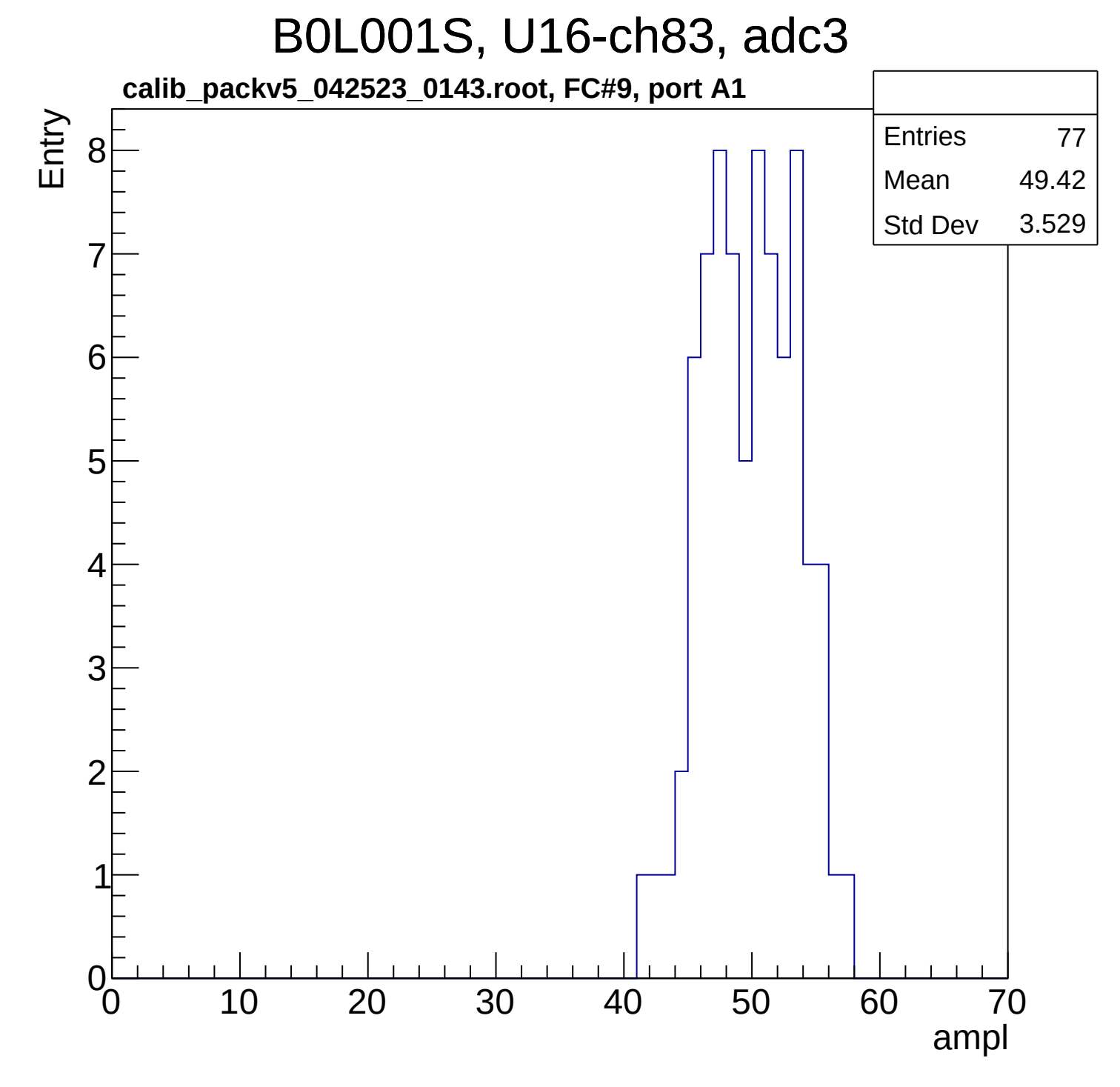
B0L001S, U16-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.42
Std Dev 3.529
Entry
ampl
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2
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4
5
6
7
8
0
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20
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40
50
60
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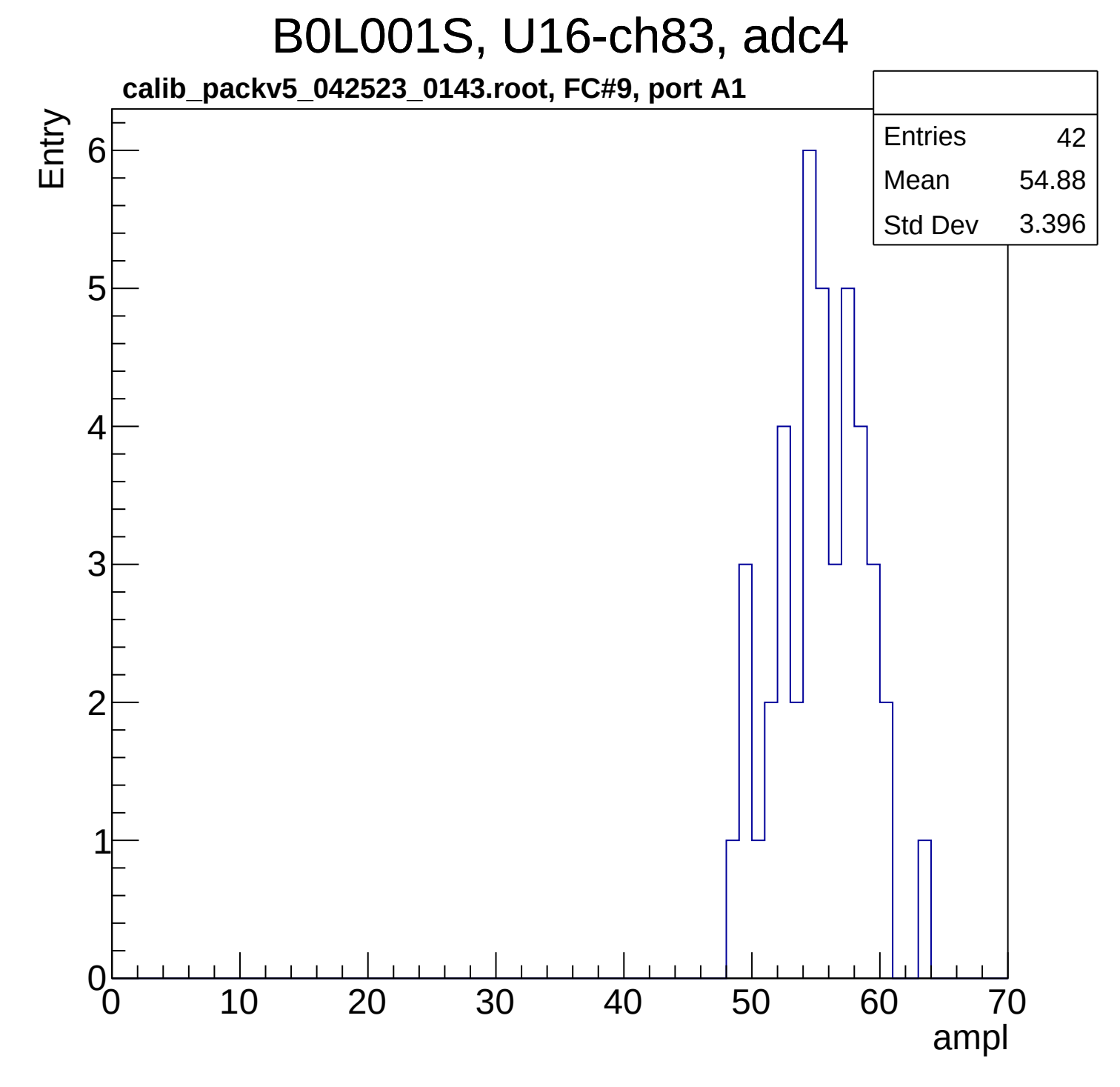
B0L001S, U16-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 54.88
Std Dev 3.396
Entry
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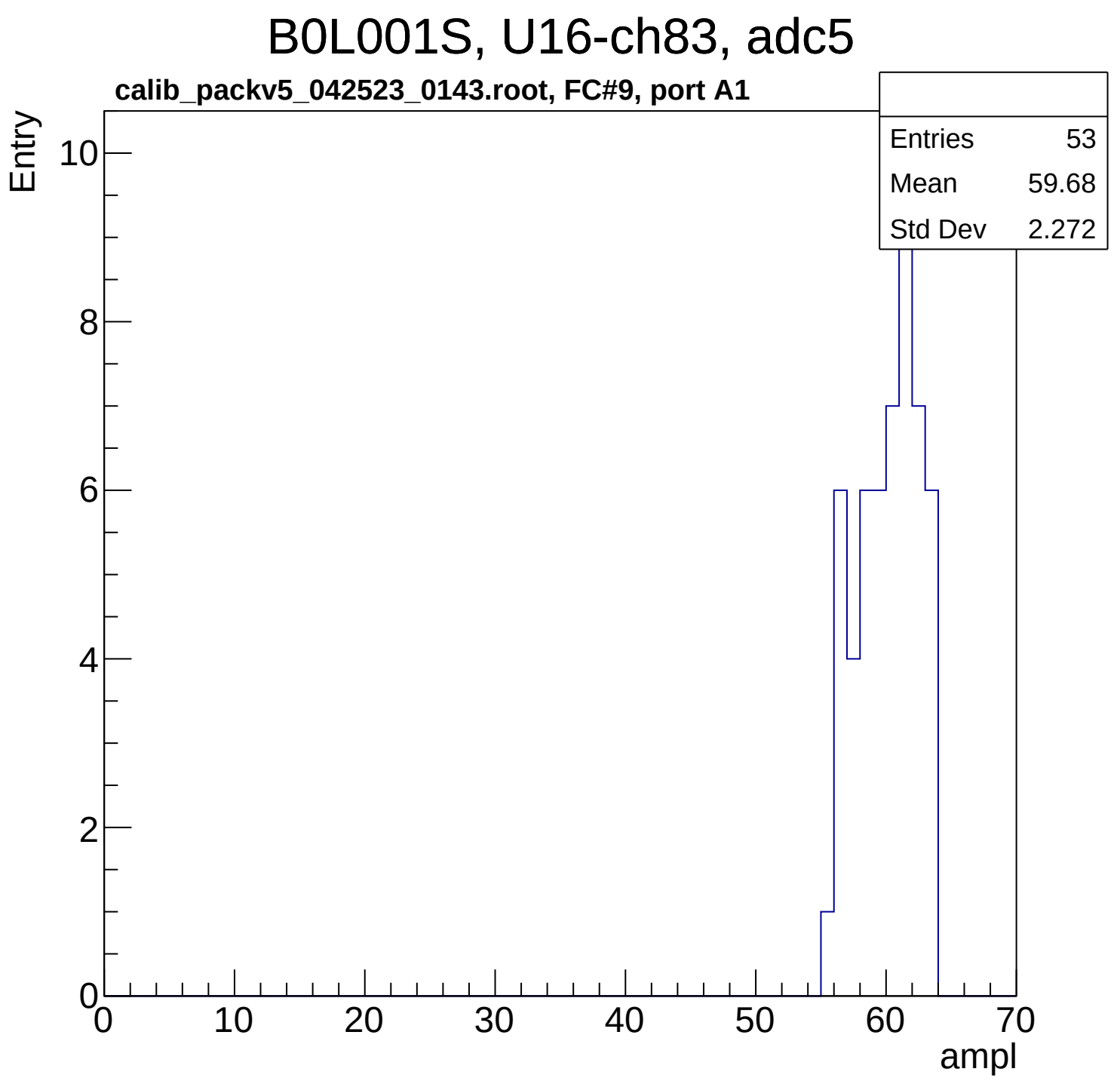
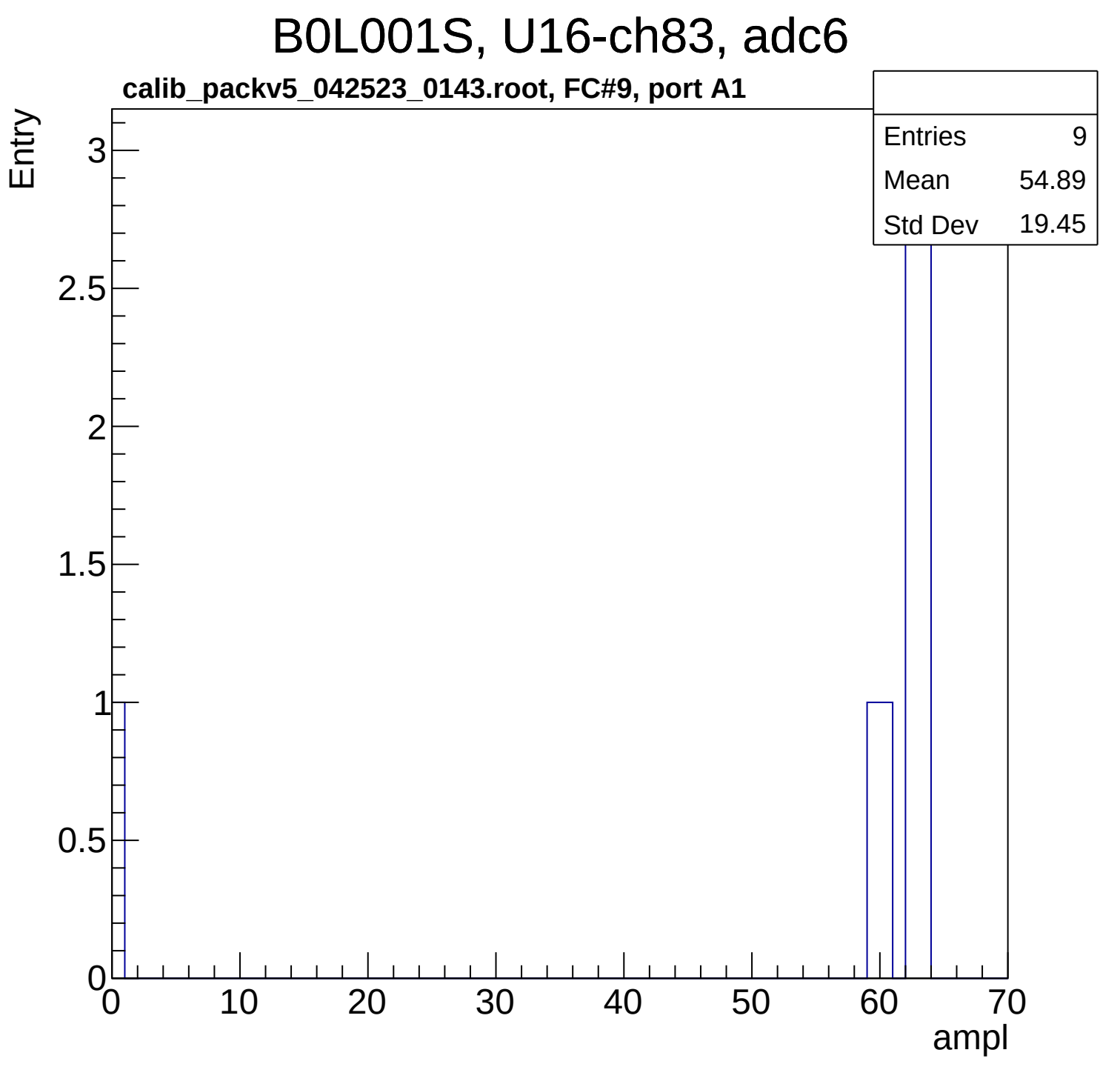
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 59.68
Std Dev 2.272
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.89
Std Dev 19.45
Entry
ampl
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0.5
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1.5
2
2.5
3
0
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20
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40
50
60
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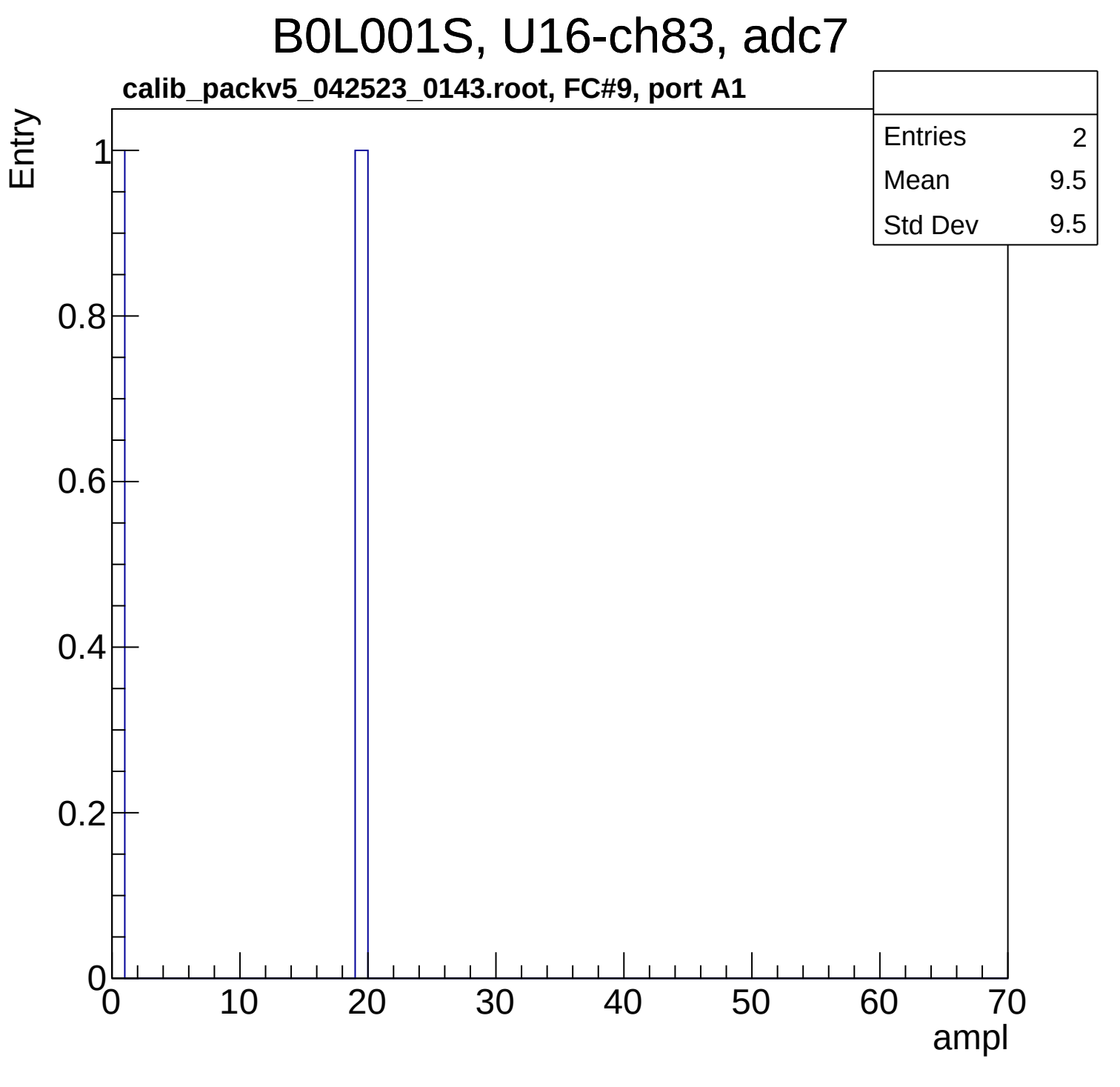
B0L001S, U16-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
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0.6
0.4
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10
20
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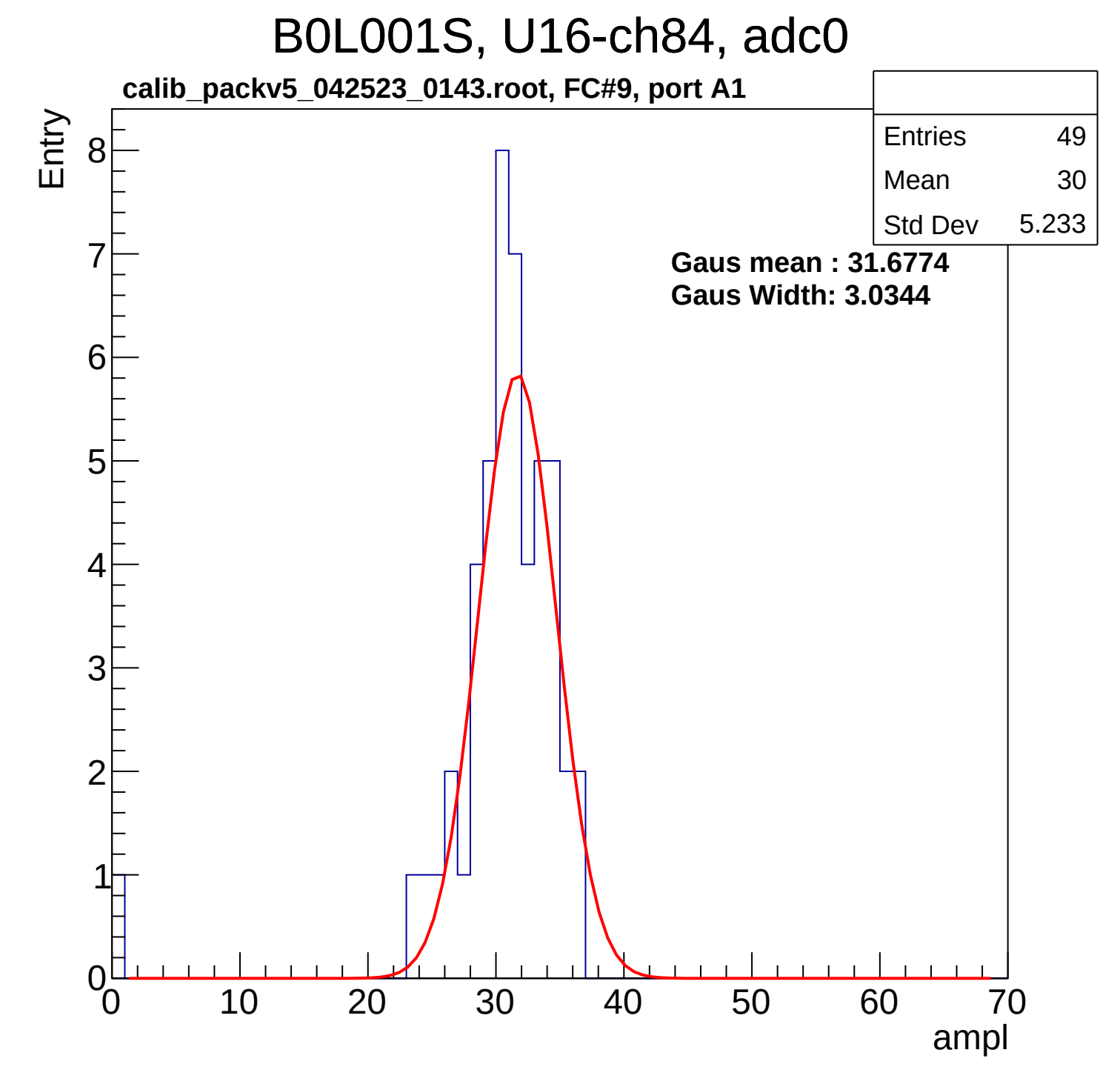
40
50
60
70
ampl


B0L001S, U16-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 30
Std Dev 5.233
Gaus mean : 31.6774
Gaus Width: 3.0344
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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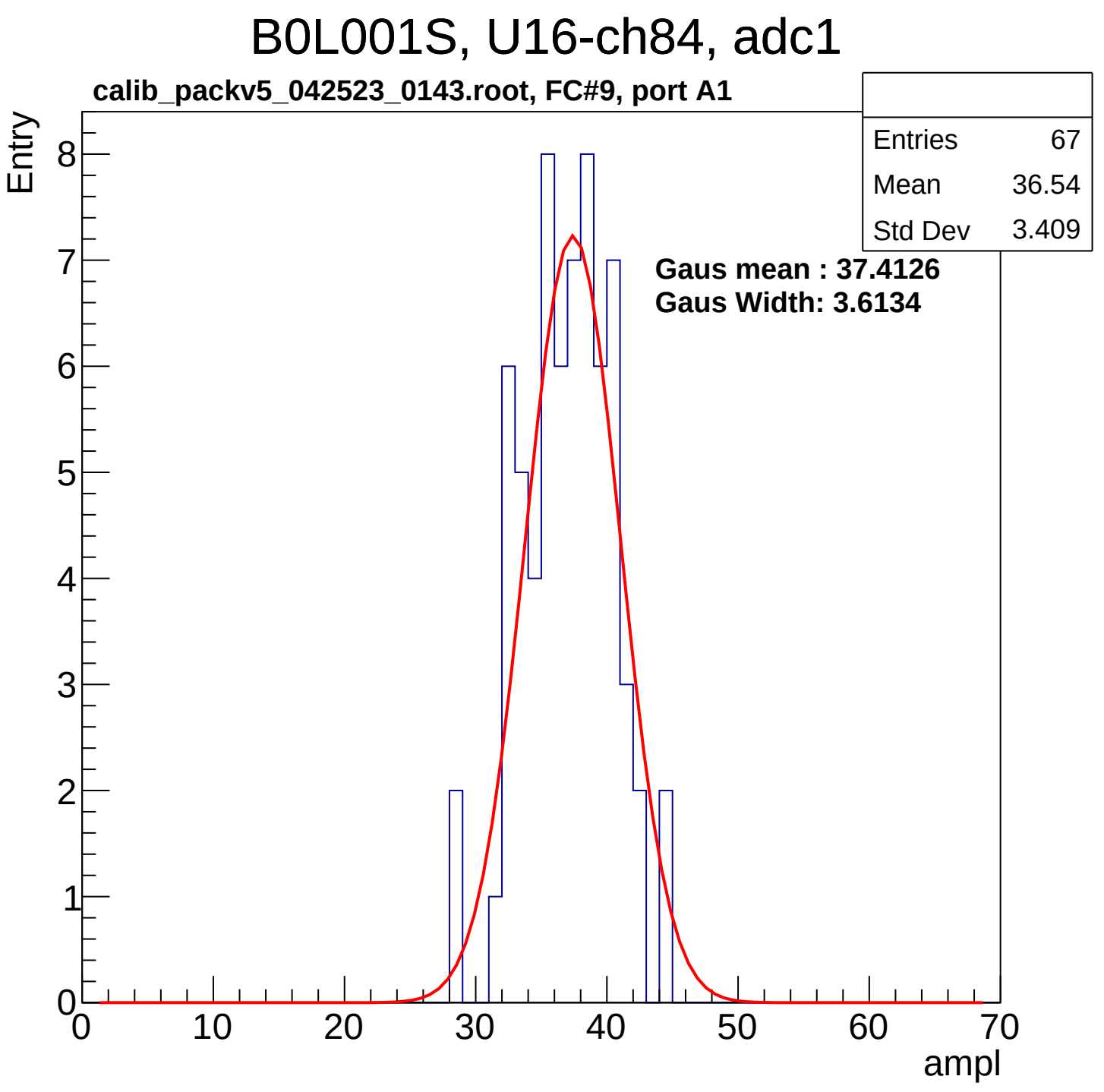
B0L001S, U16-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.54
Std Dev 3.409
Gaus mean : 37.4126
Gaus Width: 3.6134
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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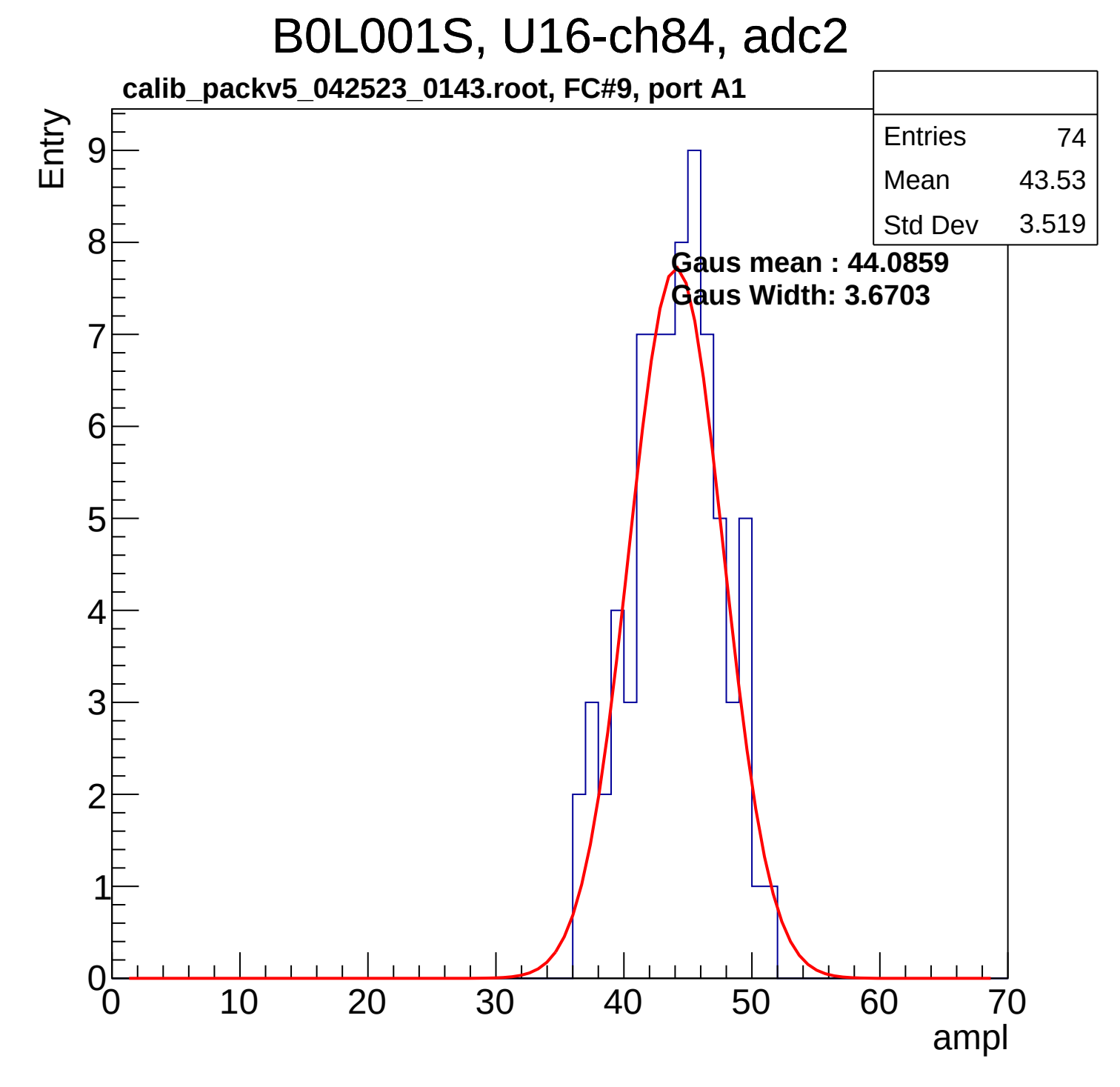
B0L001S, U16-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.53
Std Dev 3.519
Gaus mean : 44.0859
Gaus Width: 3.6703
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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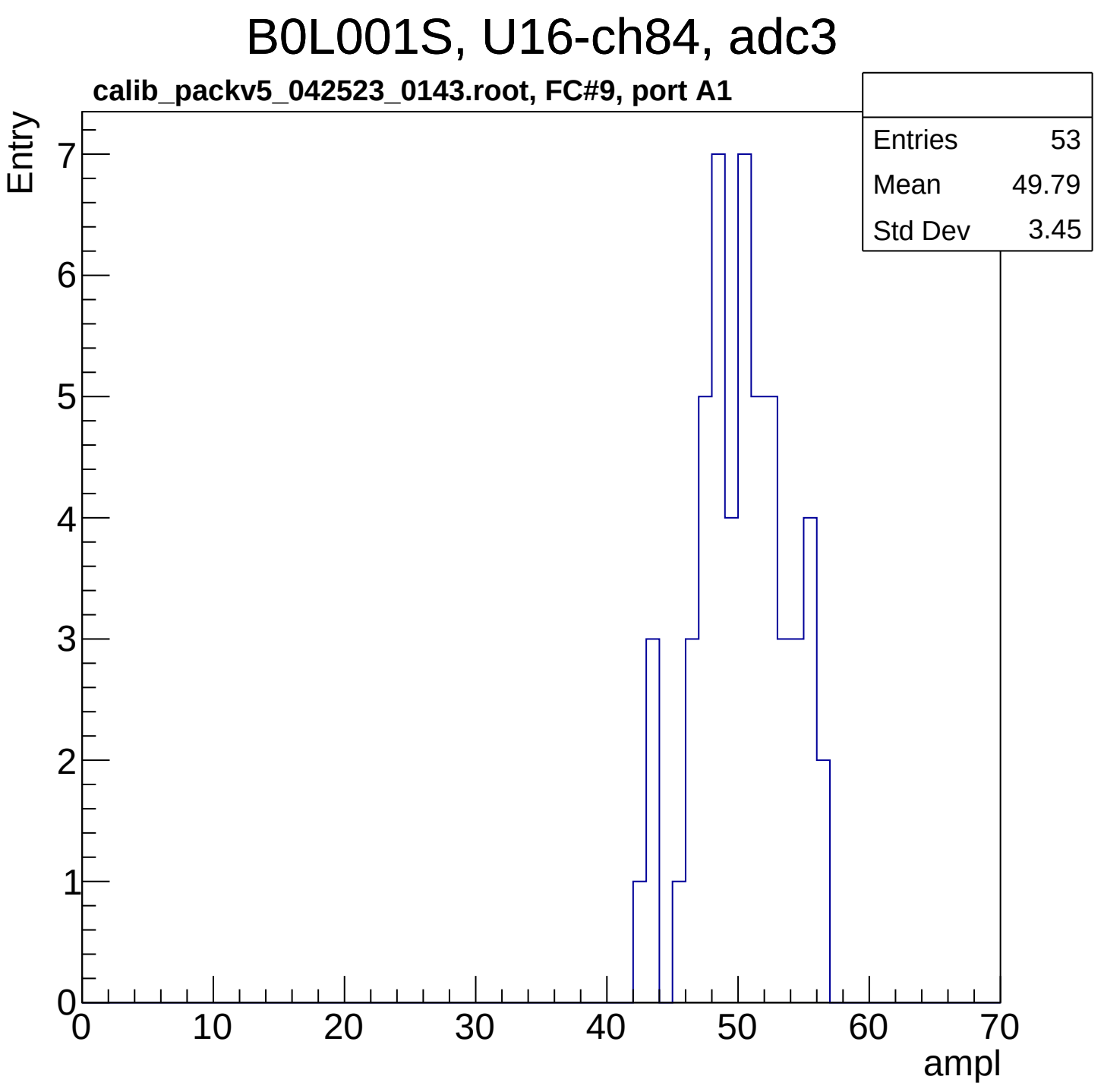
60
70


B0L001S, U16-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 49.79
Std Dev 3.45
Entry
ampl
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60
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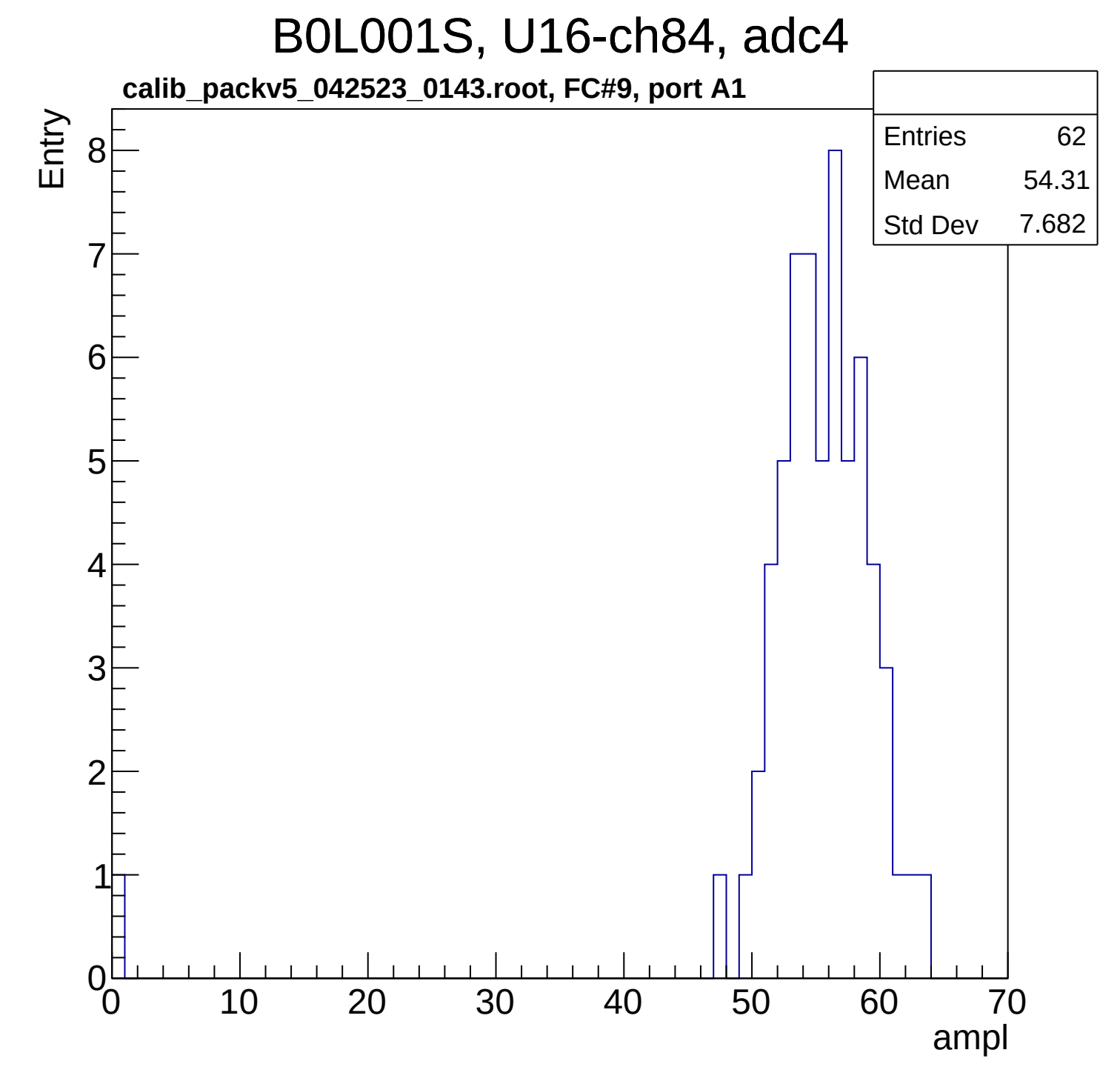
B0L001S, U16-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 54.31
Std Dev 7.682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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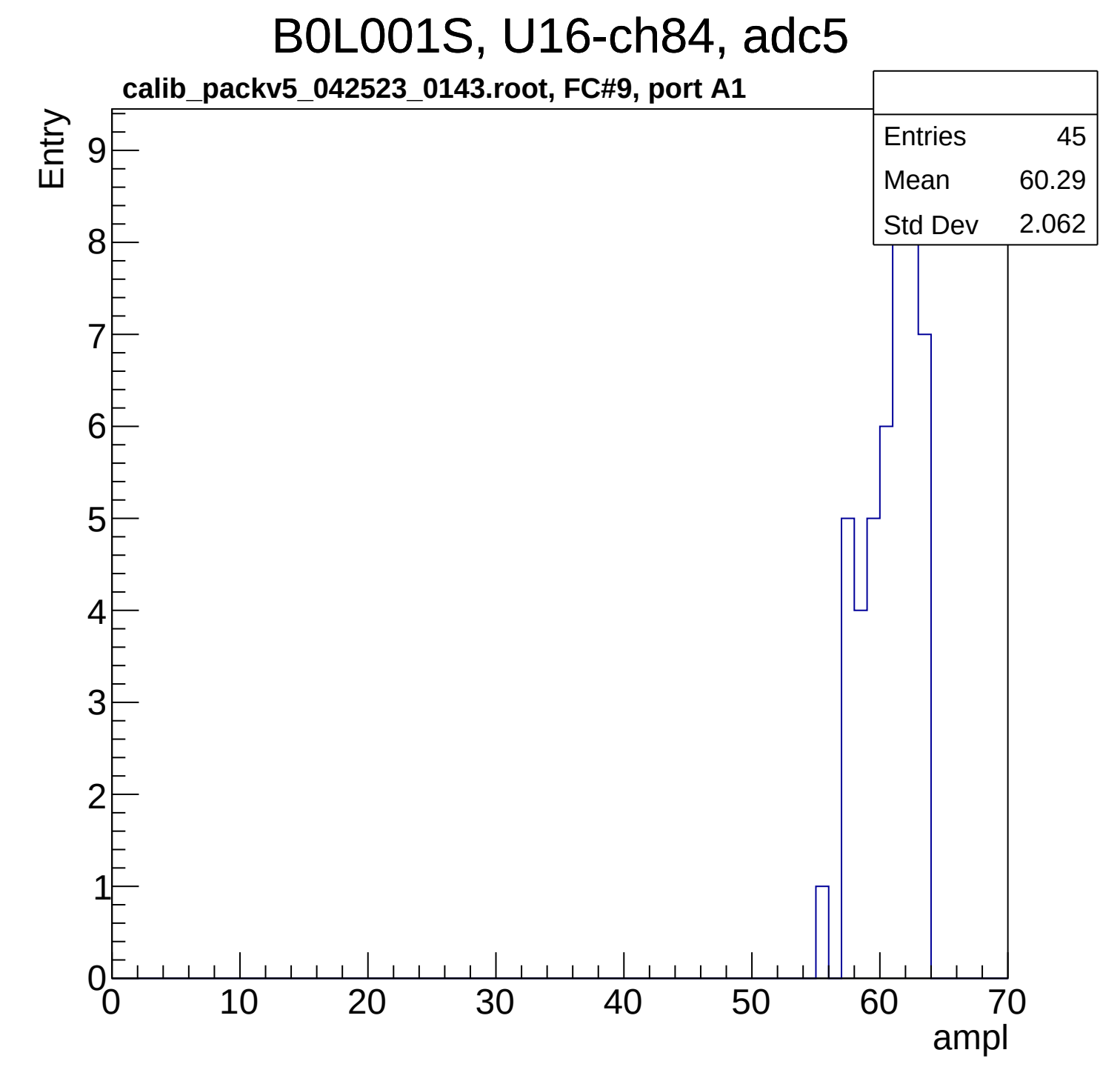
40
50
60
70


B0L001S, U16-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 60.29
Std Dev 2.062
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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60
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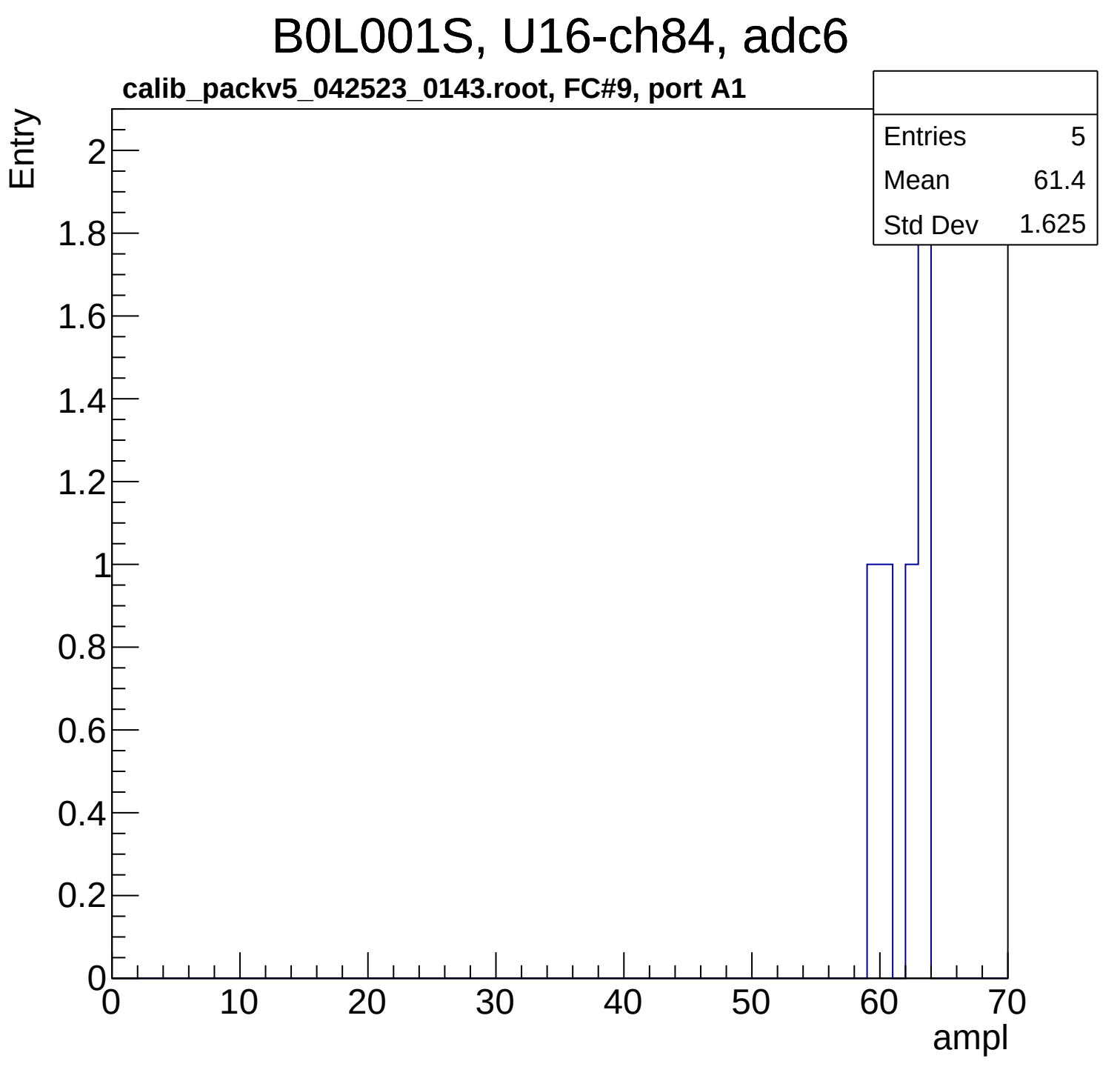
B0L001S, U16-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.625
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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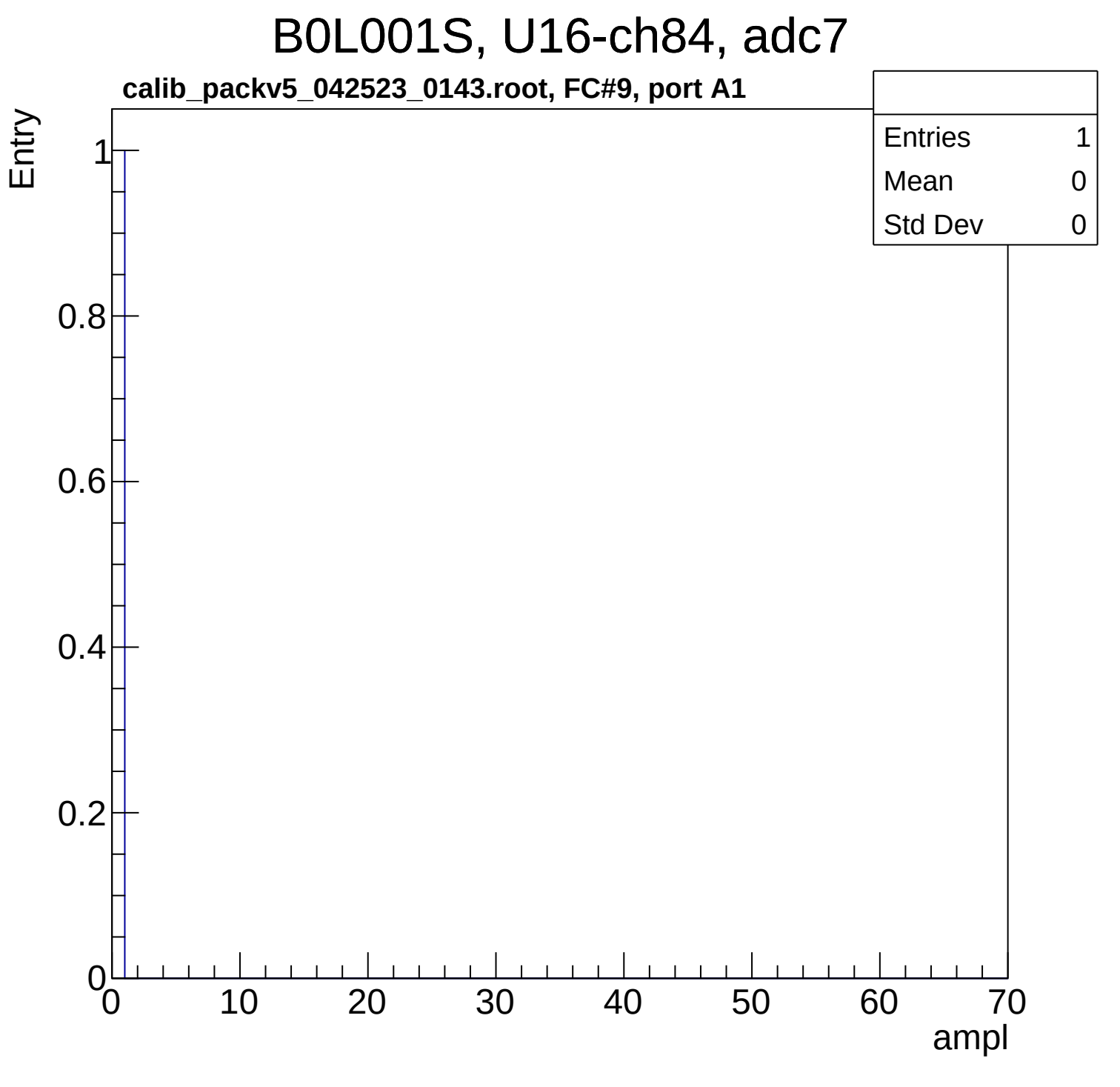
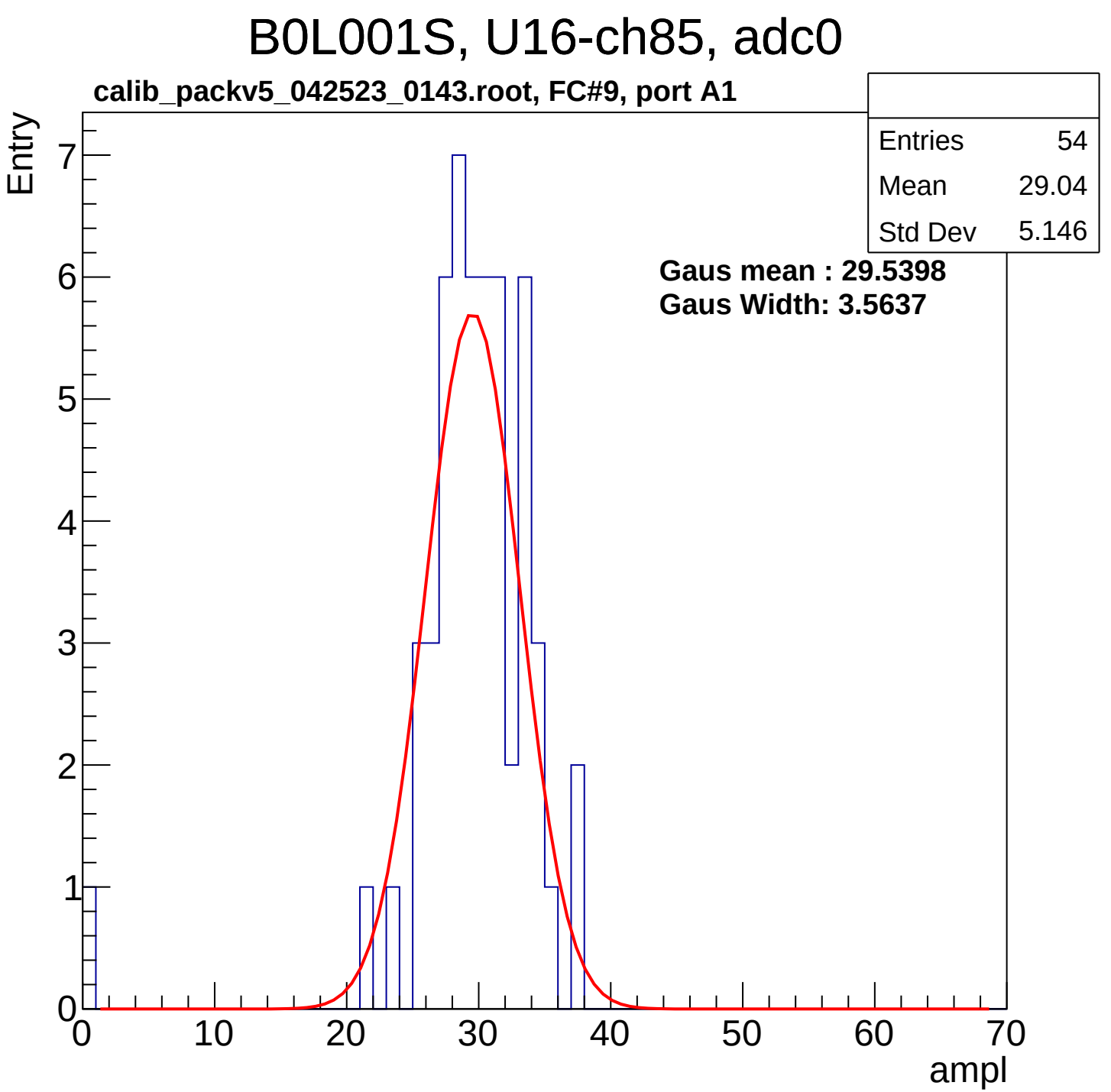

B0L001S, U16-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 29.04
Std Dev 5.146
Gaus mean : 29.5398
Gaus Width: 3.5637
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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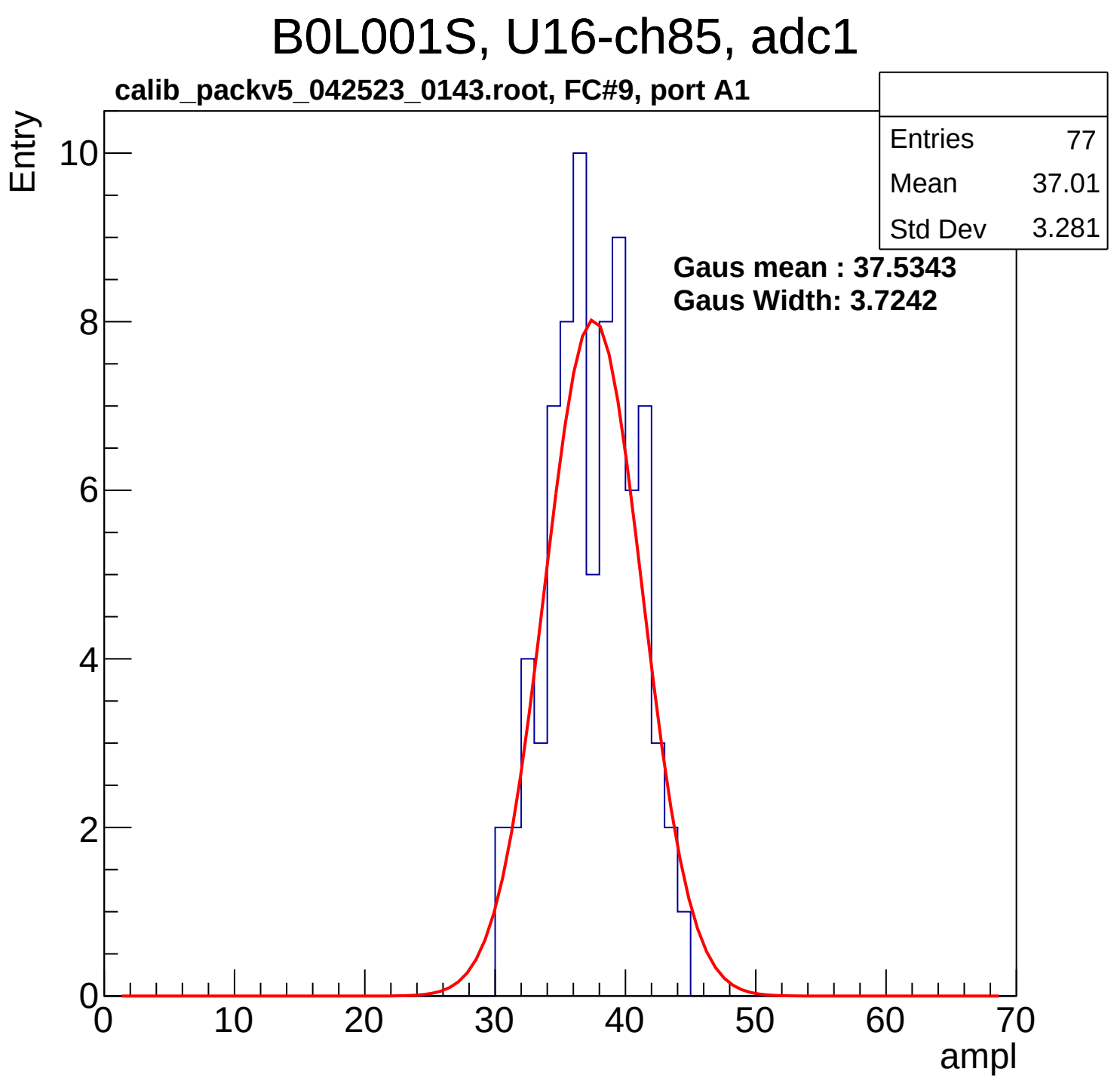
B0L001S, U16-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.01
Std Dev 3.281
Gaus mean : 37.5343
Gaus Width: 3.7242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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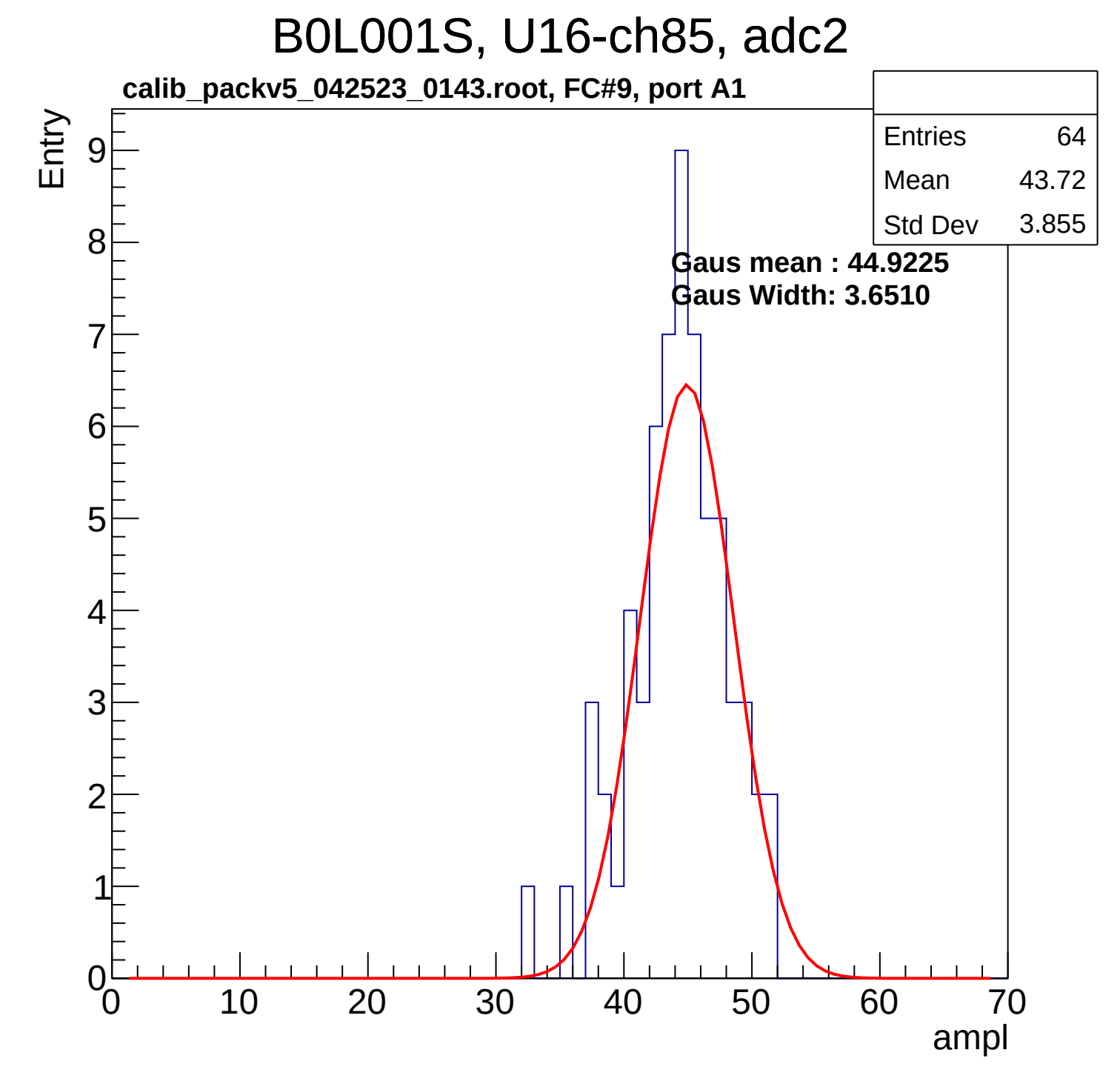
B0L001S, U16-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.72
Std Dev 3.855
Gaus mean : 44.9225
Gaus Width: 3.6510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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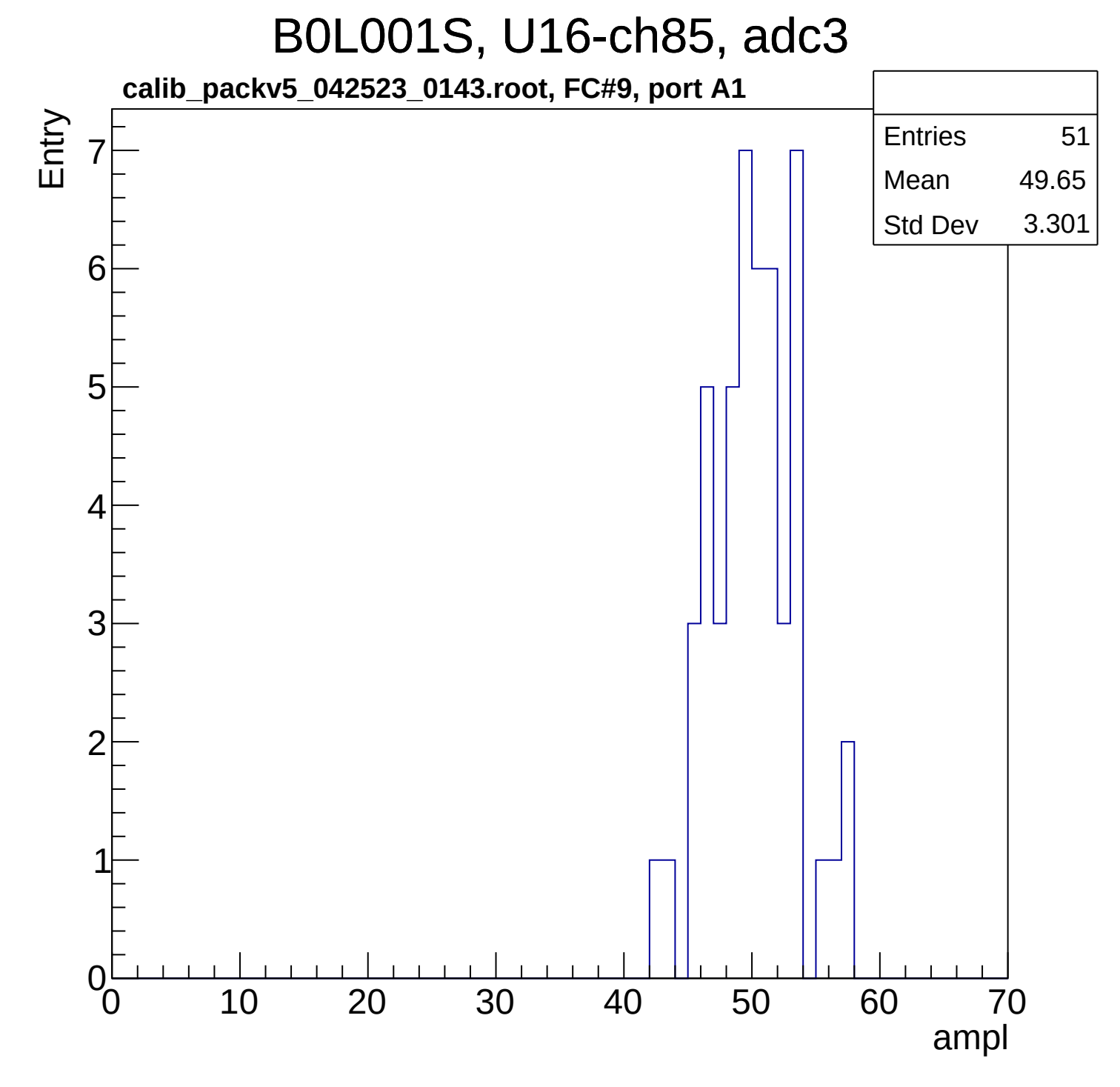
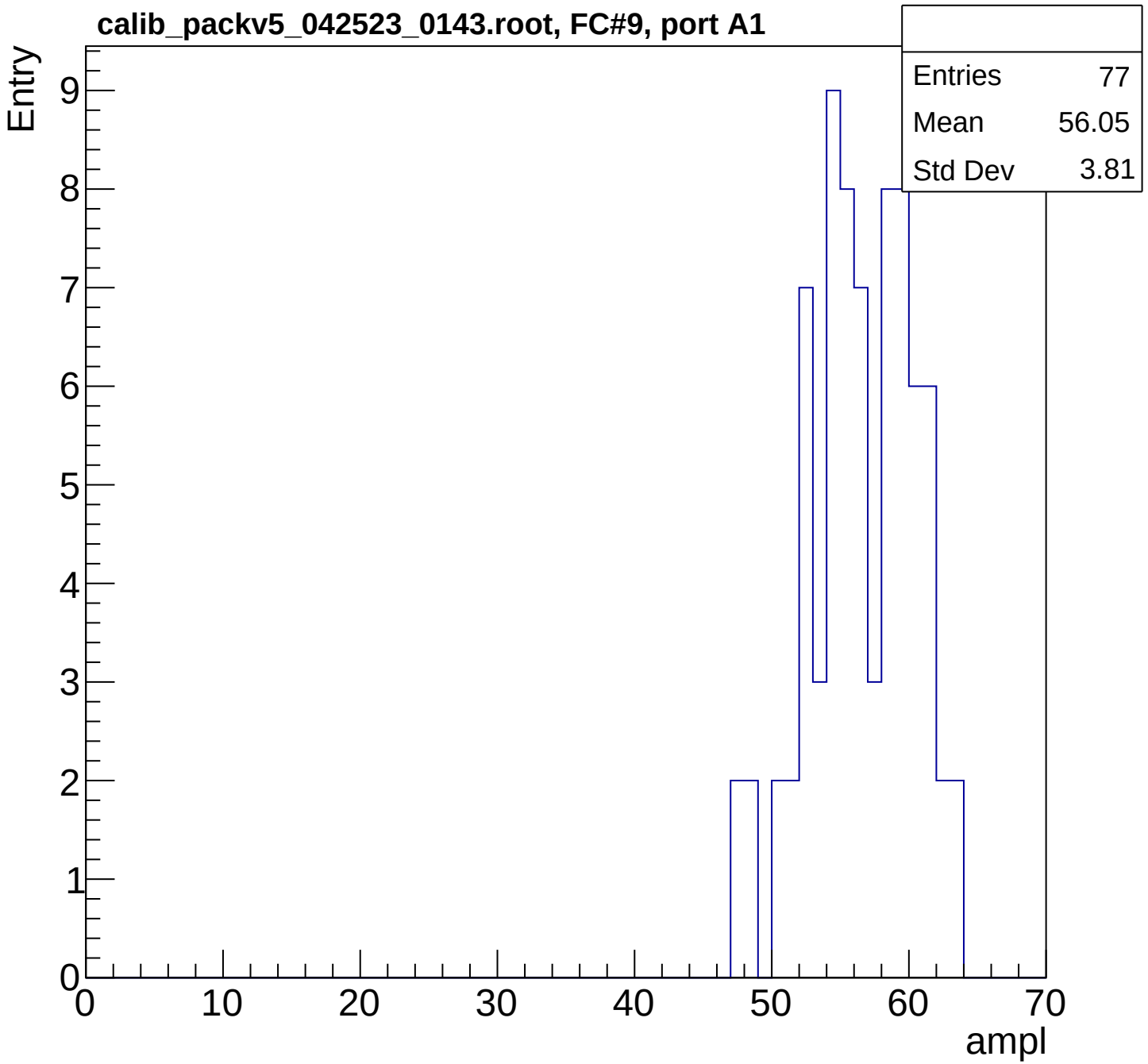

B0L001S, U16-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 49.65
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 56.05
Std Dev 3.81
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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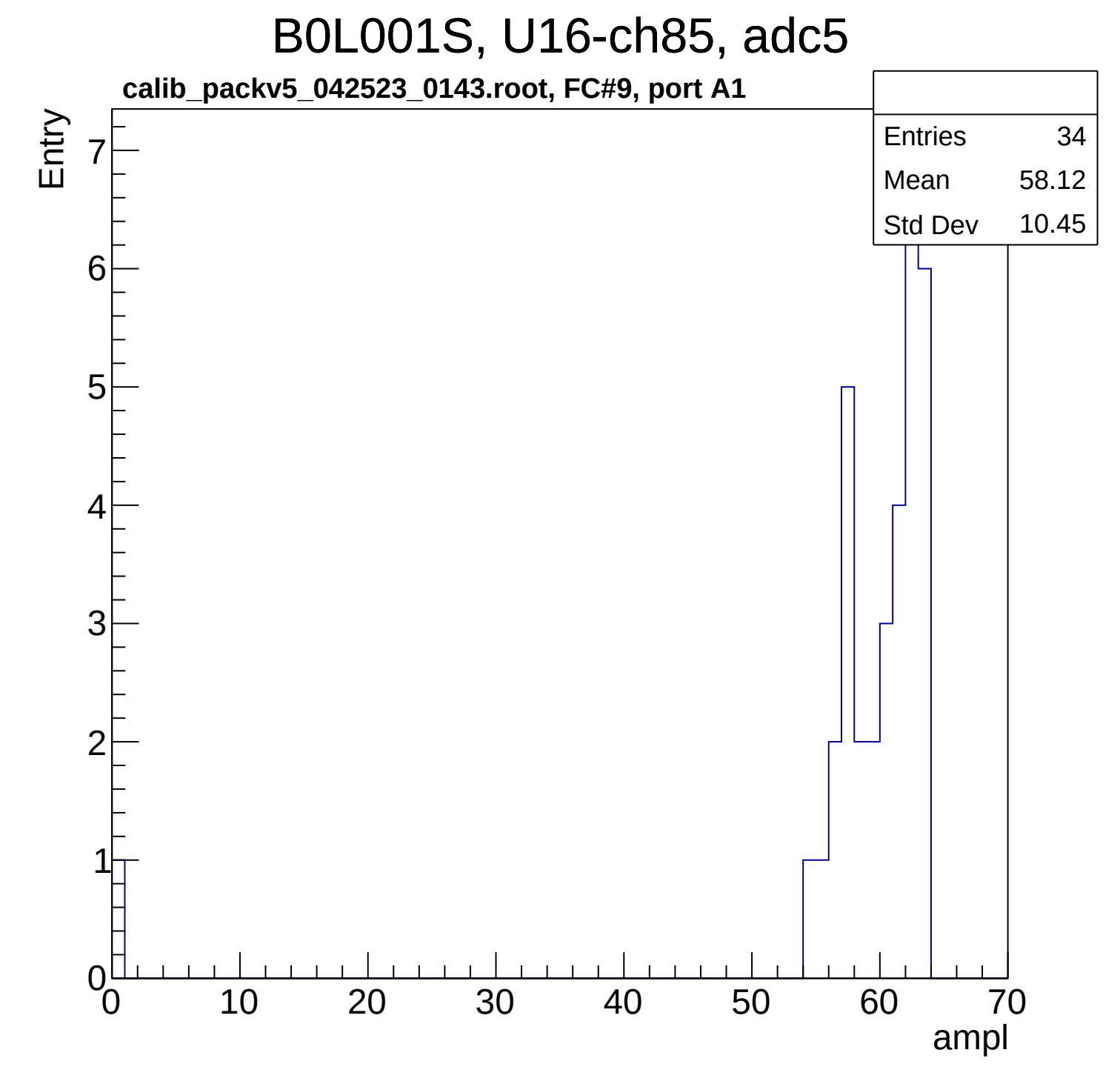
B0L001S, U16-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.12
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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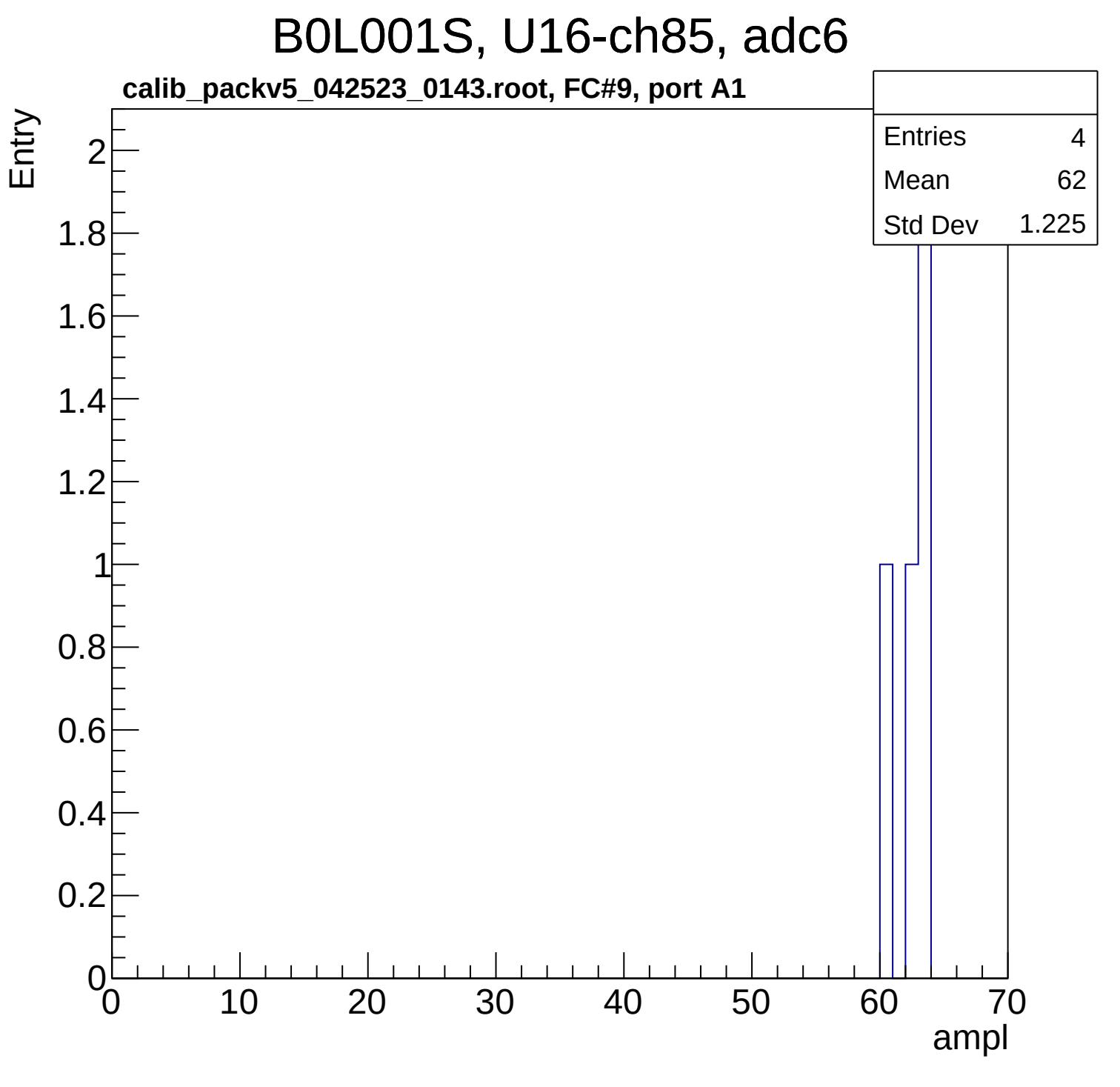
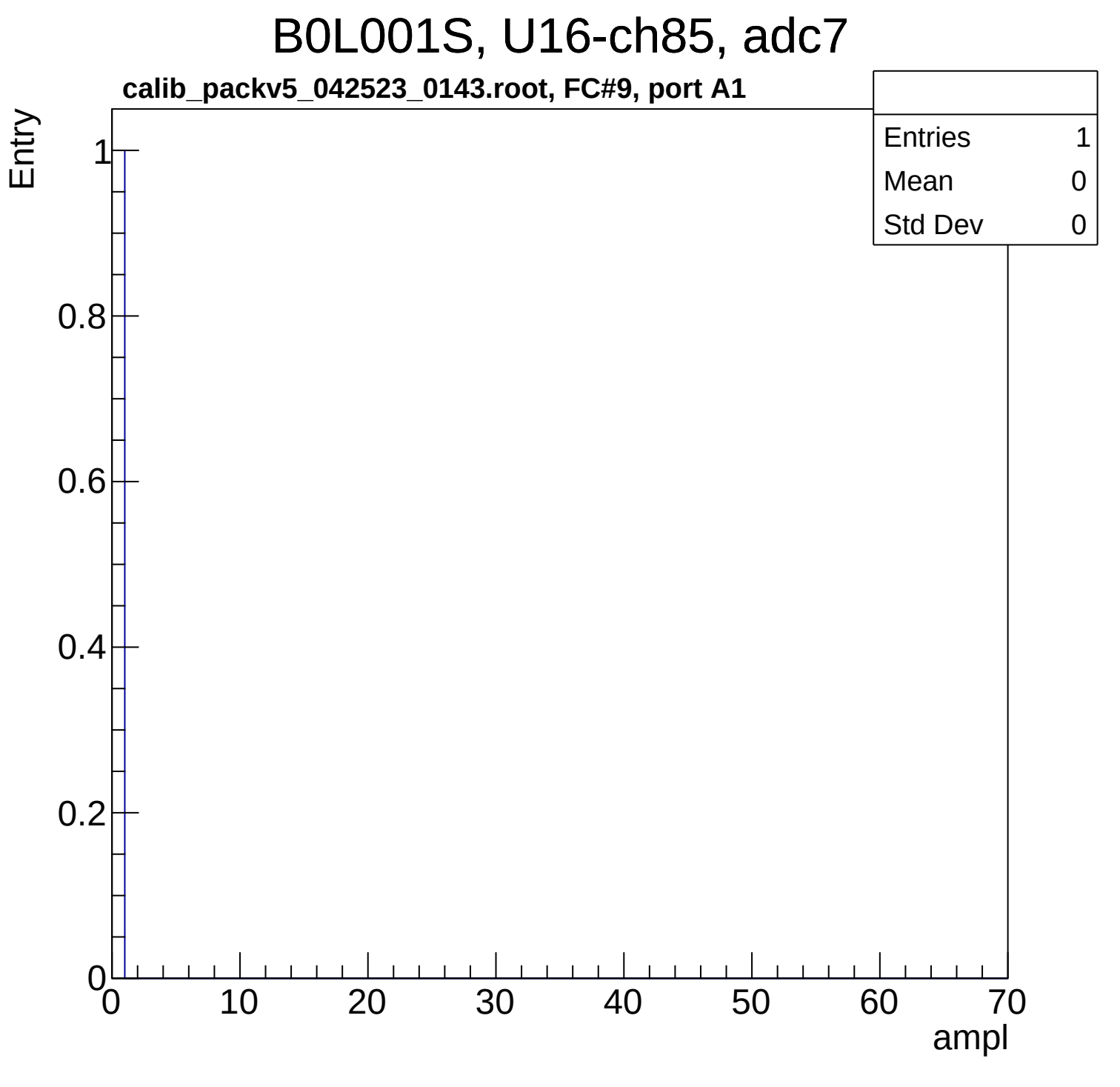
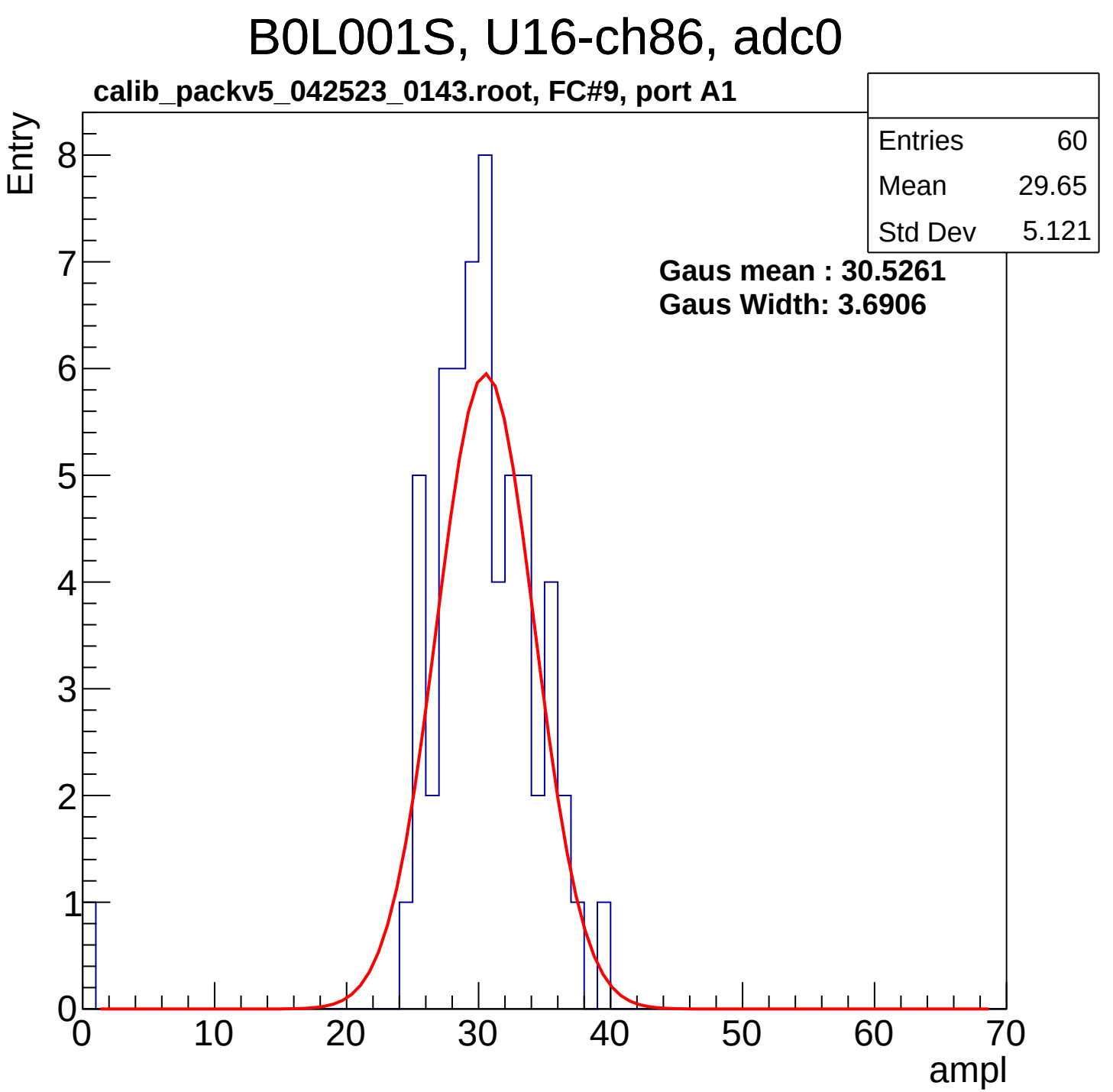

B0L001S, U16-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.65
Std Dev 5.121
Gaus mean : 30.5261
Gaus Width: 3.6906
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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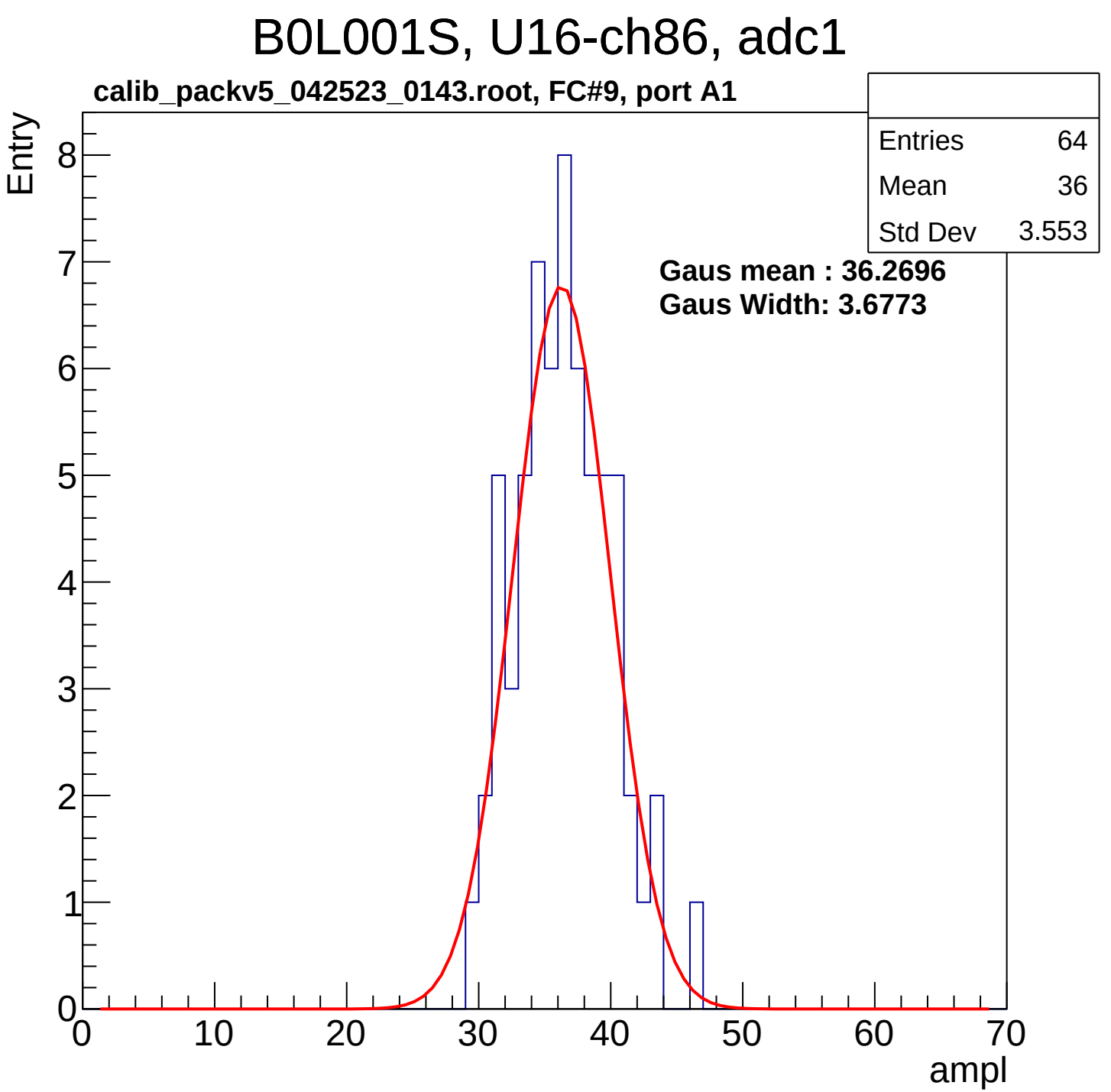
B0L001S, U16-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36
Std Dev 3.553
Gaus mean : 36.2696
Gaus Width: 3.6773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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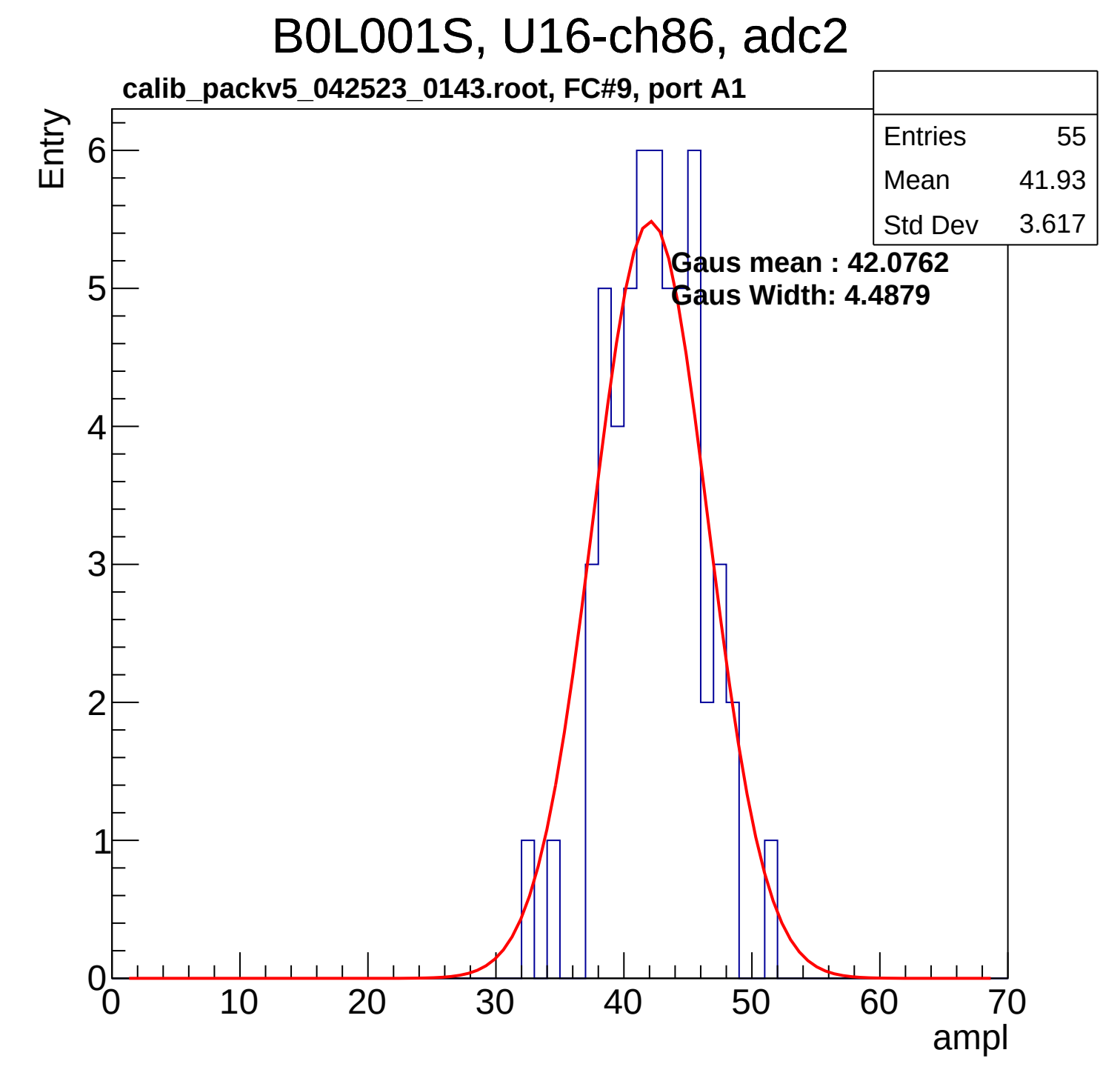
B0L001S, U16-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 41.93
Std Dev 3.617
Gaus mean : 42.0762
Gaus Width: 4.4879
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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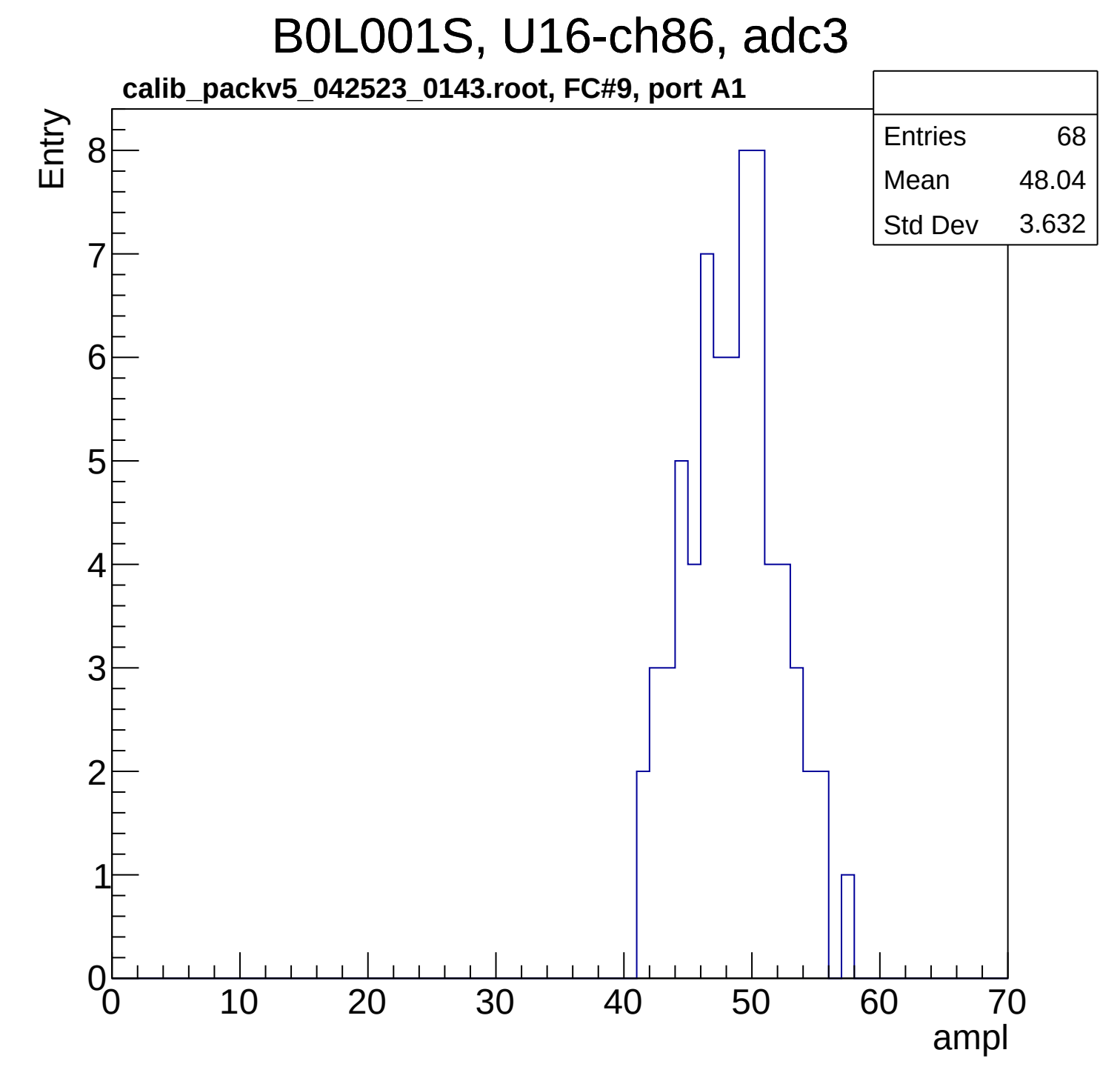
B0L001S, U16-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.04
Std Dev 3.632
Entry
ampl
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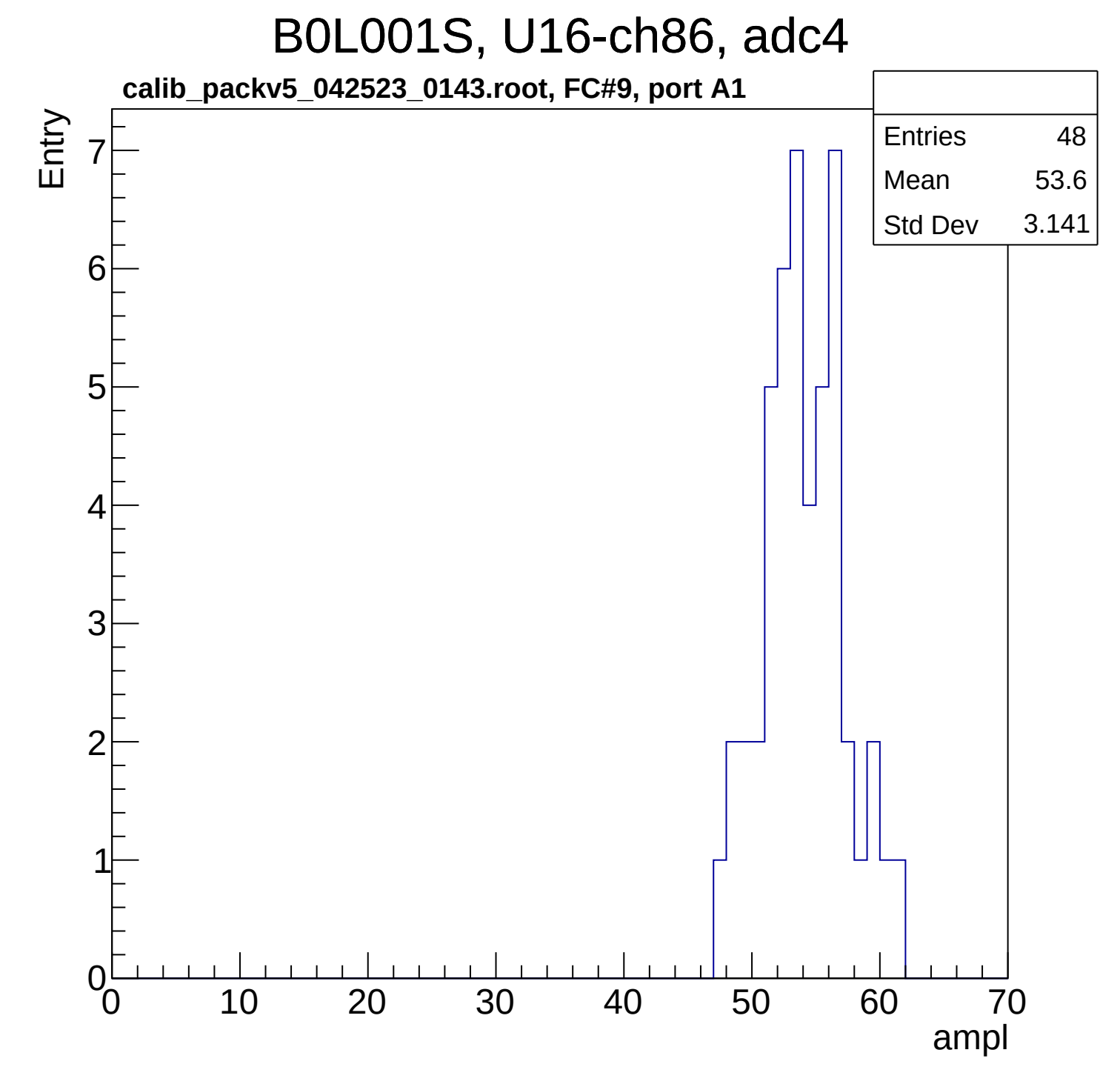
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 53.6
Std Dev 3.141
Entry
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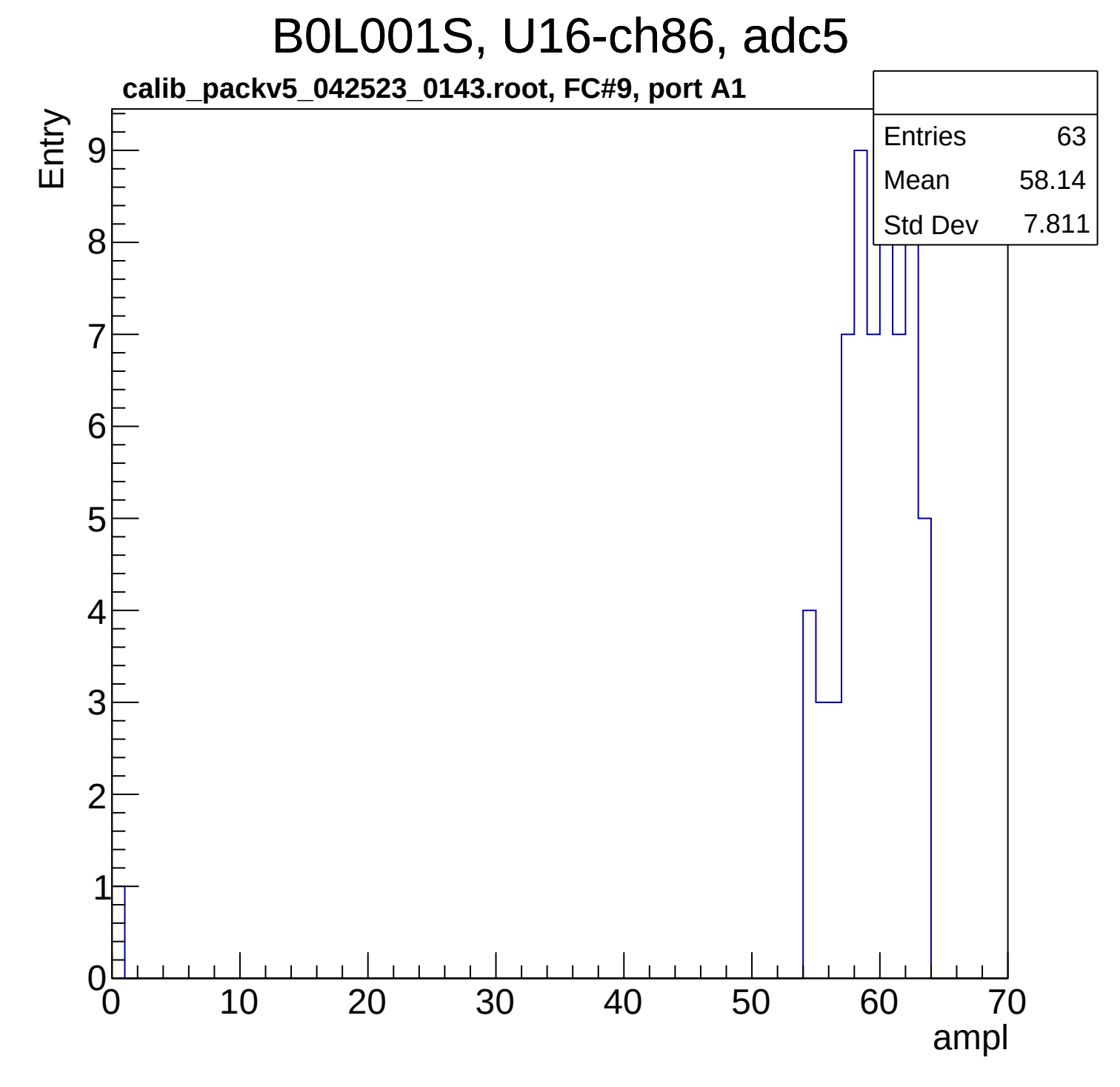
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 58.14
Std Dev 7.811
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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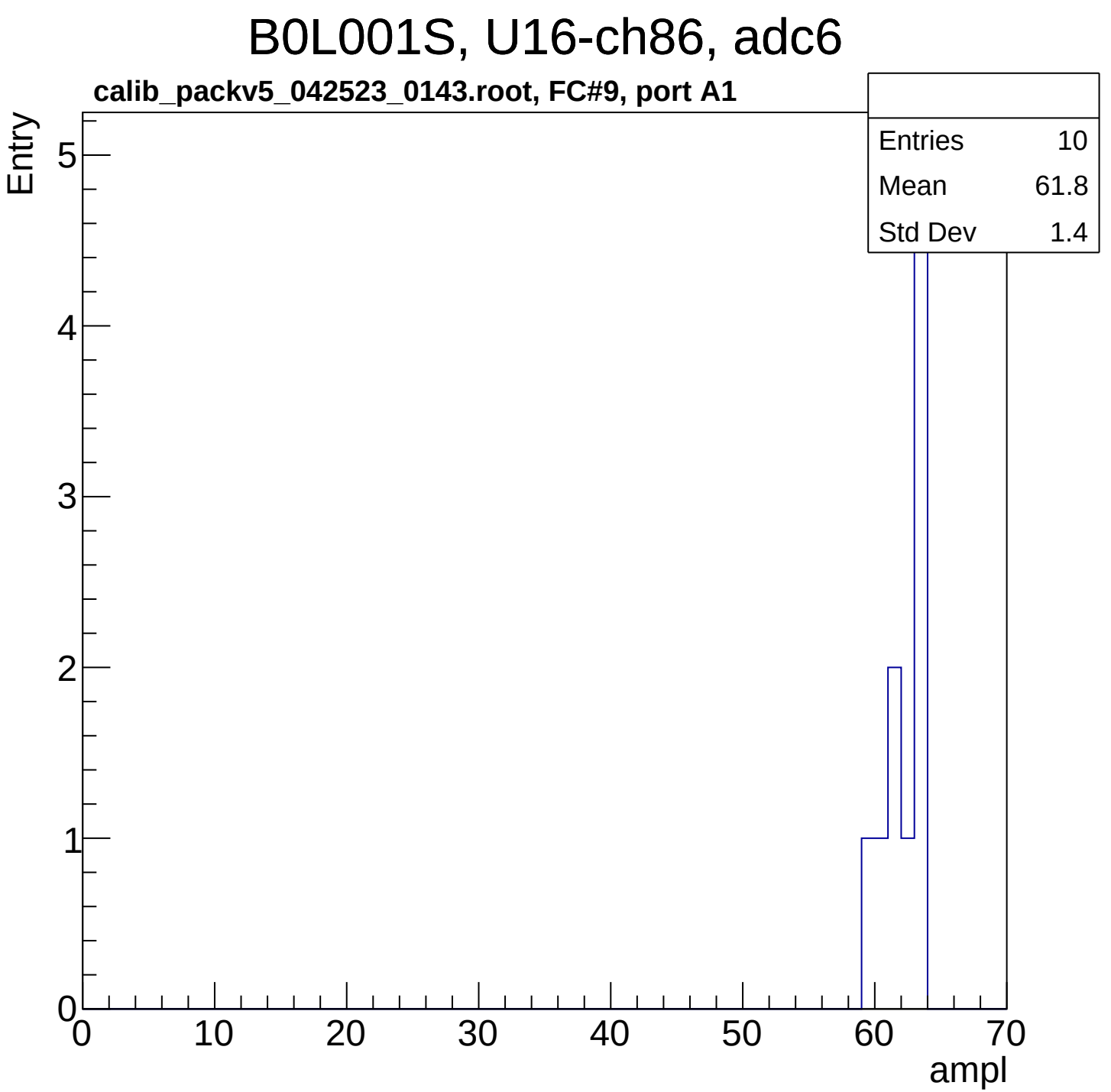
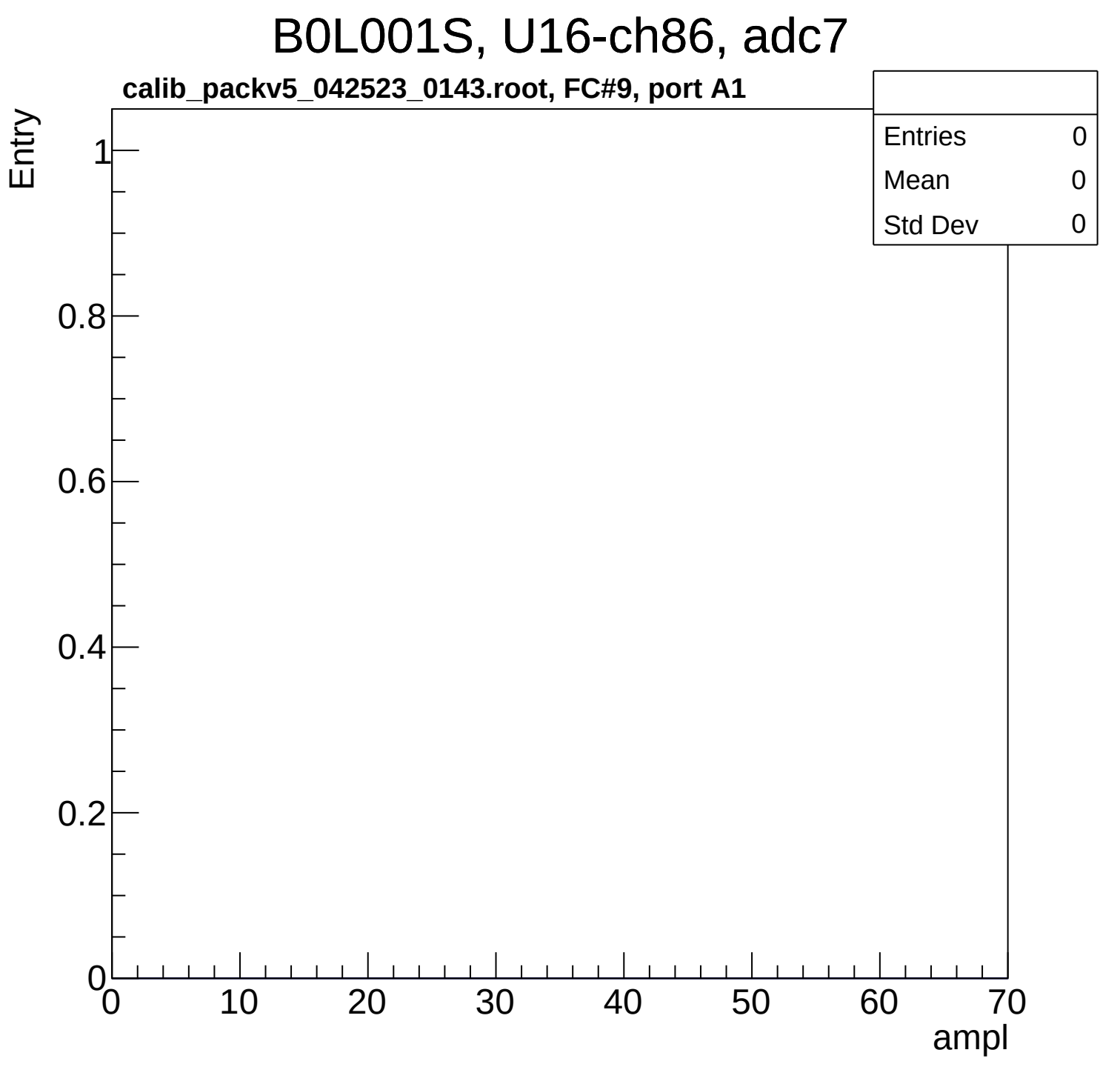
40
50
60
70


B0L001S, U16-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.8
Std Dev 1.4
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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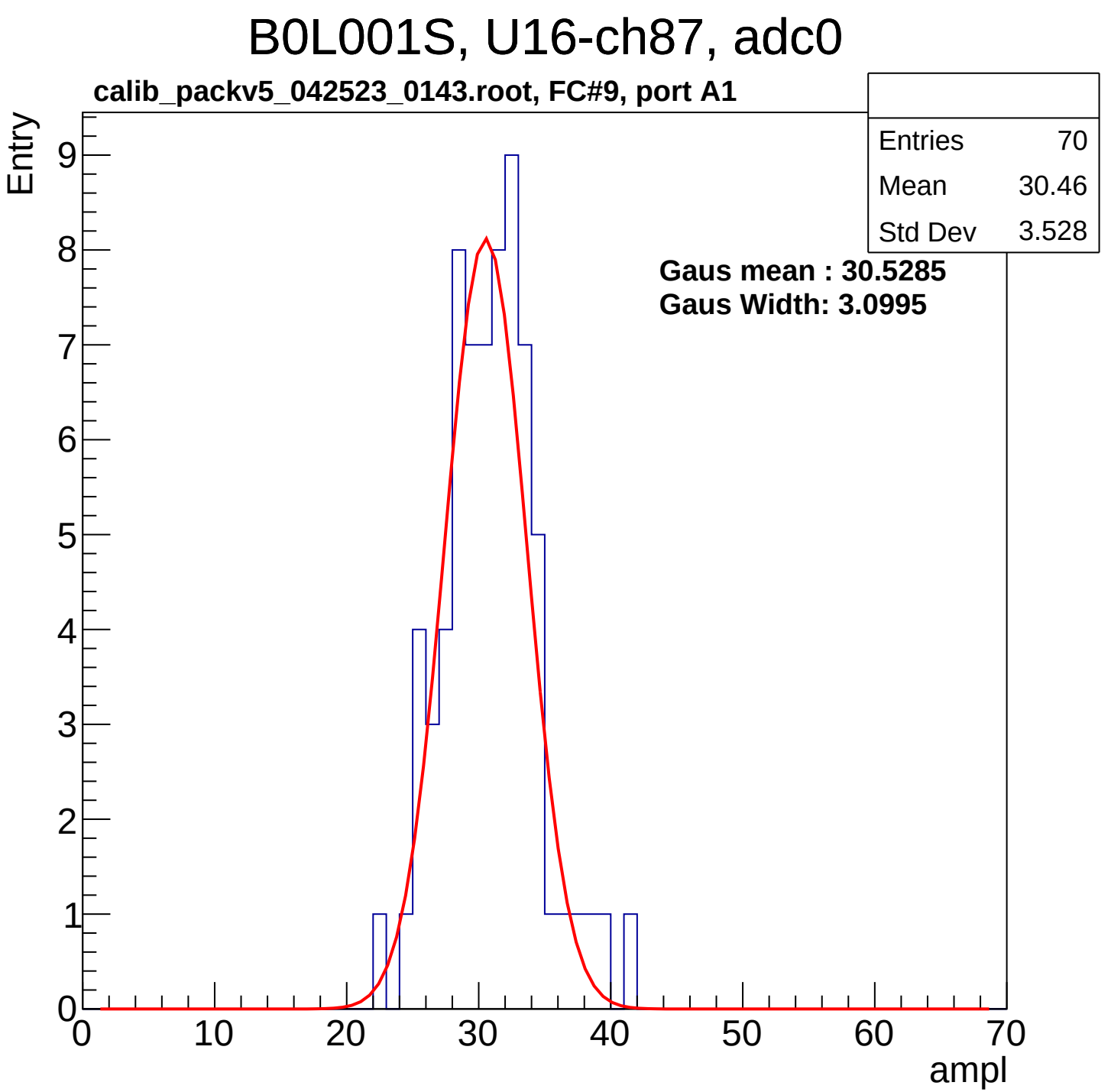
50
60
70
ampl


B0L001S, U16-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 30.46
Std Dev 3.528
Gaus mean : 30.5285
Gaus Width: 3.0995
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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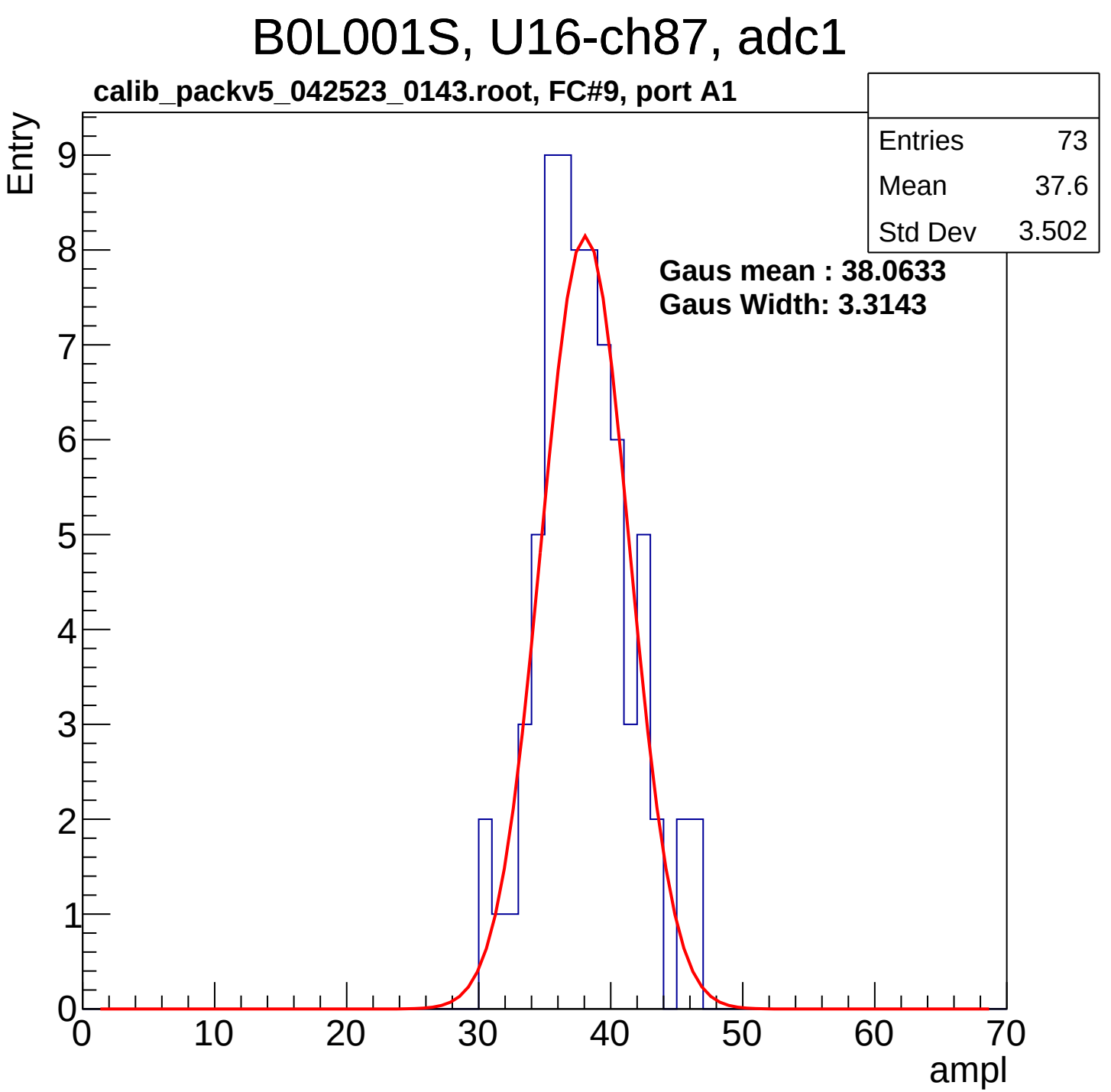
B0L001S, U16-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.6
Std Dev 3.502
Gaus mean : 38.0633
Gaus Width: 3.3143
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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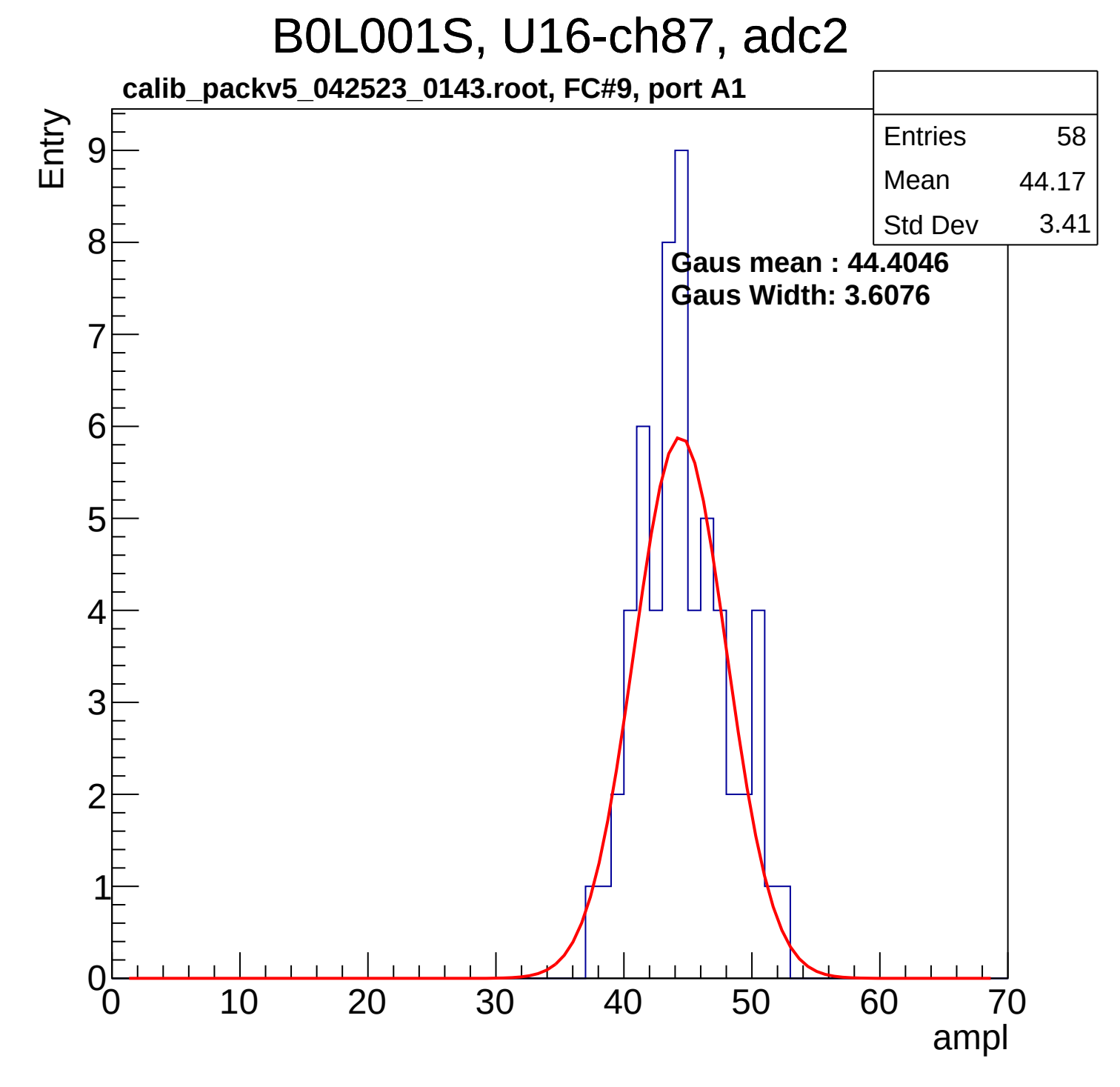
40
50
60
70


B0L001S, U16-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.17
Std Dev 3.41
Gaus mean : 44.4046
Gaus Width: 3.6076
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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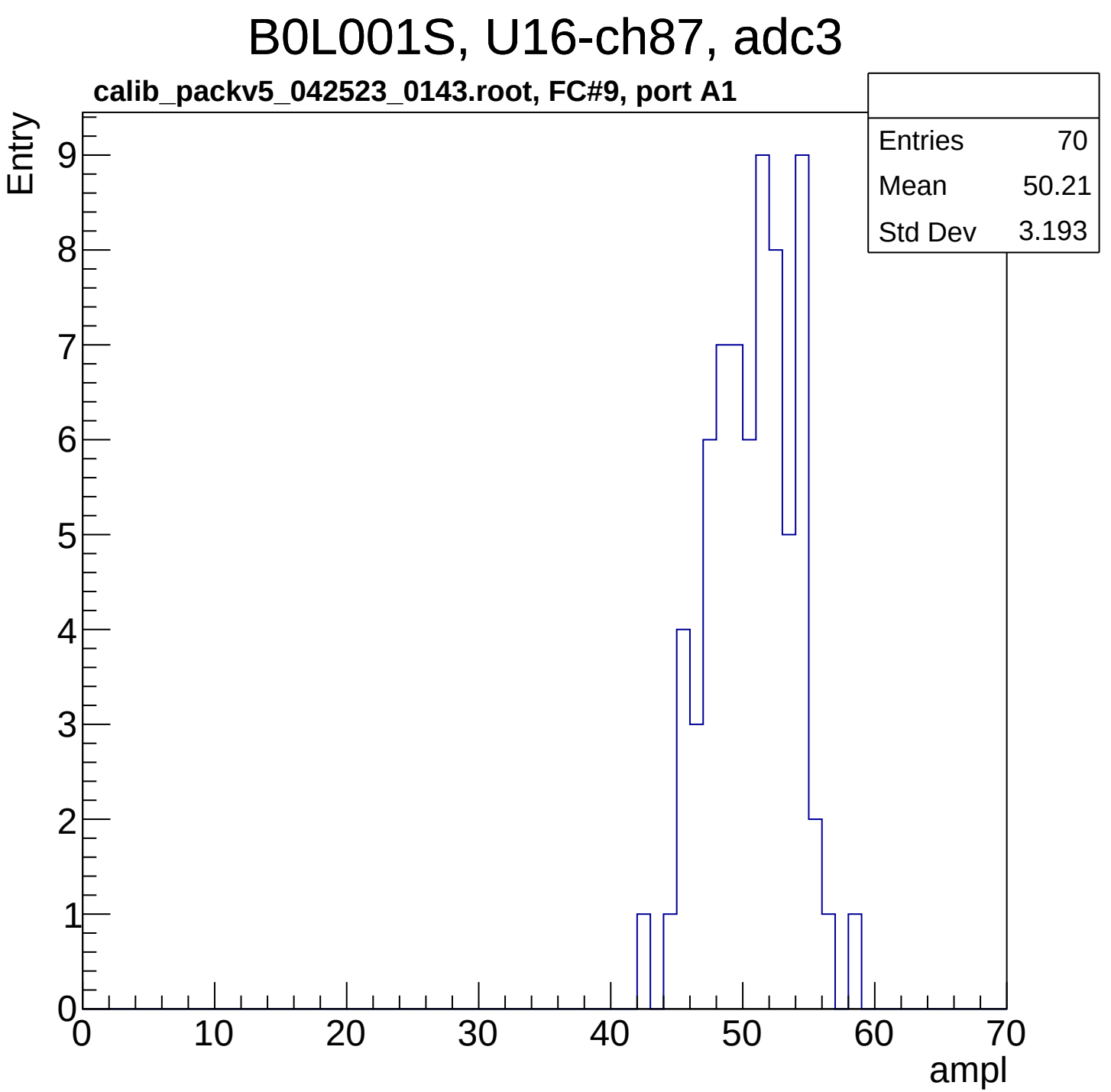
B0L001S, U16-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.21
Std Dev 3.193
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
50
60
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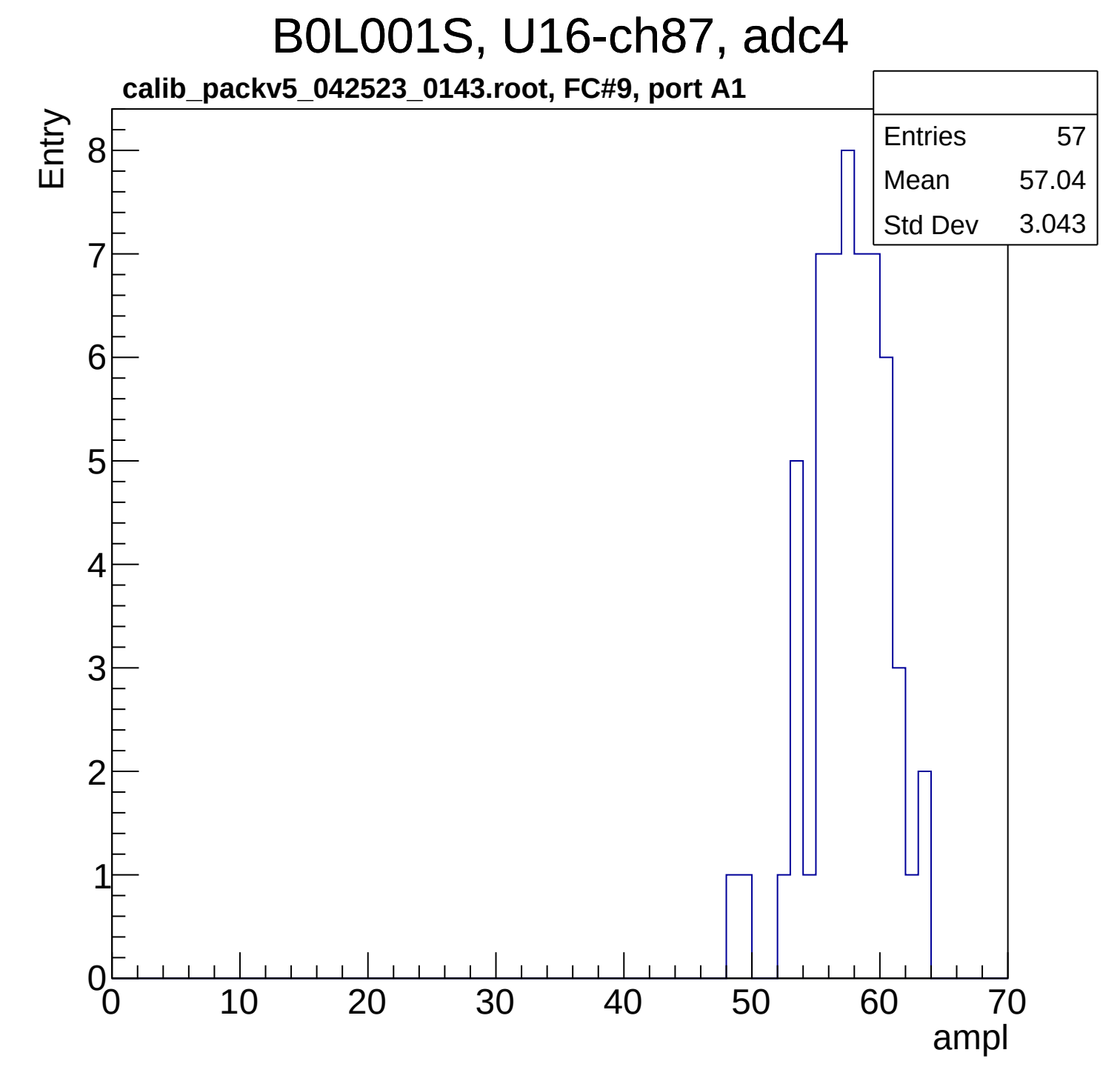
B0L001S, U16-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.04
Std Dev 3.043
Entry
ampl
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4
5
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7
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0
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60
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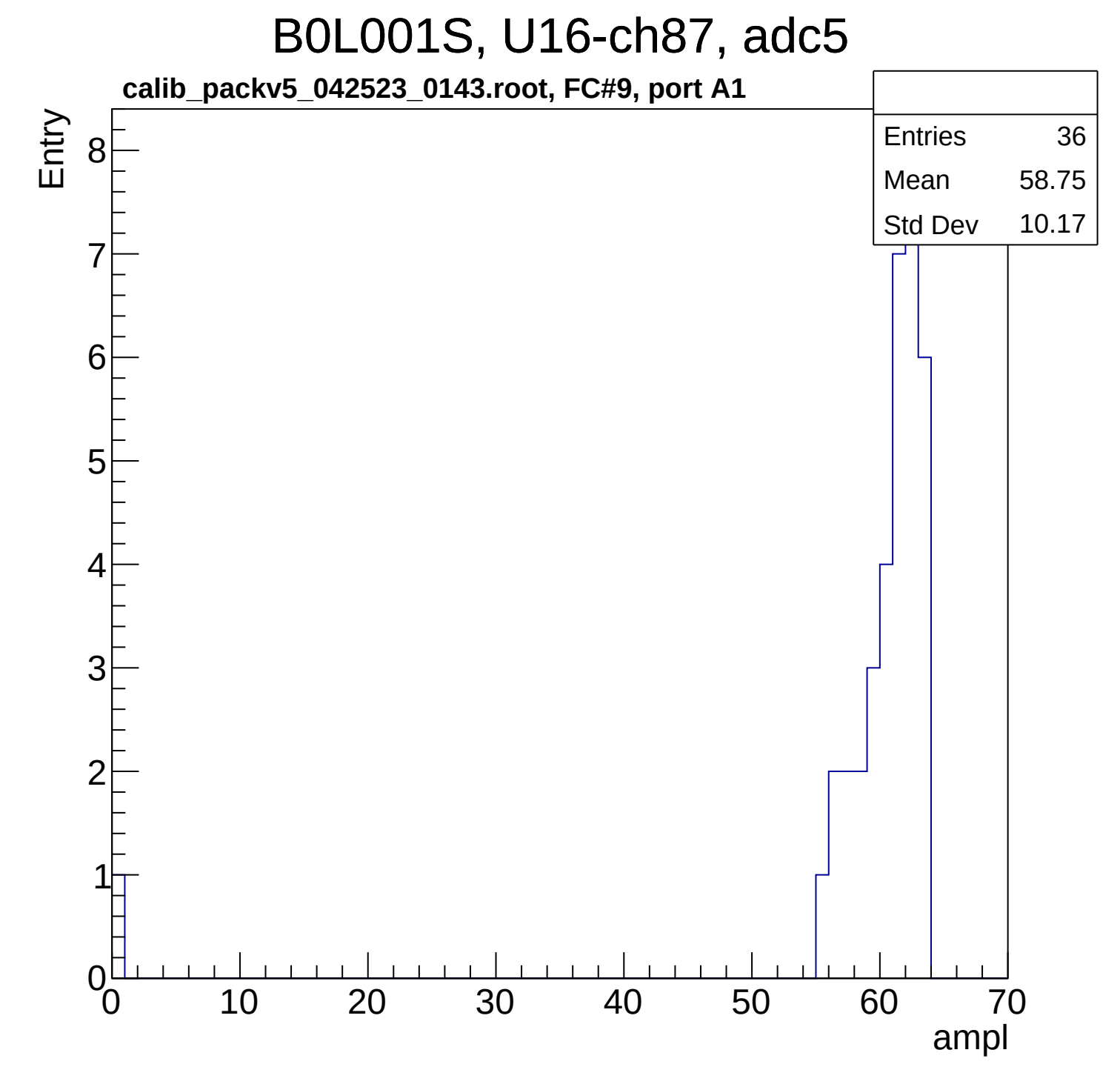
B0L001S, U16-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.75
Std Dev 10.17
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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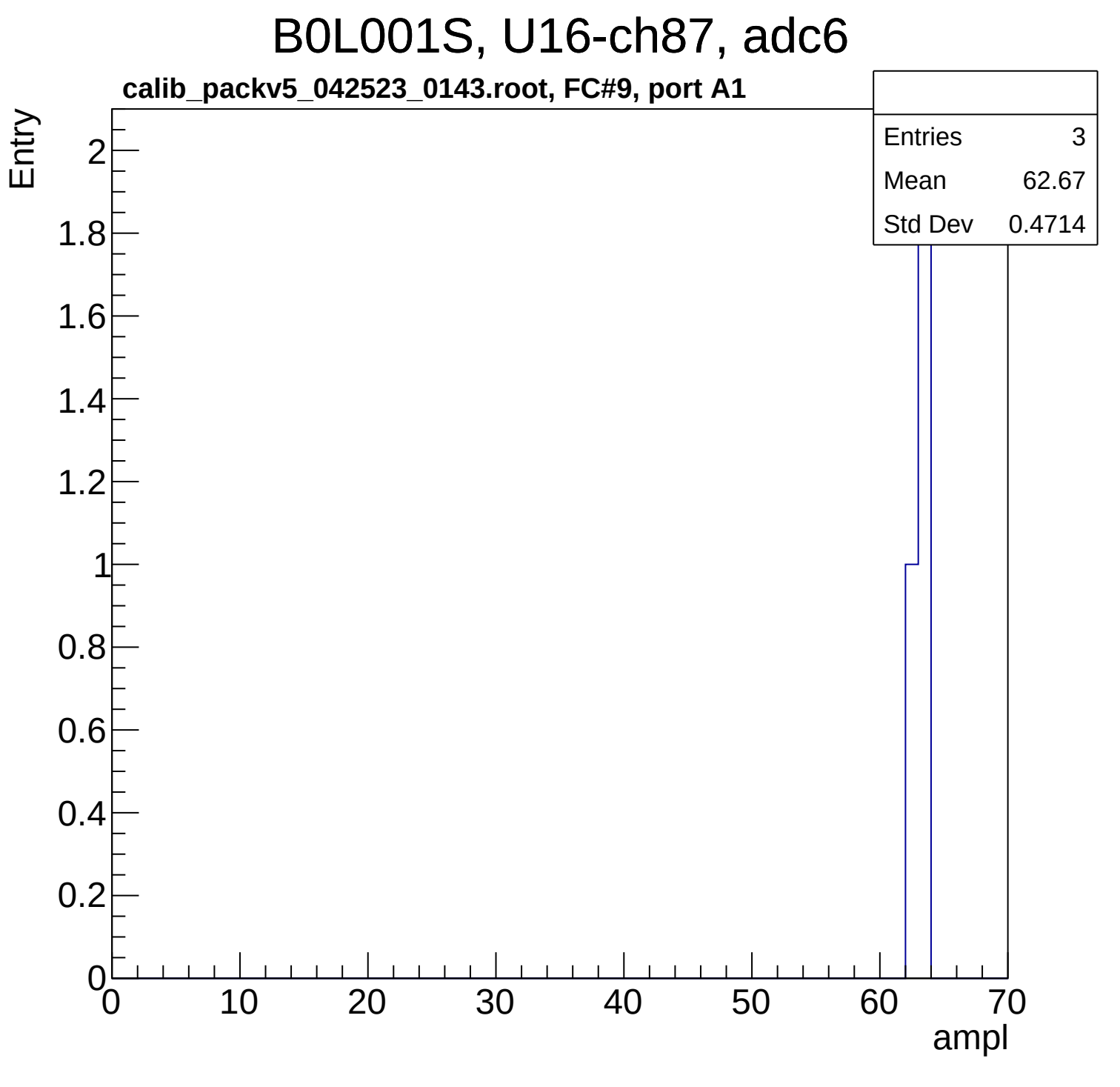
B0L001S, U16-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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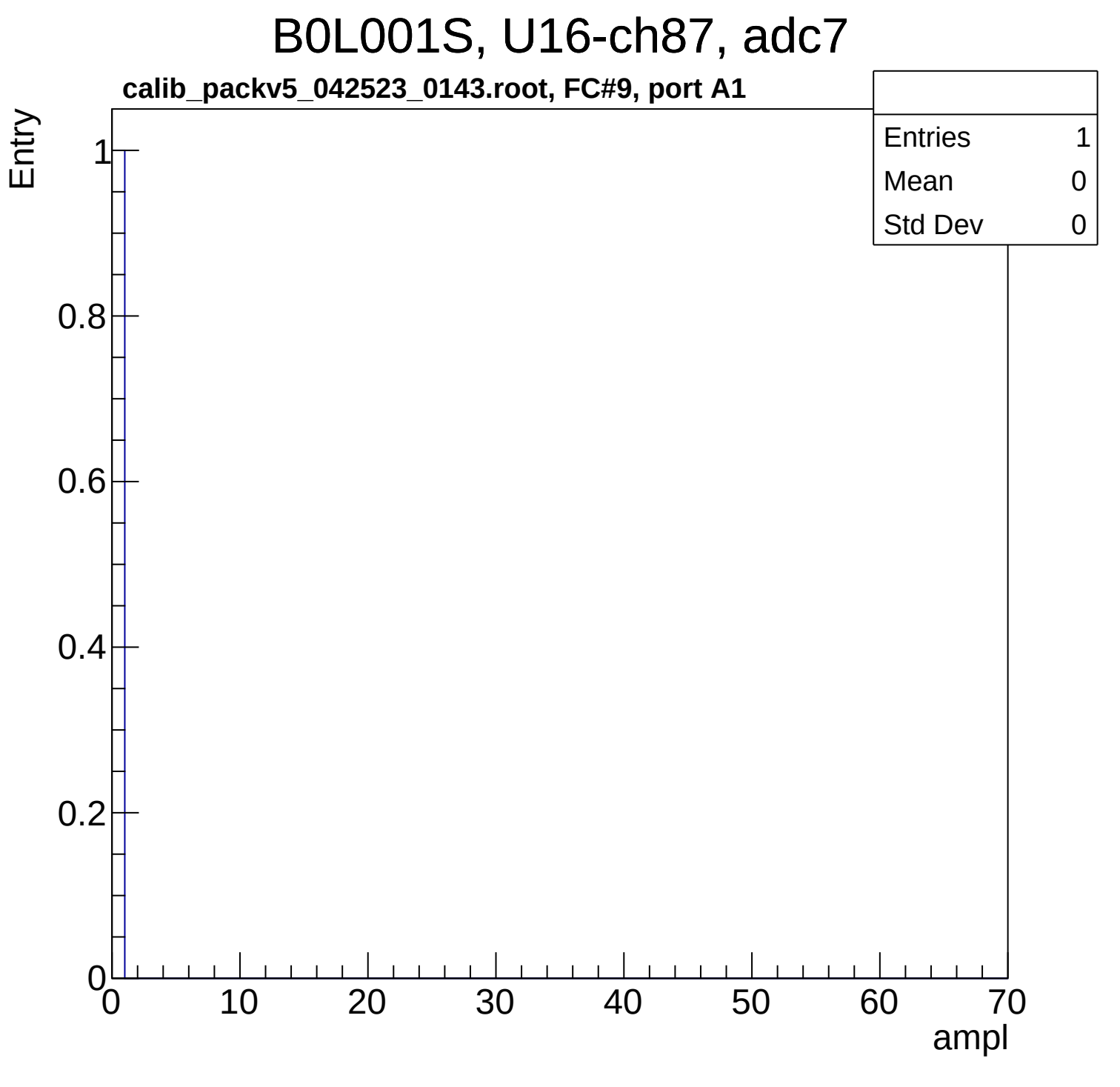
30
40
50
60
70


B0L001S, U16-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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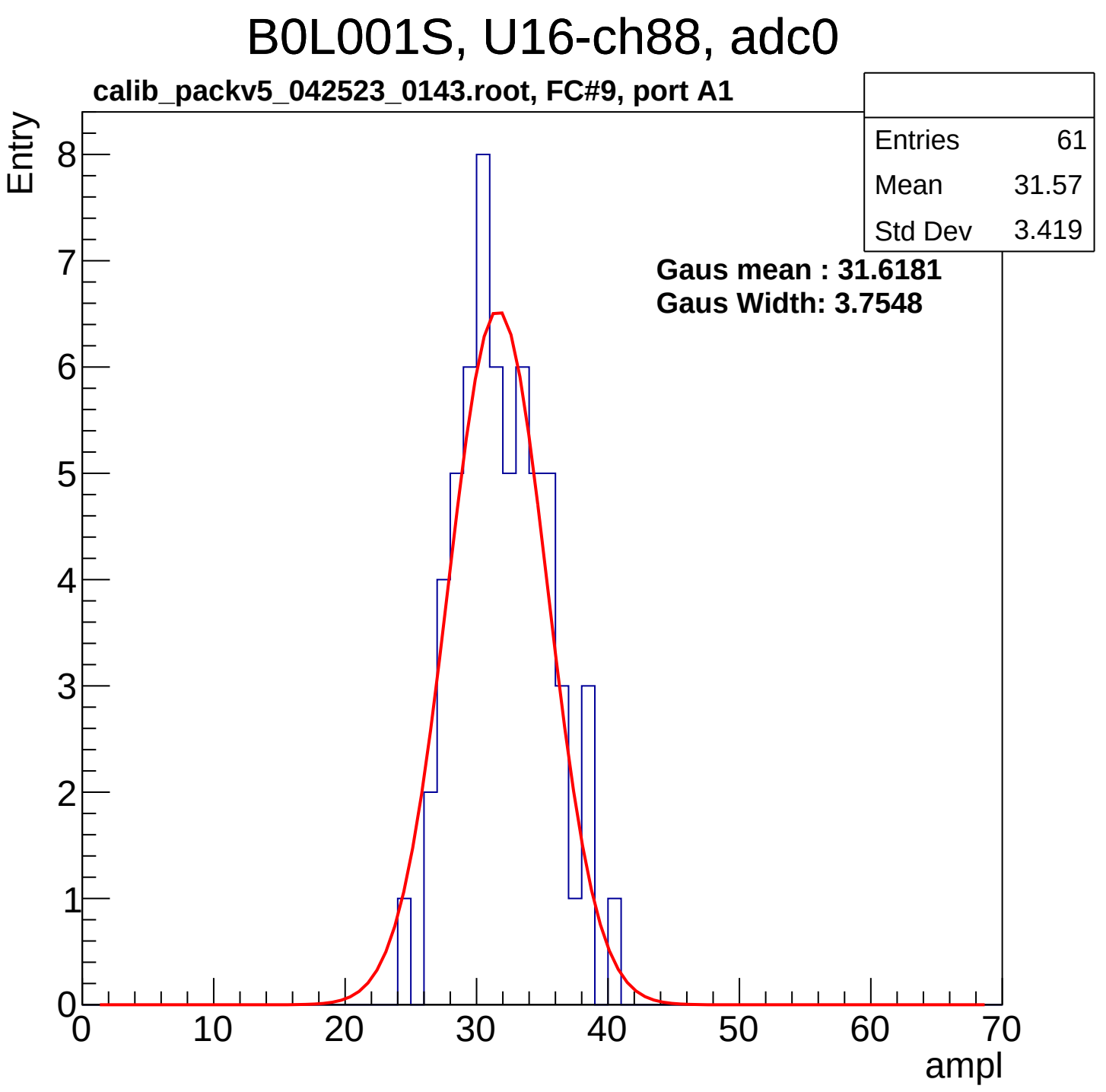
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.57
Std Dev 3.419
Gaus mean : 31.6181
Gaus Width: 3.7548
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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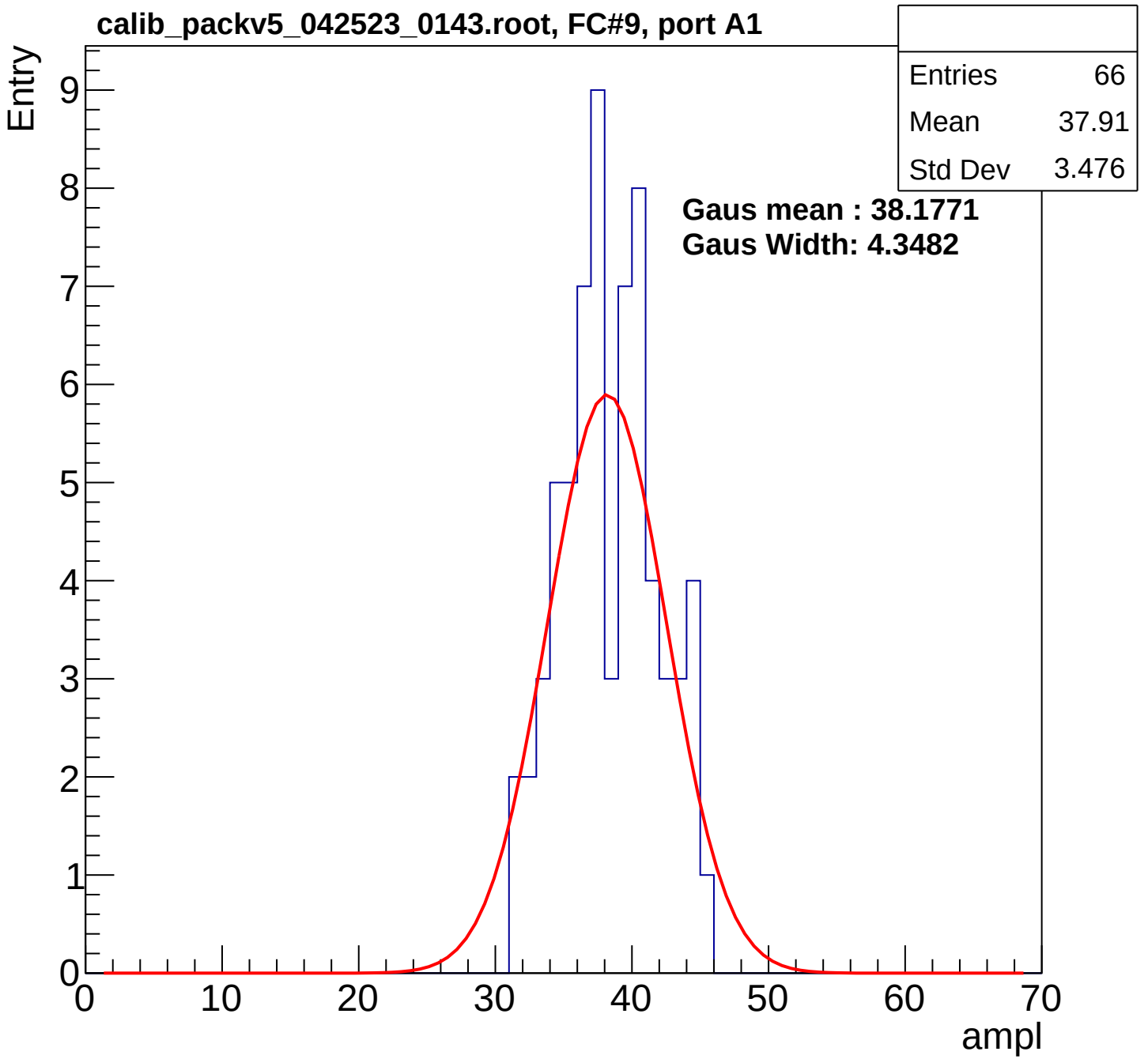
B0L001S, U16-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.91
Std Dev 3.476
Gaus mean : 38.1771
Gaus Width: 4.3482
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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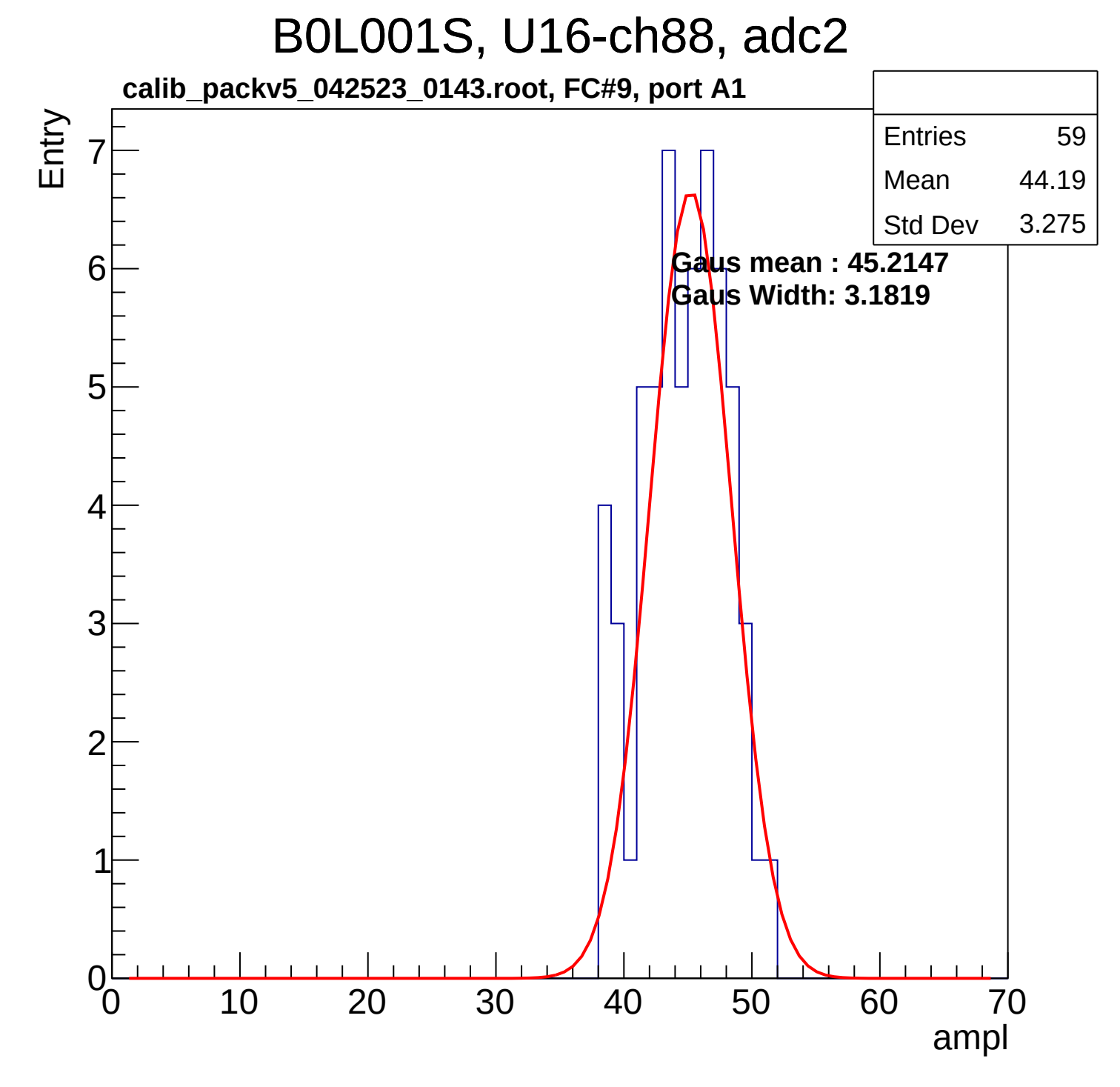
B0L001S, U16-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.19
Std Dev 3.275
Gaus mean : 45.2147
Gaus Width: 3.1819
Entry
ampl
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1
2
3
4
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6
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0
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40
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60
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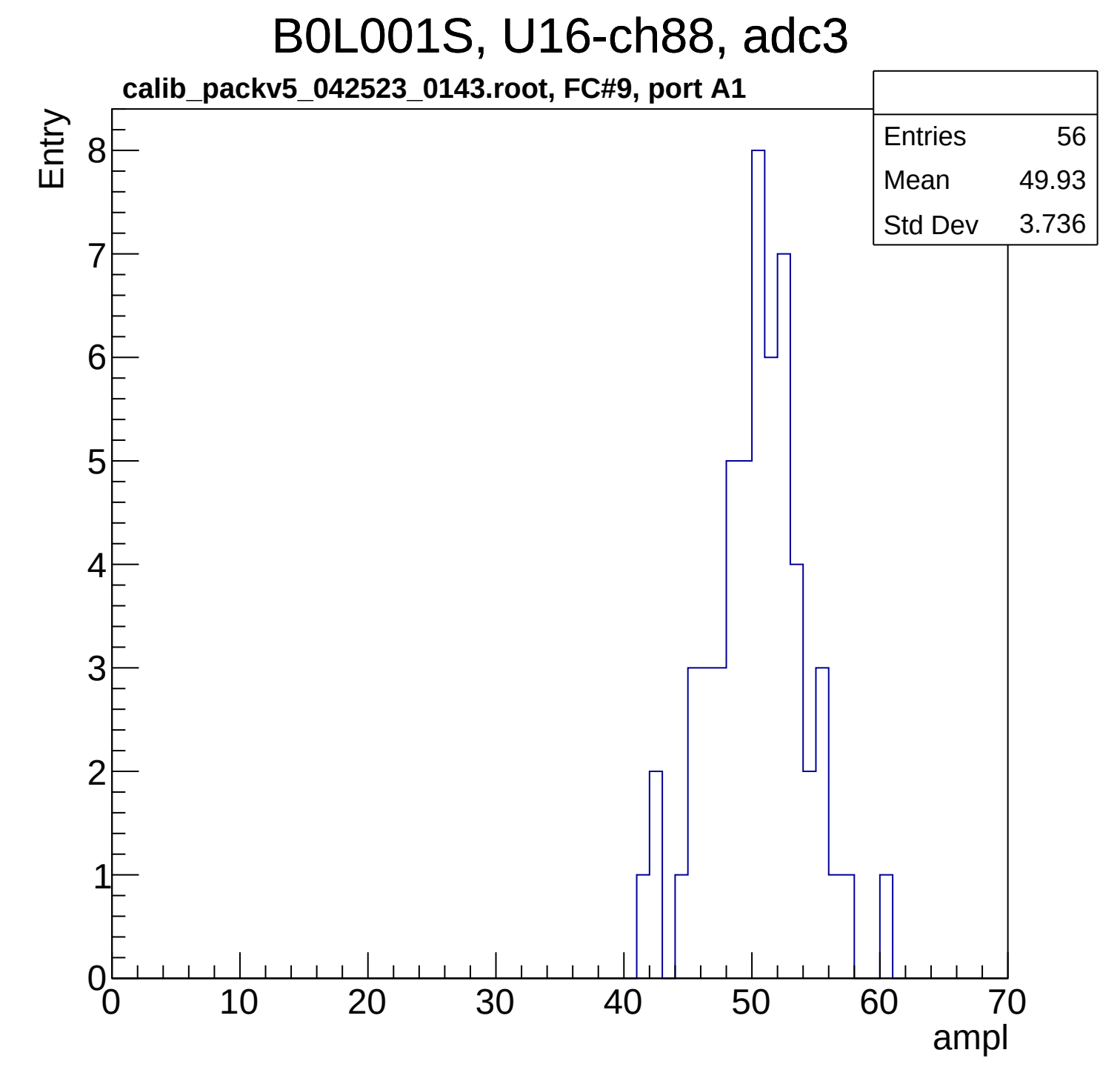
B0L001S, U16-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.93
Std Dev 3.736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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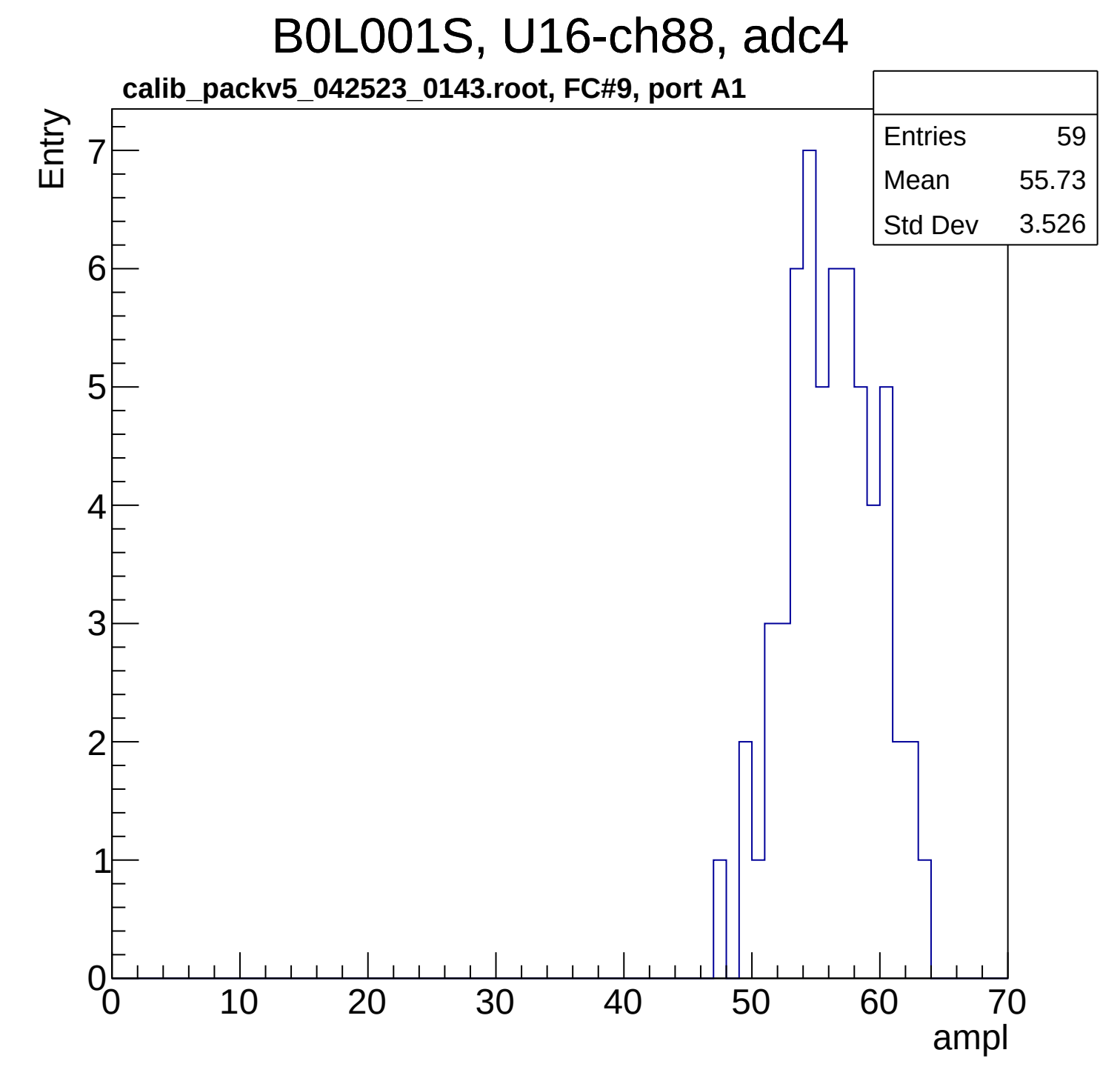
B0L001S, U16-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.73
Std Dev 3.526
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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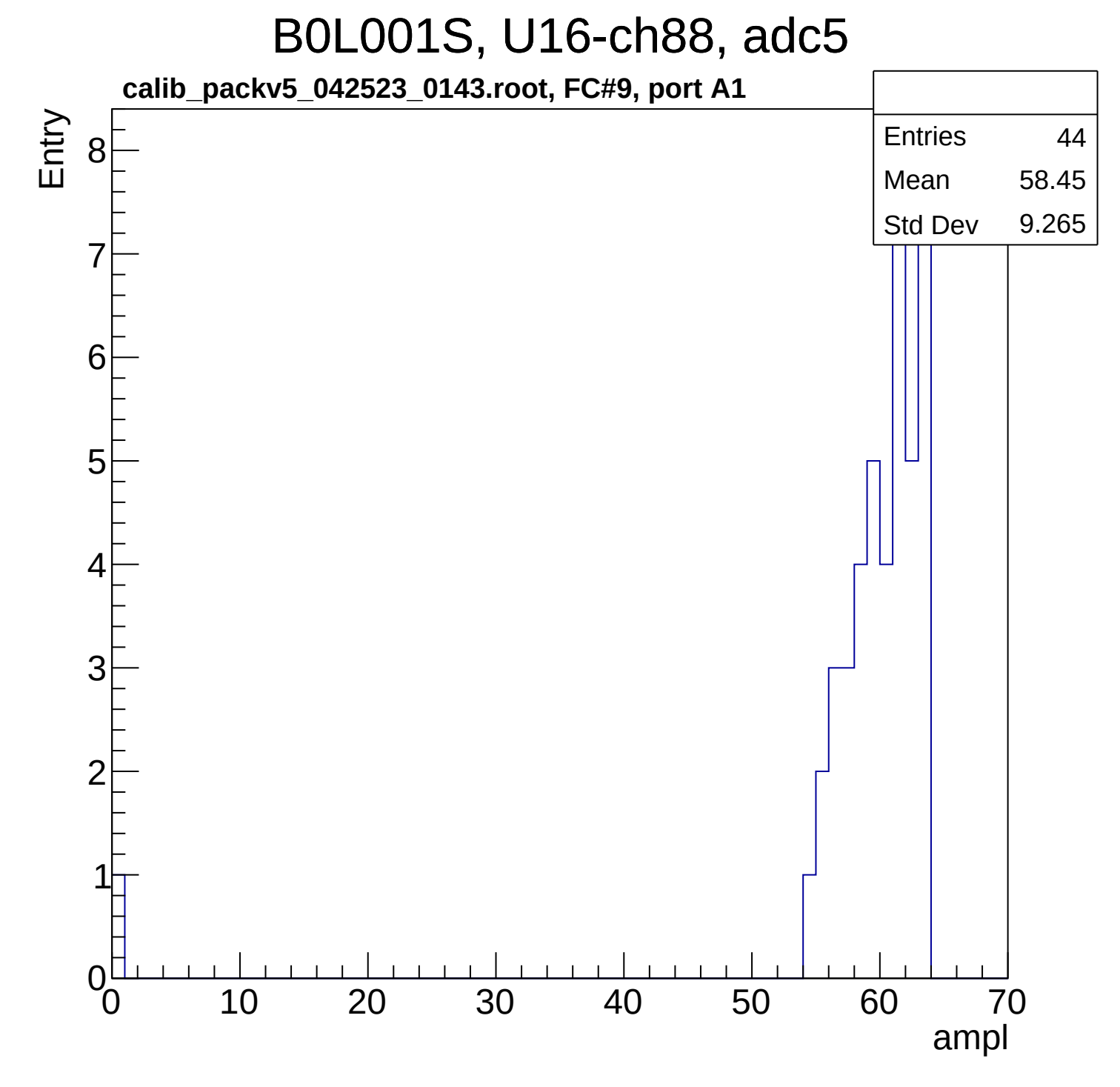
B0L001S, U16-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.45
Std Dev 9.265
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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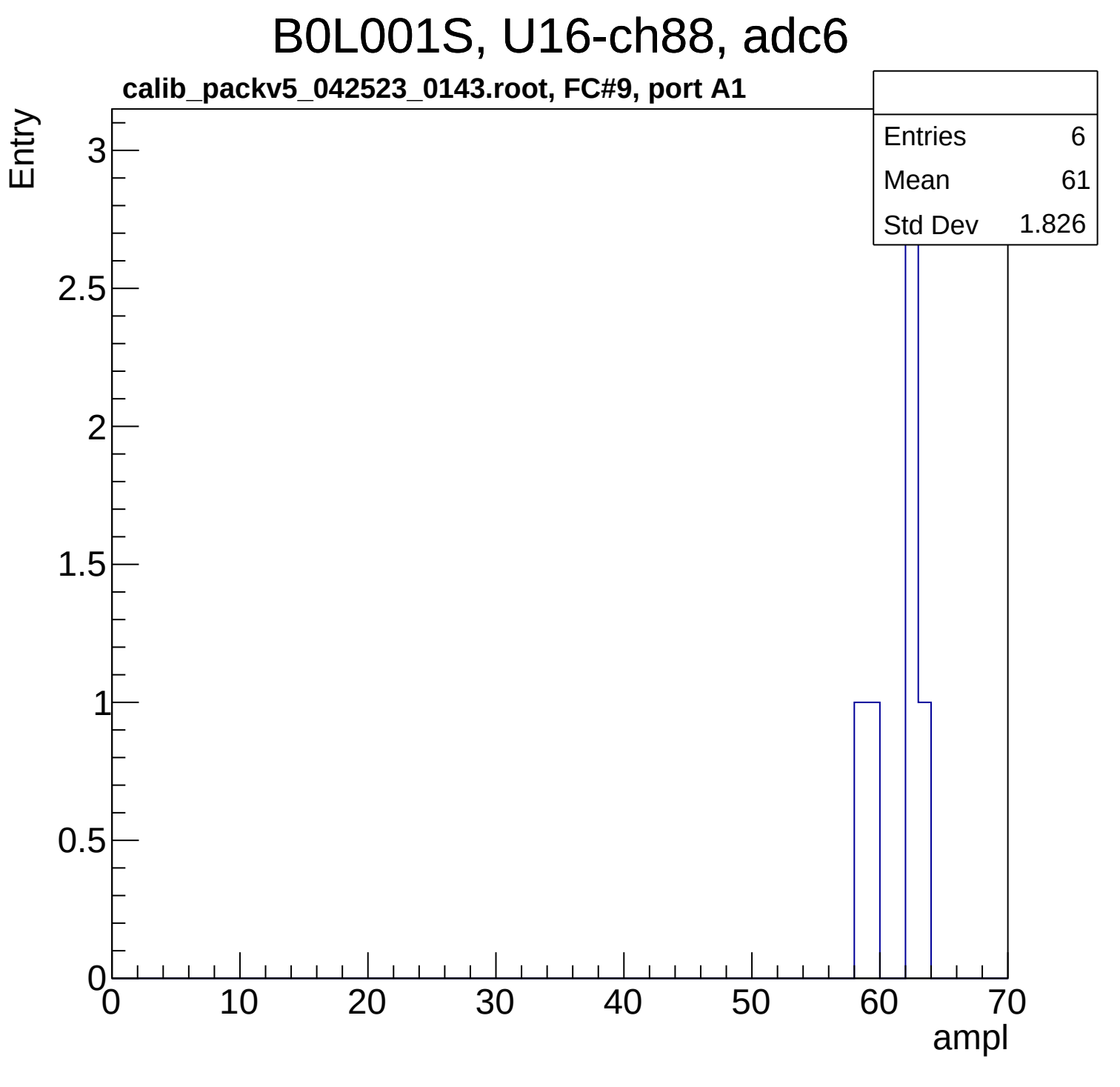
B0L001S, U16-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61
Std Dev 1.826
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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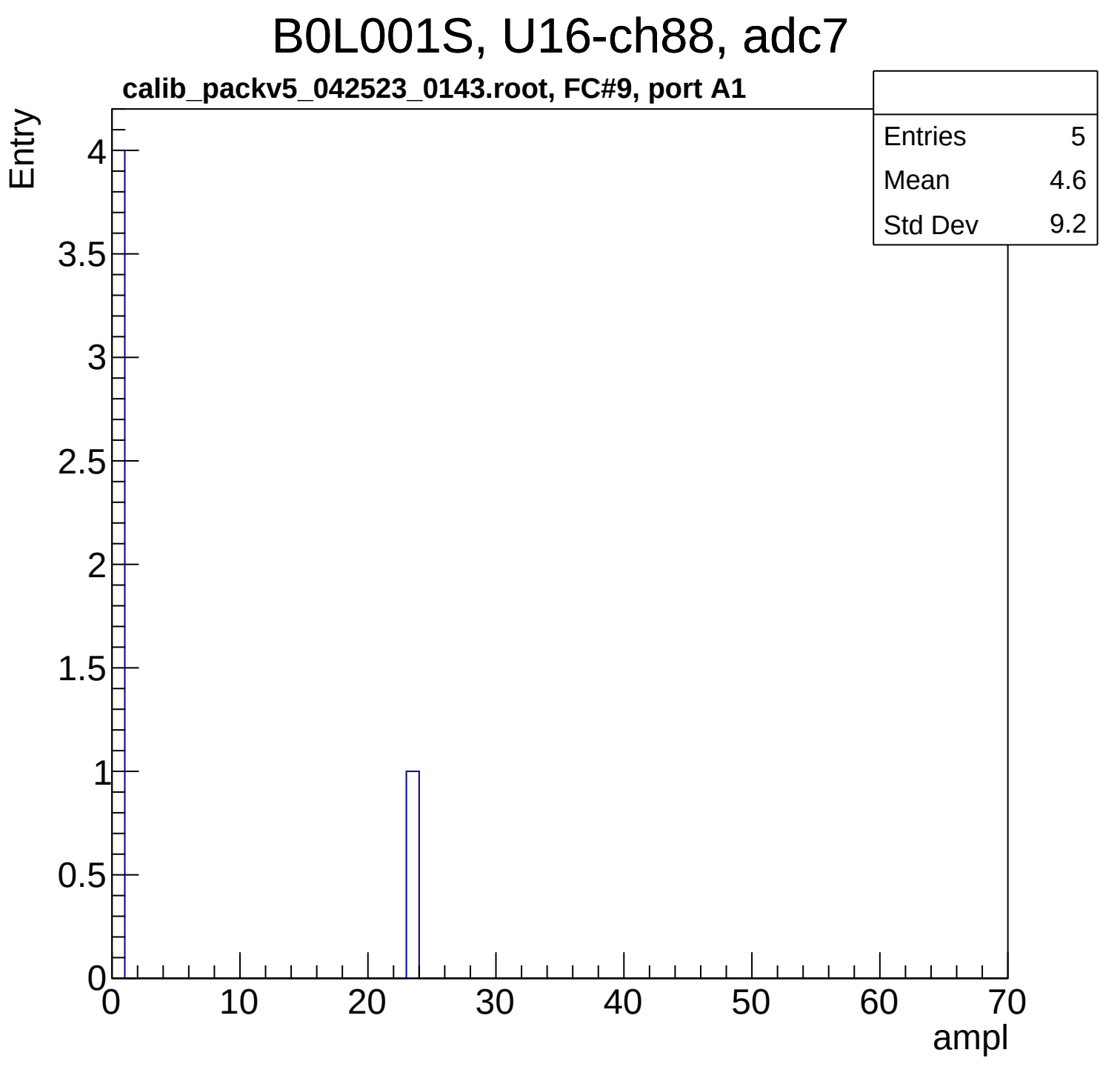
B0L001S, U16-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 4.6
Std Dev 9.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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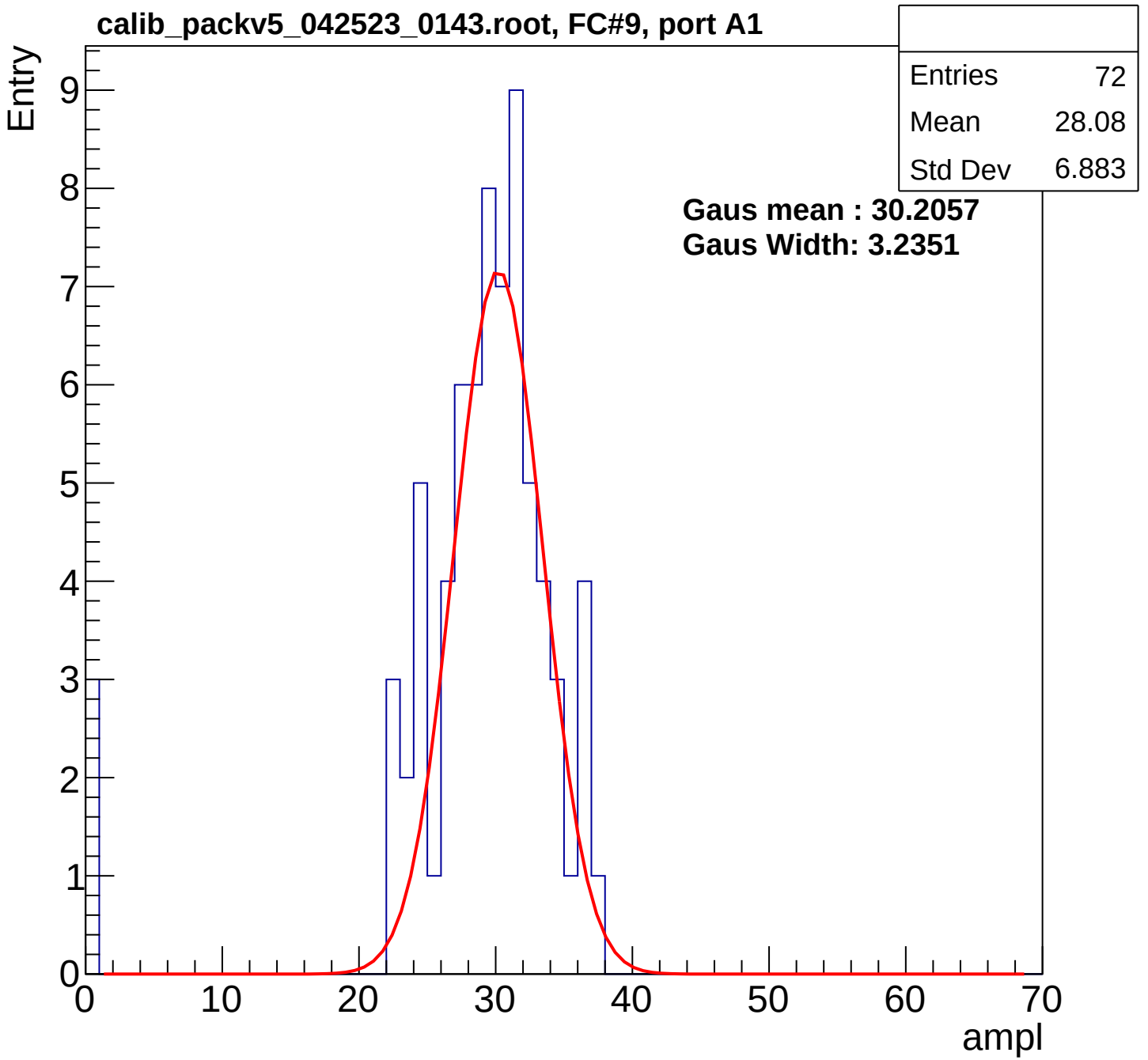
B0L001S, U16-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 28.08
Std Dev 6.883
Gaus mean : 30.2057
Gaus Width: 3.2351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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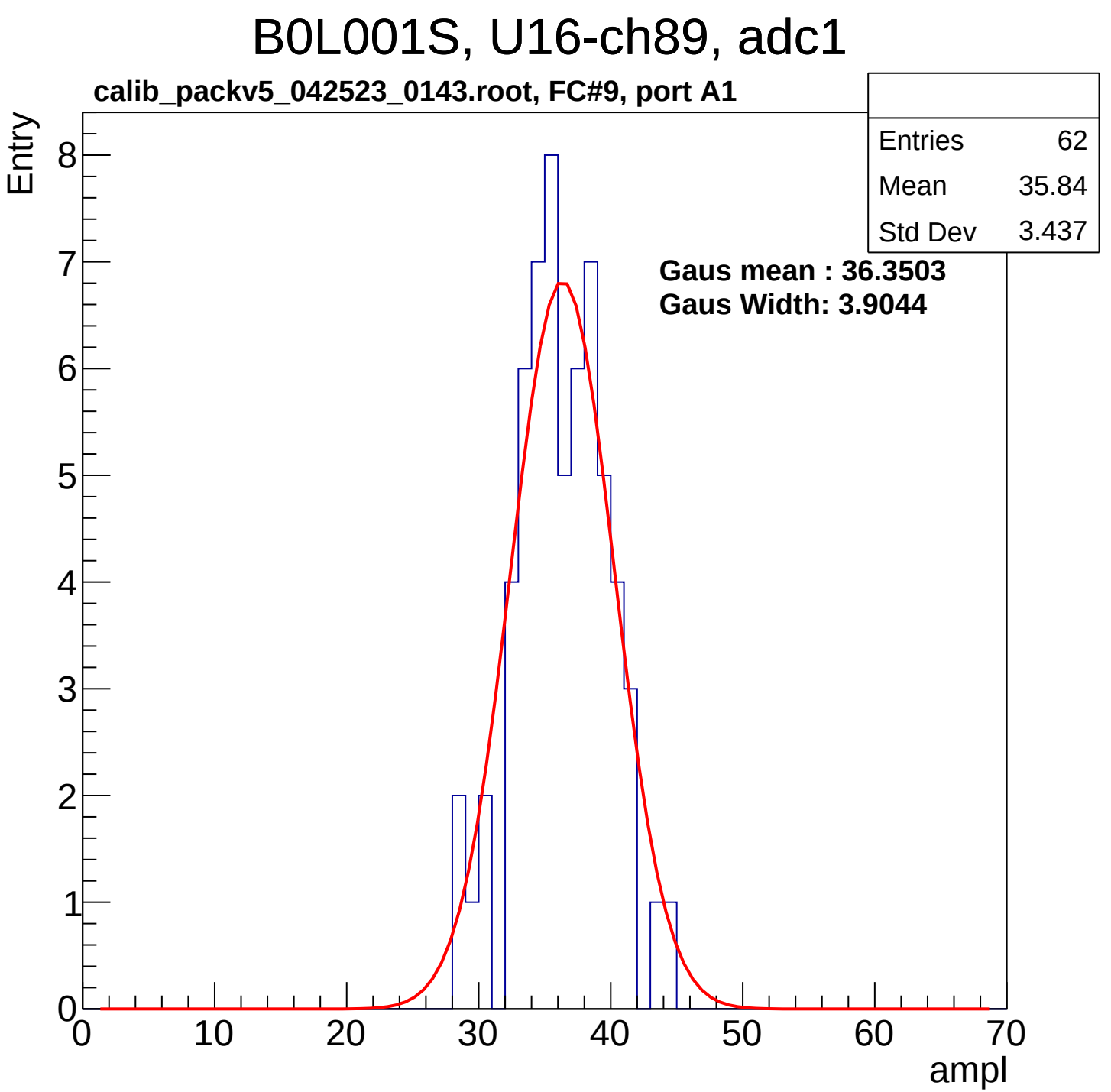
B0L001S, U16-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 35.84
Std Dev 3.437
Gaus mean : 36.3503
Gaus Width: 3.9044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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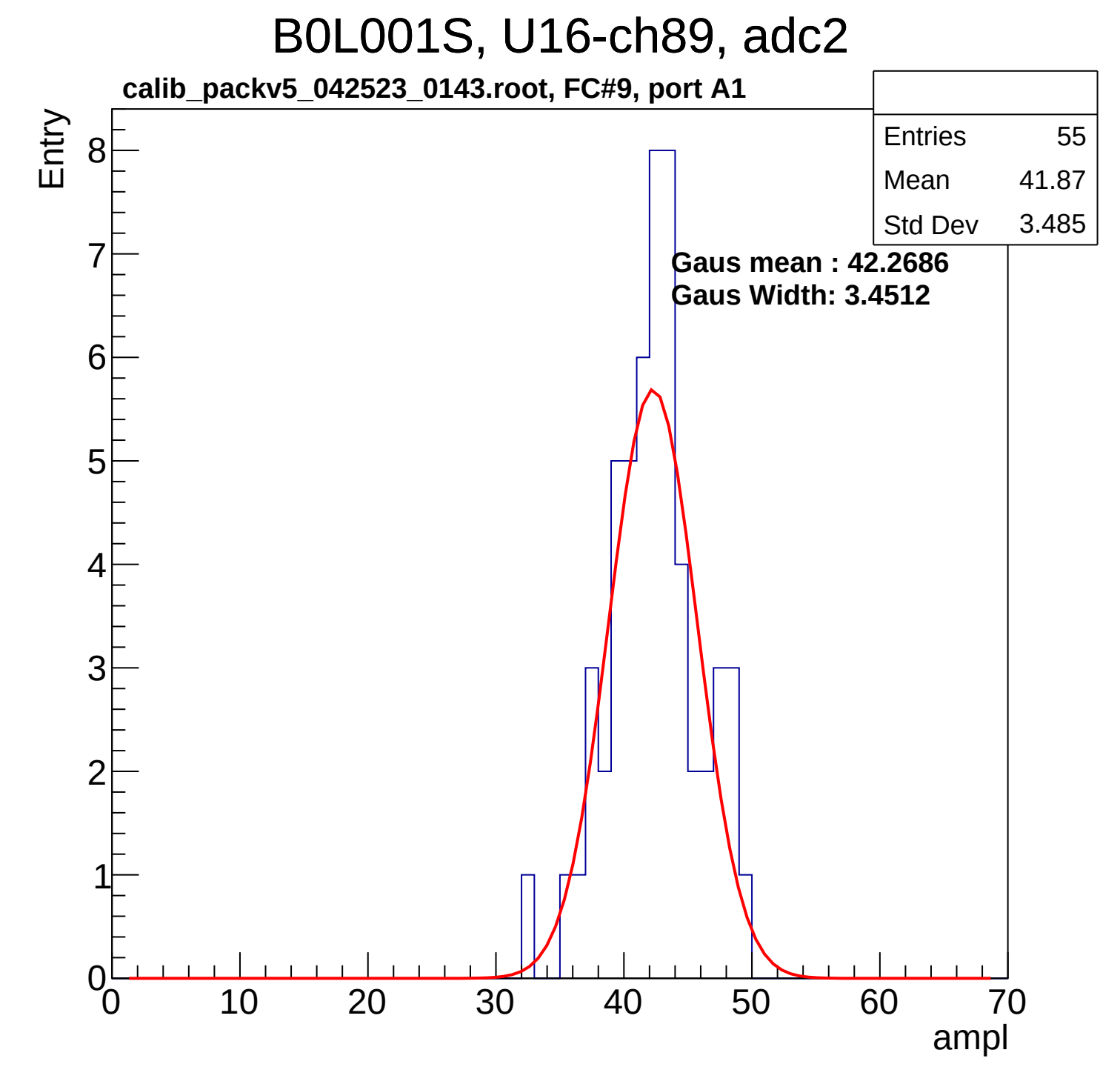
B0L001S, U16-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 41.87
Std Dev 3.485
Gaus mean : 42.2686
Gaus Width: 3.4512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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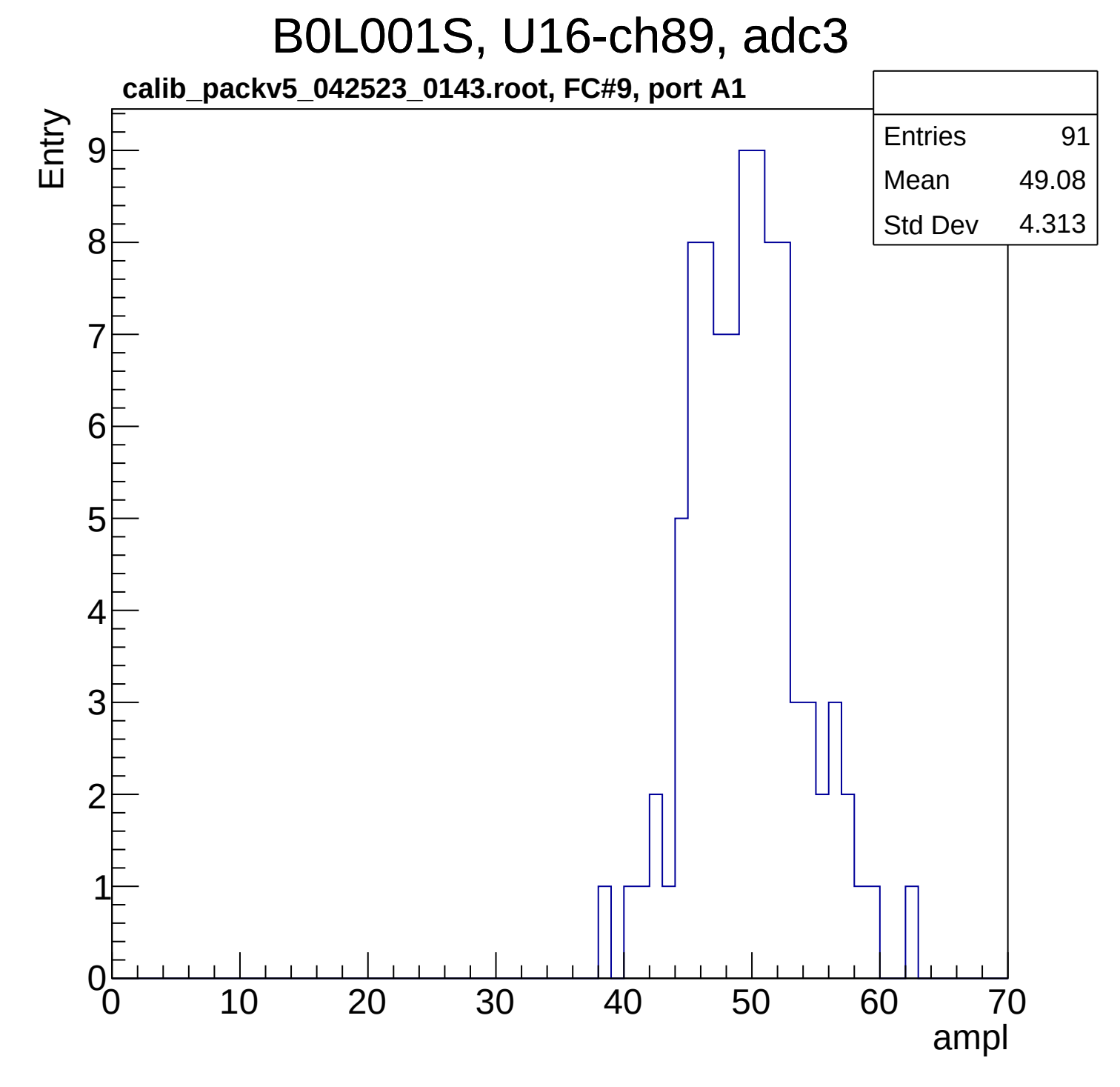
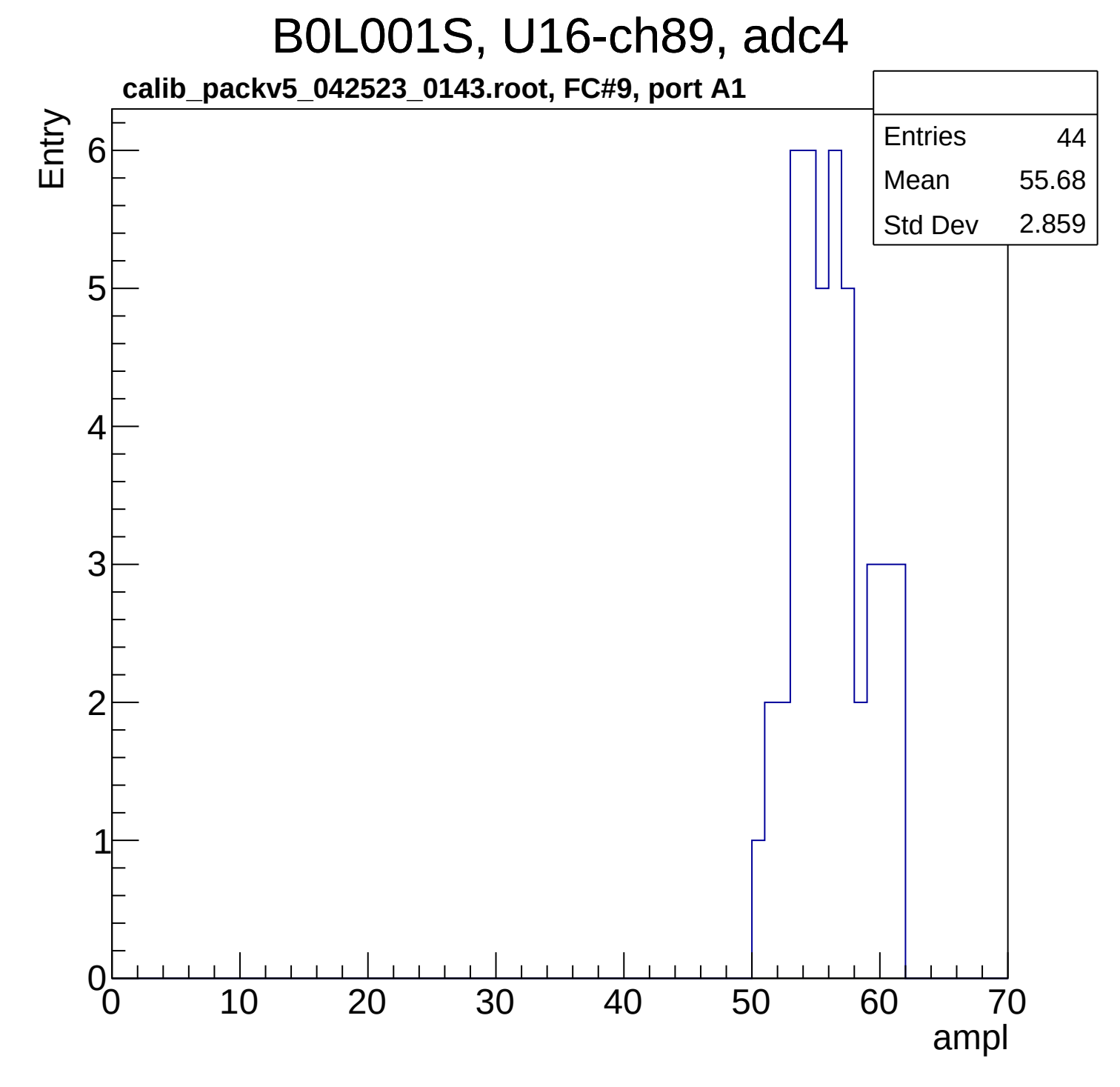
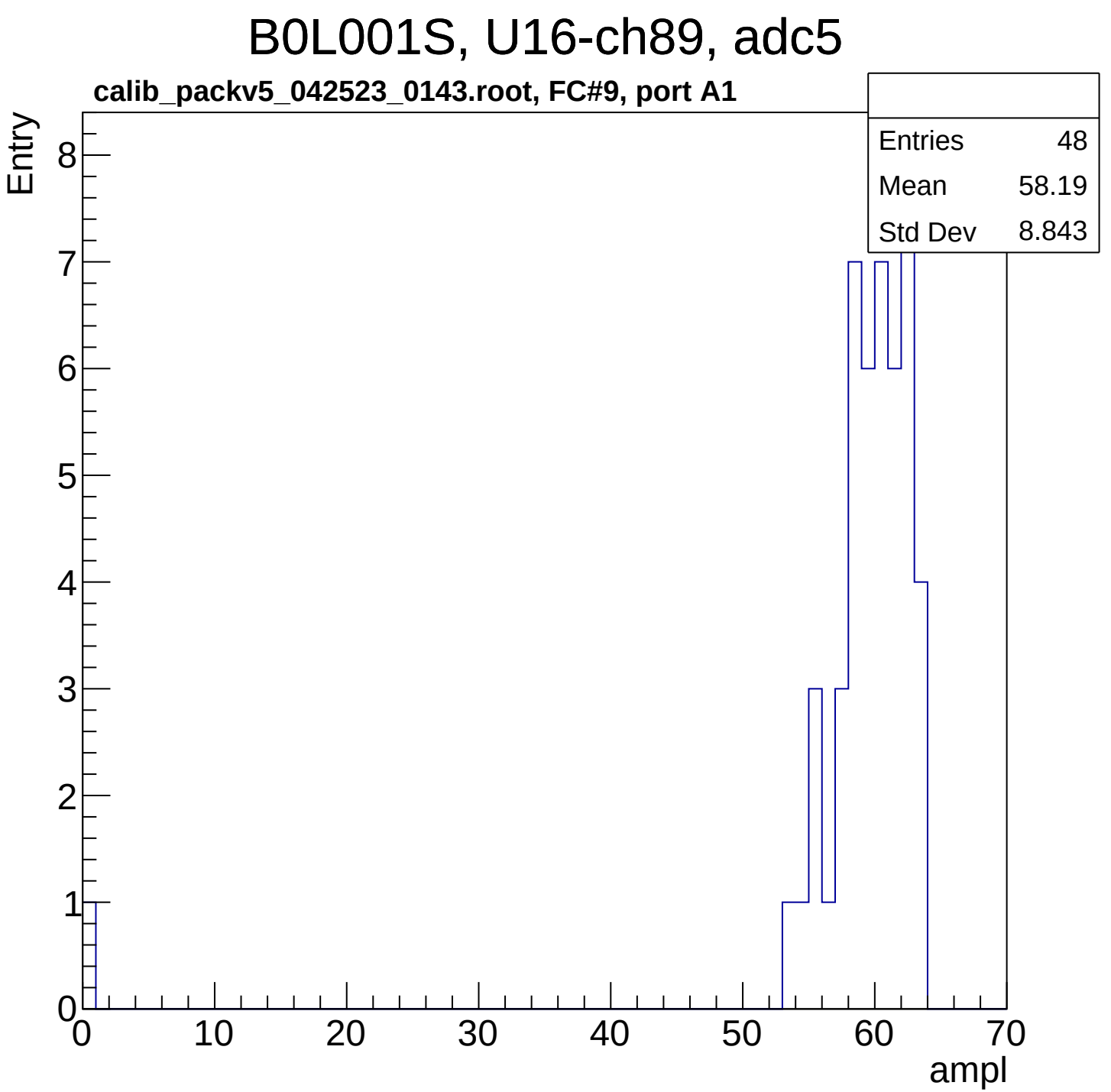
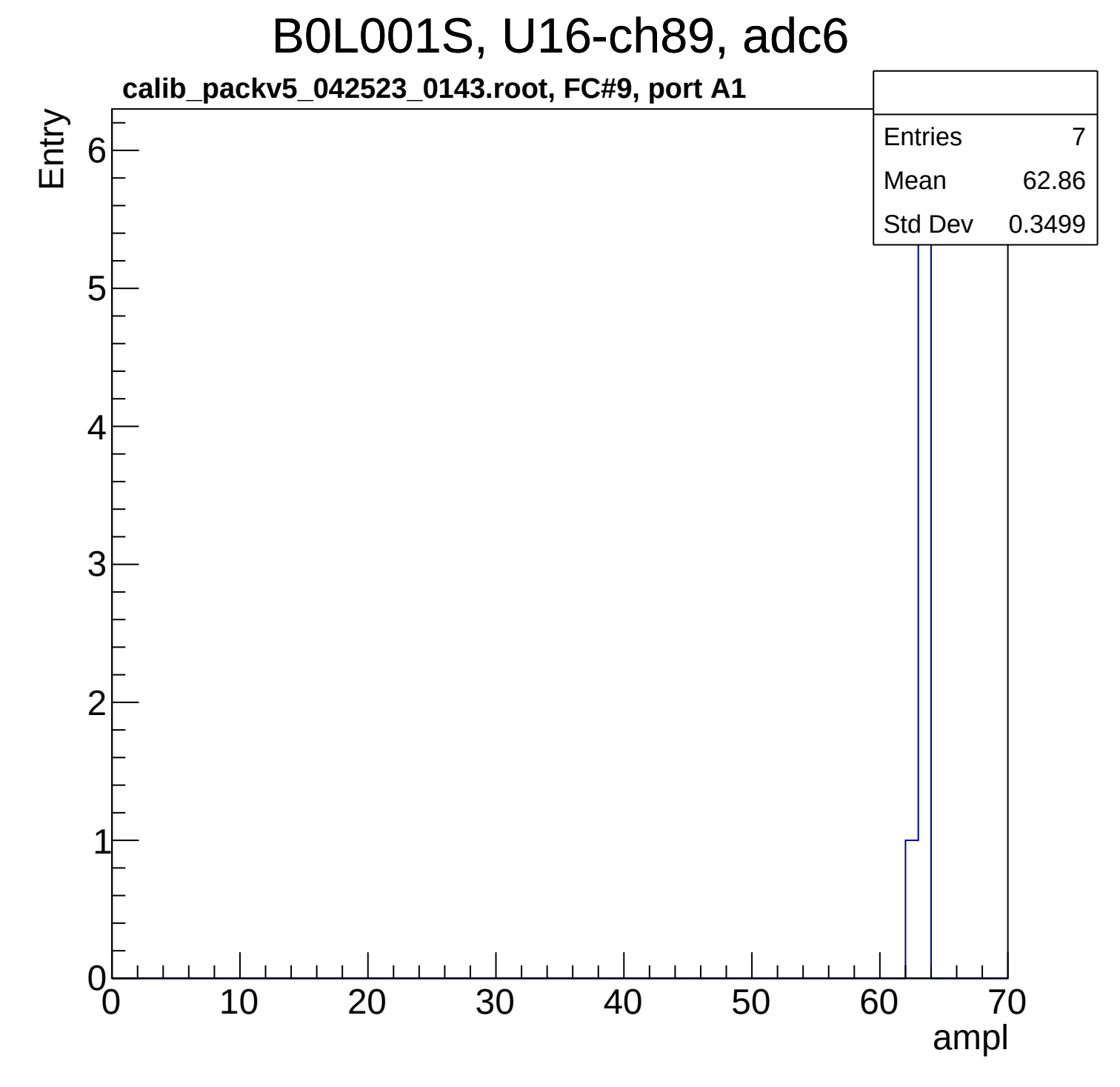
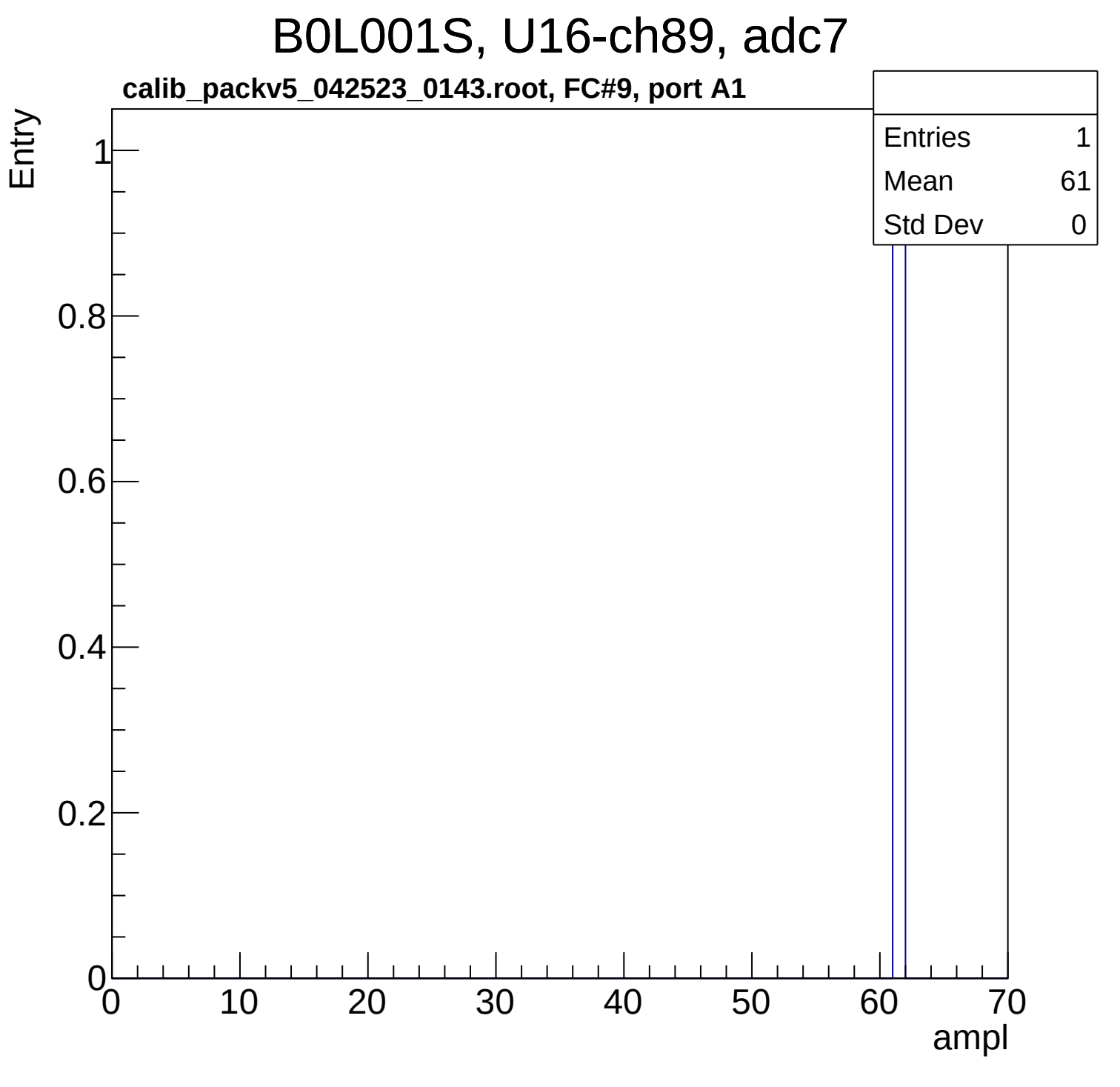
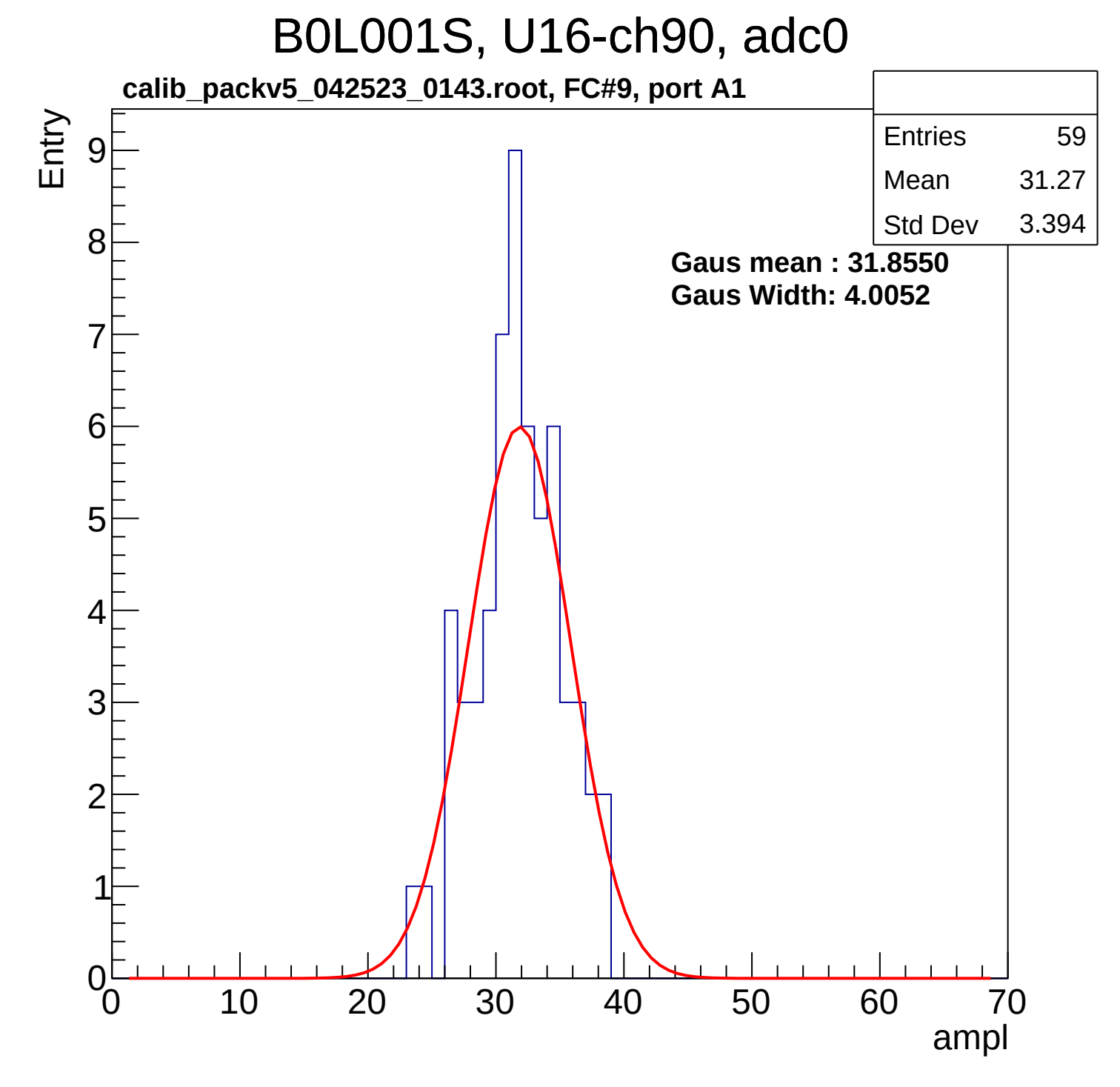

B0L001S, U16-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 49.08
Std Dev 4.313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

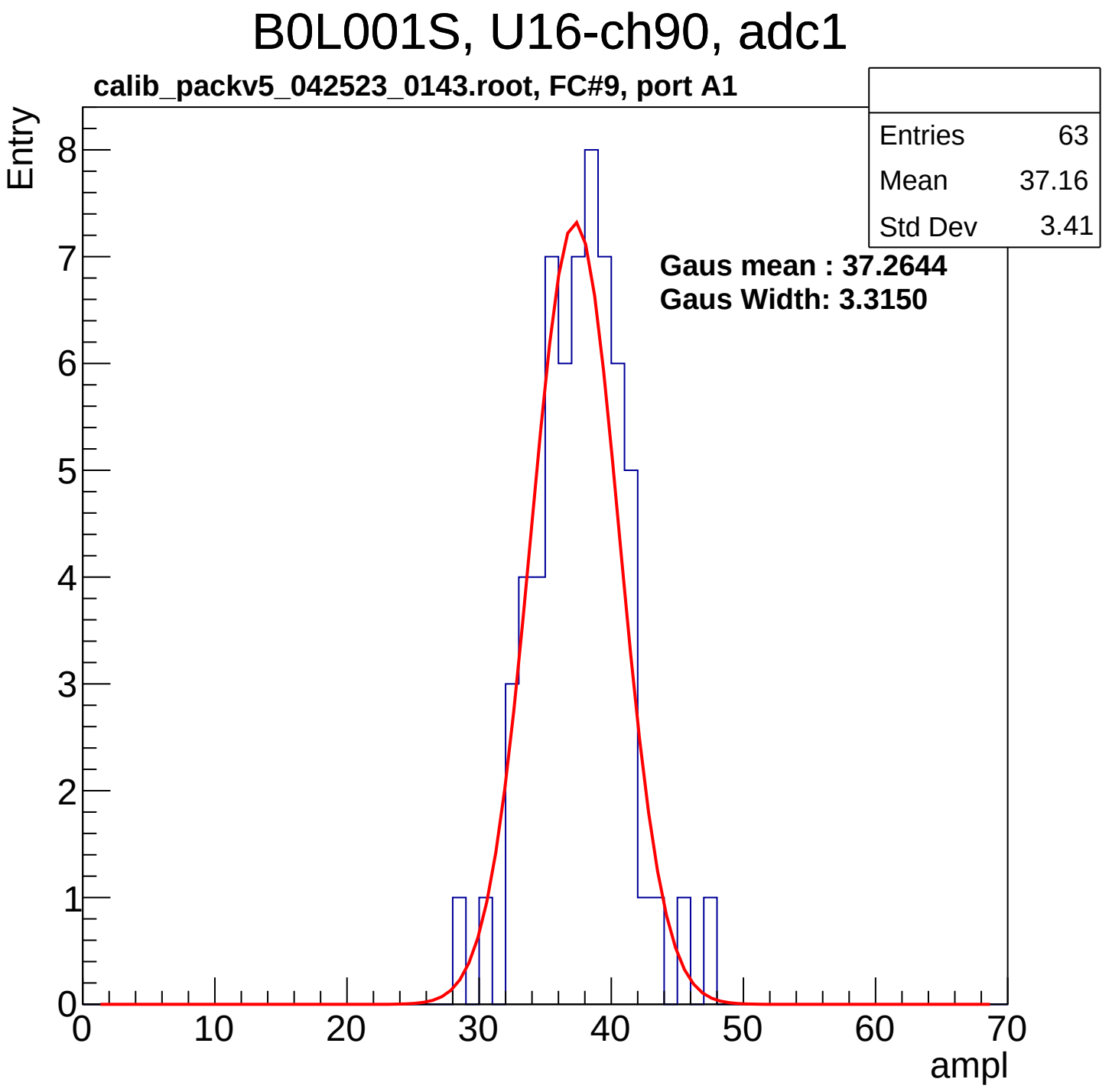

B0L001S, U16-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 55.68
Std Dev 2.859
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

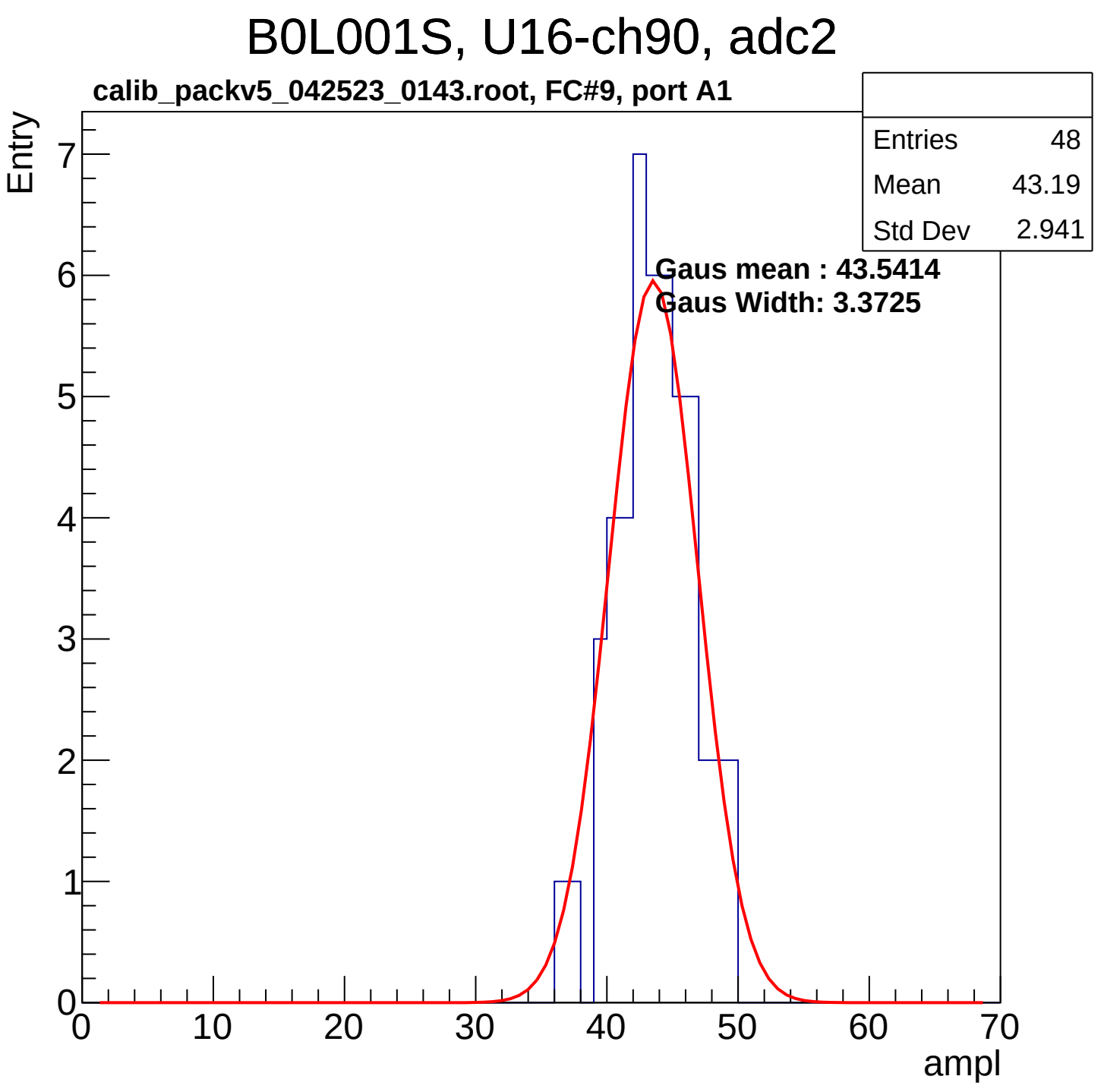

B0L001S, U16-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.19
Std Dev 8.843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

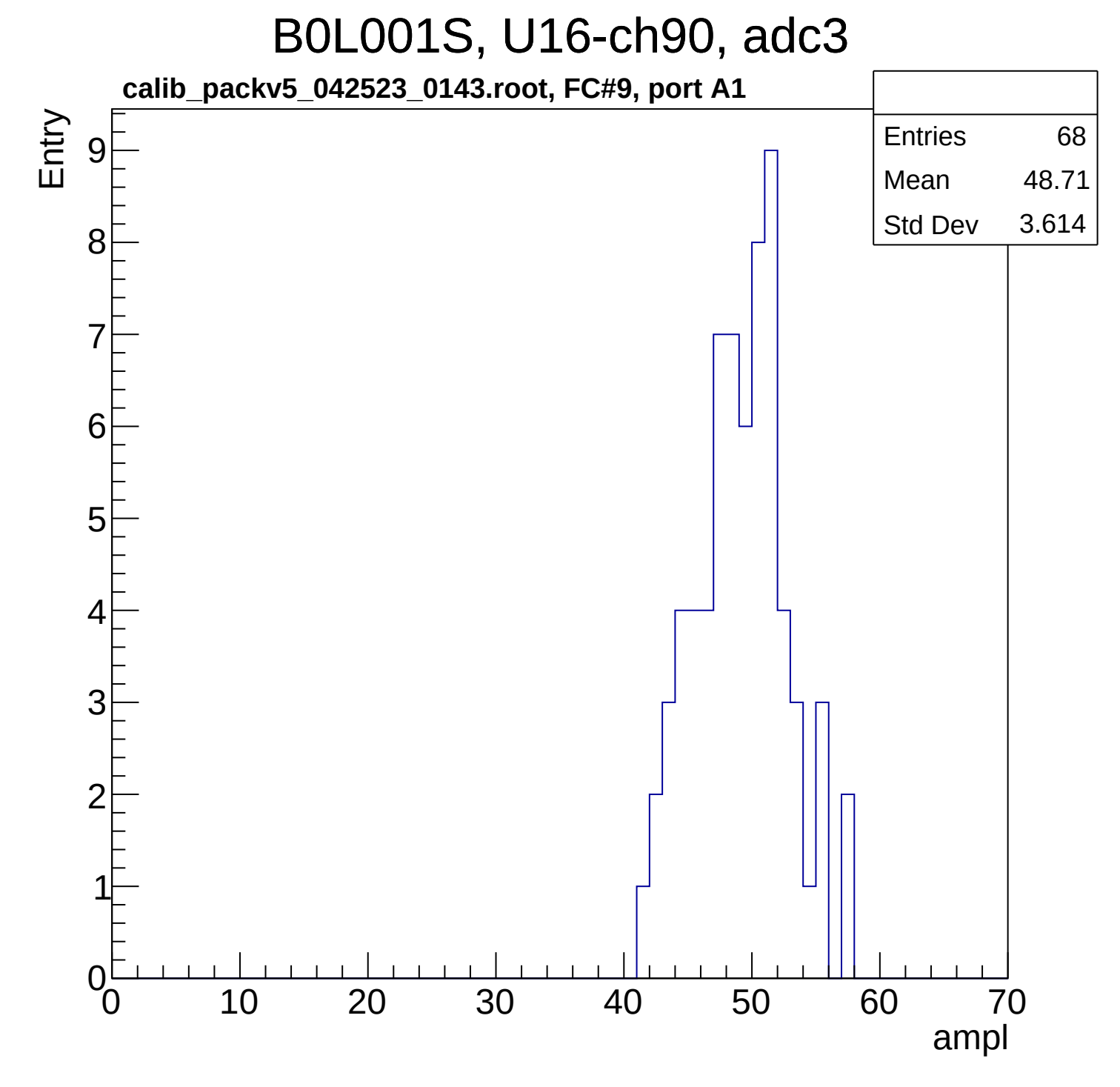

B0L001S, U16-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.86
Std Dev 0.3499
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 31.27
Std Dev 3.394
Gaus mean : 31.8550
Gaus Width: 4.0052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.16
Std Dev 3.41
Gaus mean : 37.2644
Gaus Width: 3.3150
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 43.19
Std Dev 2.941
Gaus mean : 43.5414
Gaus Width: 3.3725
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.71
Std Dev 3.614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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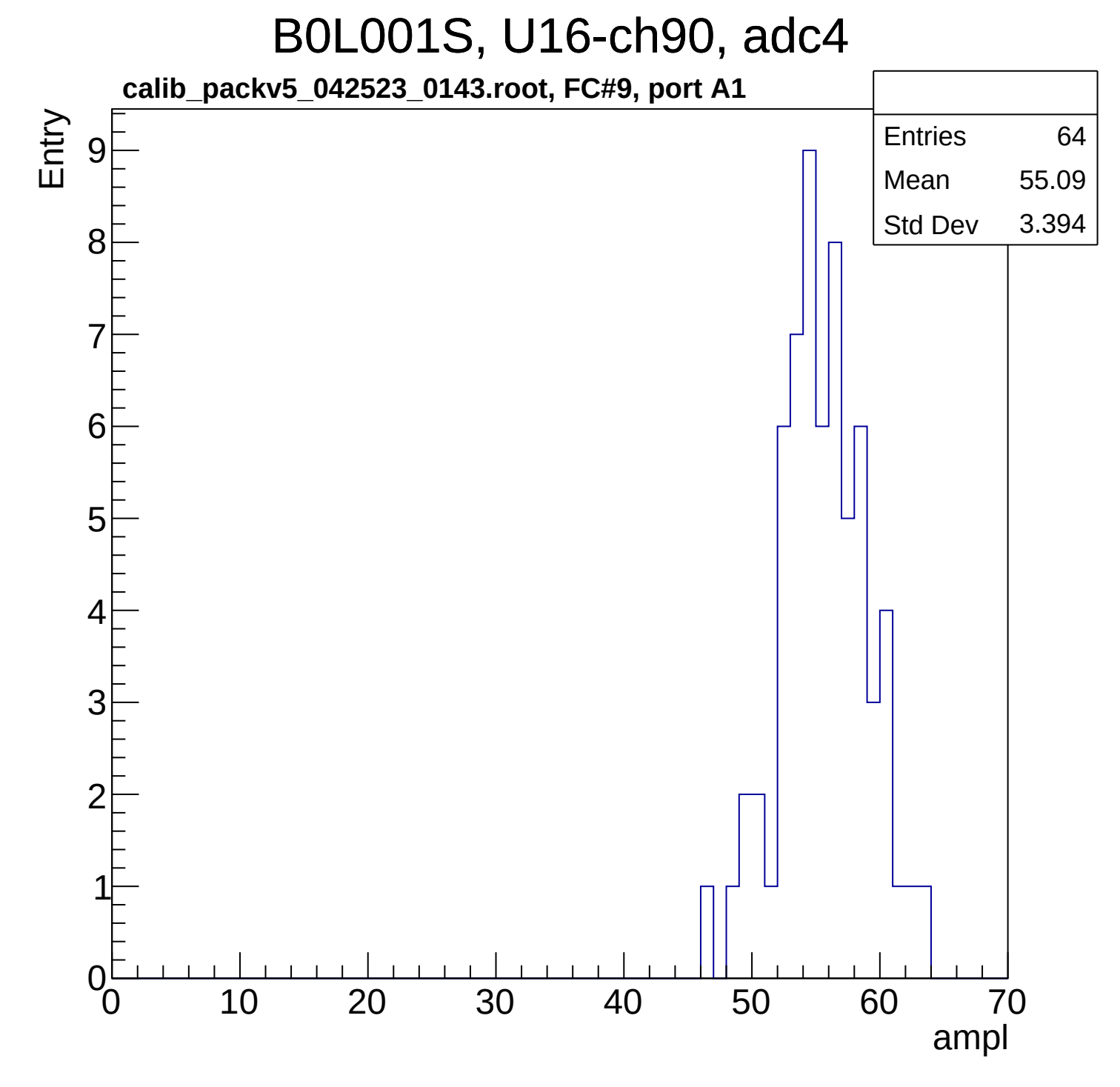
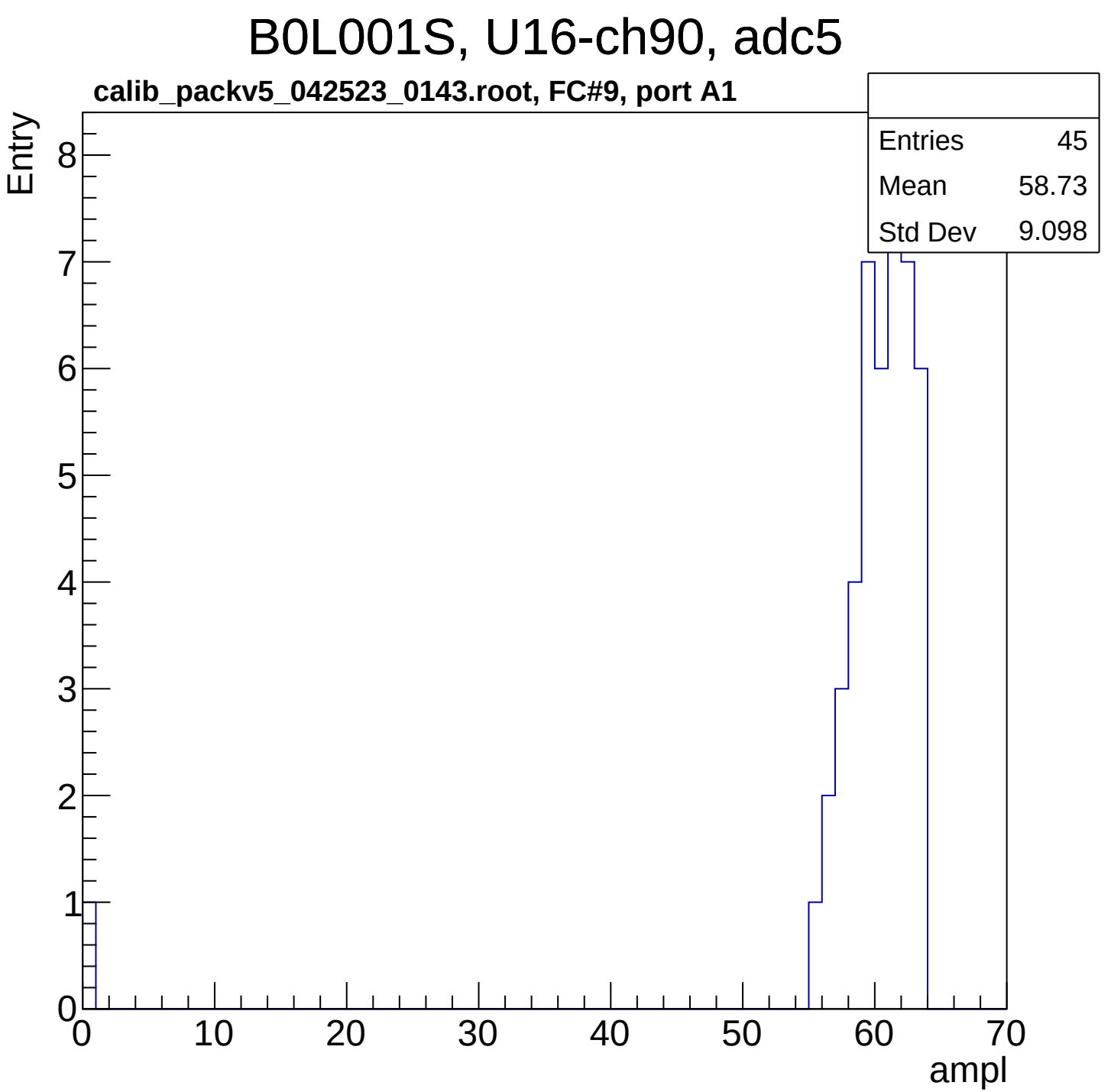
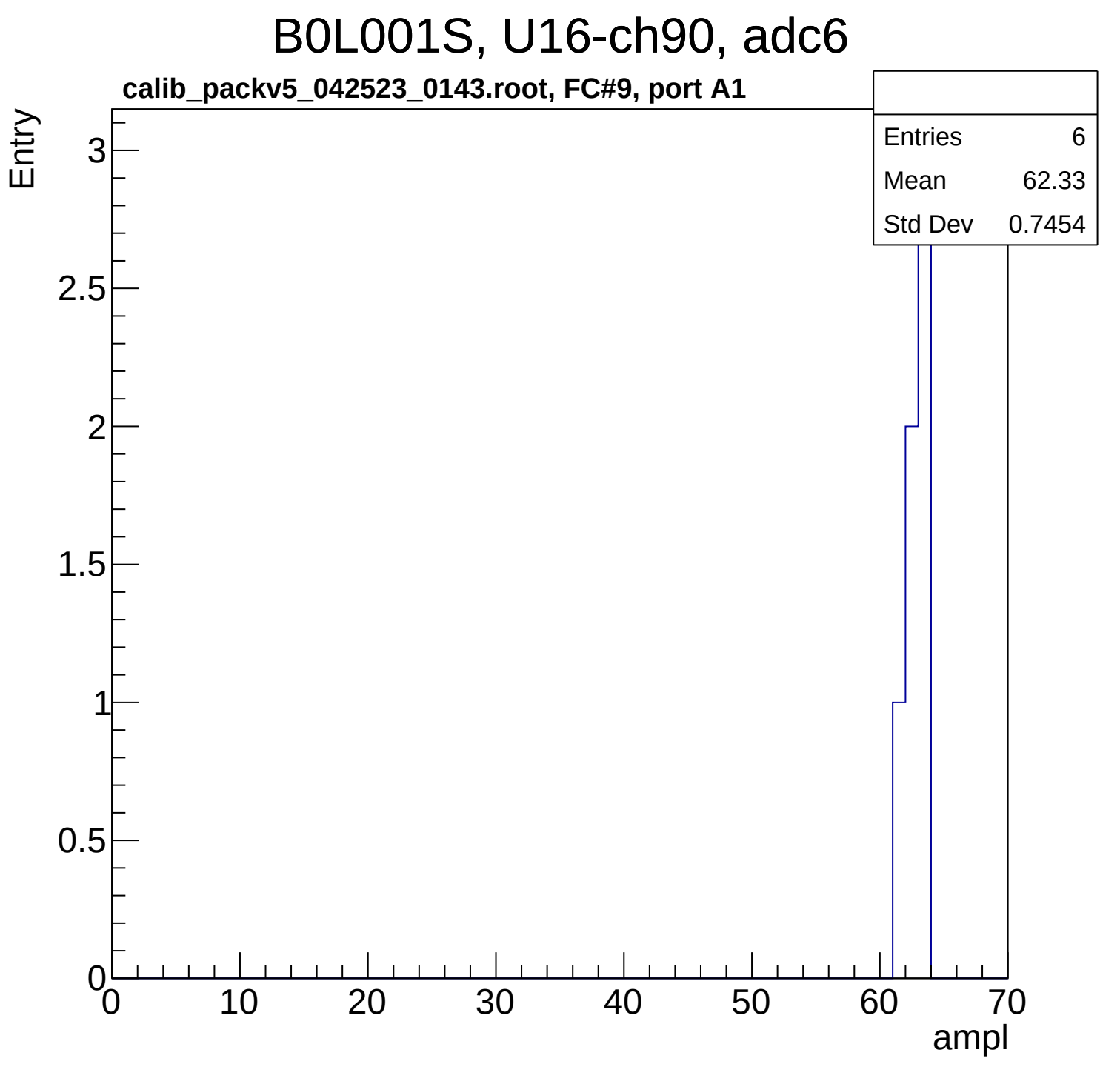

B0L001S, U16-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.09
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.73
Std Dev 9.098
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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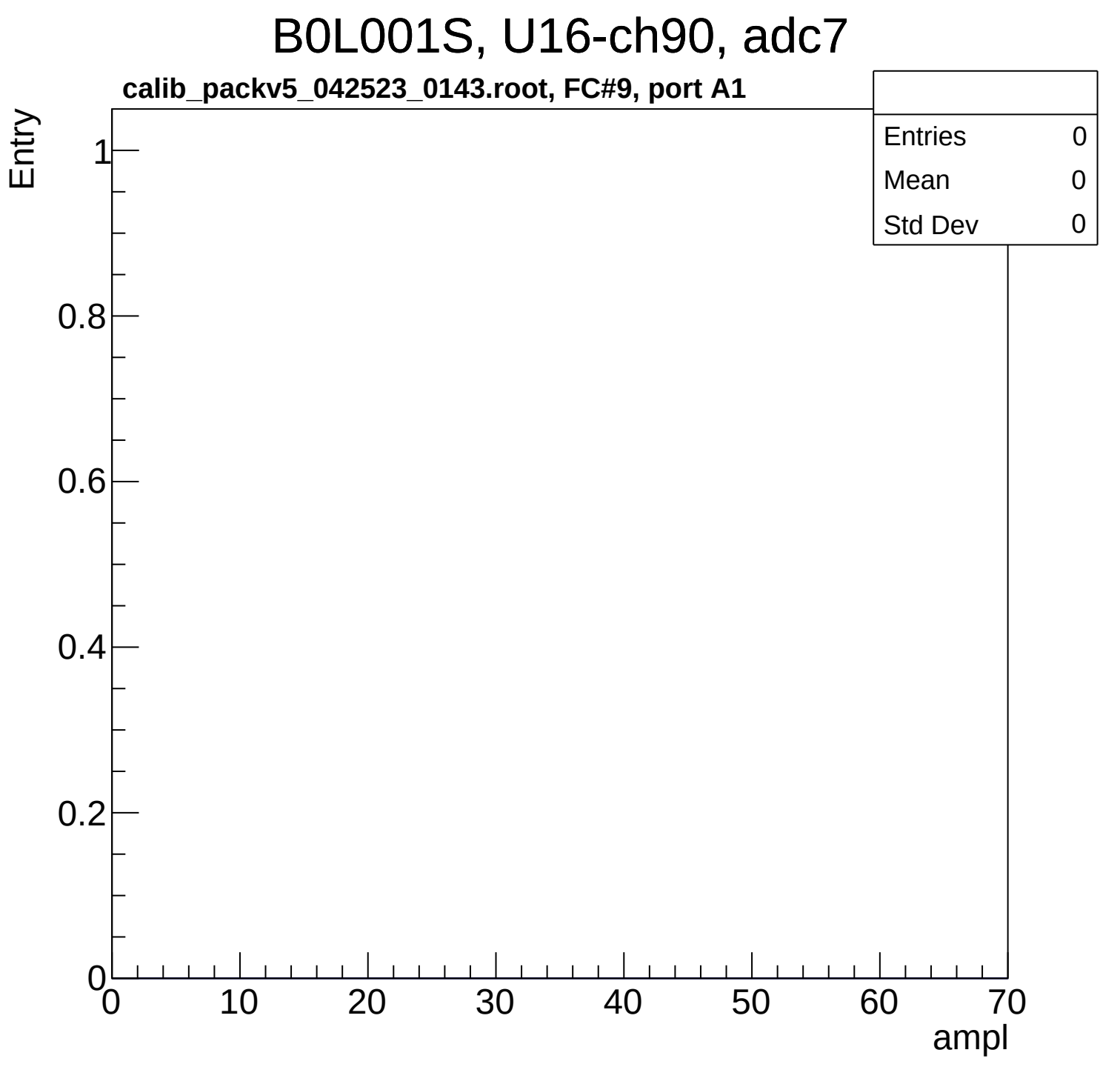
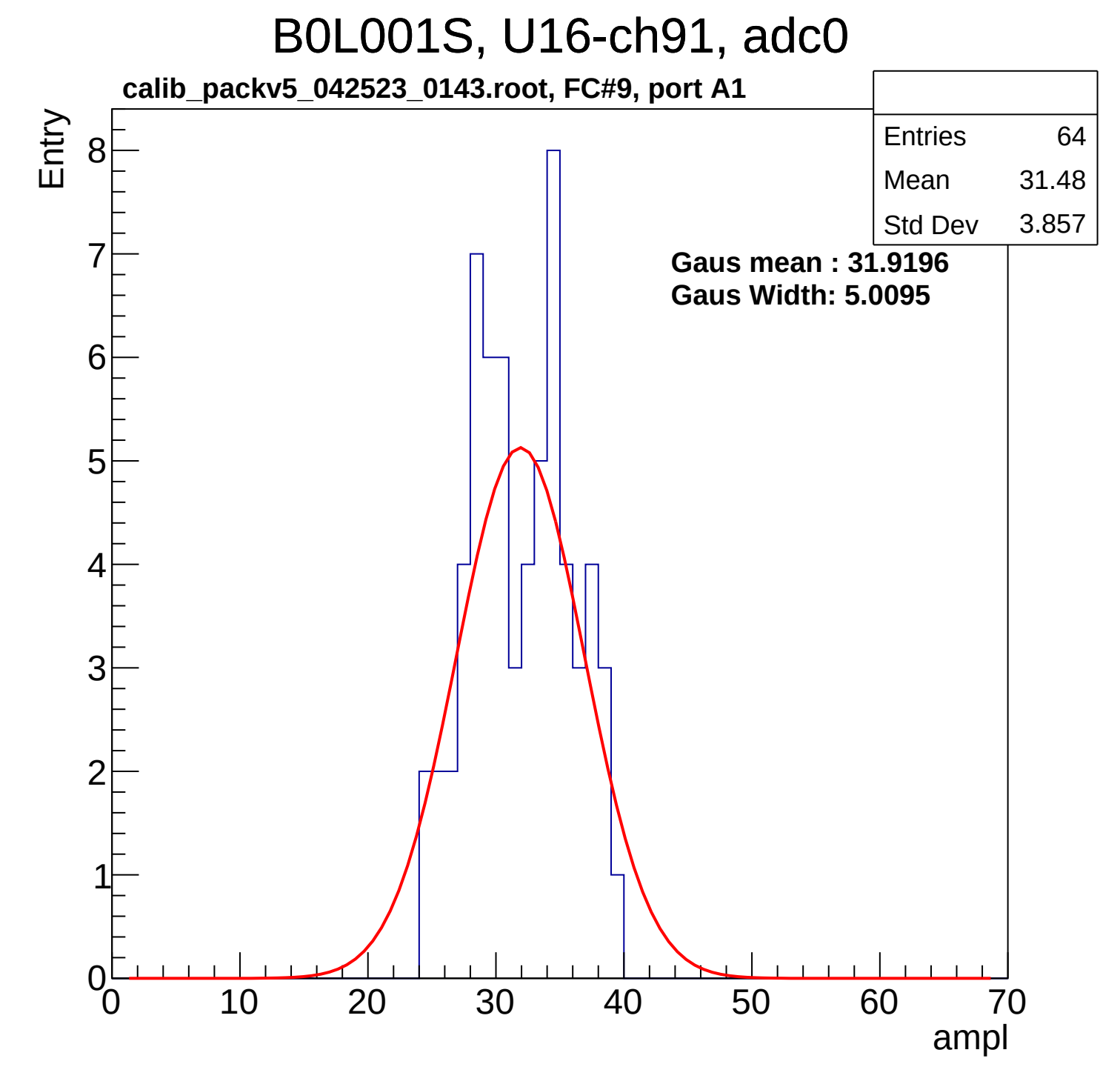
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 31.48
Std Dev 3.857
Gaus mean : 31.9196
Gaus Width: 5.0095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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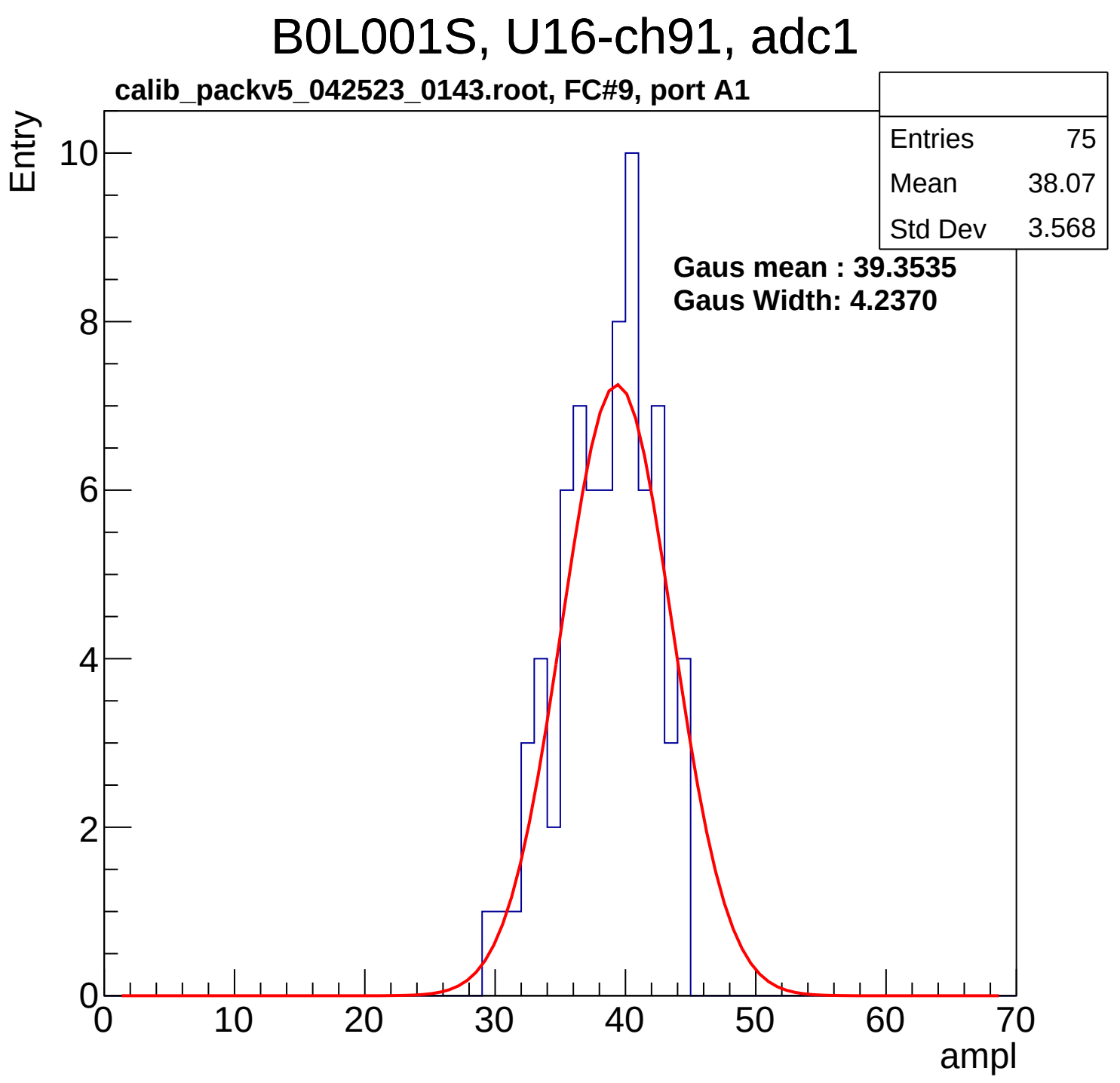
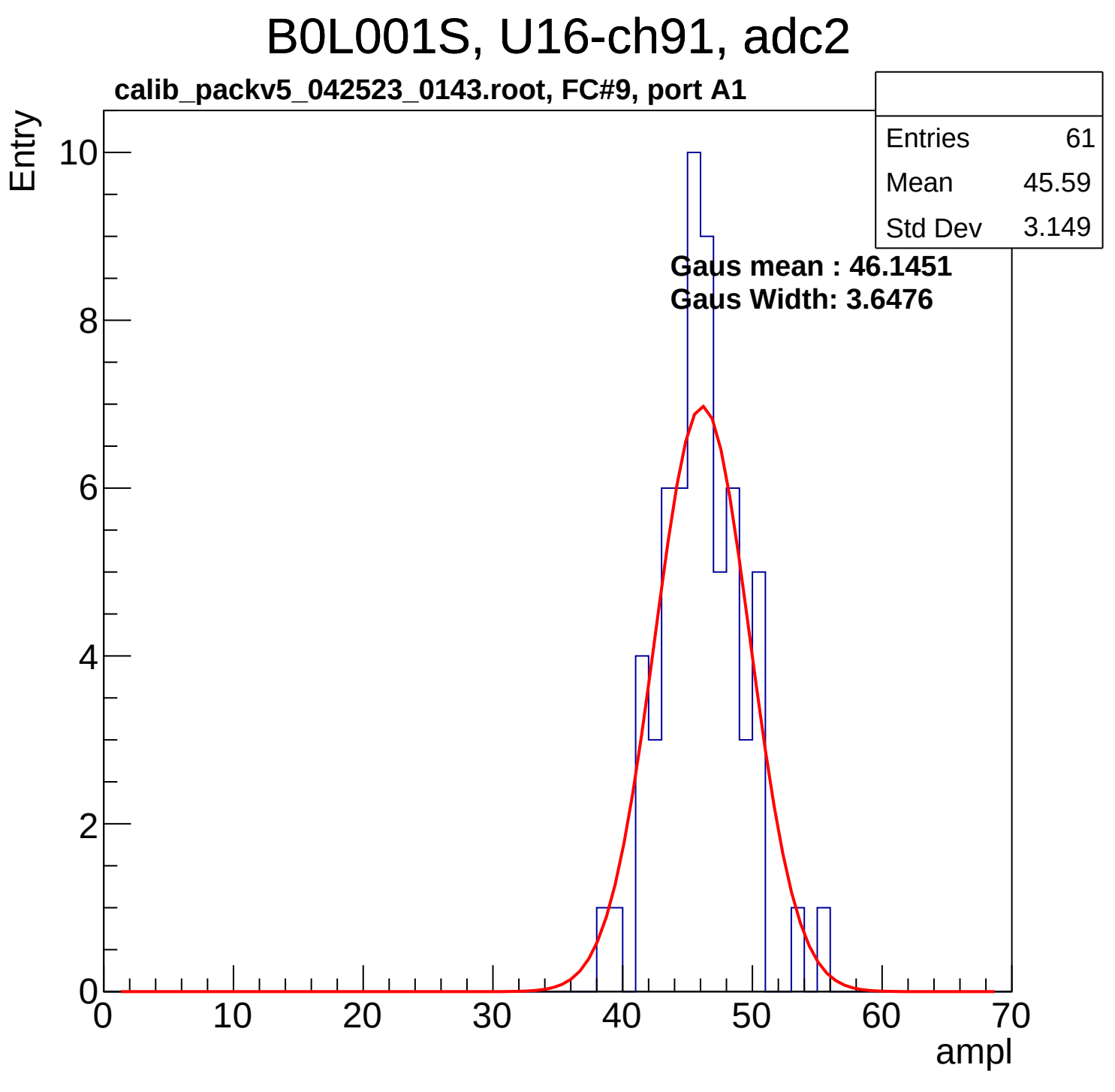
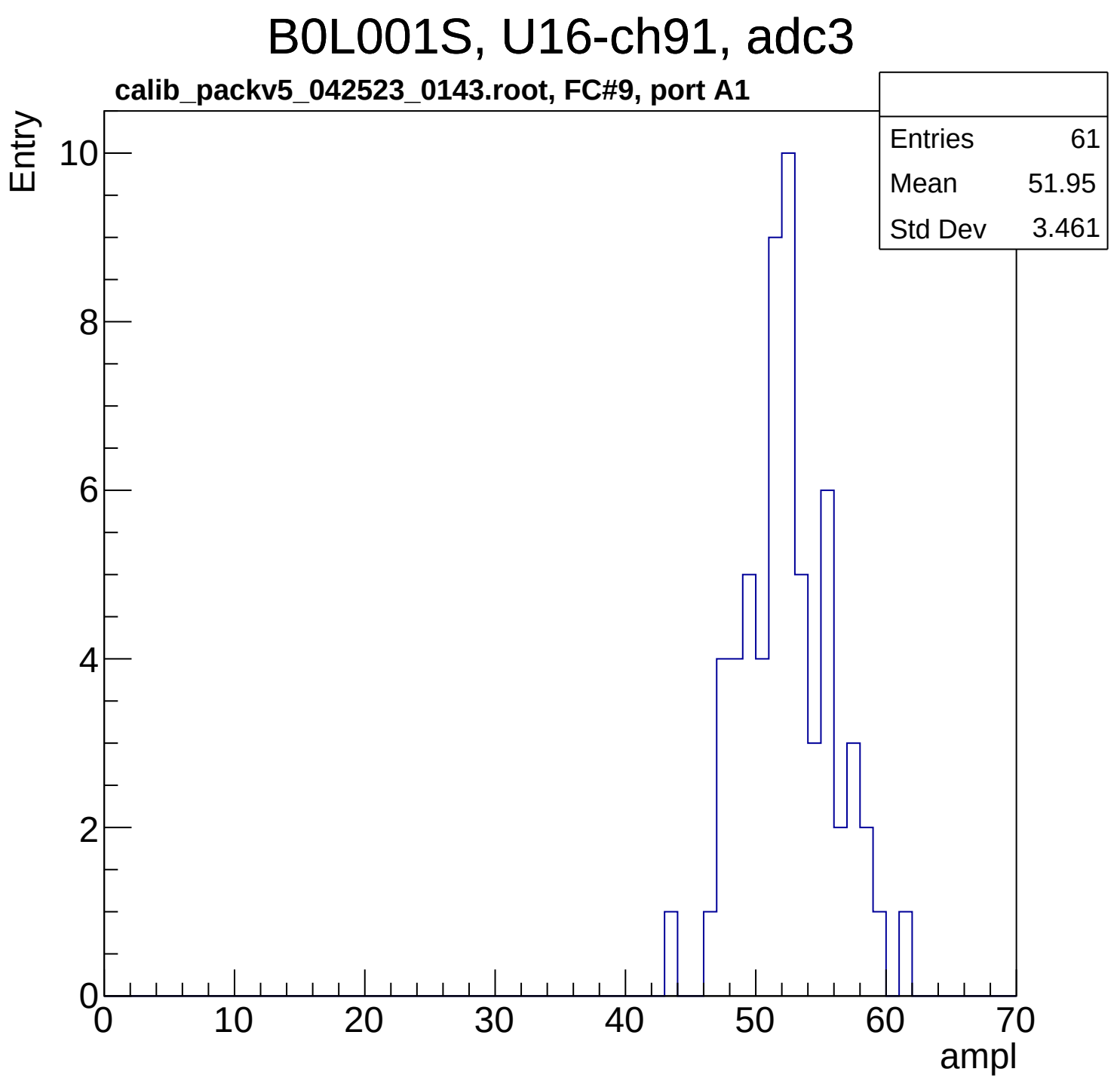
40
50
60
70


B0L001S, U16-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.07
Std Dev 3.568
Gaus mean : 39.3535
Gaus Width: 4.2370
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.59
Std Dev 3.149
Gaus mean : 46.1451
Gaus Width: 3.6476
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.95
Std Dev 3.461
Entry
ampl
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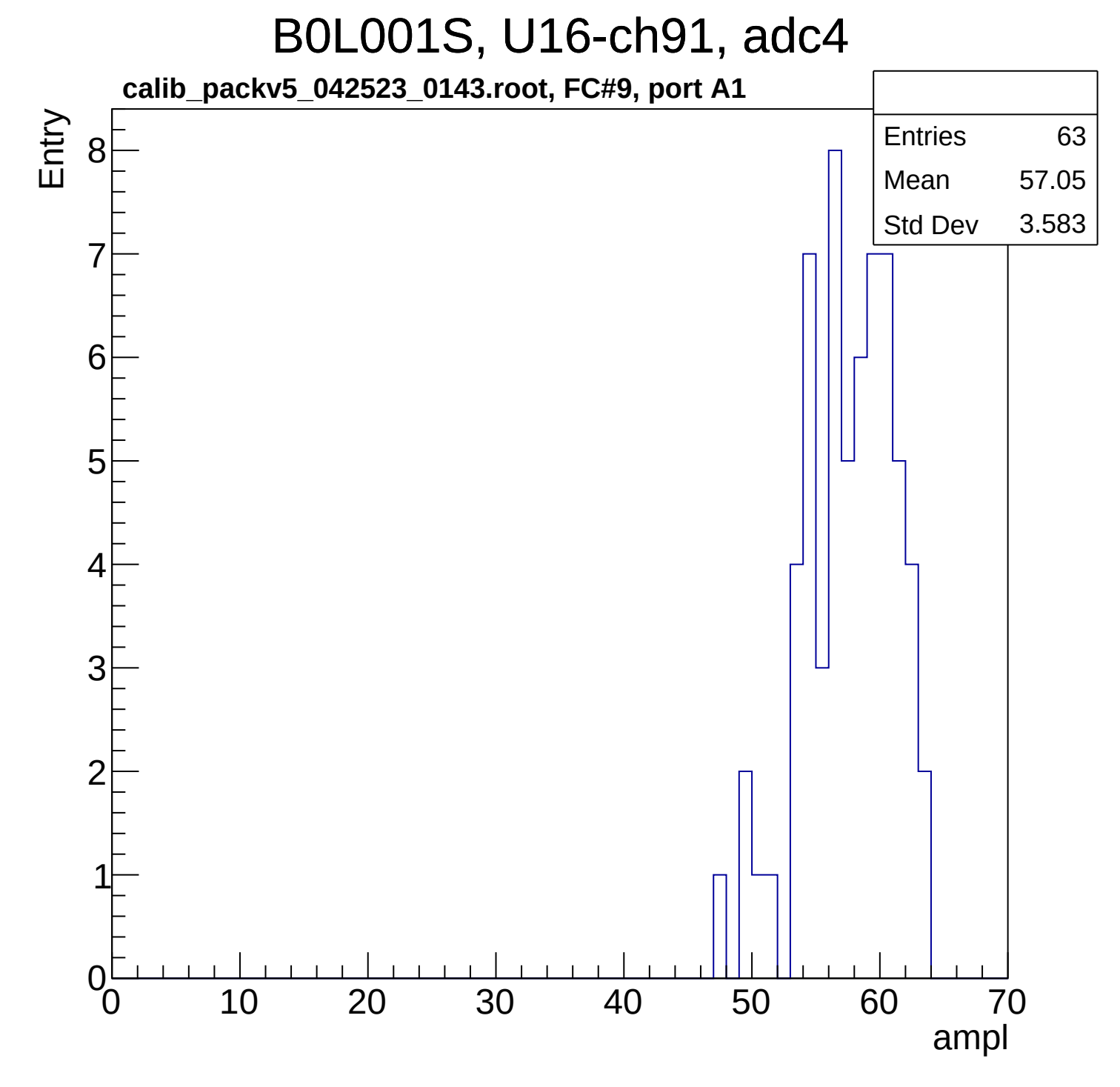
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 57.05
Std Dev 3.583
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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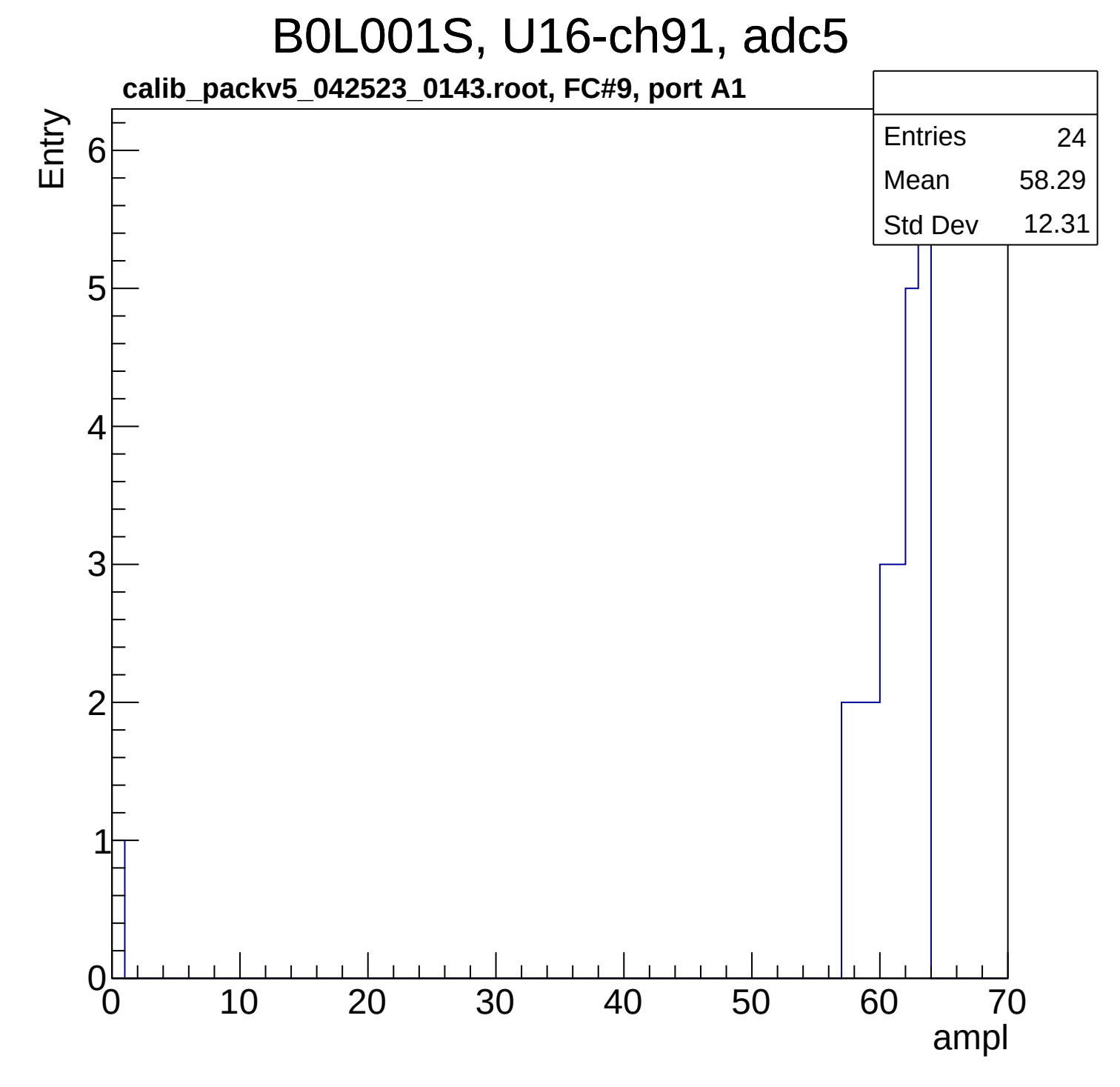
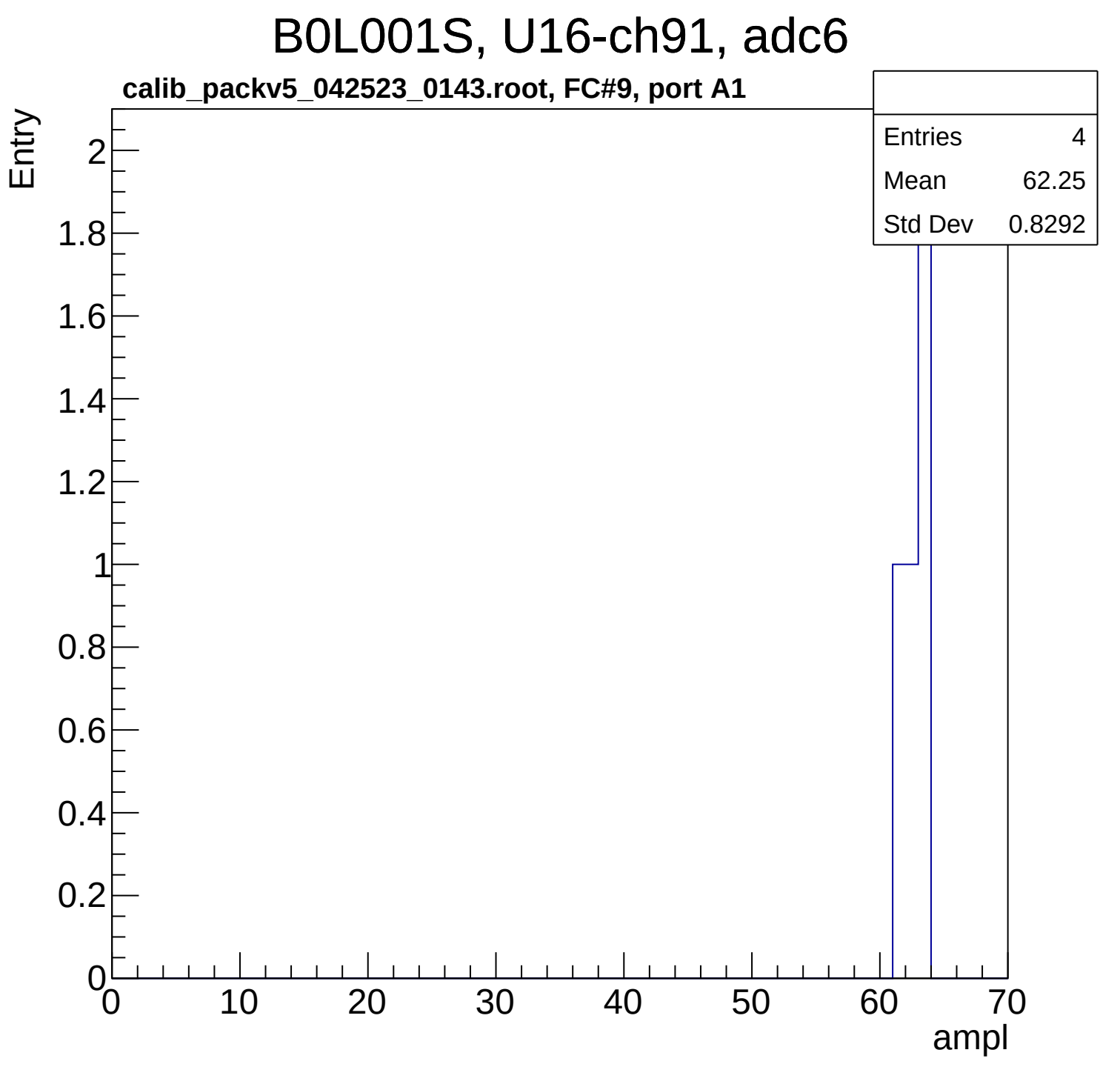
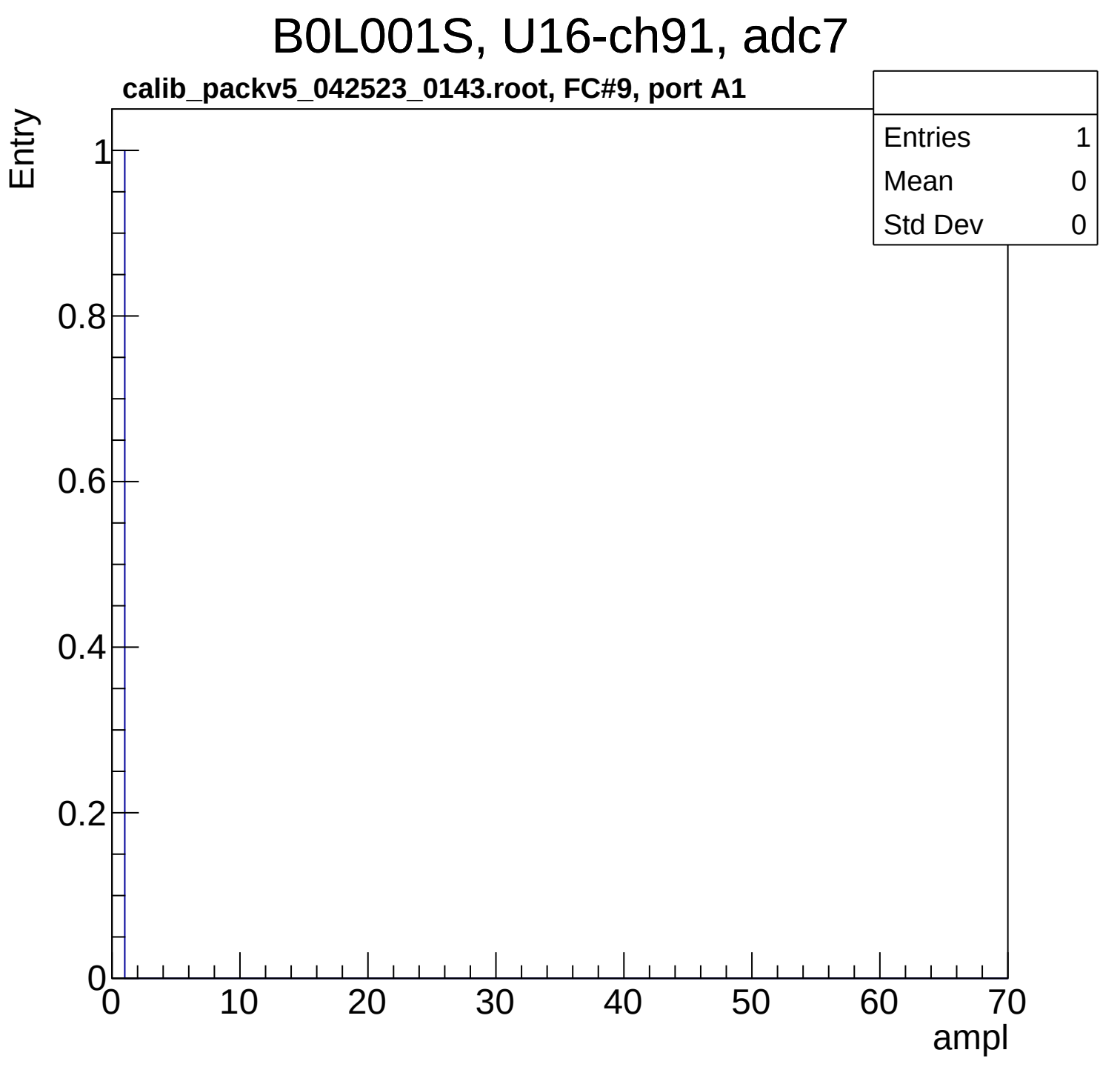

B0L001S, U16-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 24
Mean 58.29
Std Dev 12.31
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
30
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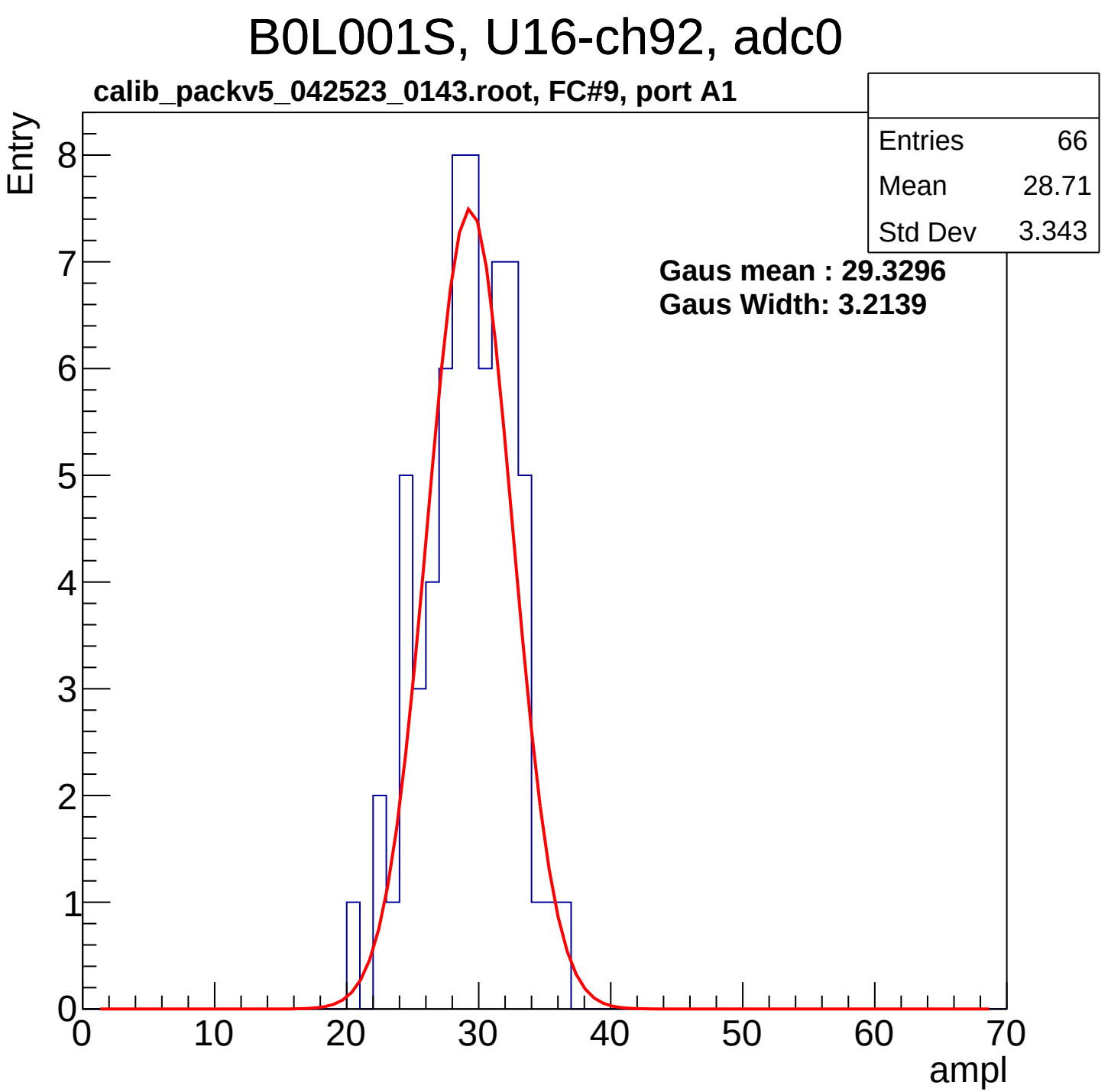
50
60
70
ampl


B0L001S, U16-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.71
Std Dev 3.343
Gaus mean : 29.3296
Gaus Width: 3.2139
Entry
ampl
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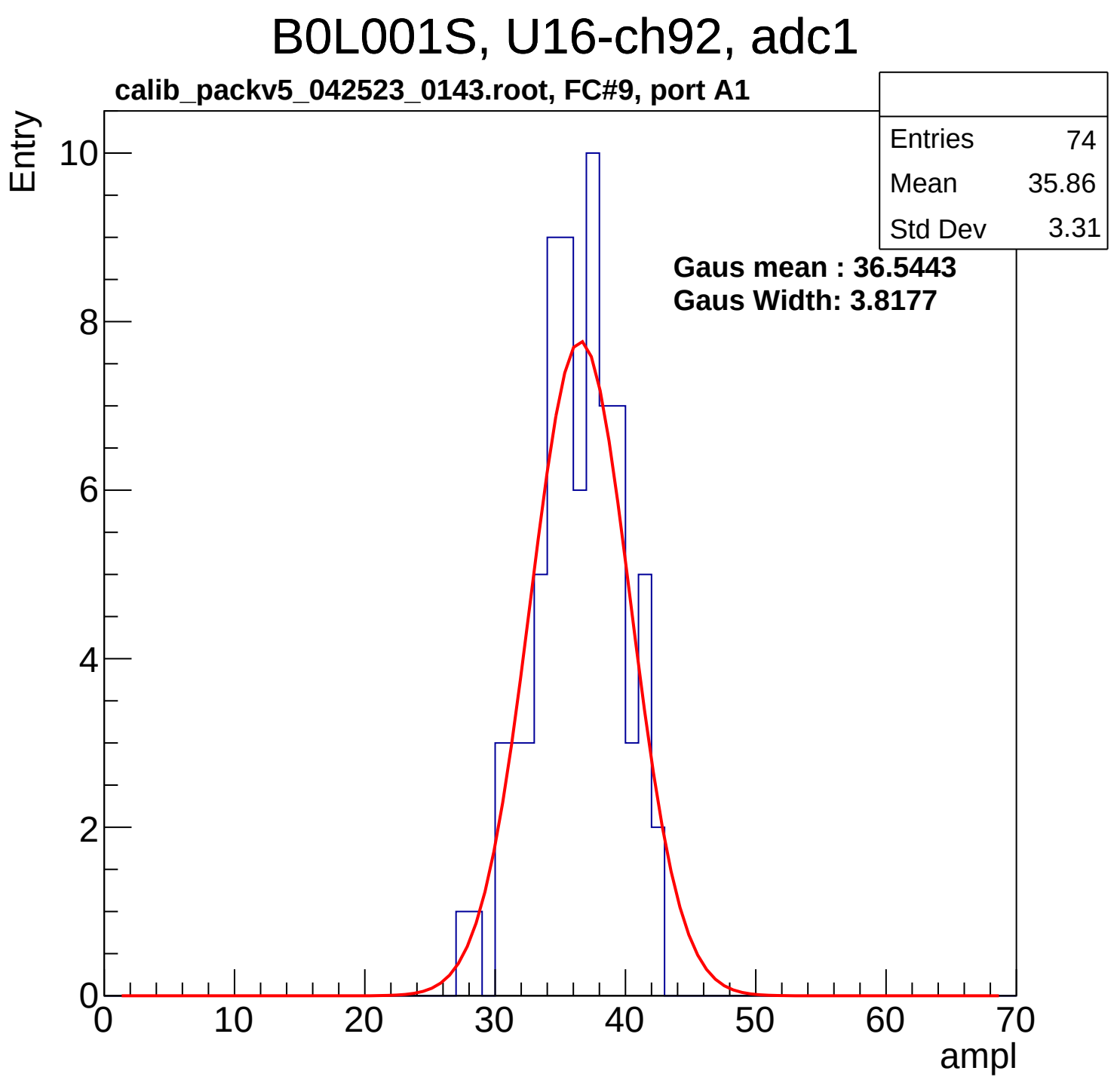
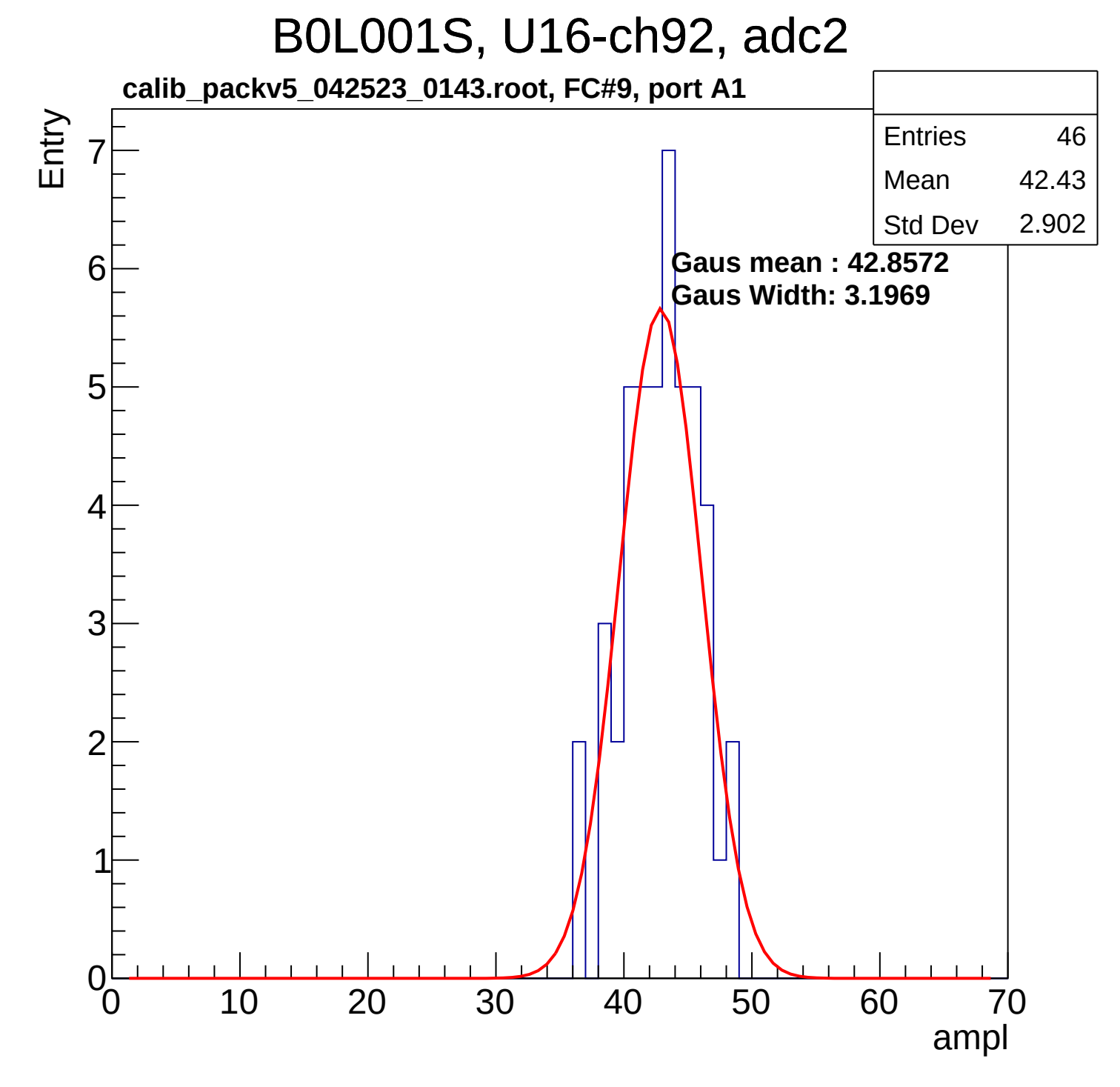
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 35.86
Std Dev 3.31
Gaus mean : 36.5443
Gaus Width: 3.8177
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 42.43
Std Dev 2.902
Gaus mean : 42.8572
Gaus Width: 3.1969
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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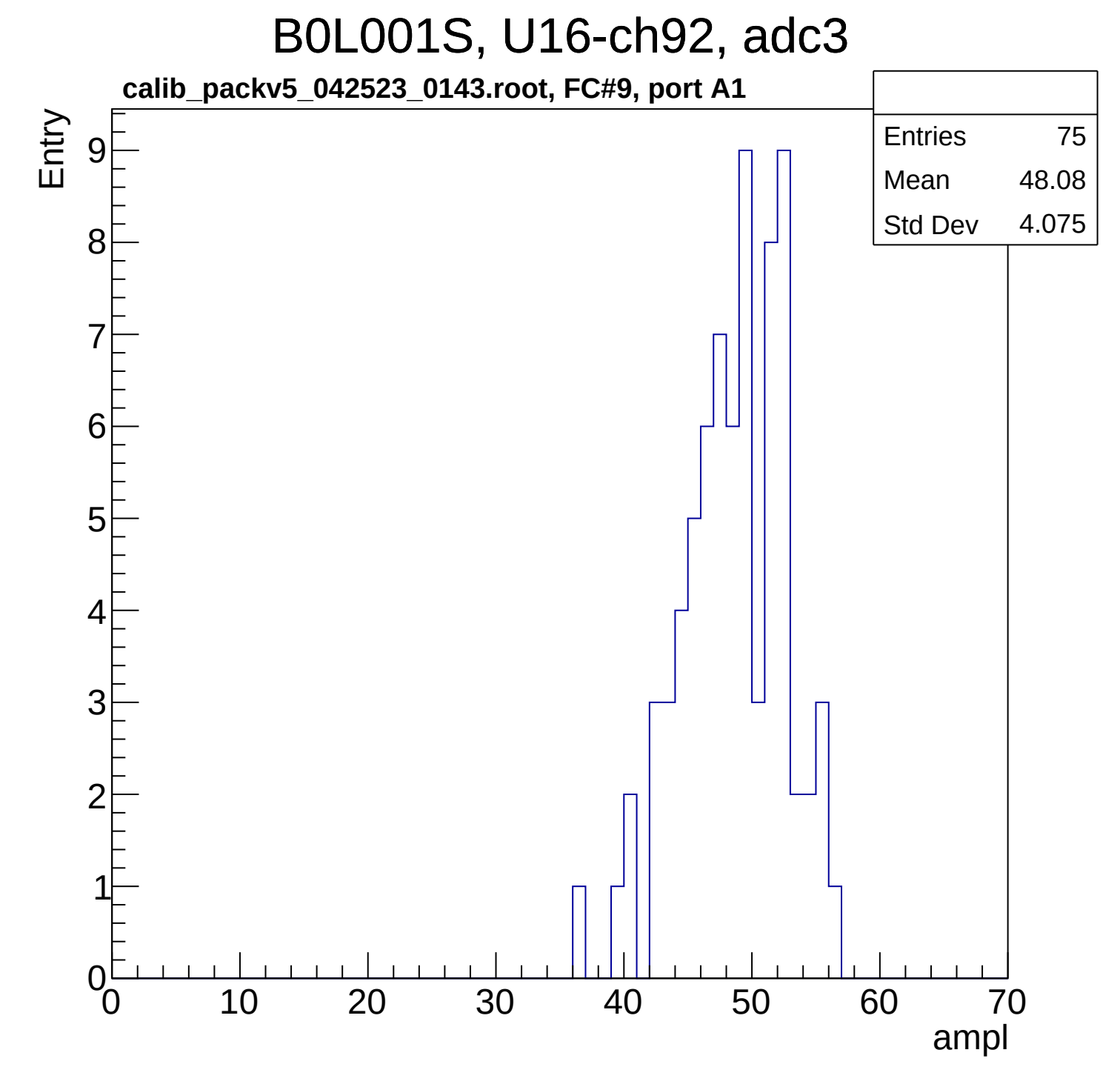
B0L001S, U16-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 48.08
Std Dev 4.075
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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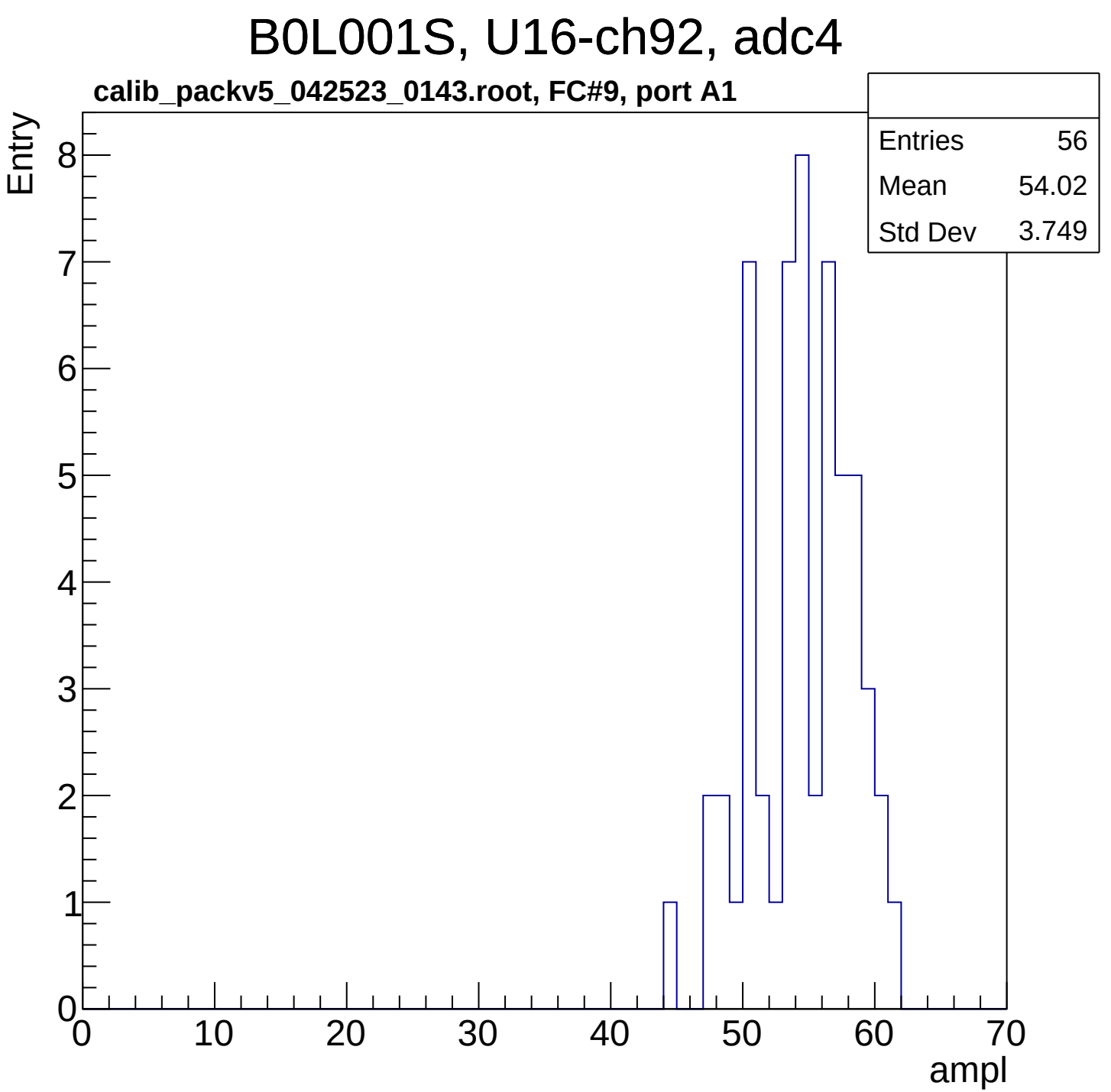
B0L001S, U16-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 54.02
Std Dev 3.749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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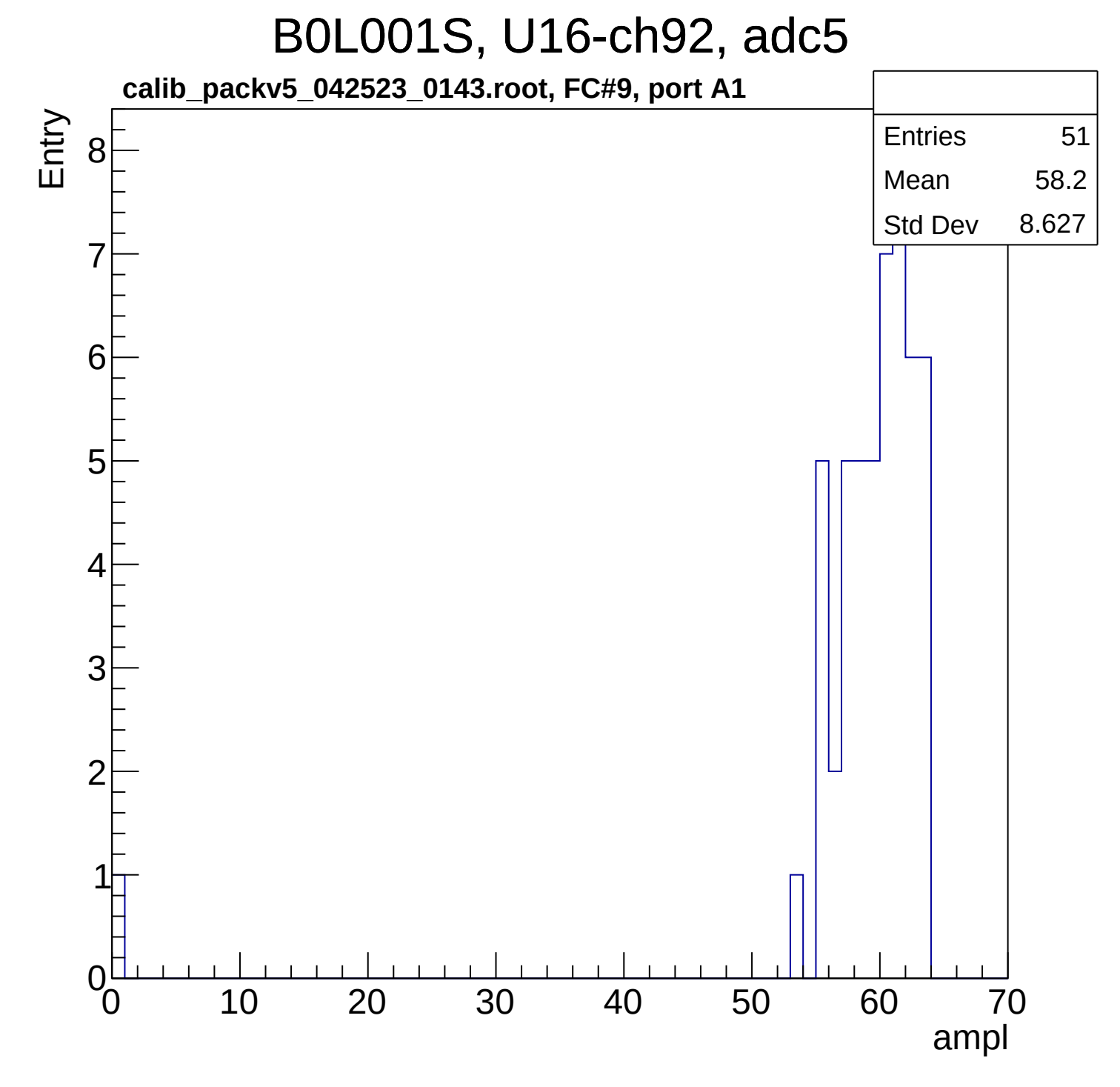
B0L001S, U16-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.2
Std Dev 8.627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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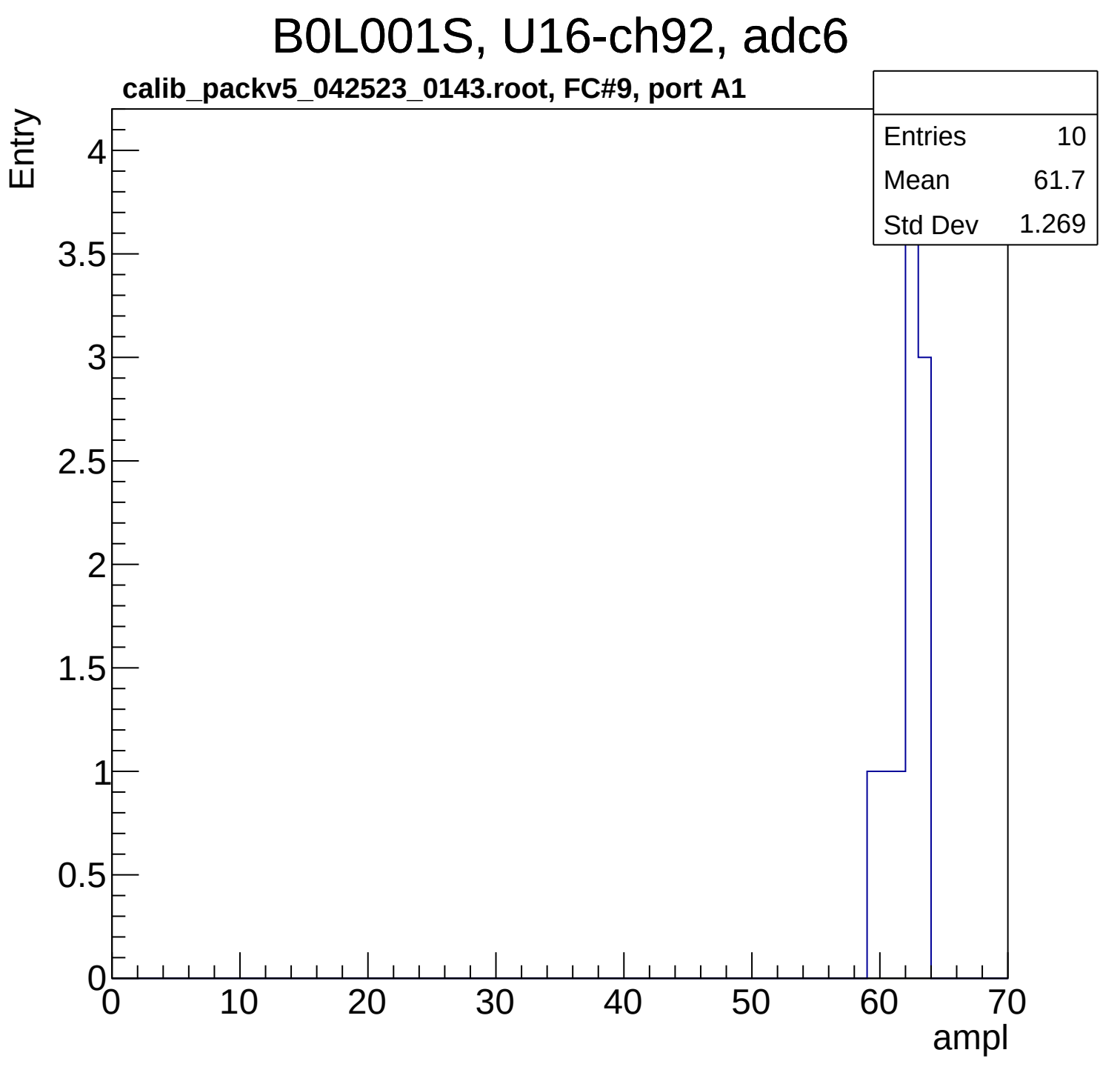
B0L001S, U16-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.7
Std Dev 1.269
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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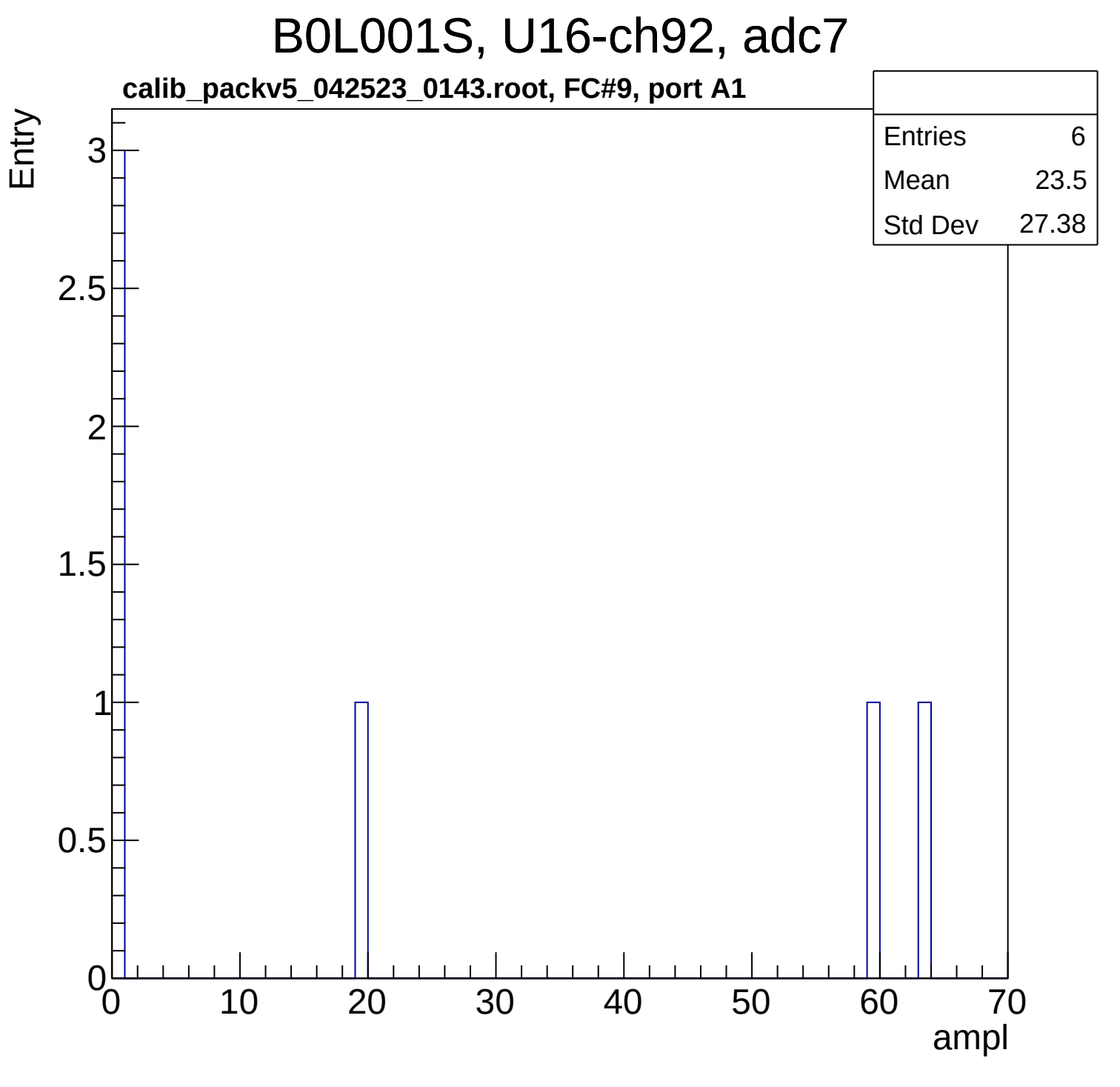
B0L001S, U16-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 23.5
Std Dev 27.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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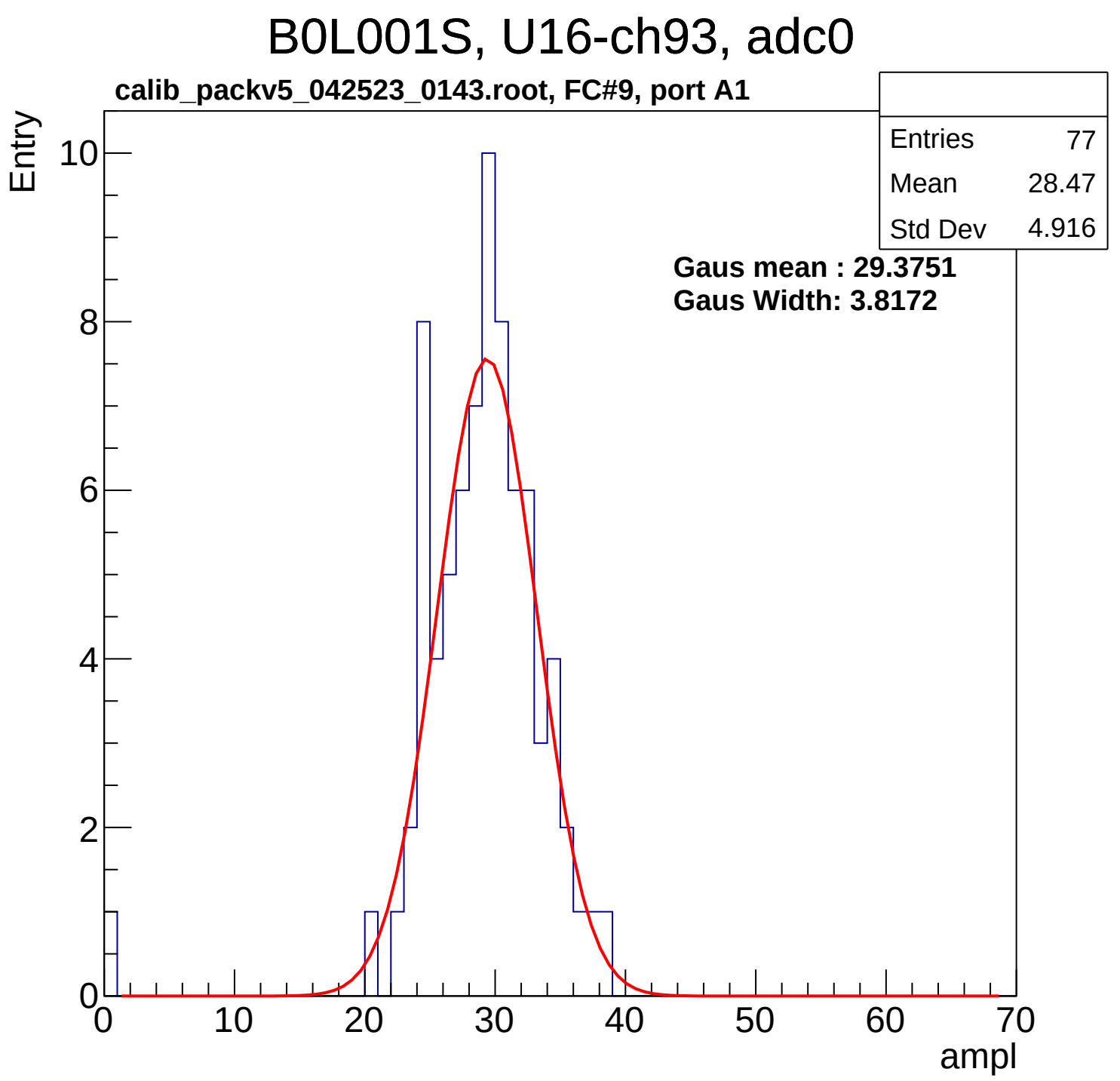
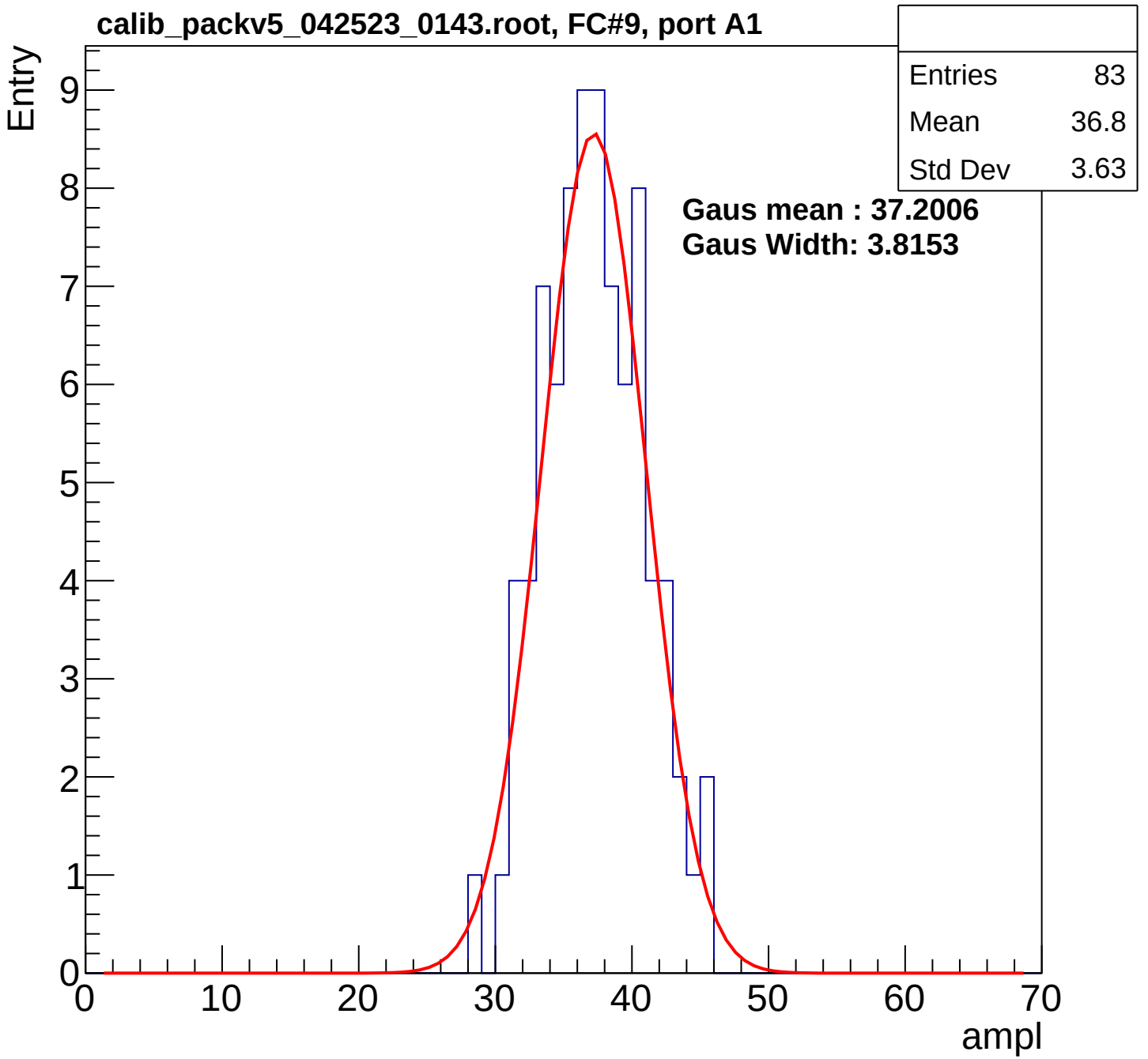

B0L001S, U16-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 28.47
Std Dev 4.916
Gaus mean : 29.3751
Gaus Width: 3.8172
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 36.8
Std Dev 3.63
Gaus mean : 37.2006
Gaus Width: 3.8153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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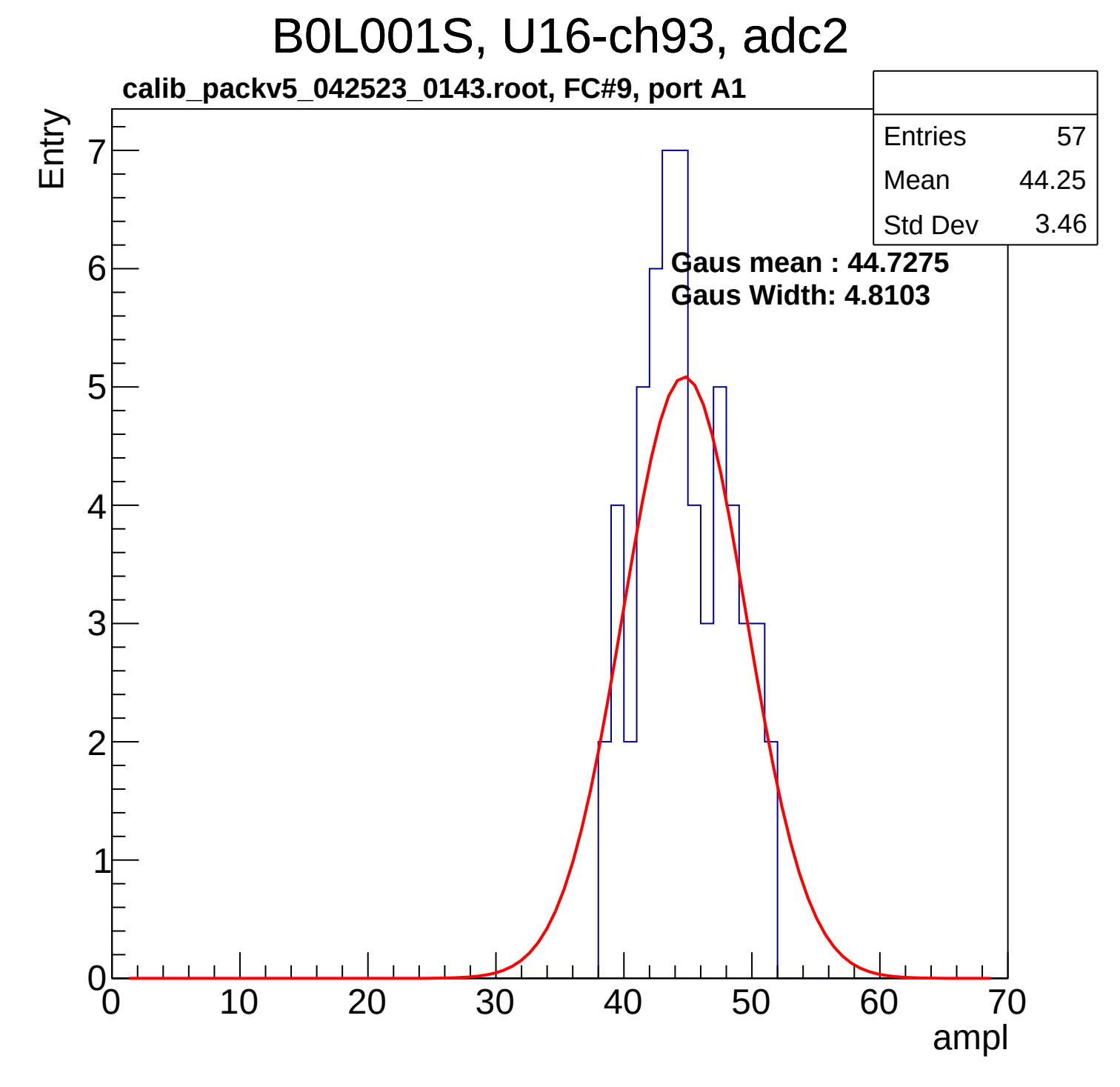
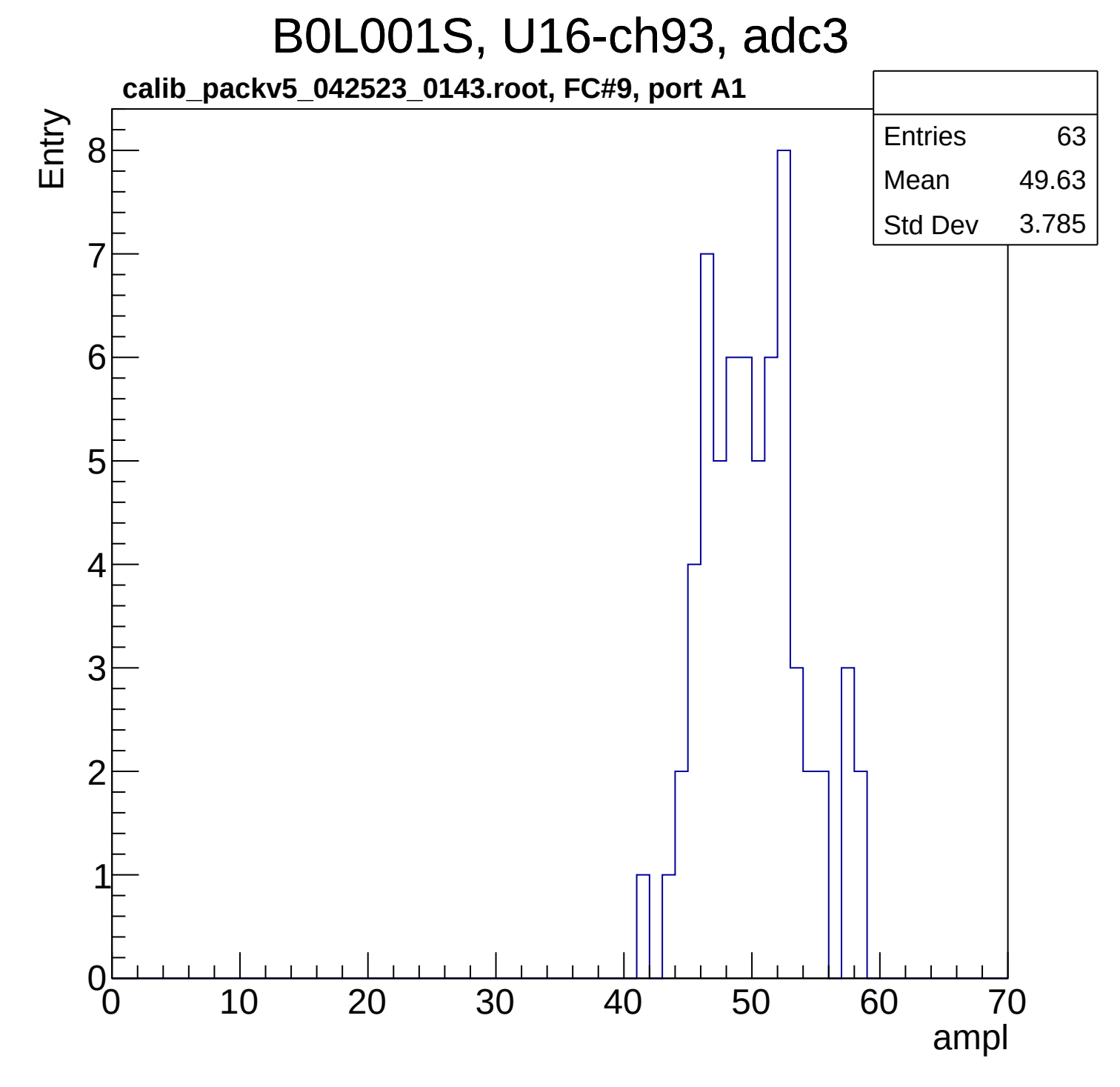
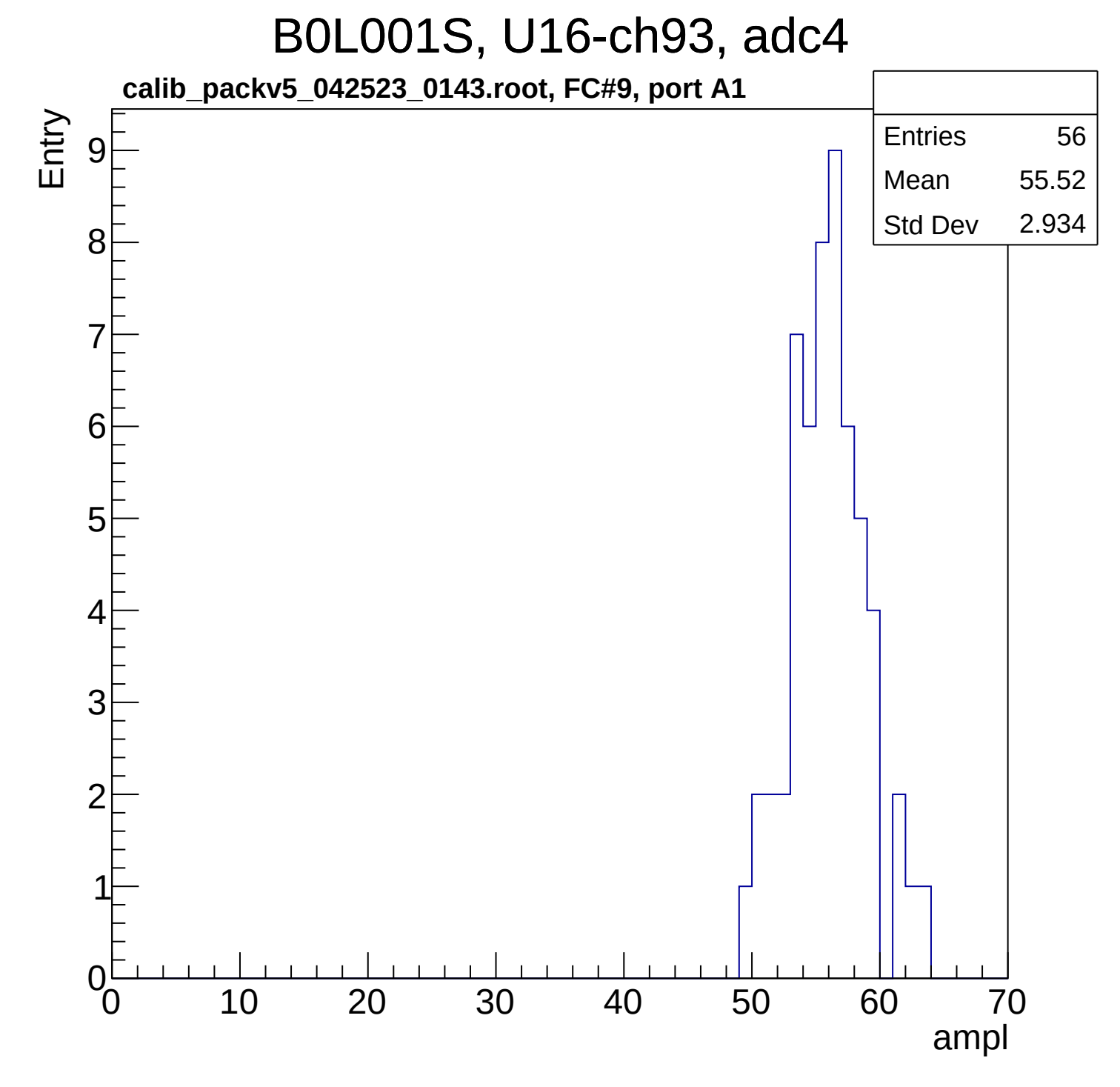

B0L001S, U16-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.25
Std Dev 3.46
Gaus mean : 44.7275
Gaus Width: 4.8103
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.63
Std Dev 3.785
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.52
Std Dev 2.934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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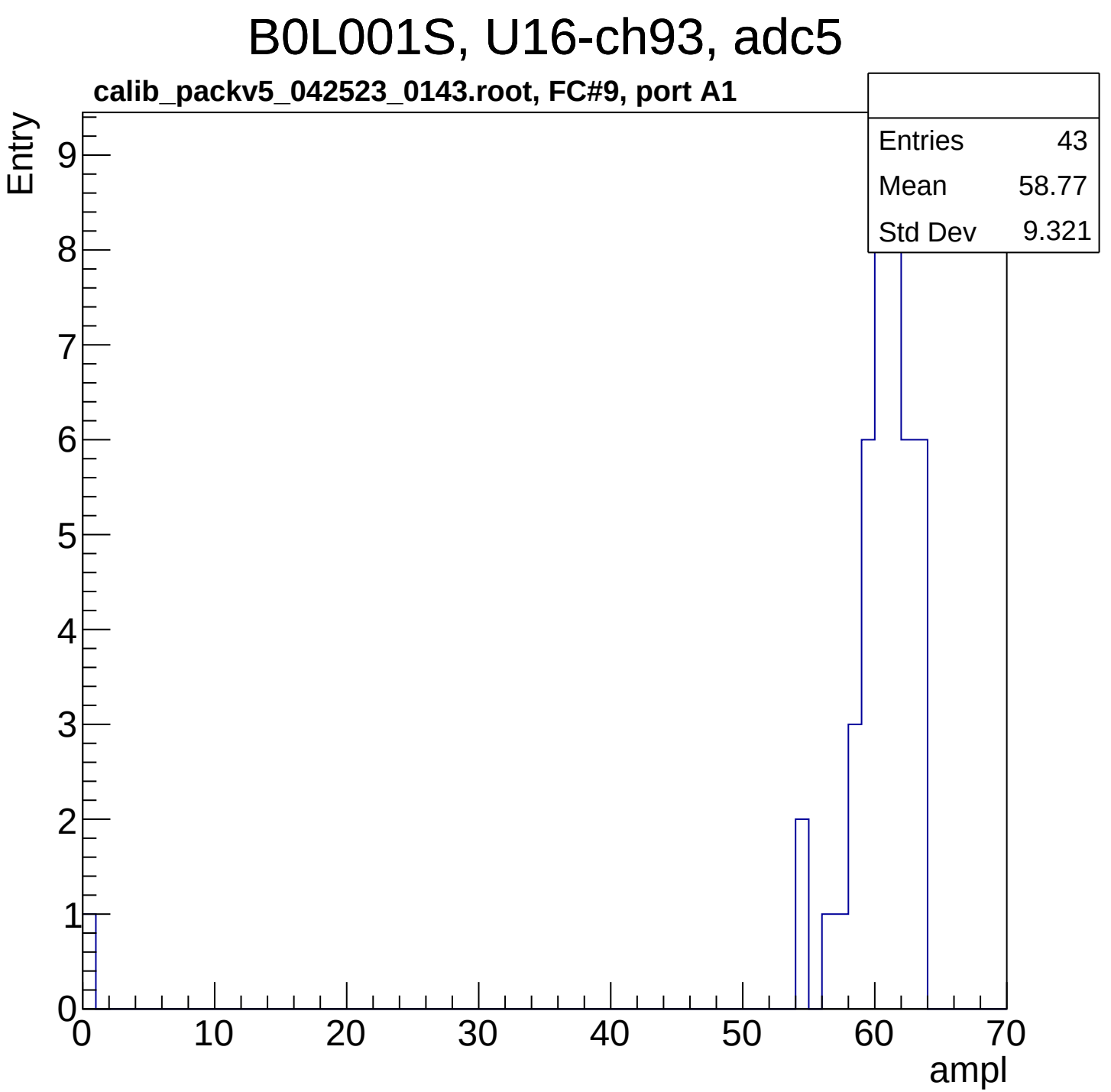
B0L001S, U16-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.77
Std Dev 9.321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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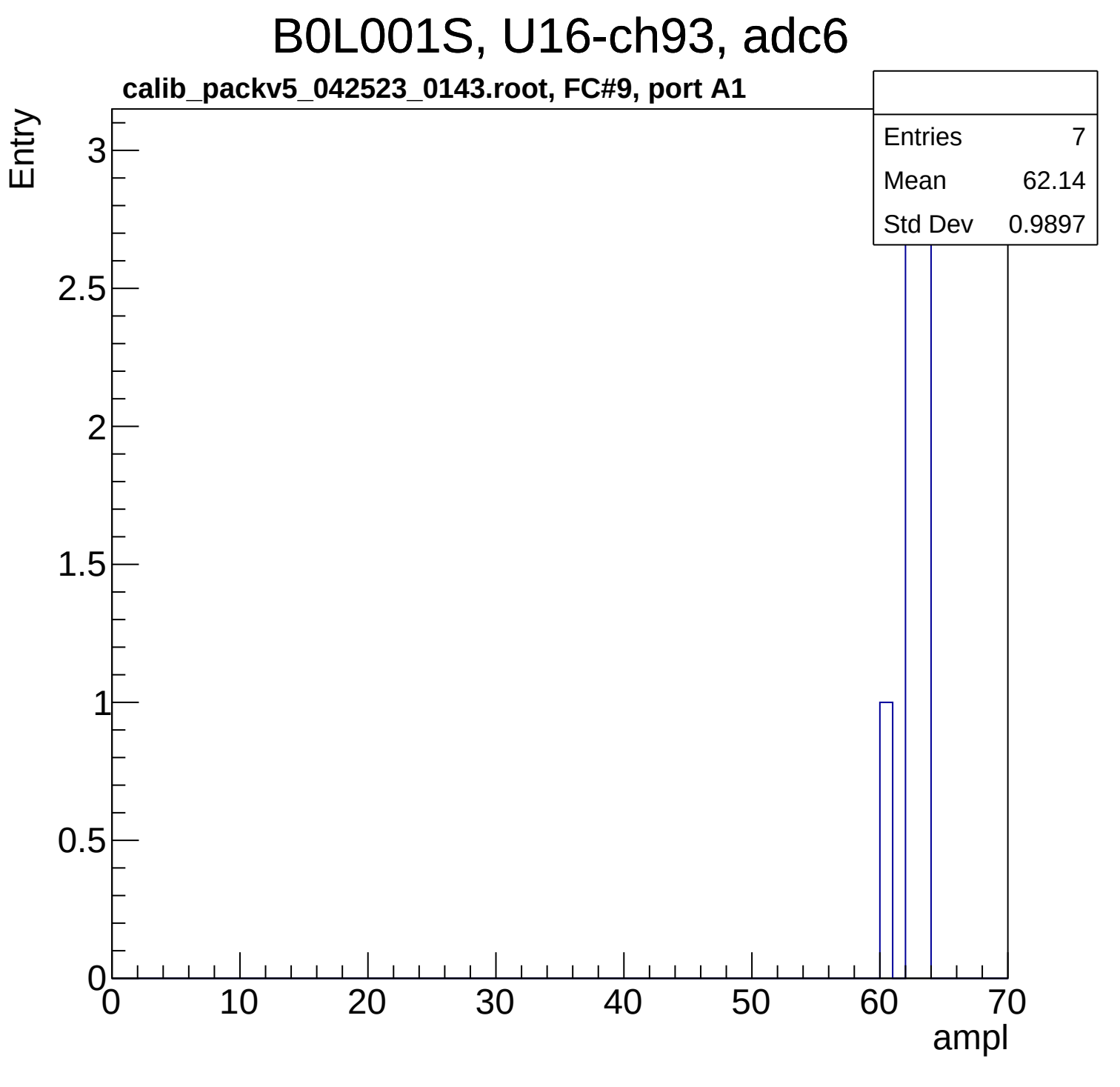
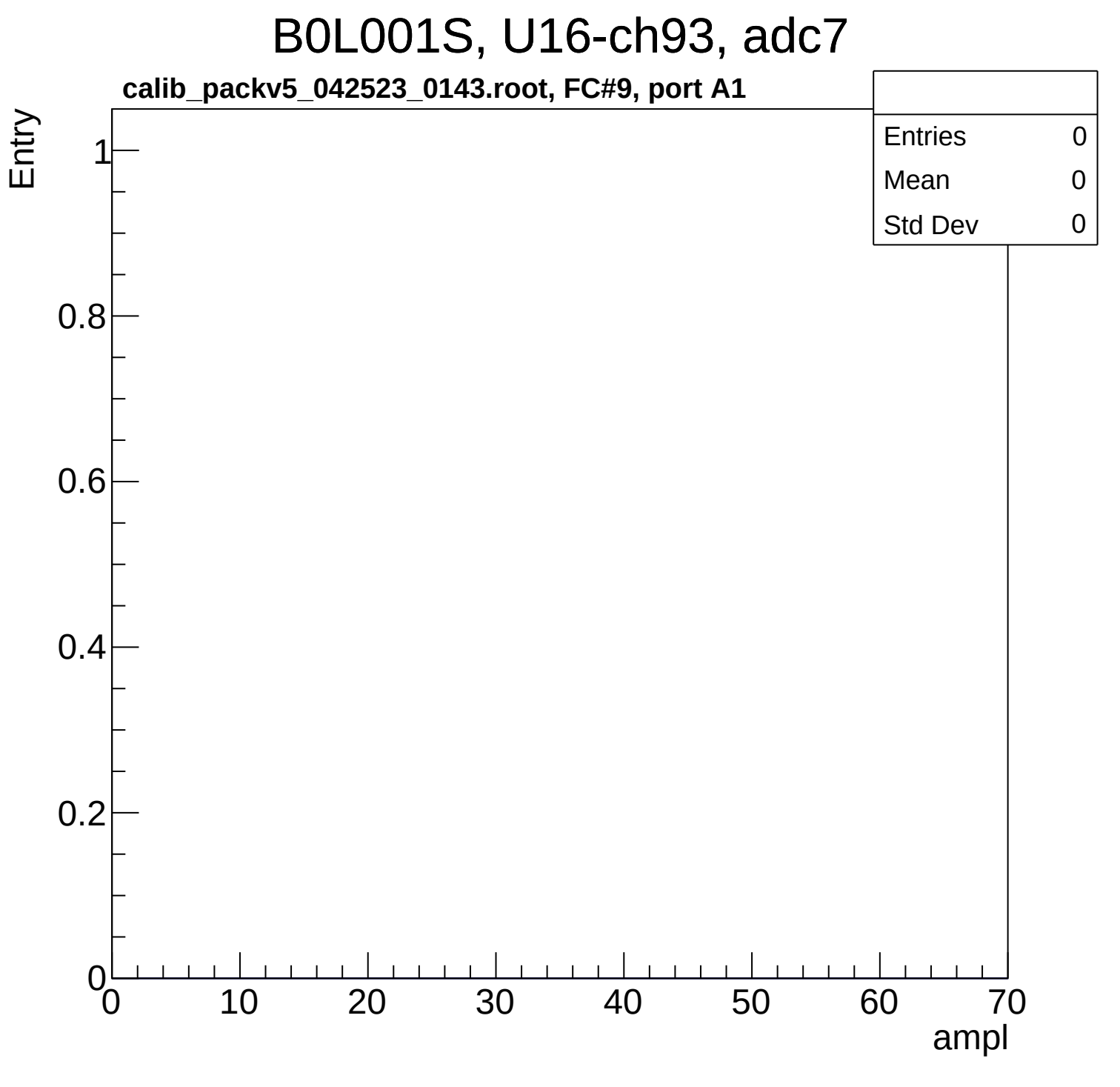
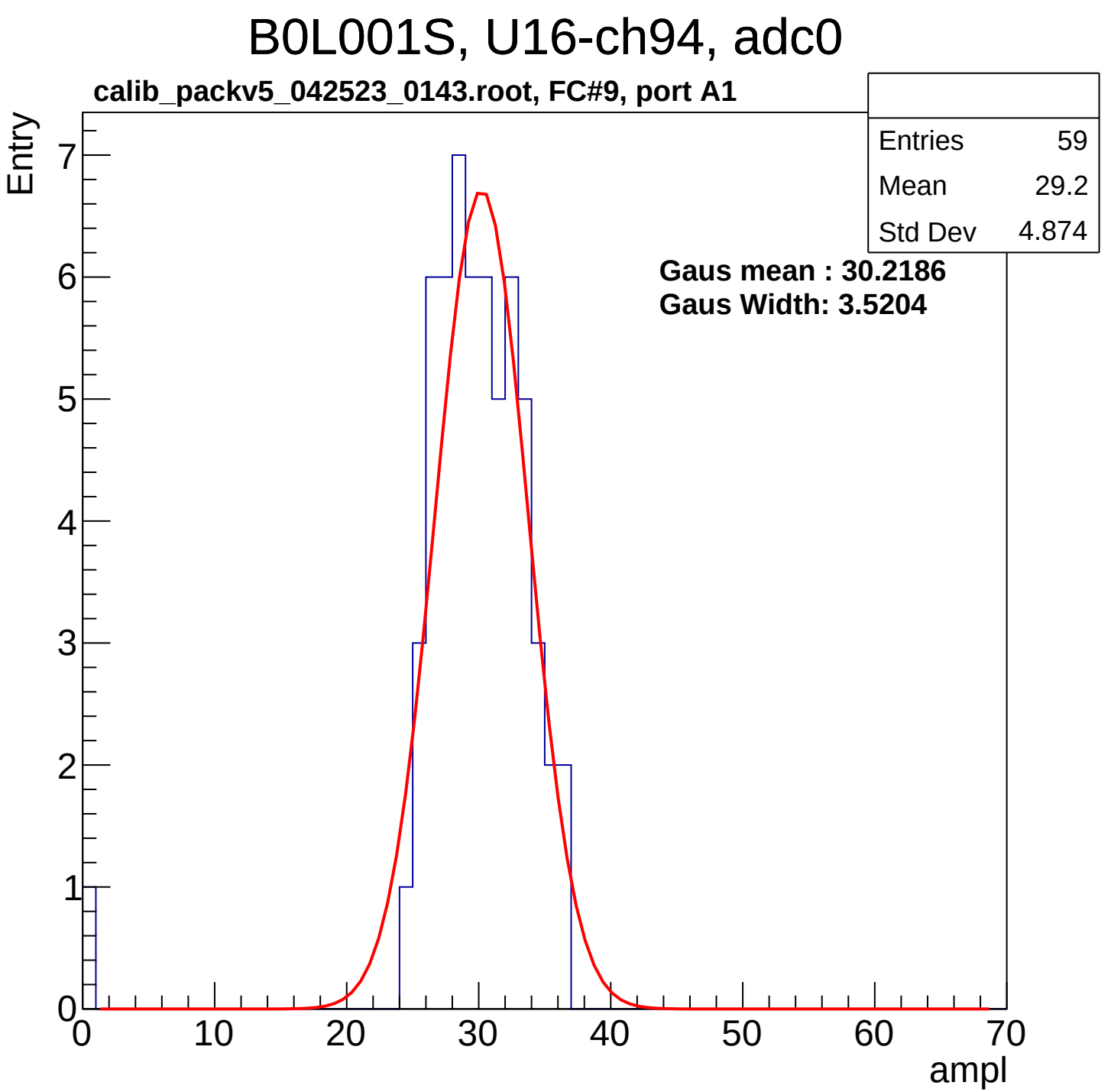
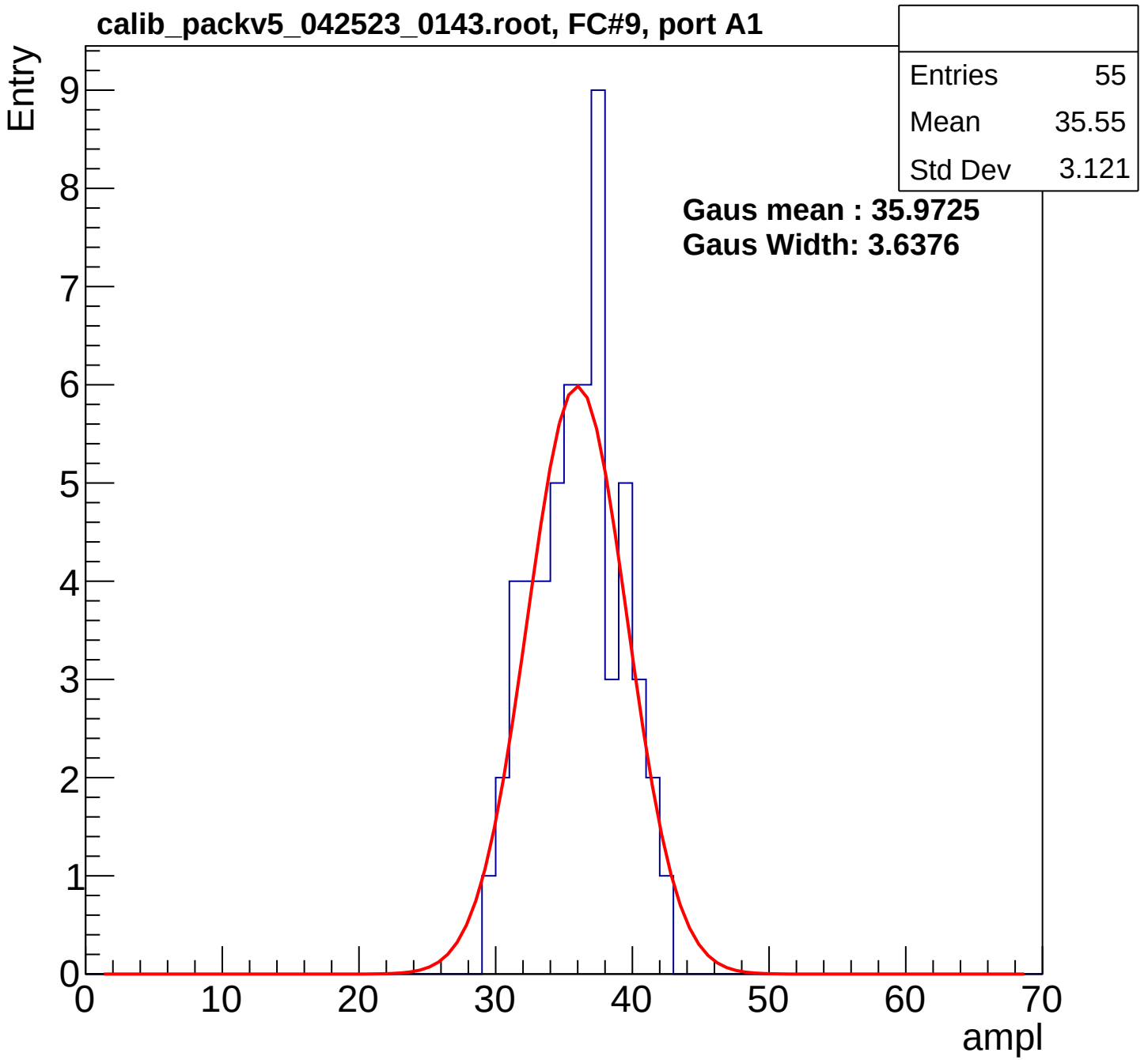
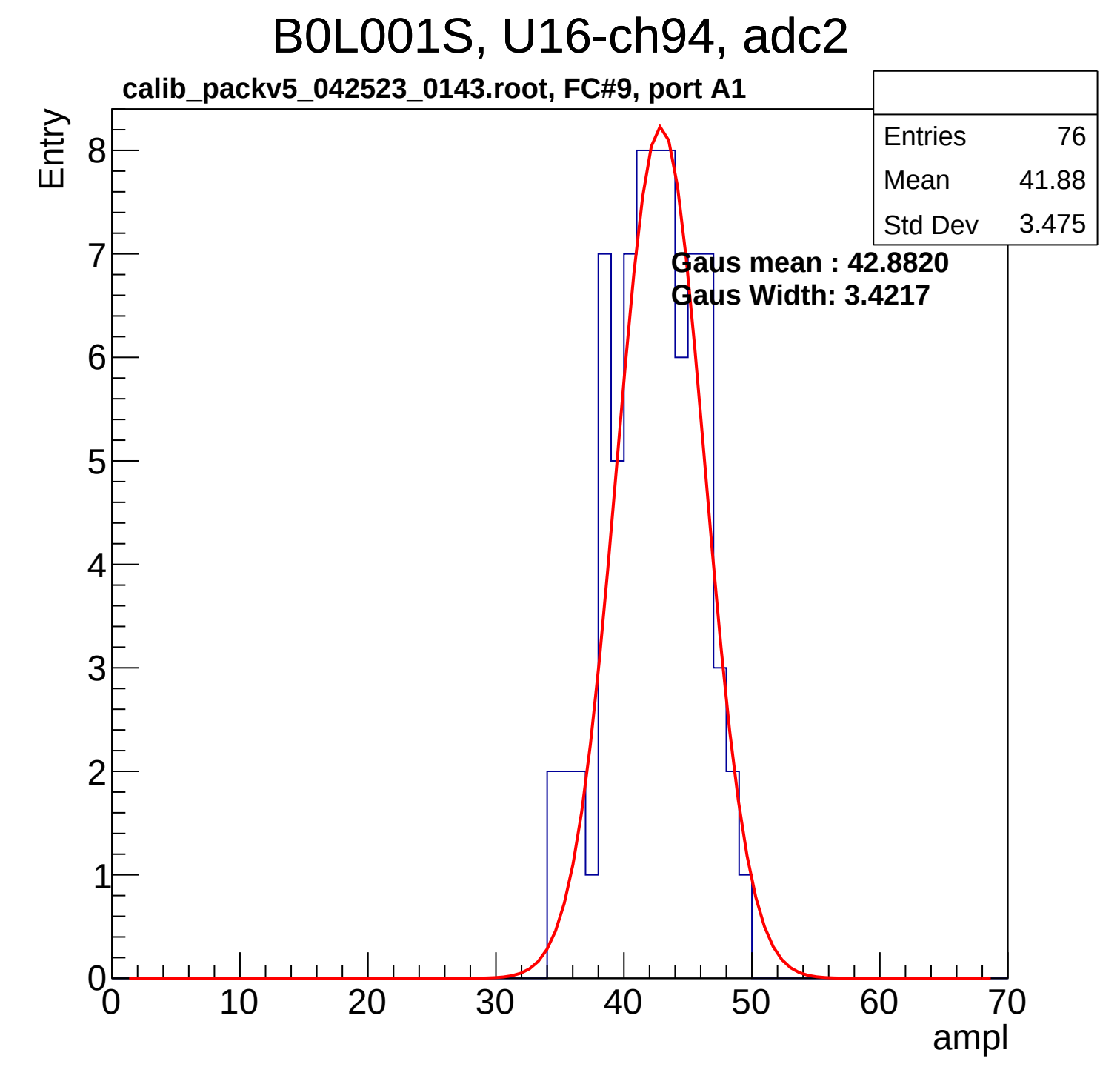
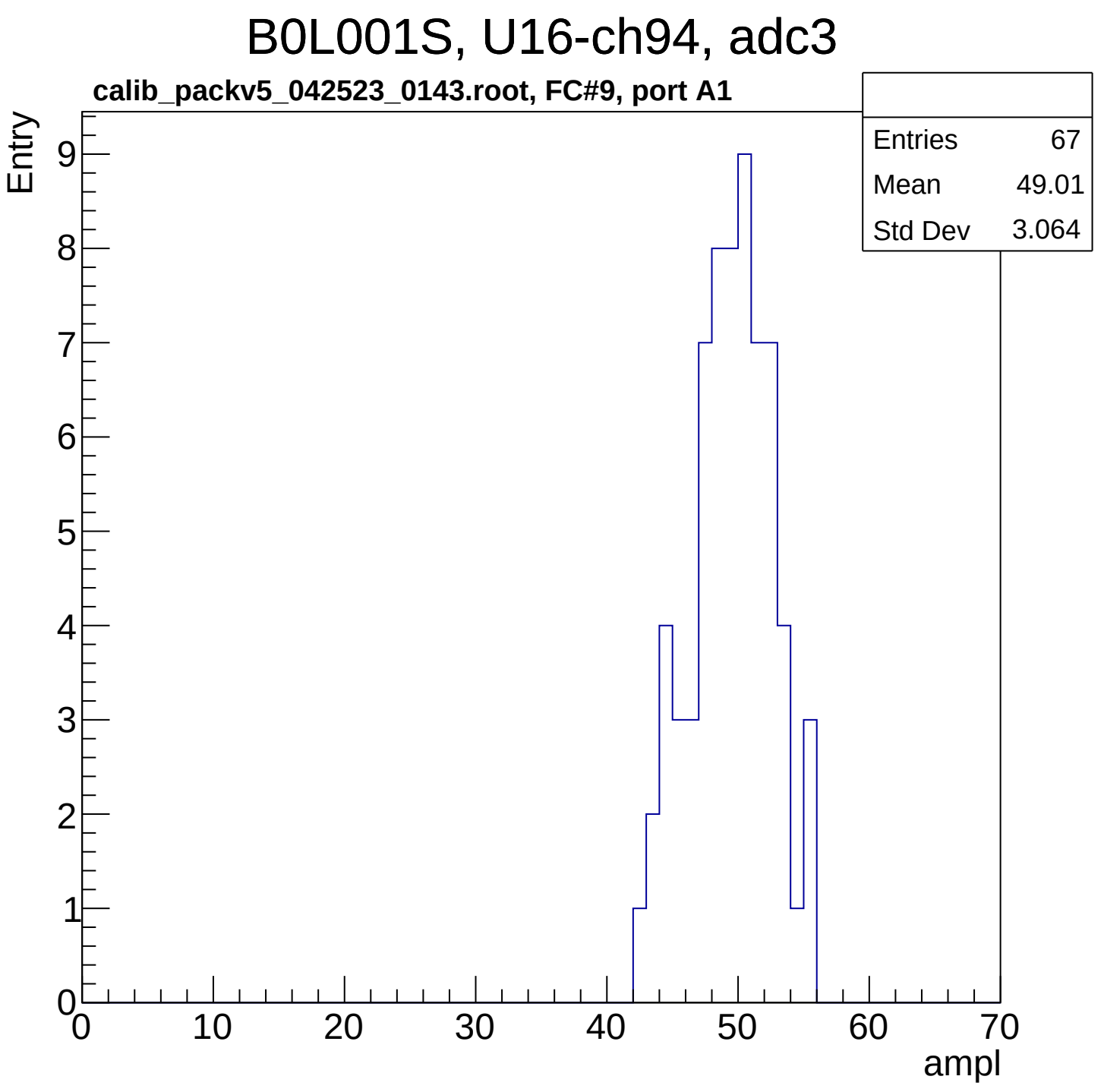

B0L001S, U16-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

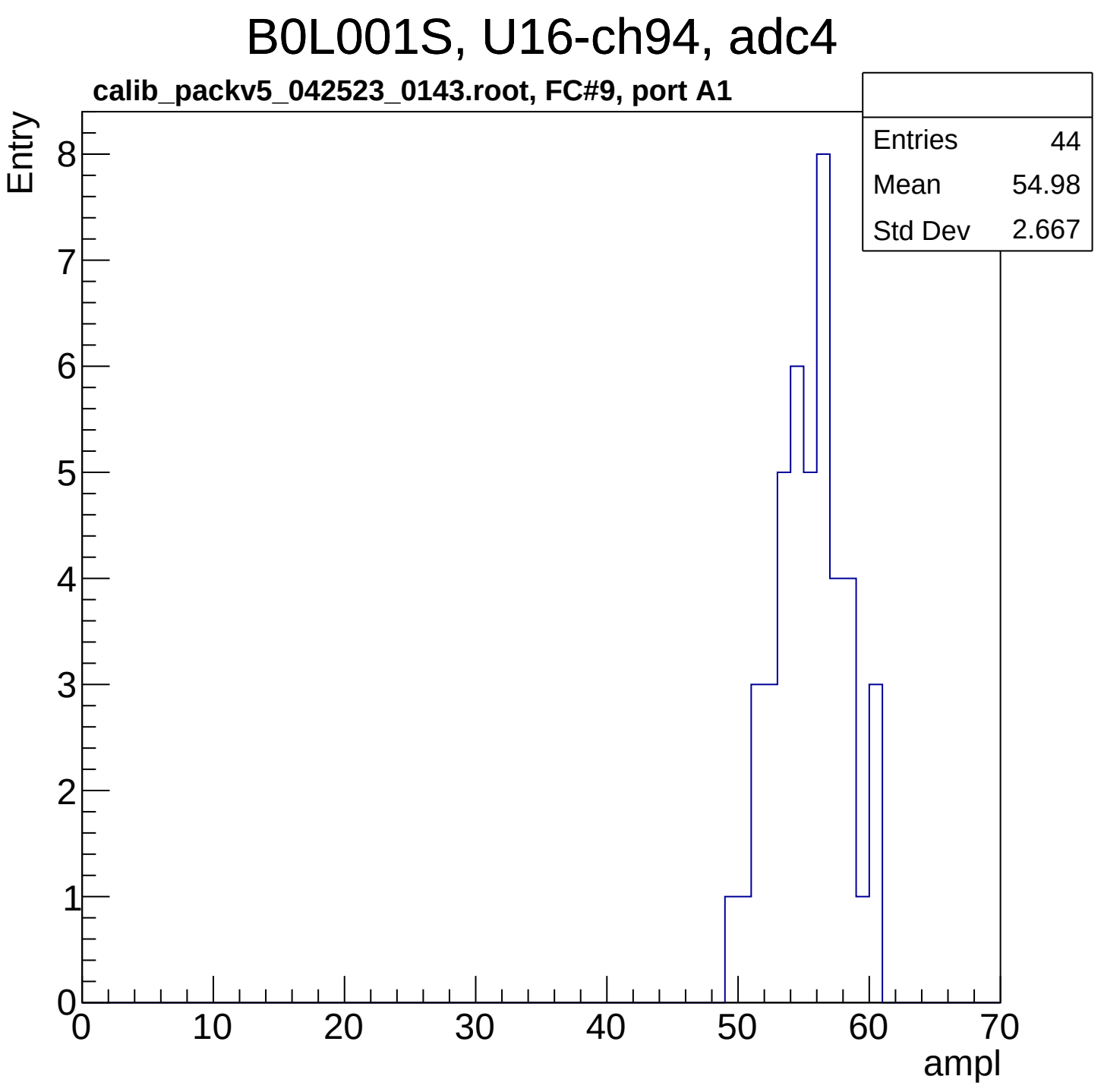

B0L001S, U16-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

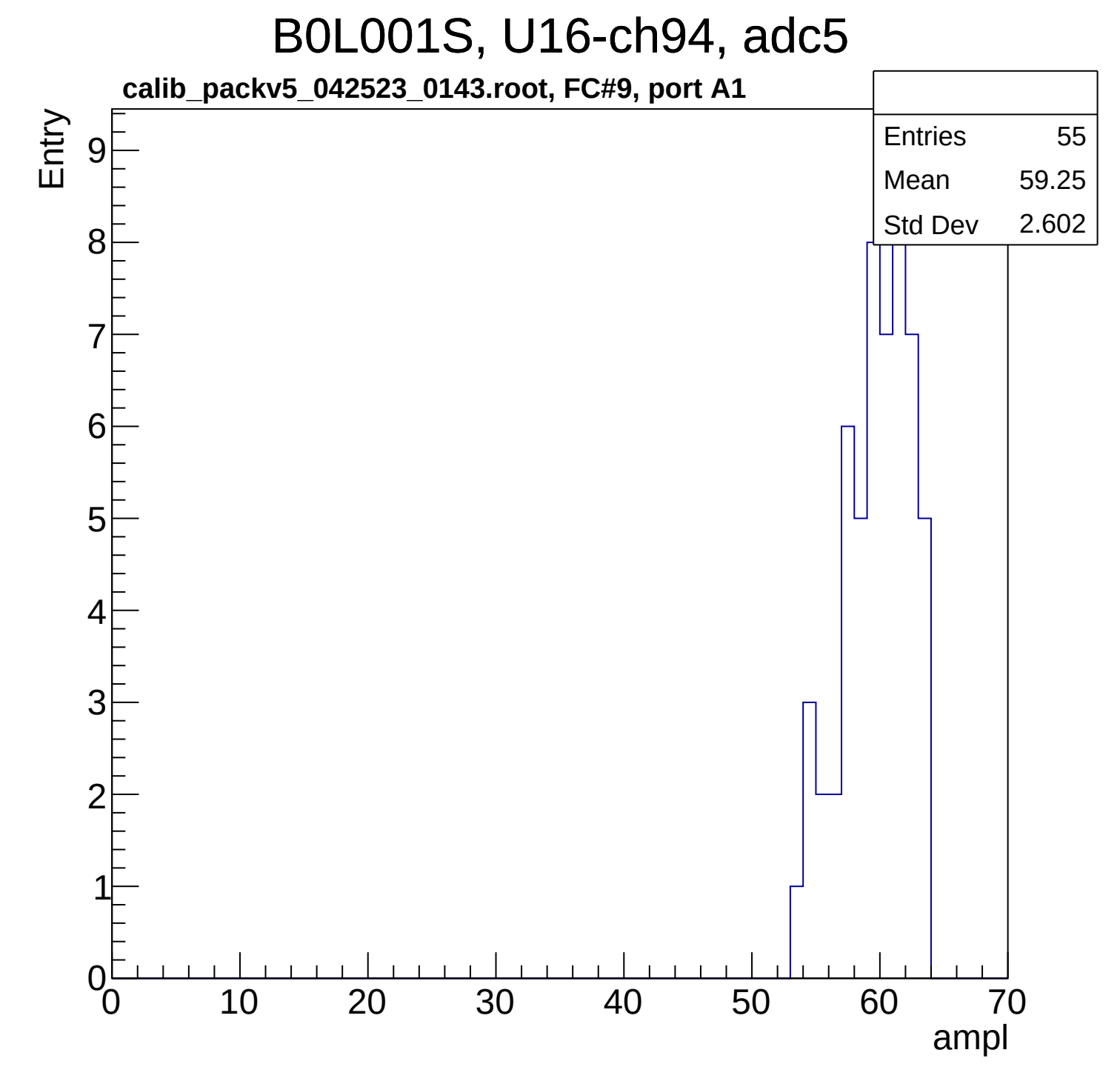

B0L001S, U16-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.2
Std Dev 4.874
Gaus mean : 30.2186
Gaus Width: 3.5204
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

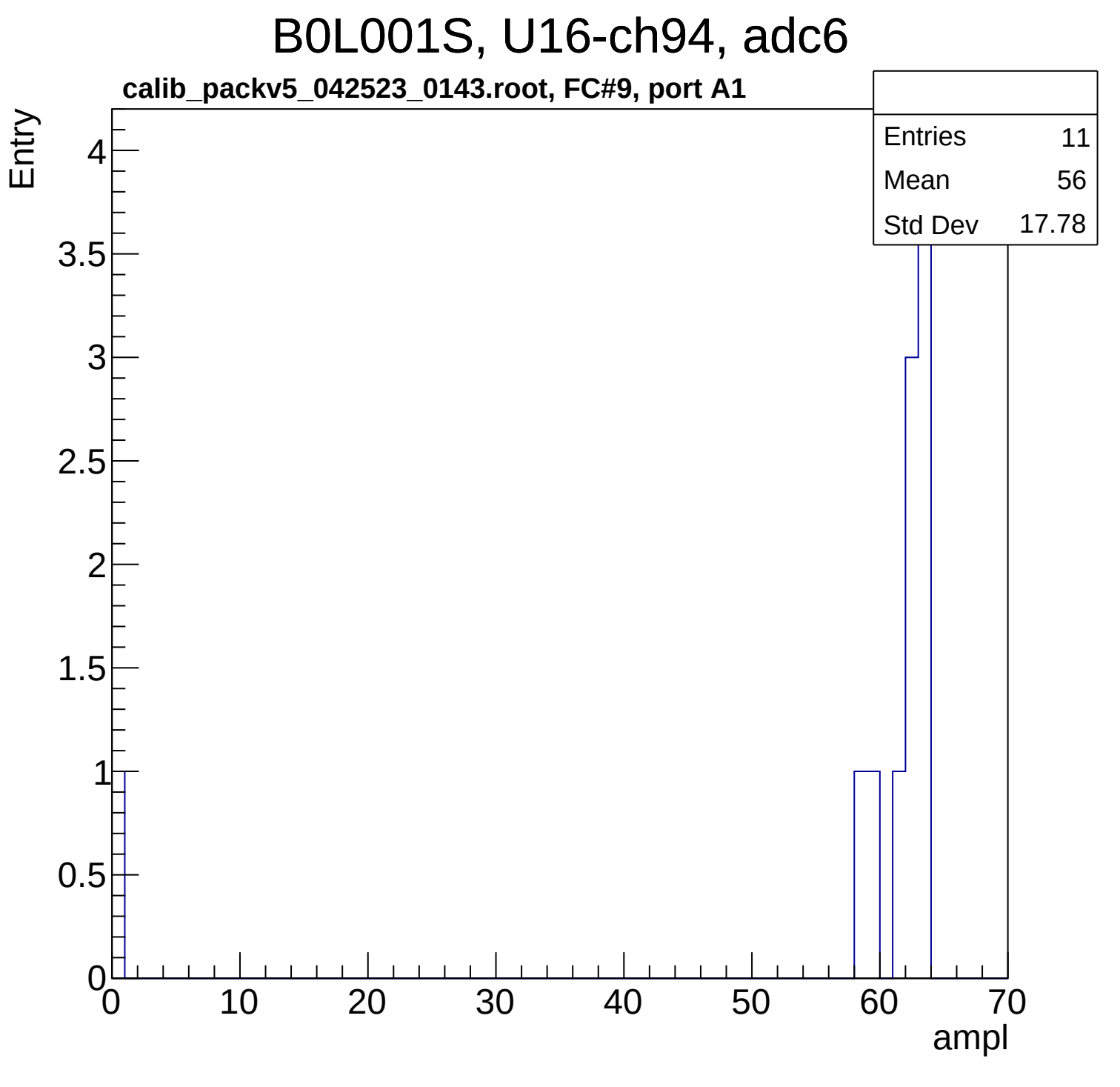

B0L001S, U16-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 35.55
Std Dev 3.121
Gaus mean : 35.9725
Gaus Width: 3.6376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 41.88
Std Dev 3.475
Gaus mean : 42.8820
Gaus Width: 3.4217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.01
Std Dev 3.064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 54.98
Std Dev 2.667
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 59.25
Std Dev 2.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 56
Std Dev 17.78
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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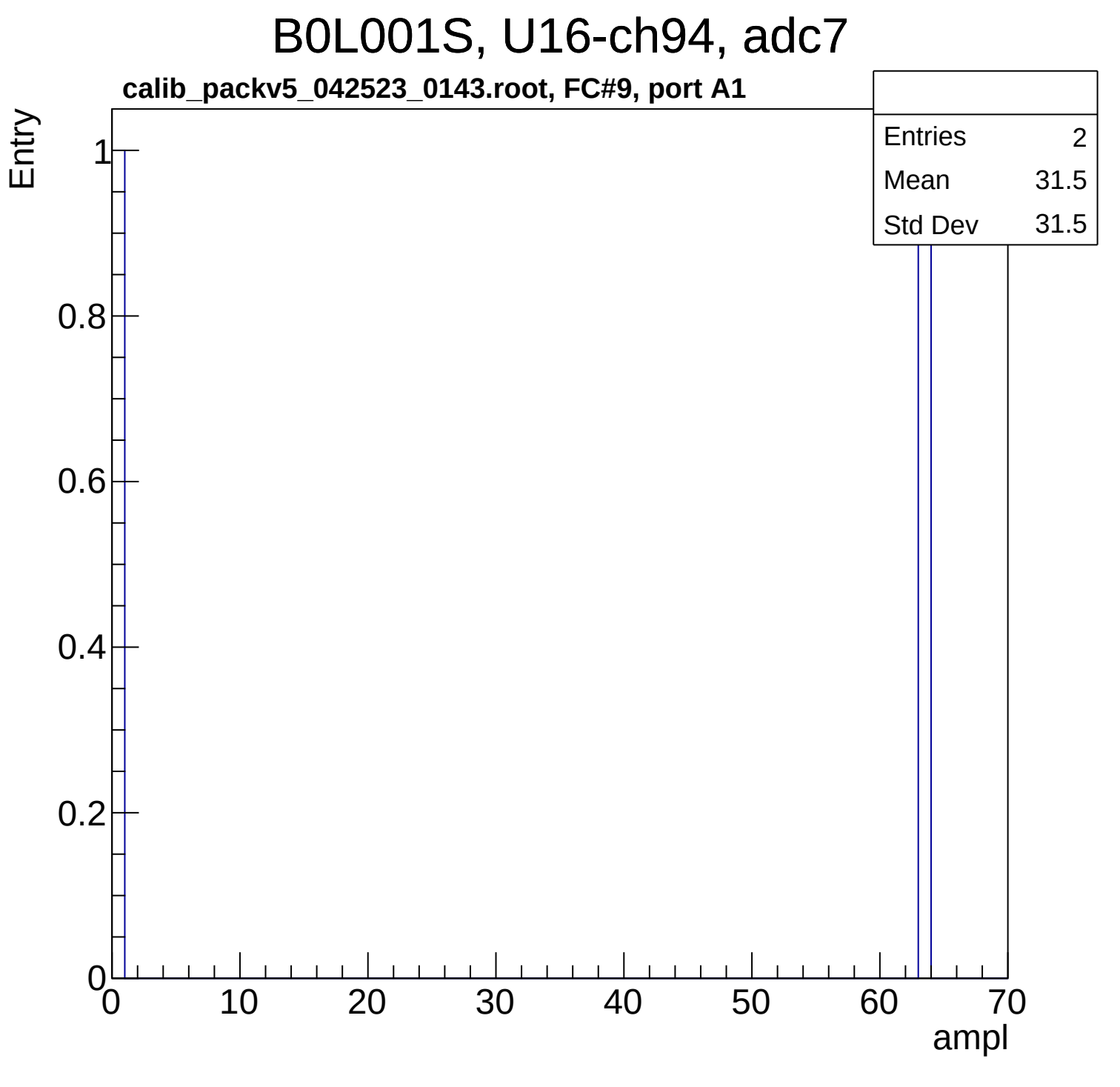
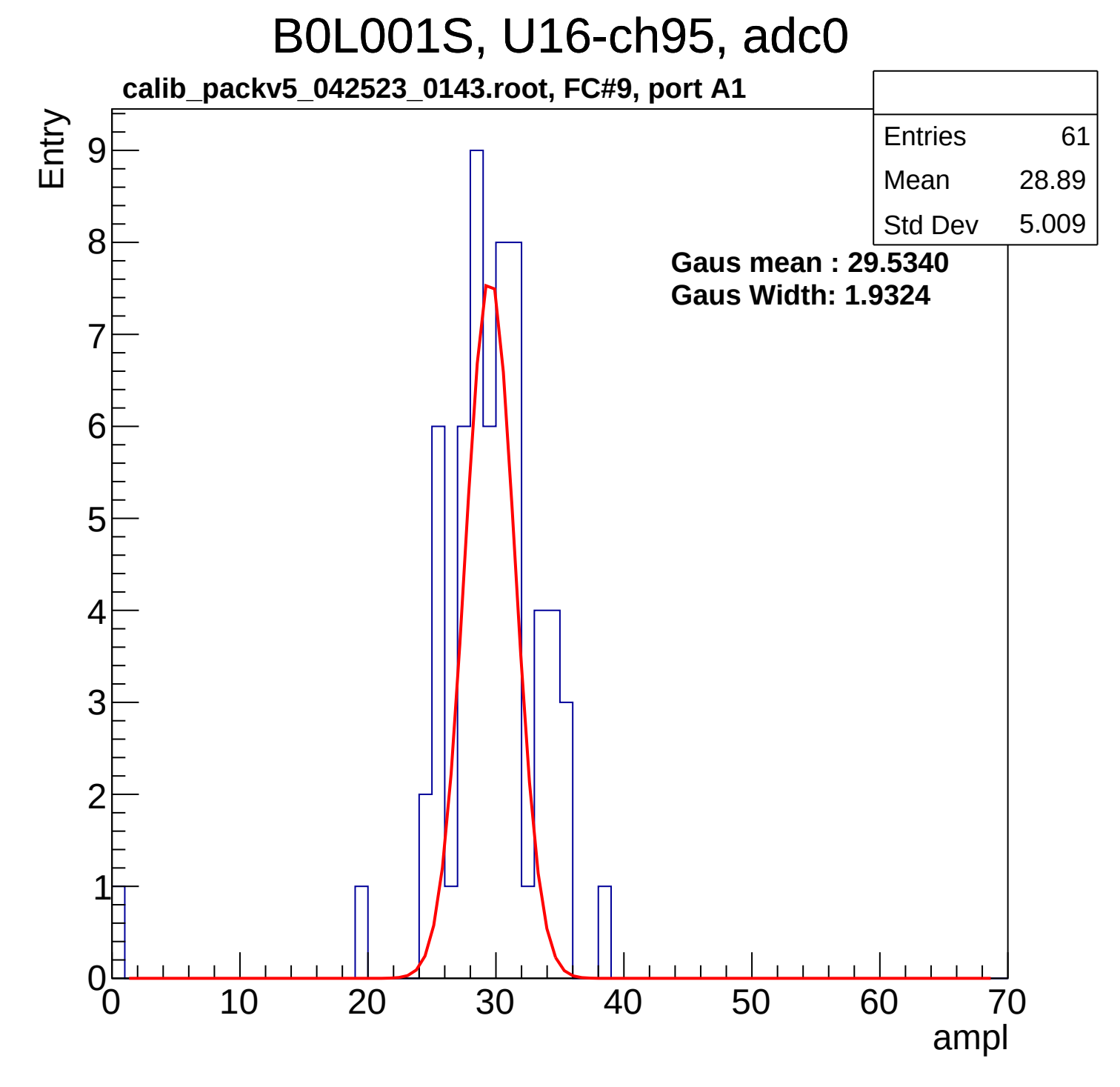
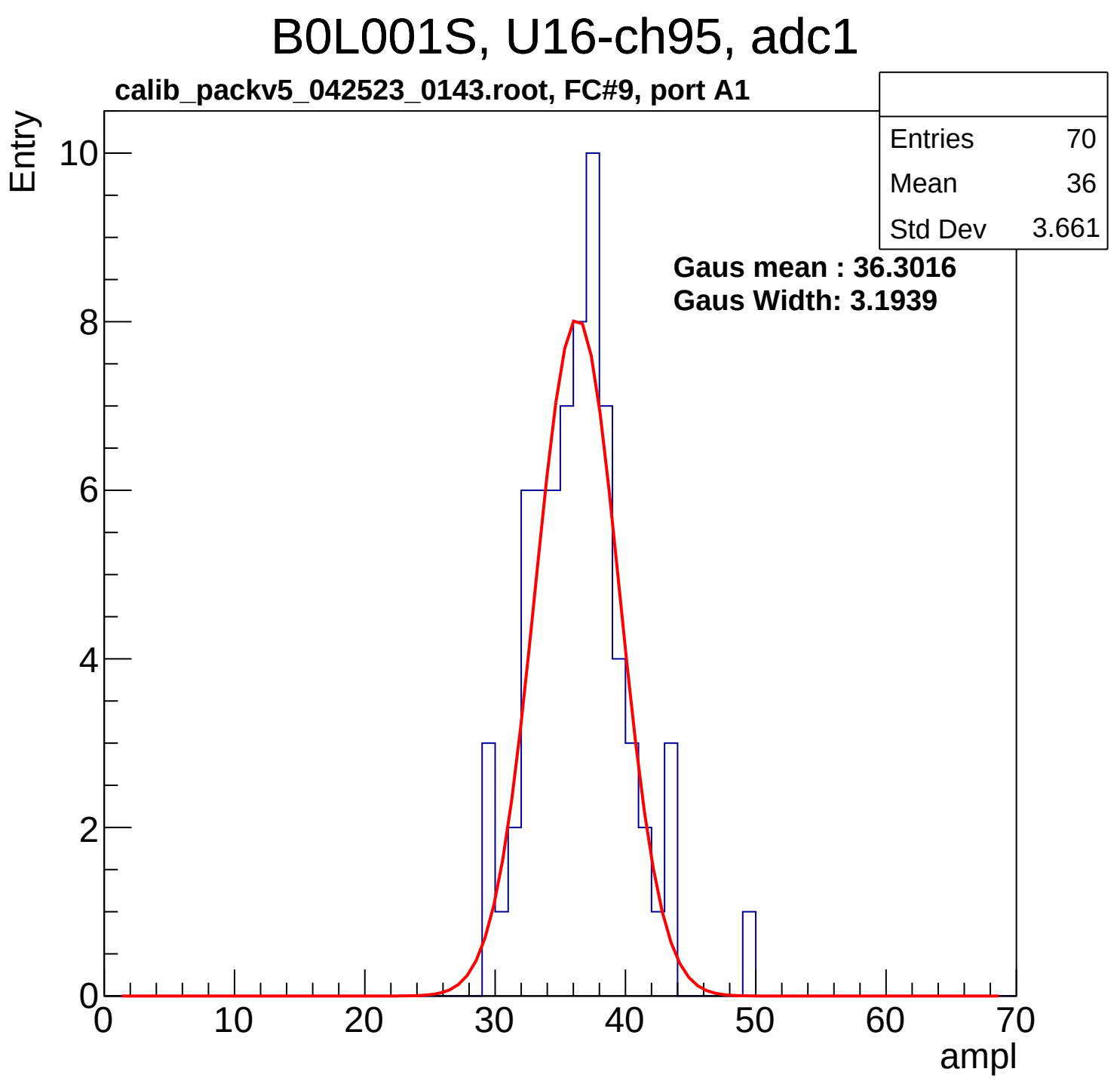
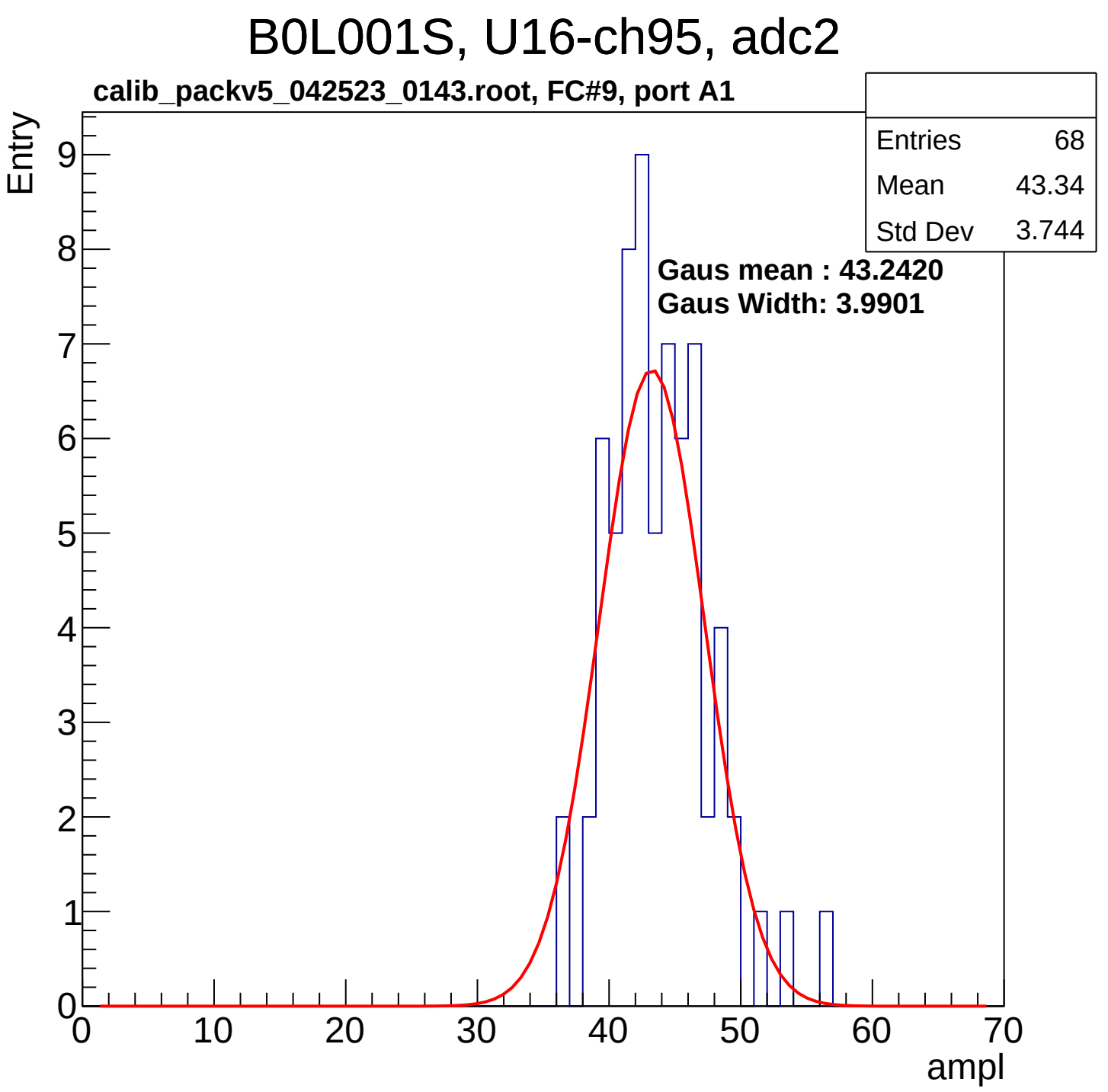
40
50
60
70


B0L001S, U16-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 28.89
Std Dev 5.009
Gaus mean : 29.5340
Gaus Width: 1.9324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36
Std Dev 3.661
Gaus mean : 36.3016
Gaus Width: 3.1939
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.34
Std Dev 3.744
Gaus mean : 43.2420
Gaus Width: 3.9901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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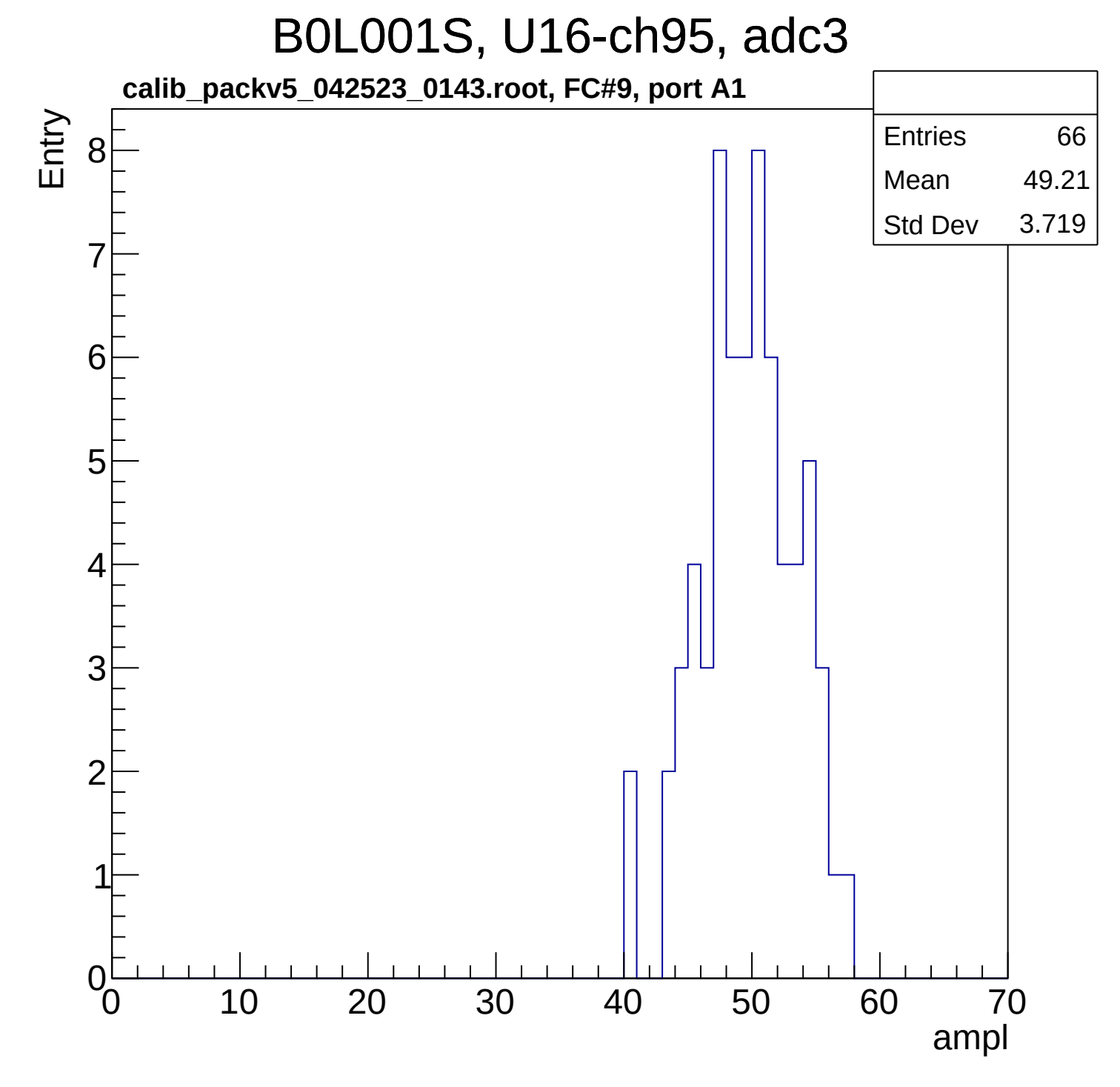
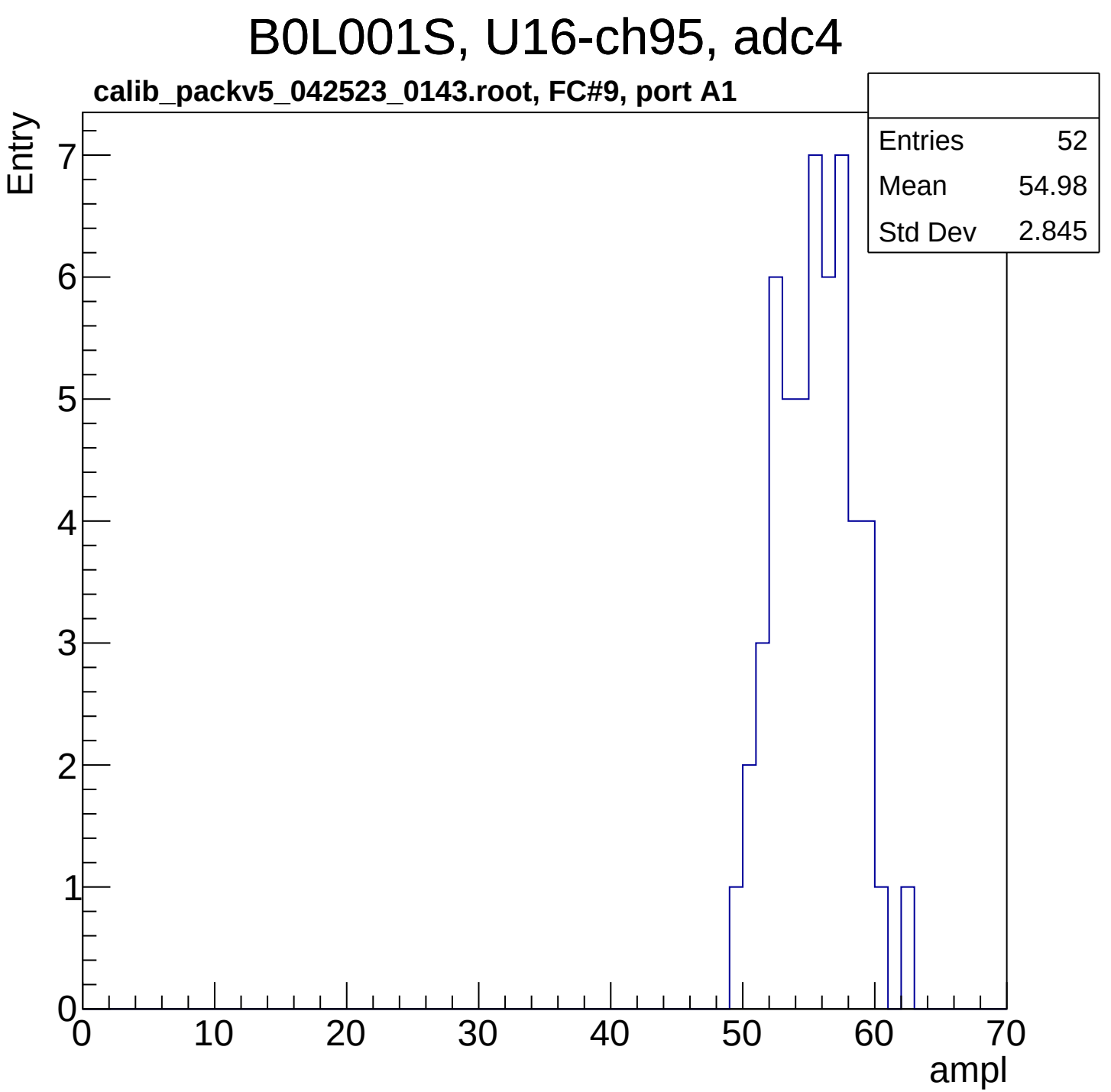
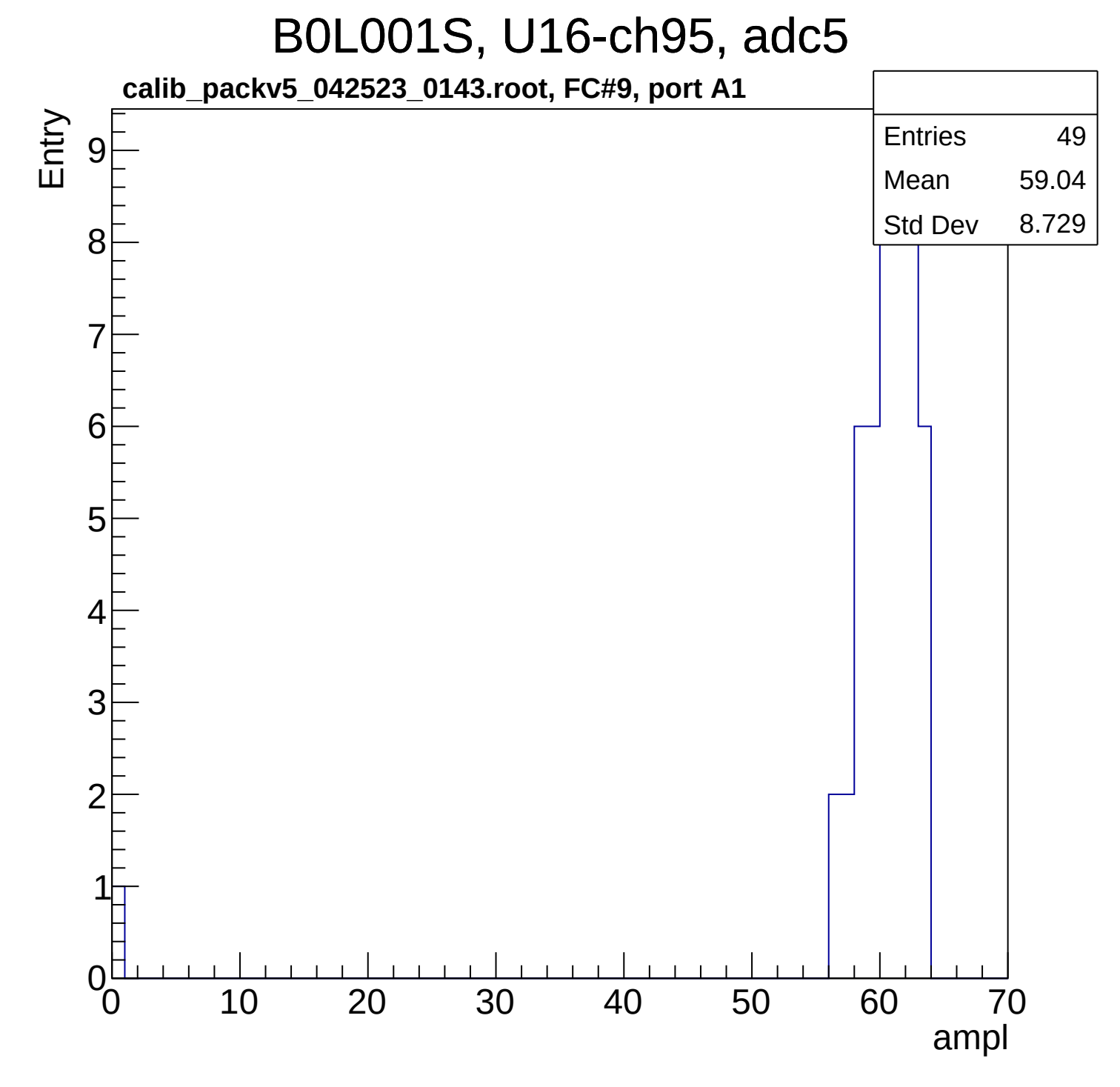

B0L001S, U16-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.21
Std Dev 3.719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 54.98
Std Dev 2.845
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 59.04
Std Dev 8.729
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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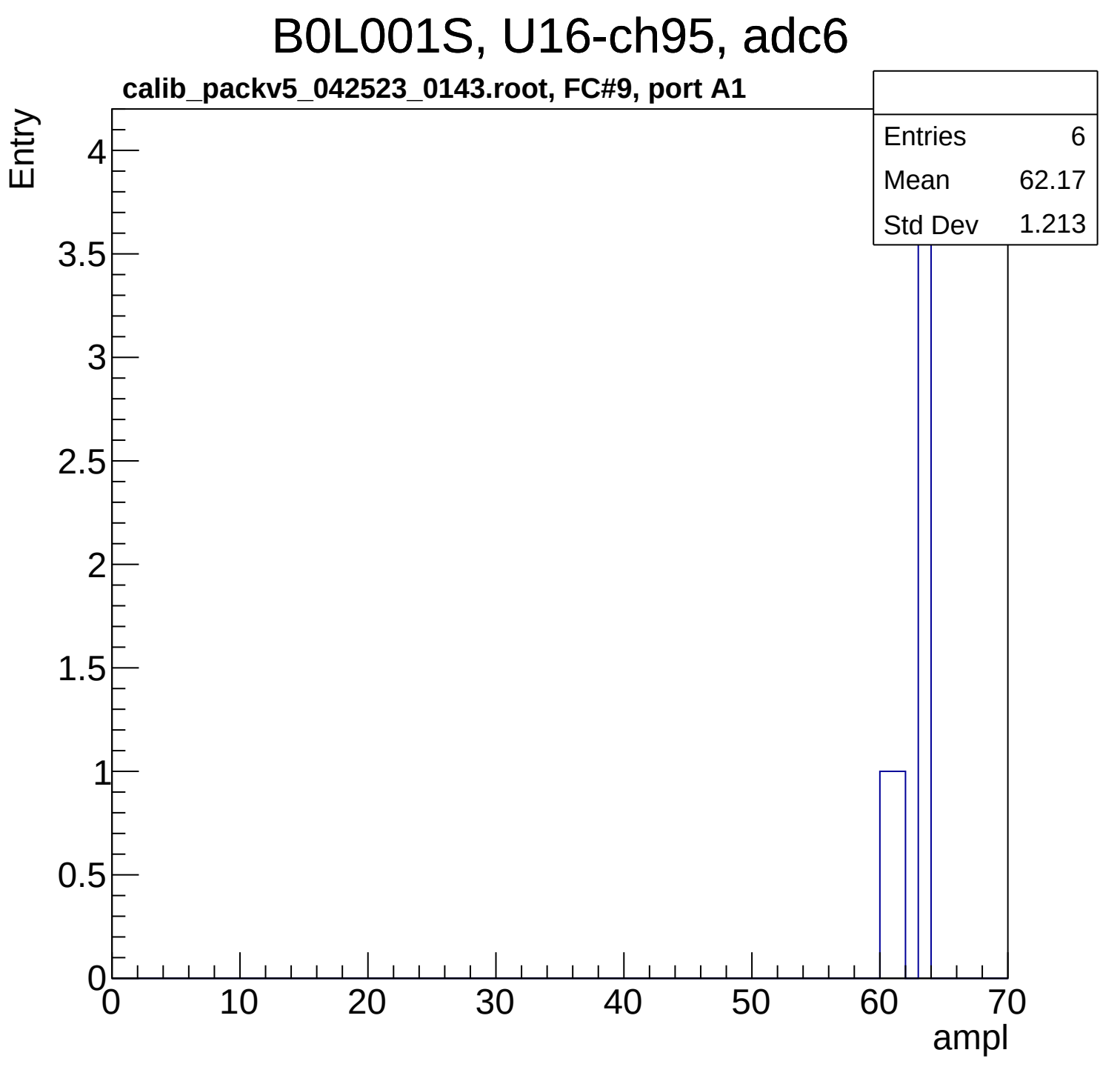
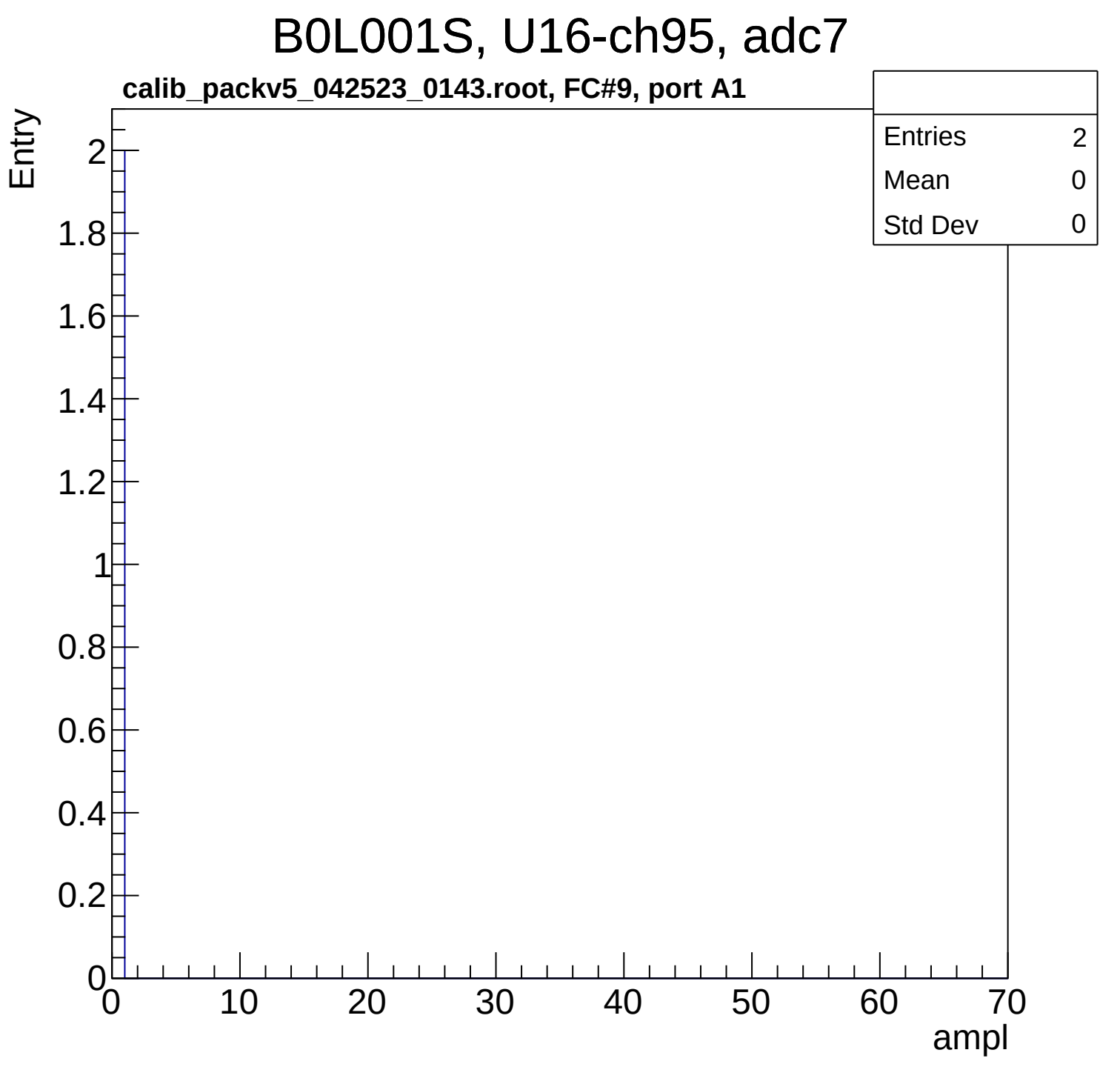
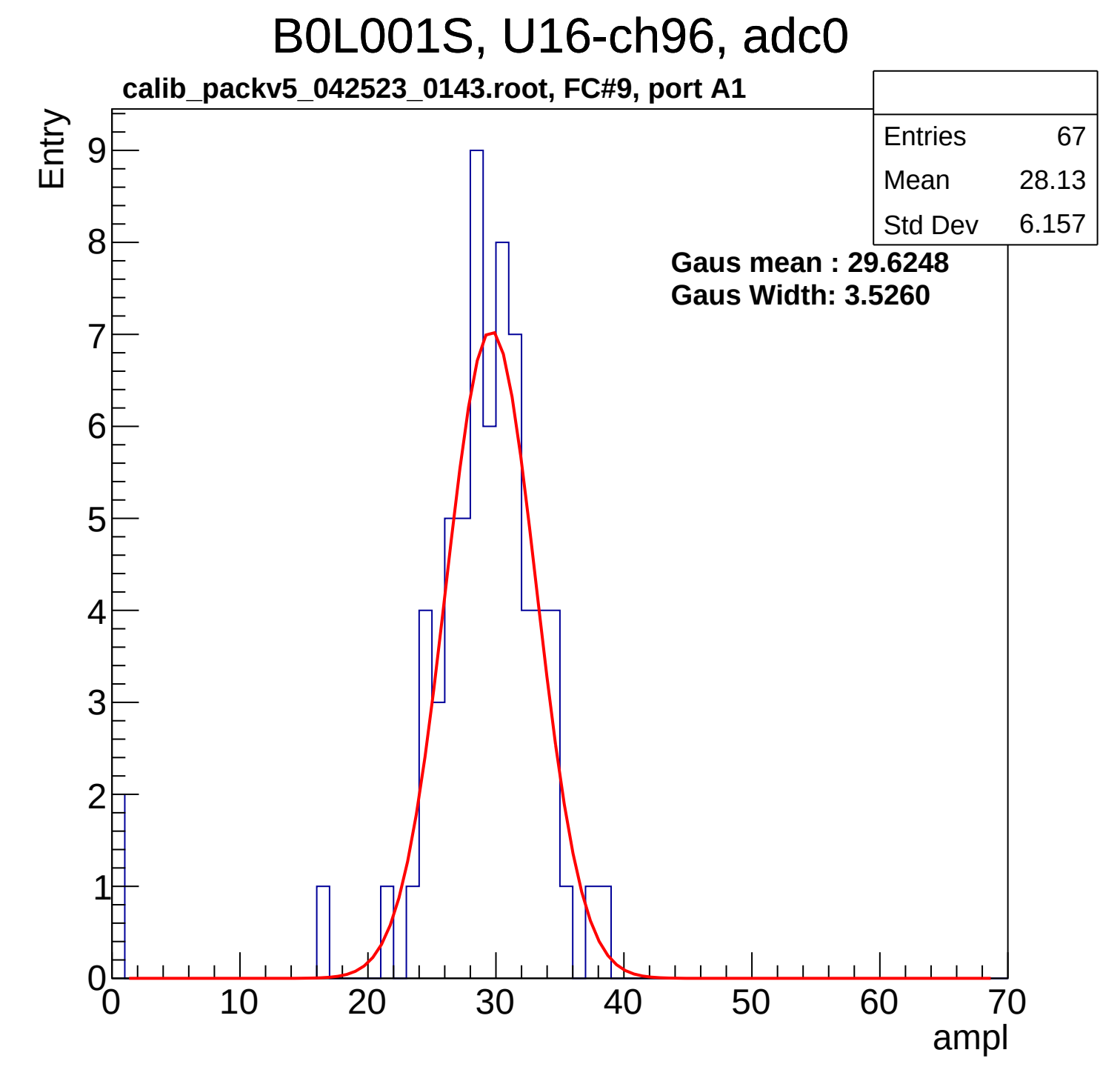

B0L001S, U16-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 28.13
Std Dev 6.157
Gaus mean : 29.6248
Gaus Width: 3.5260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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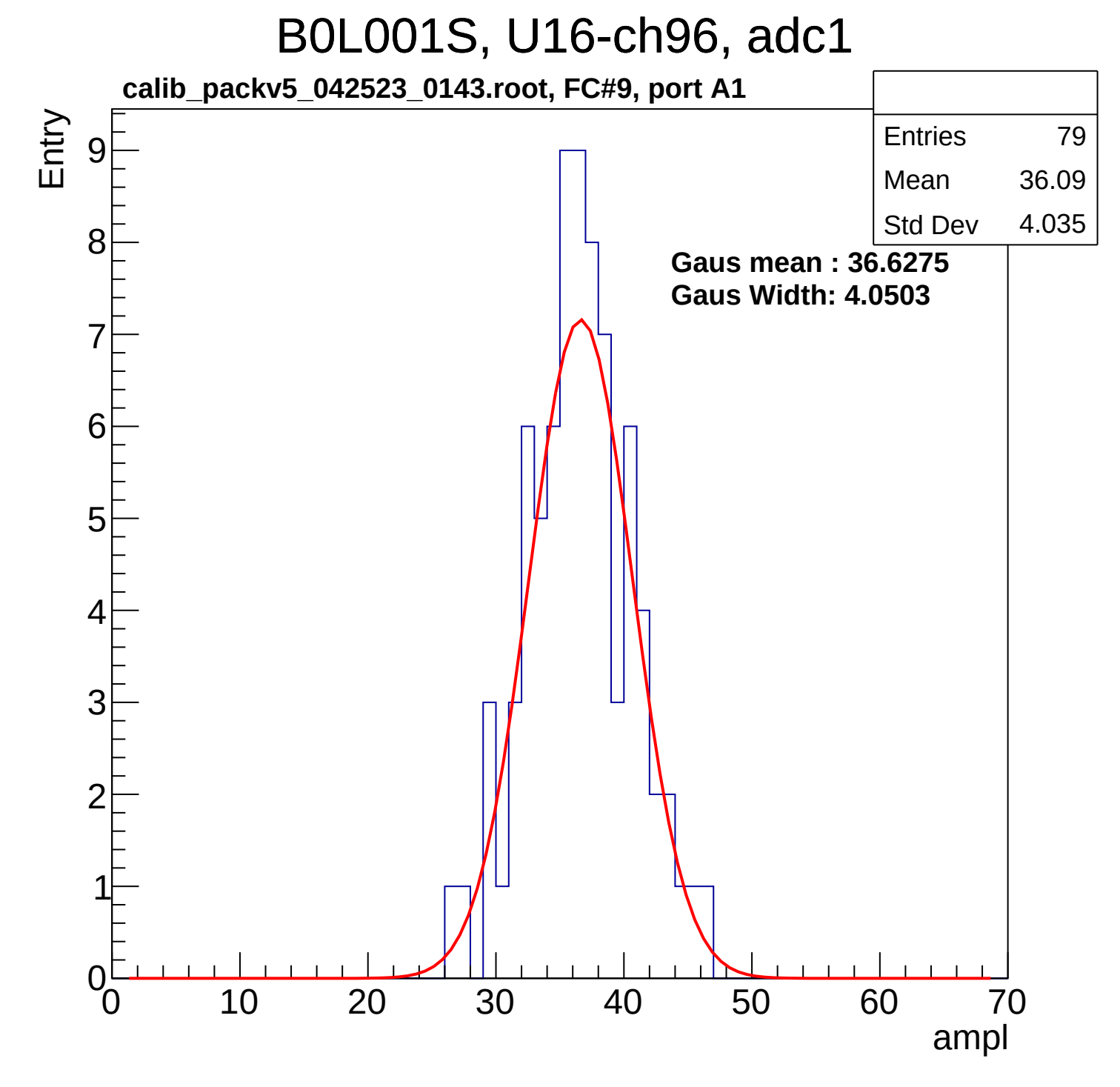
B0L001S, U16-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 36.09
Std Dev 4.035
Gaus mean : 36.6275
Gaus Width: 4.0503
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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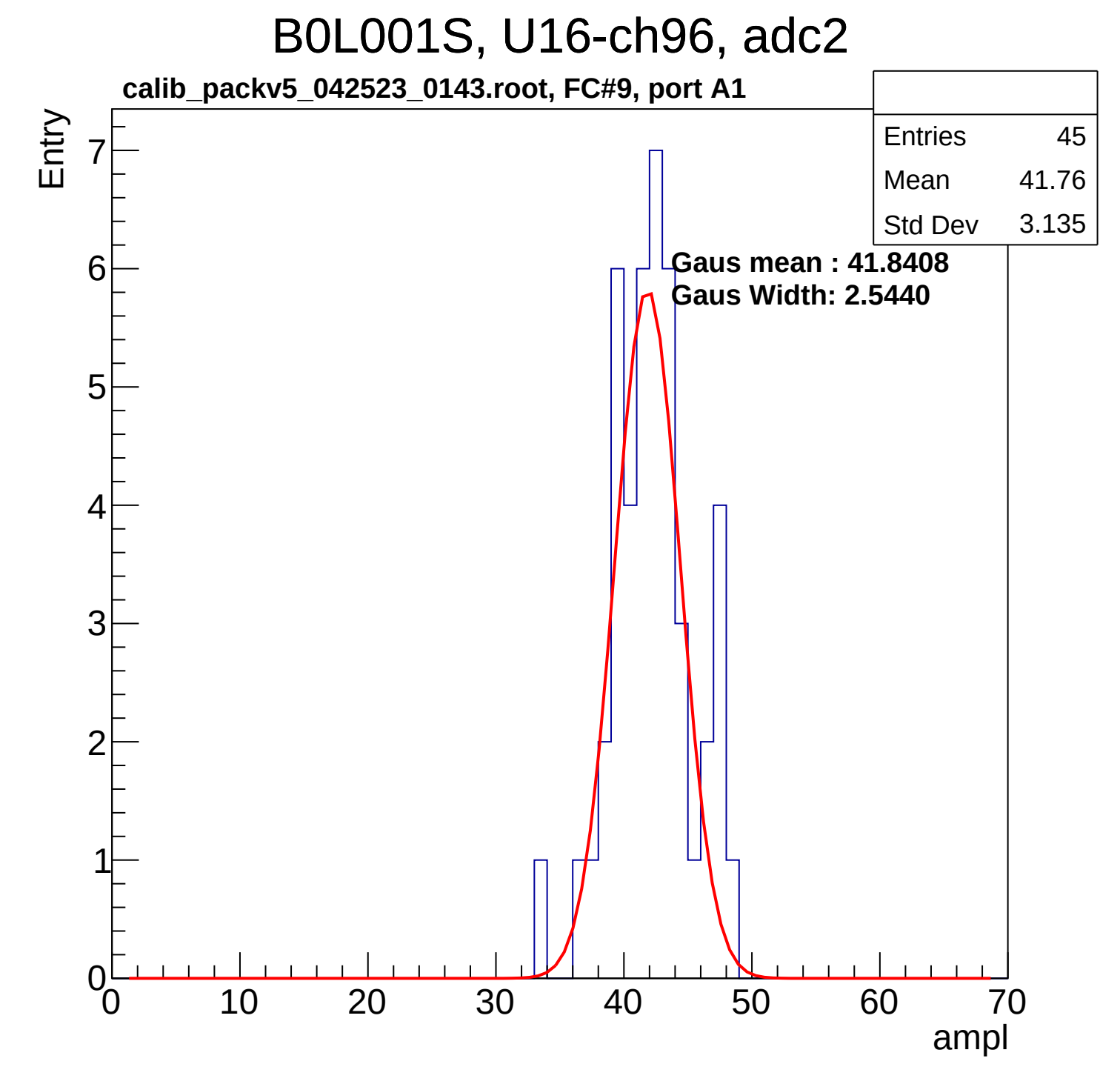
B0L001S, U16-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 41.76
Std Dev 3.135
Gaus mean : 41.8408
Gaus Width: 2.5440
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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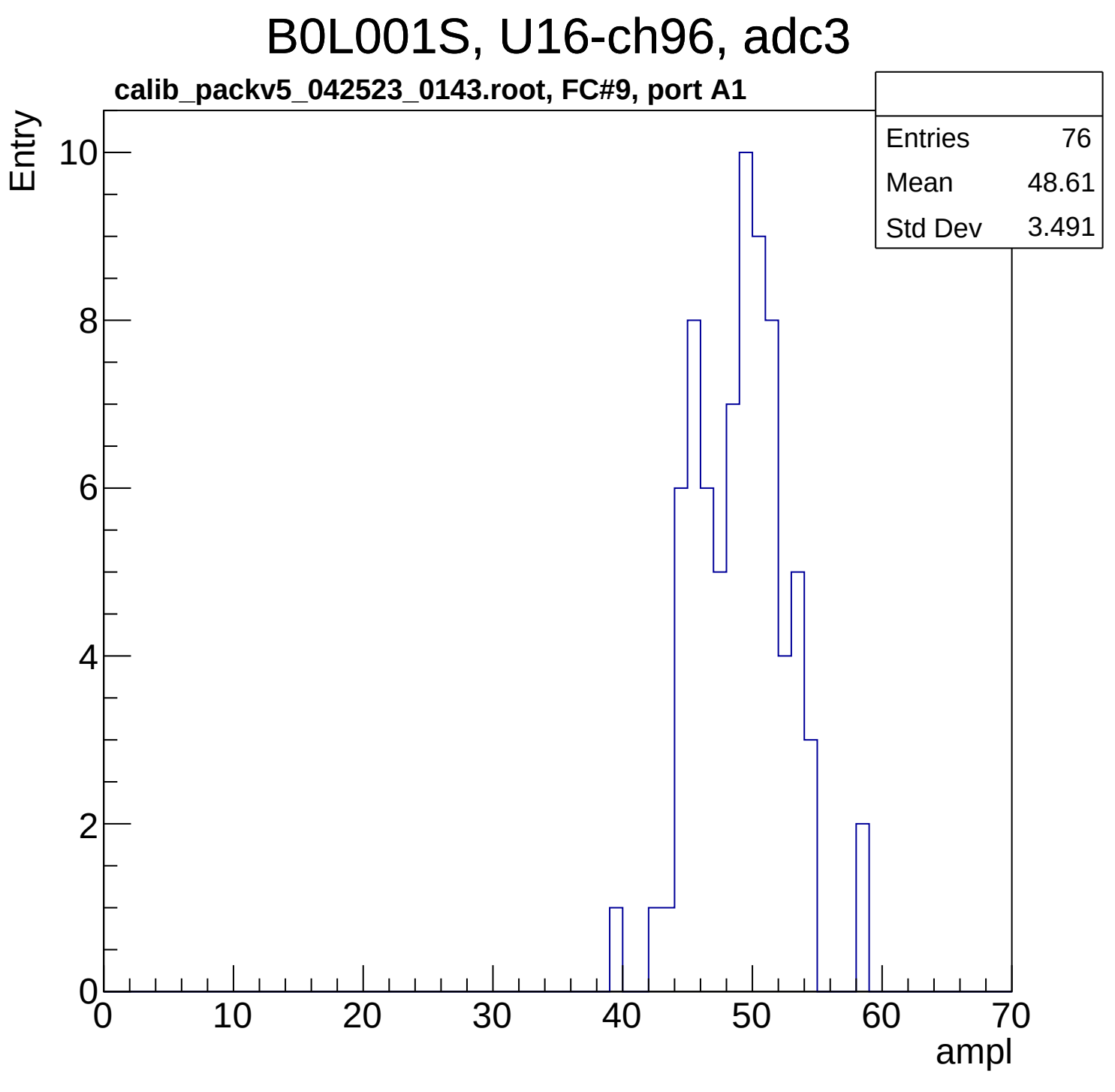
B0L001S, U16-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 48.61
Std Dev 3.491
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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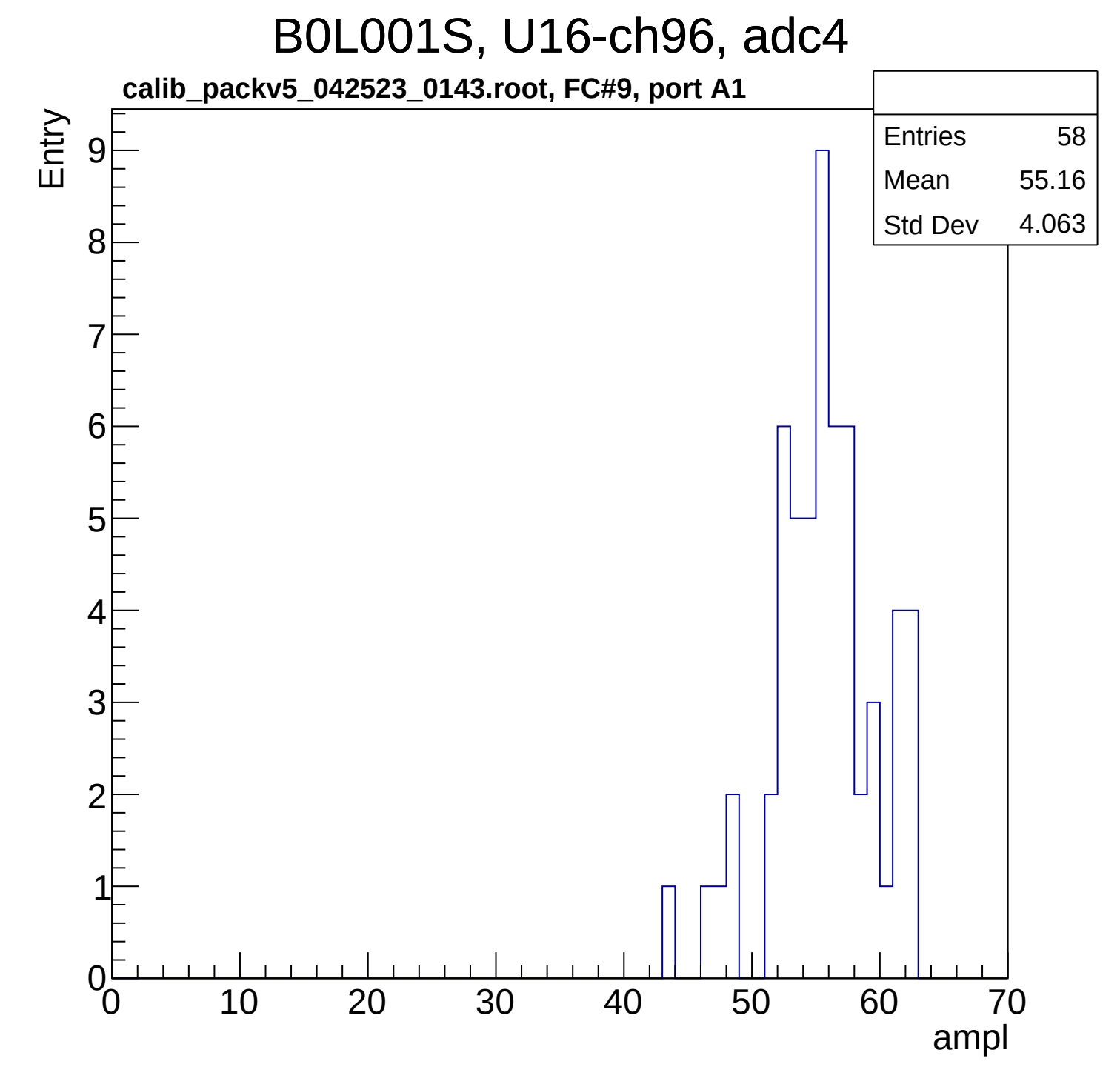
B0L001S, U16-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.16
Std Dev 4.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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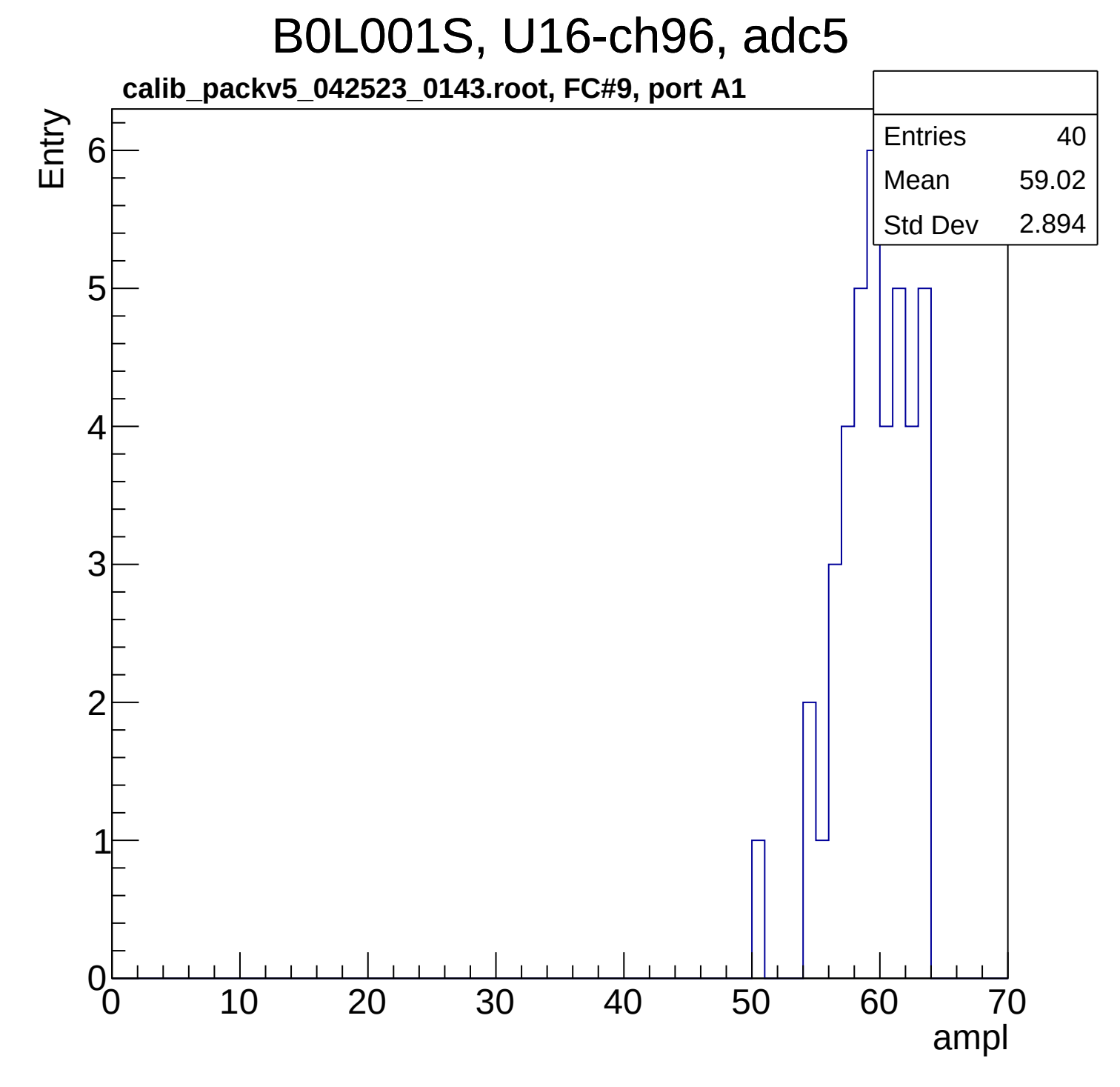
B0L001S, U16-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.02
Std Dev 2.894
Entry
ampl
0
1
2
3
4
5
6
0
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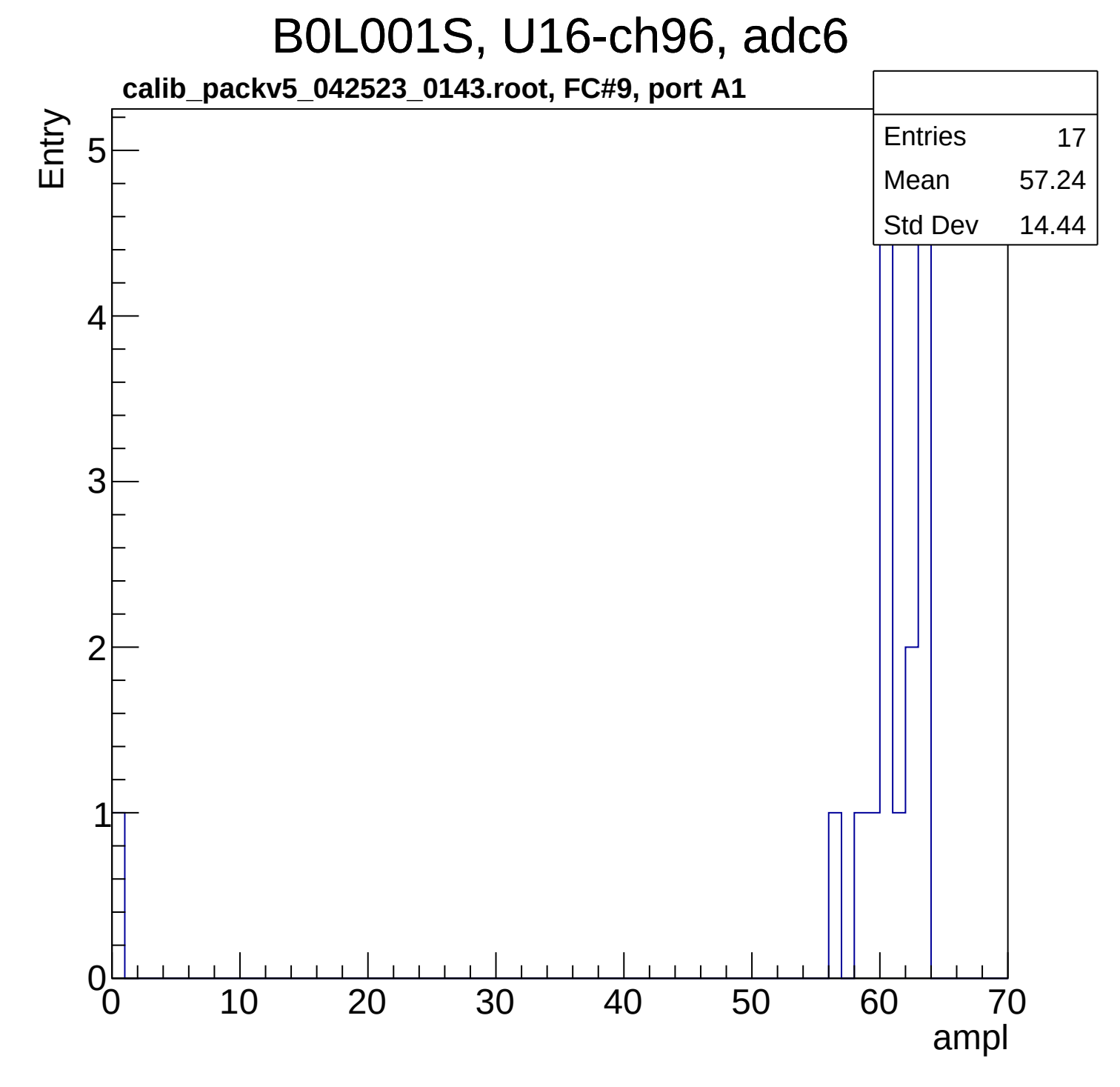
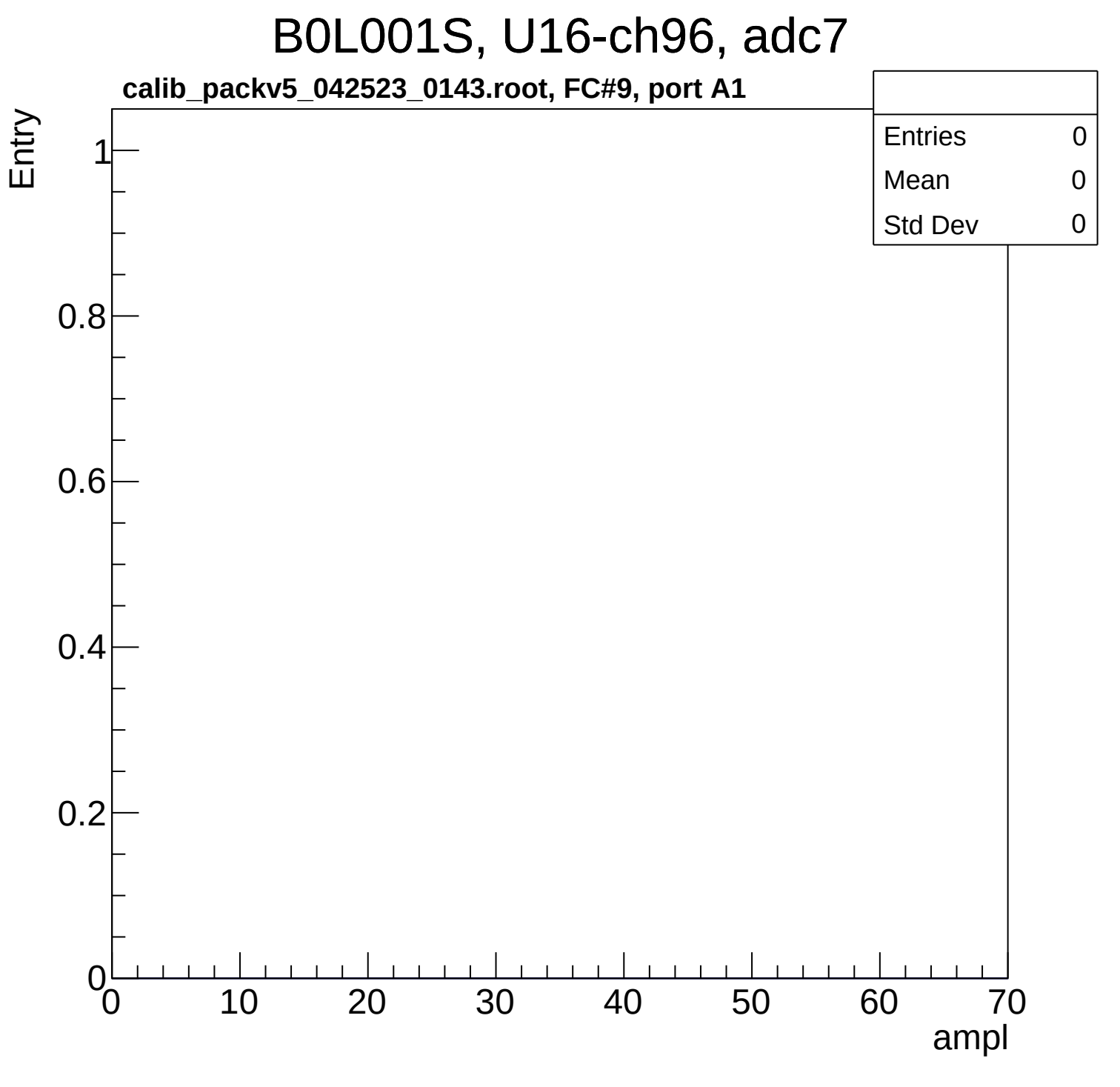
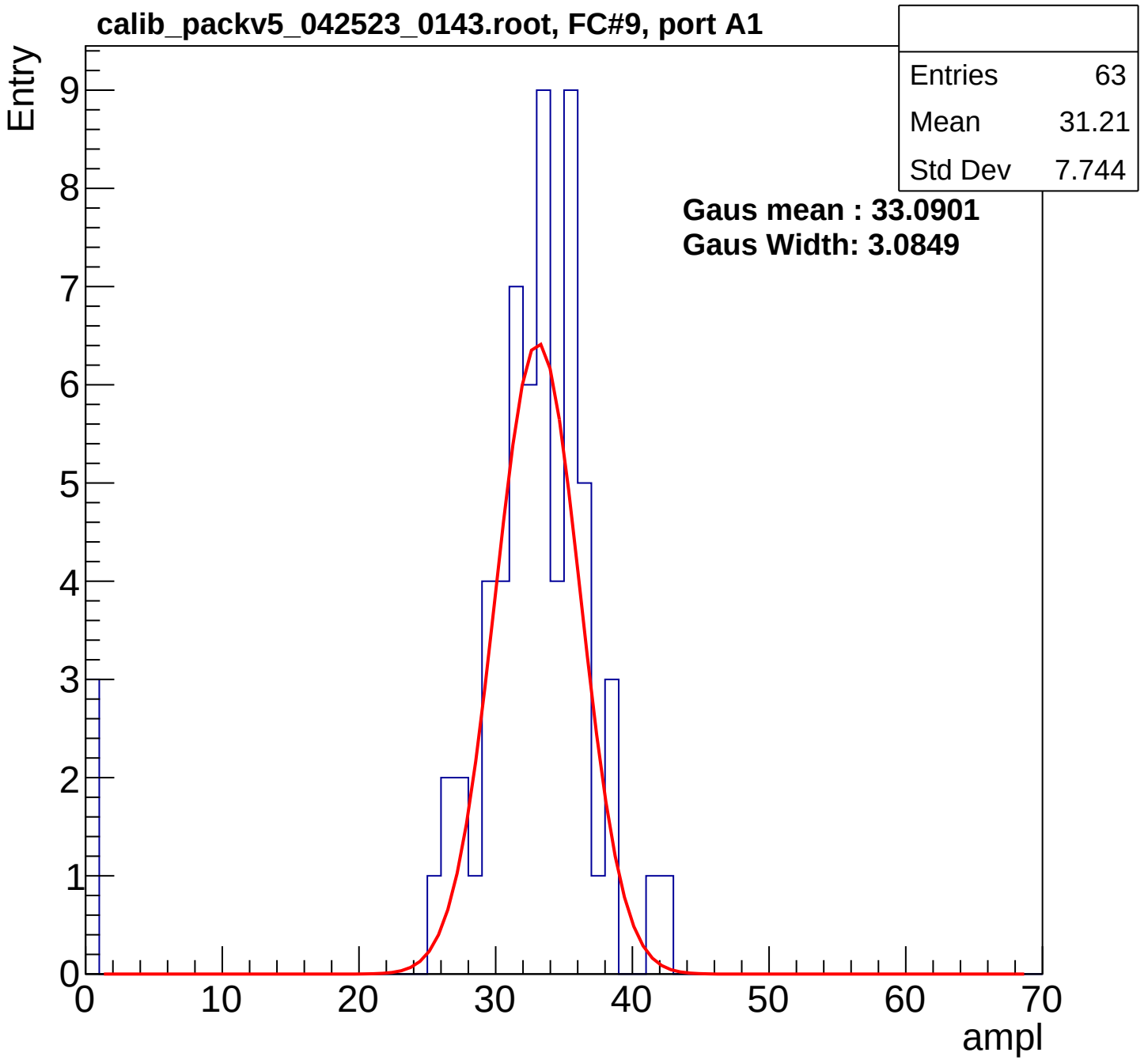

B0L001S, U16-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 57.24
Std Dev 14.44
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.21
Std Dev 7.744
Gaus mean : 33.0901
Gaus Width: 3.0849
Entry
ampl
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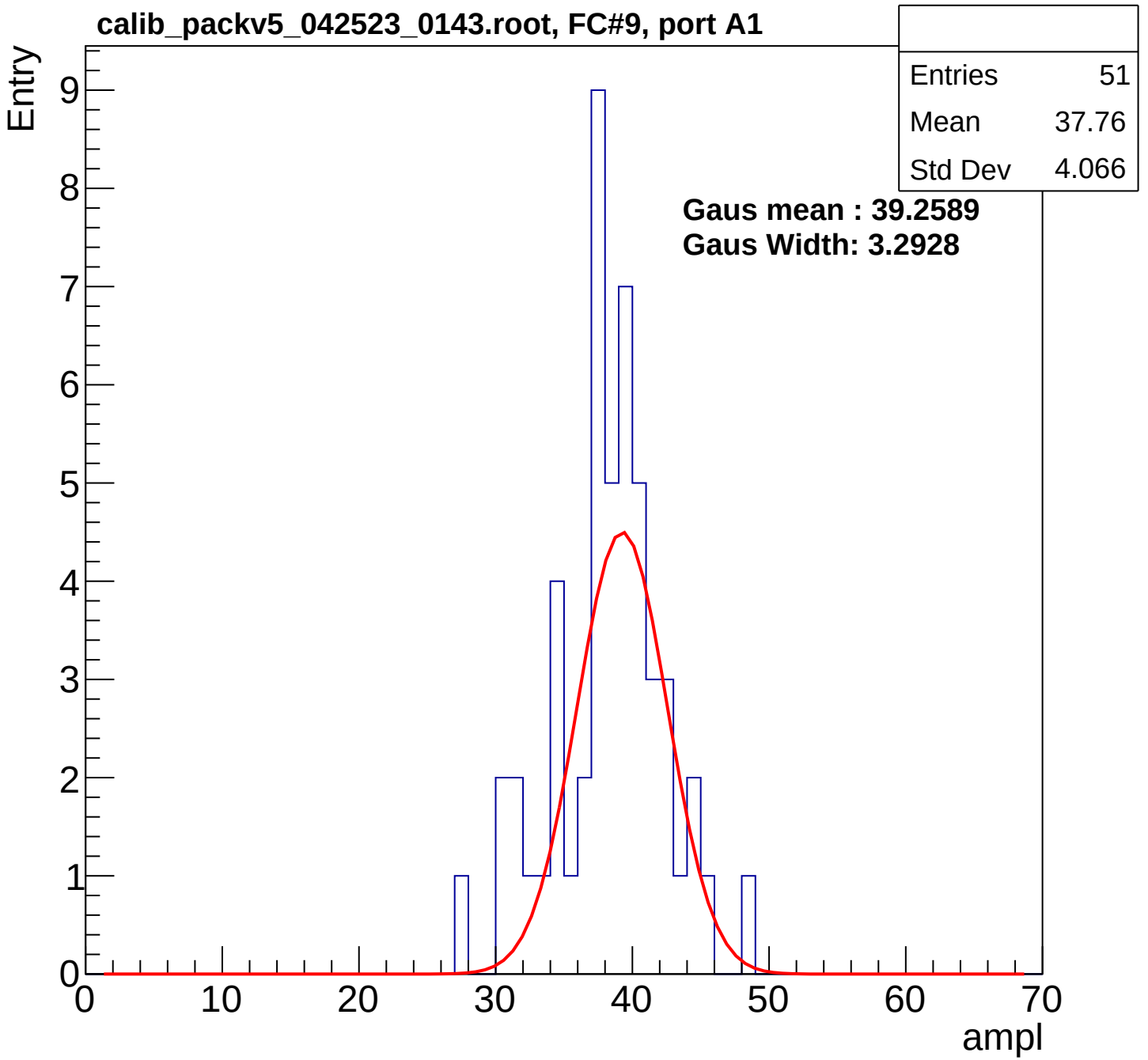
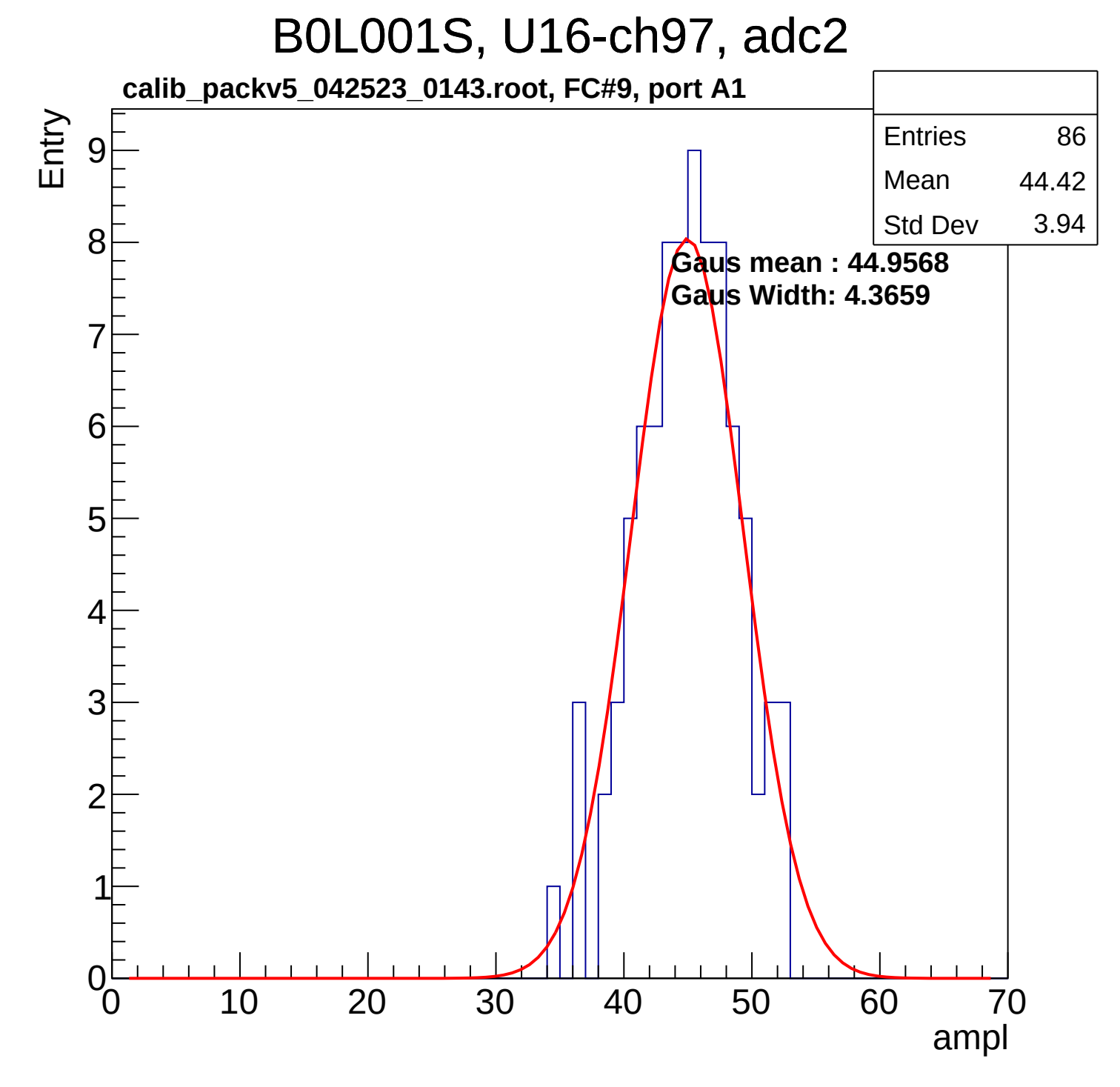
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 37.76
Std Dev 4.066
Gaus mean : 39.2589
Gaus Width: 3.2928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 44.42
Std Dev 3.94
Gaus mean : 44.9568
Gaus Width: 4.3659
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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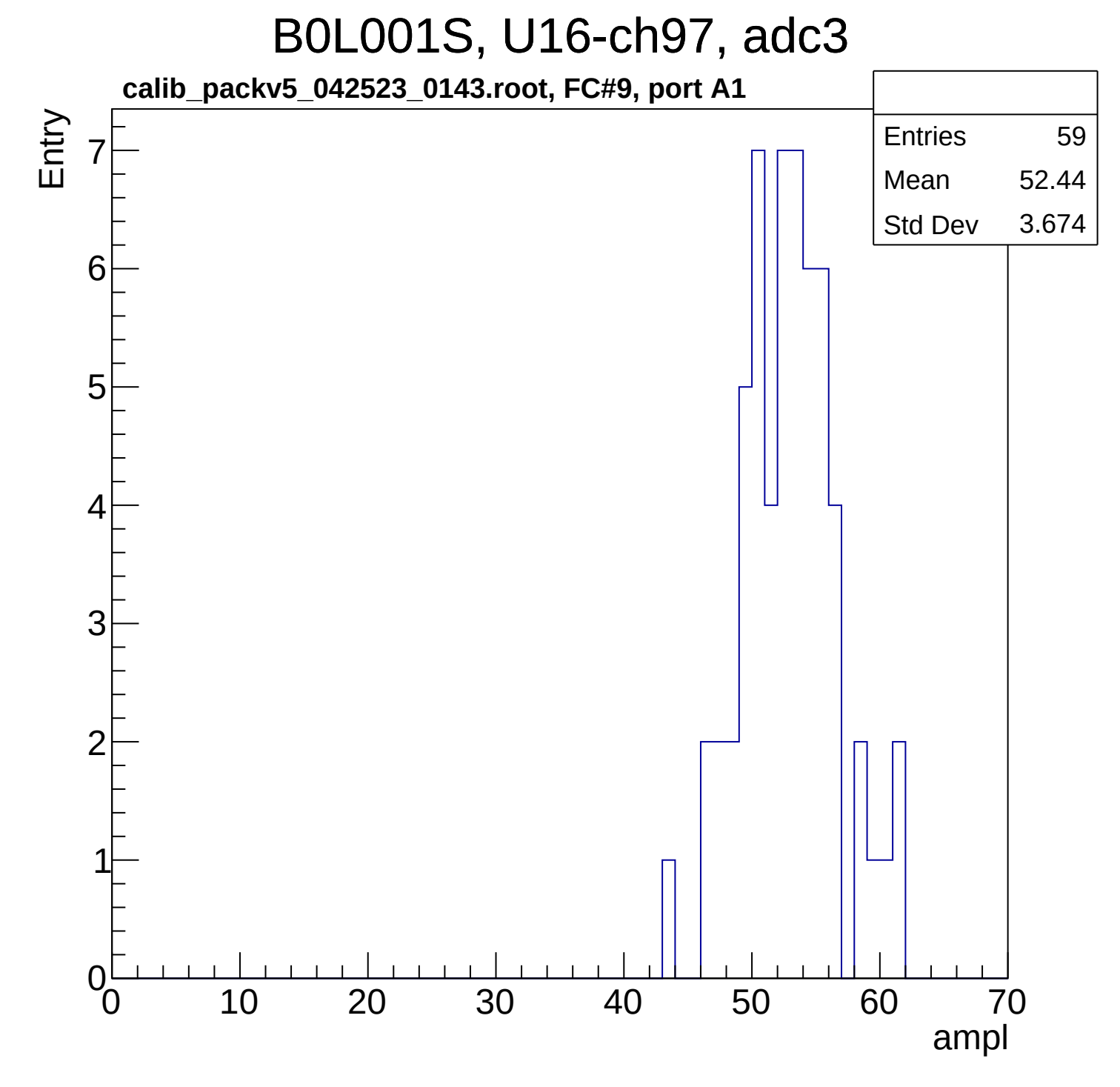
B0L001S, U16-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 52.44
Std Dev 3.674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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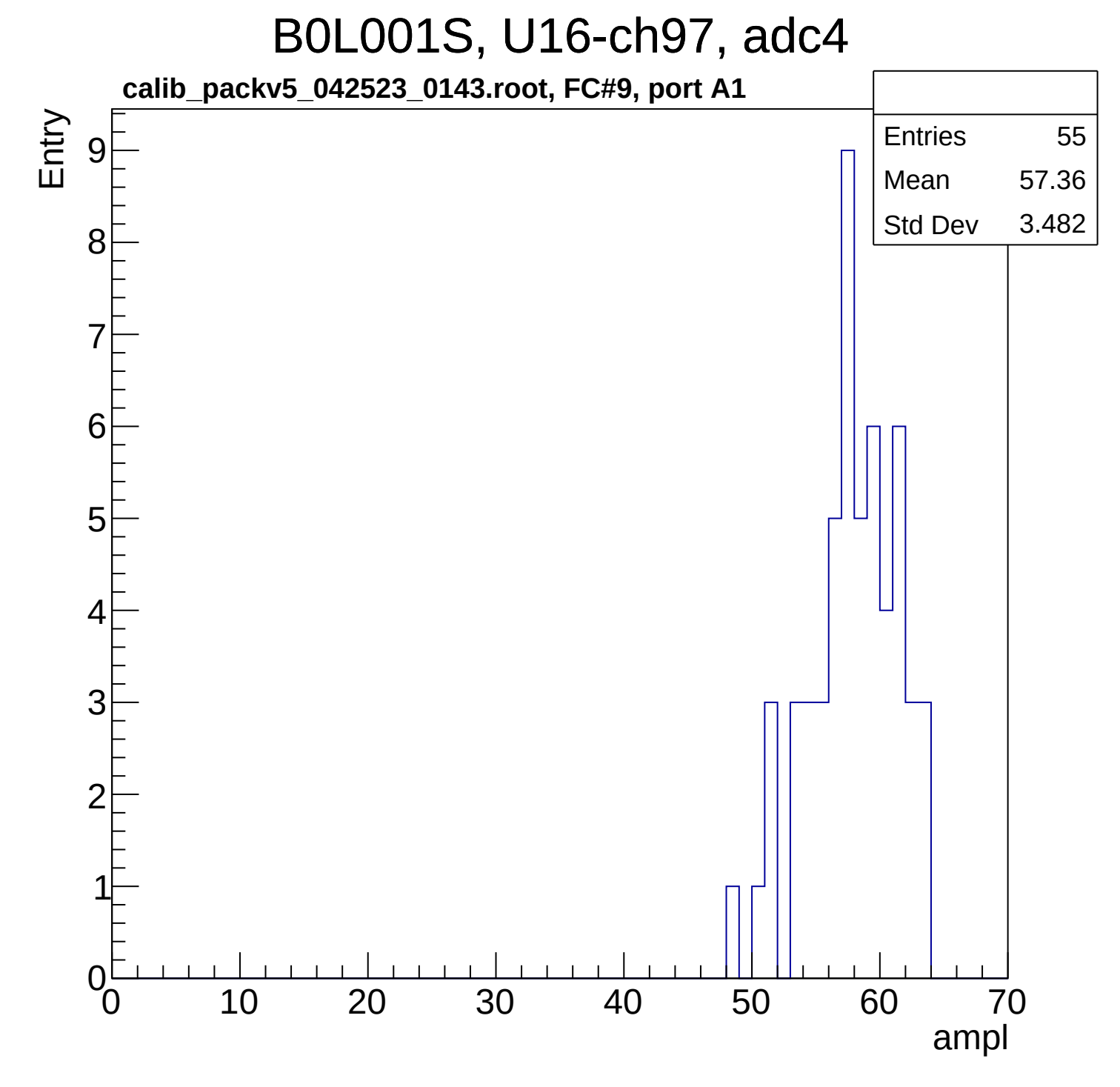
B0L001S, U16-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.36
Std Dev 3.482
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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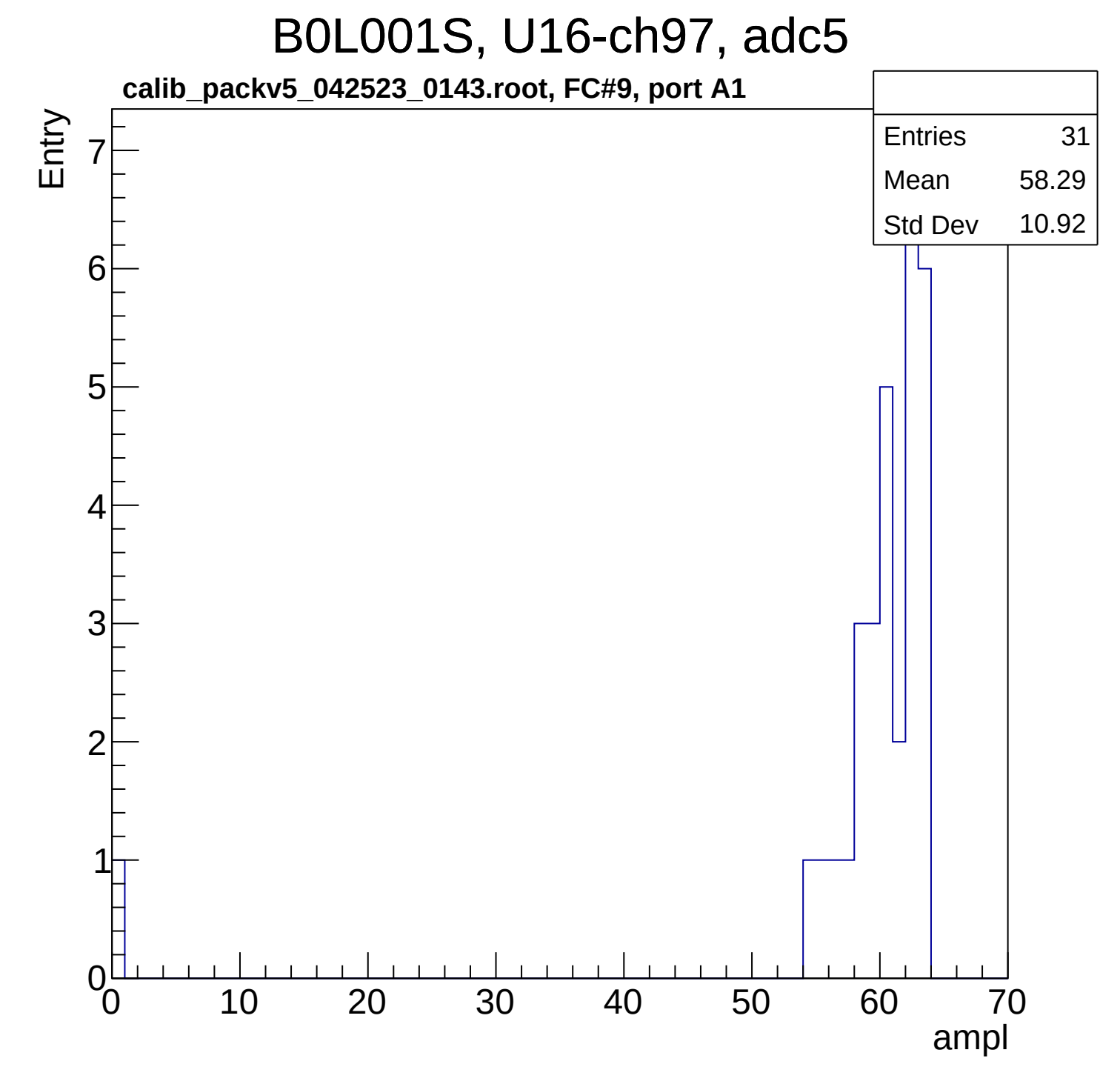
B0L001S, U16-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.29
Std Dev 10.92
Entry
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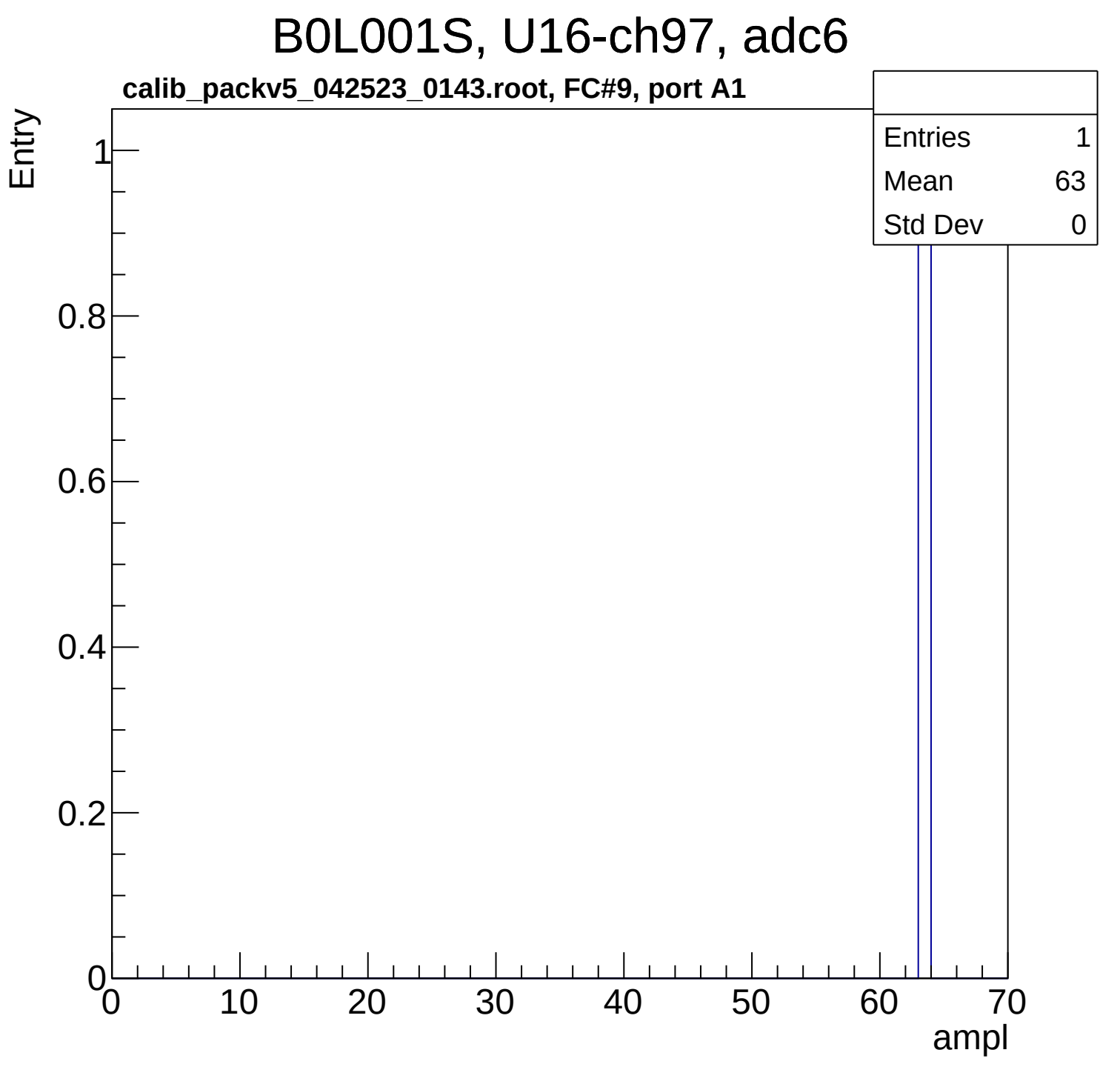
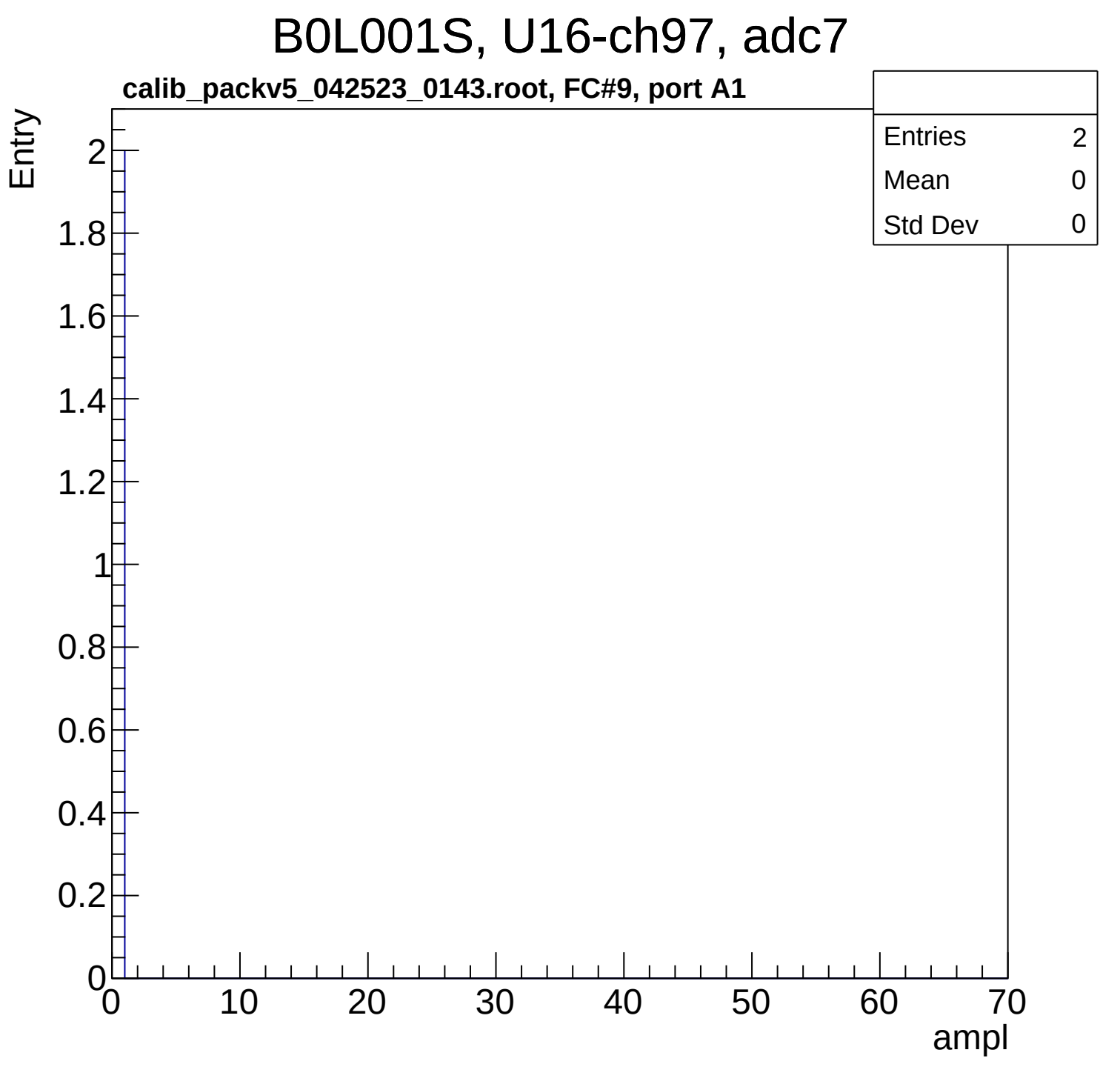
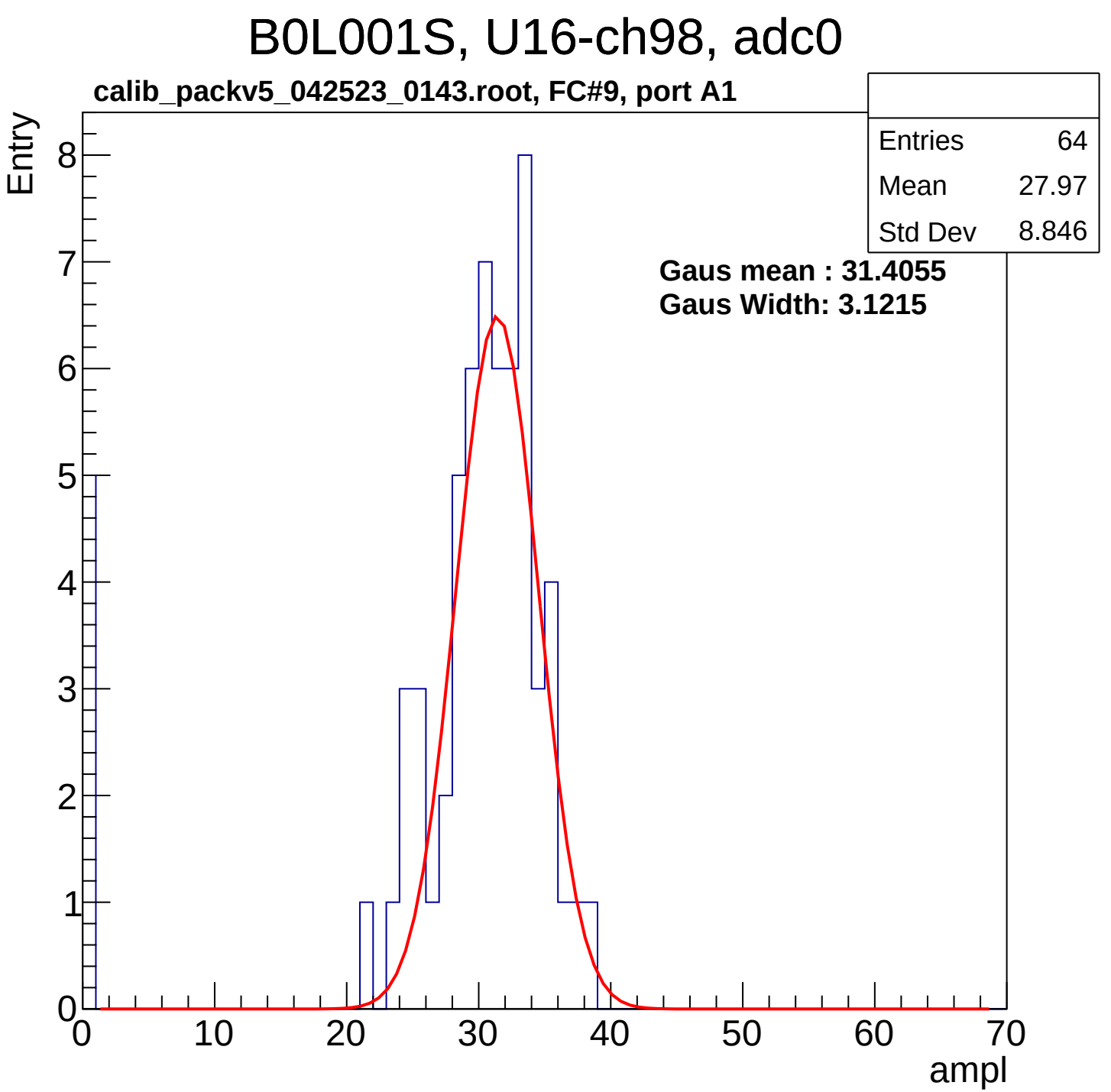
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 27.97
Std Dev 8.846
Gaus mean : 31.4055
Gaus Width: 3.1215
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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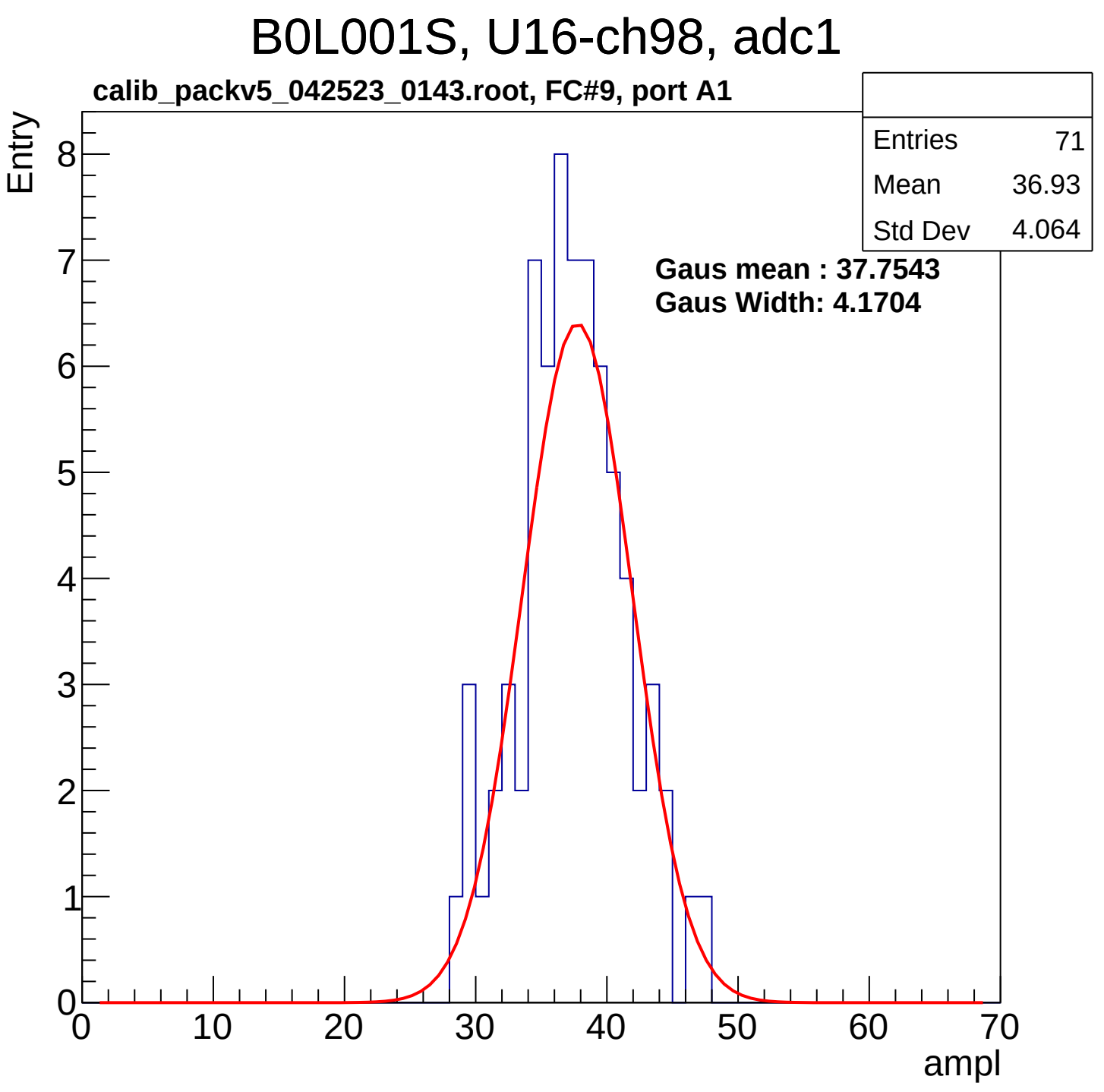
B0L001S, U16-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.93
Std Dev 4.064
Gaus mean : 37.7543
Gaus Width: 4.1704
Entry
ampl
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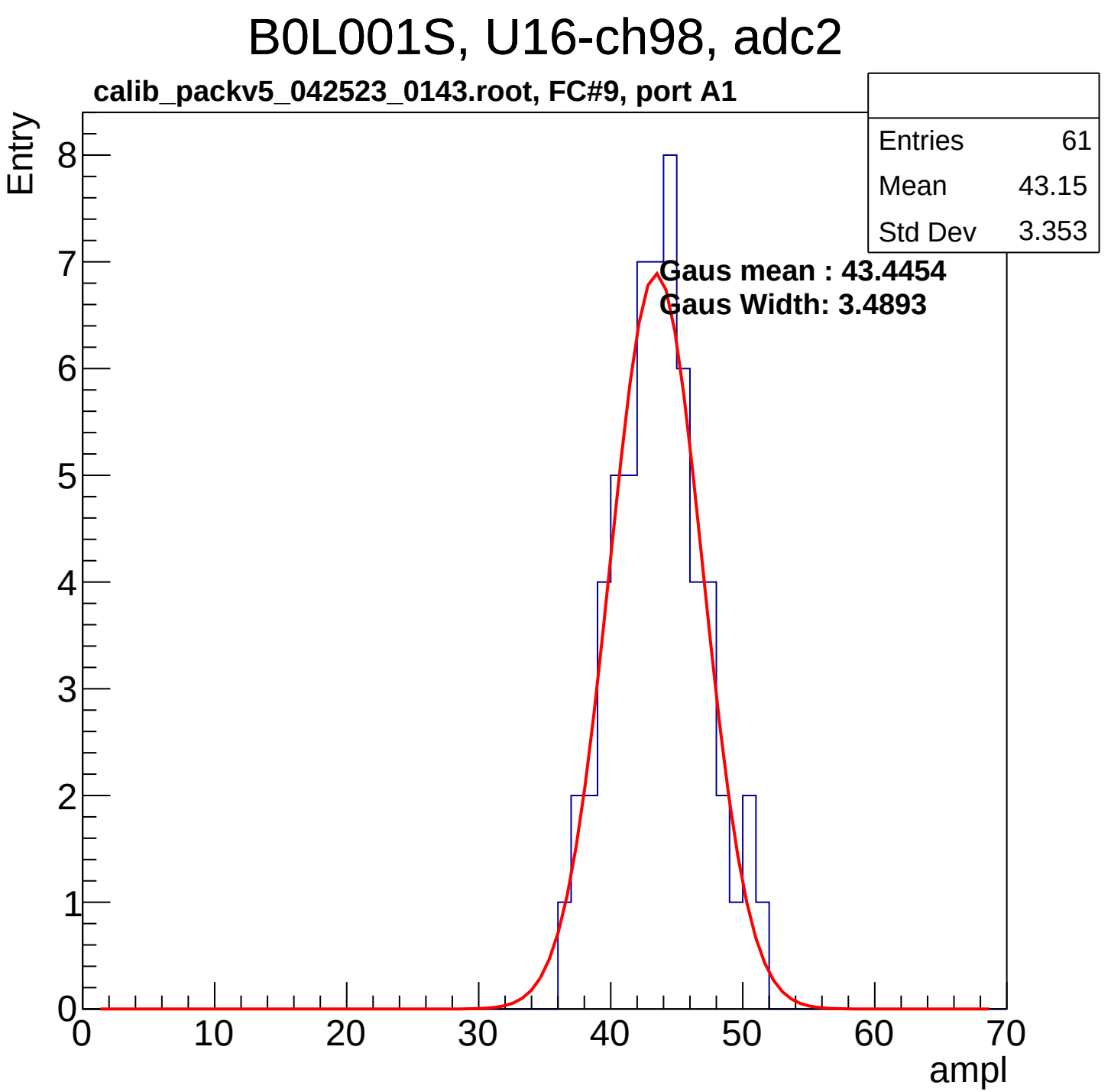
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.15
Std Dev 3.353
Gaus mean : 43.4454
Gaus Width: 3.4893
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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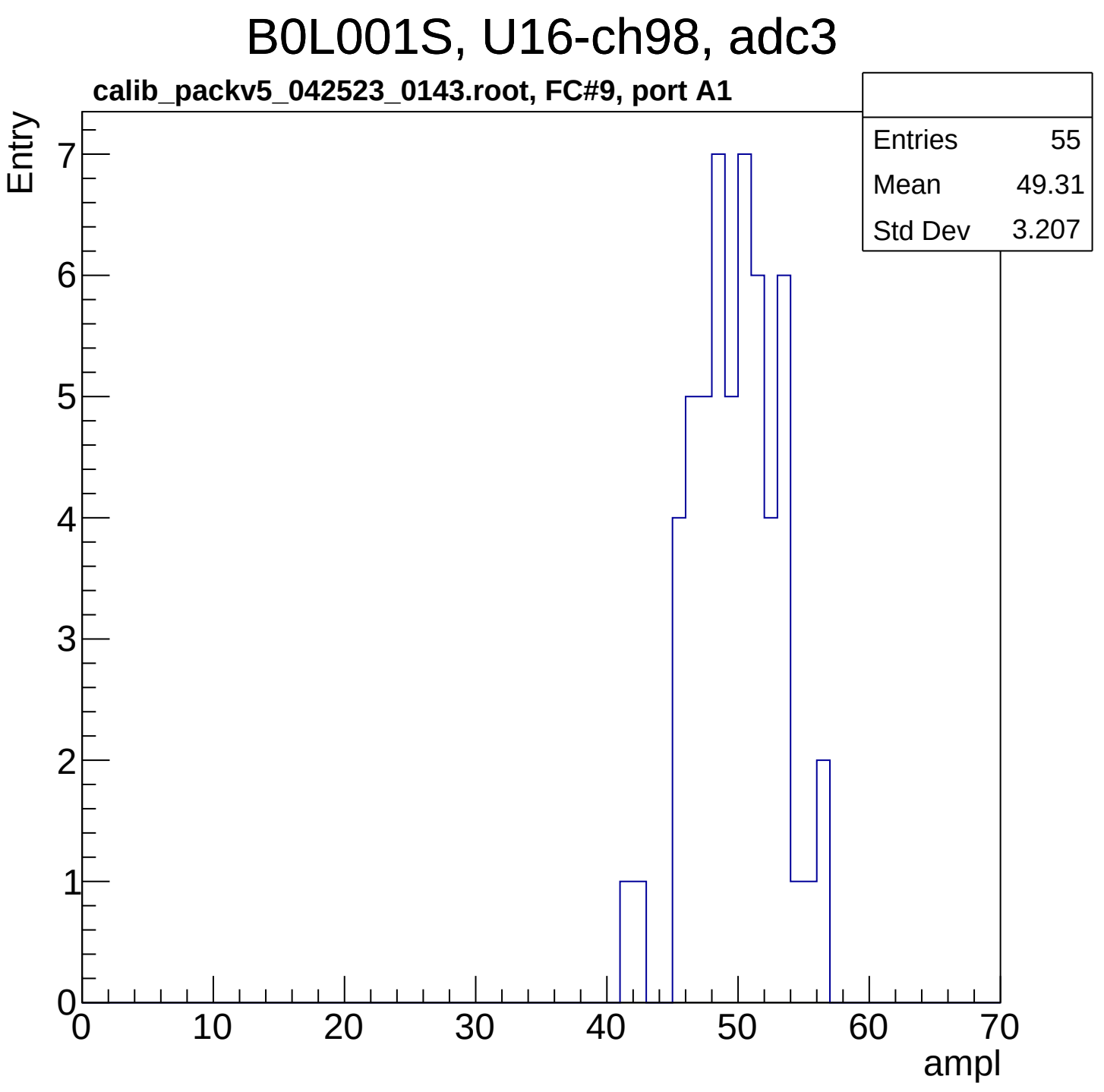
B0L001S, U16-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.31
Std Dev 3.207
Entry
ampl
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1
2
3
4
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0
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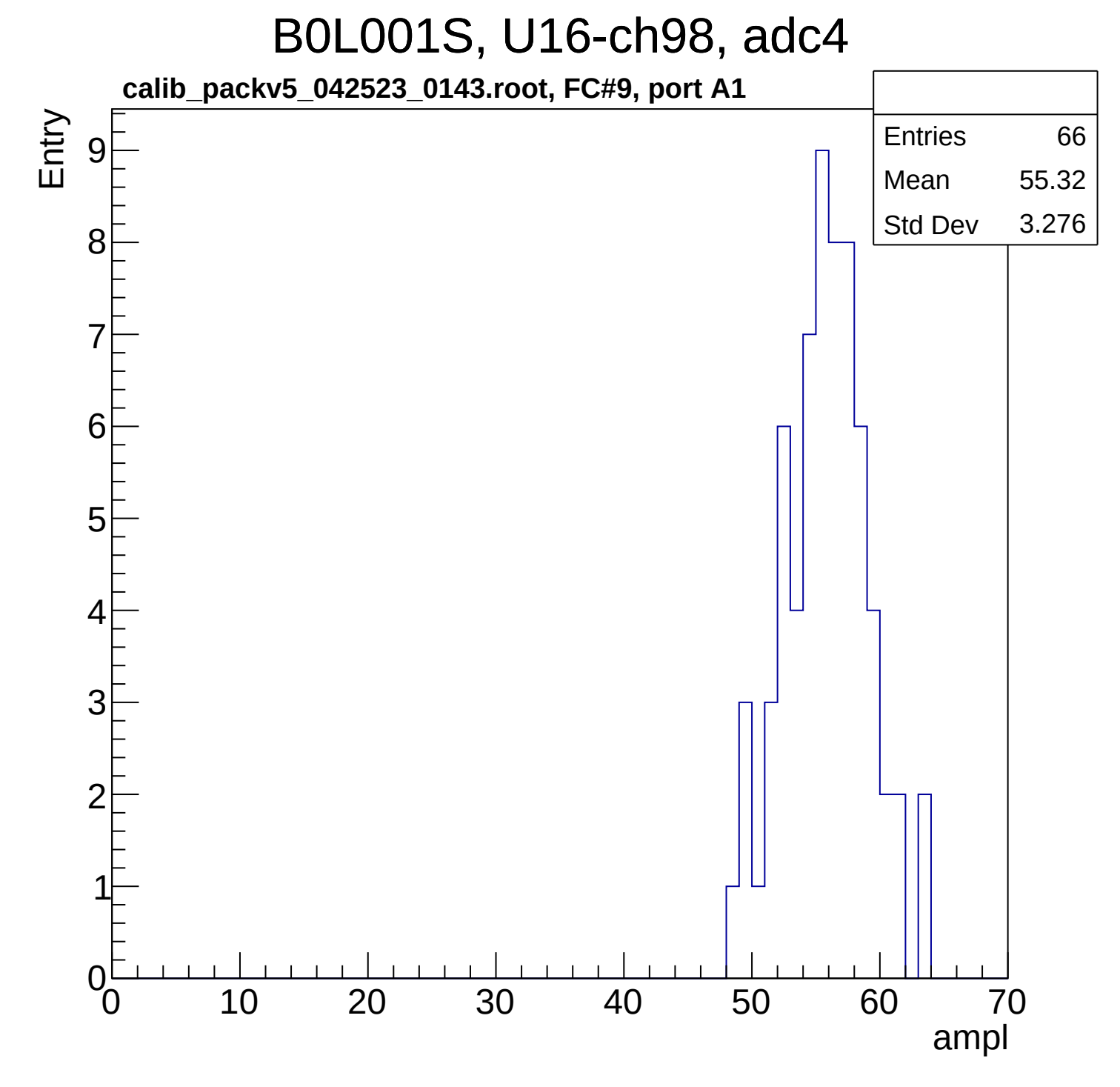
40
50
60
70


B0L001S, U16-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.32
Std Dev 3.276
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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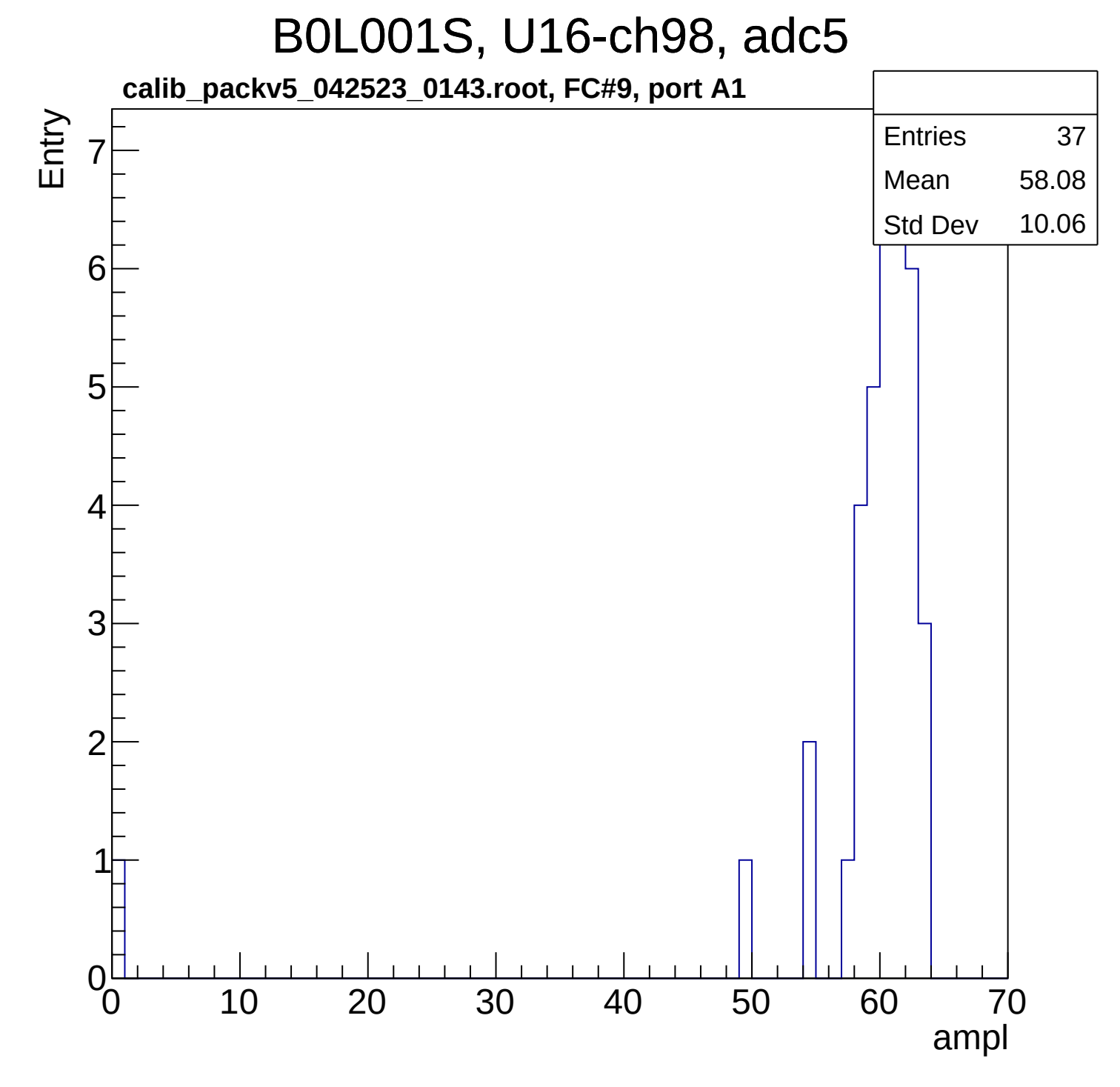
B0L001S, U16-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.08
Std Dev 10.06
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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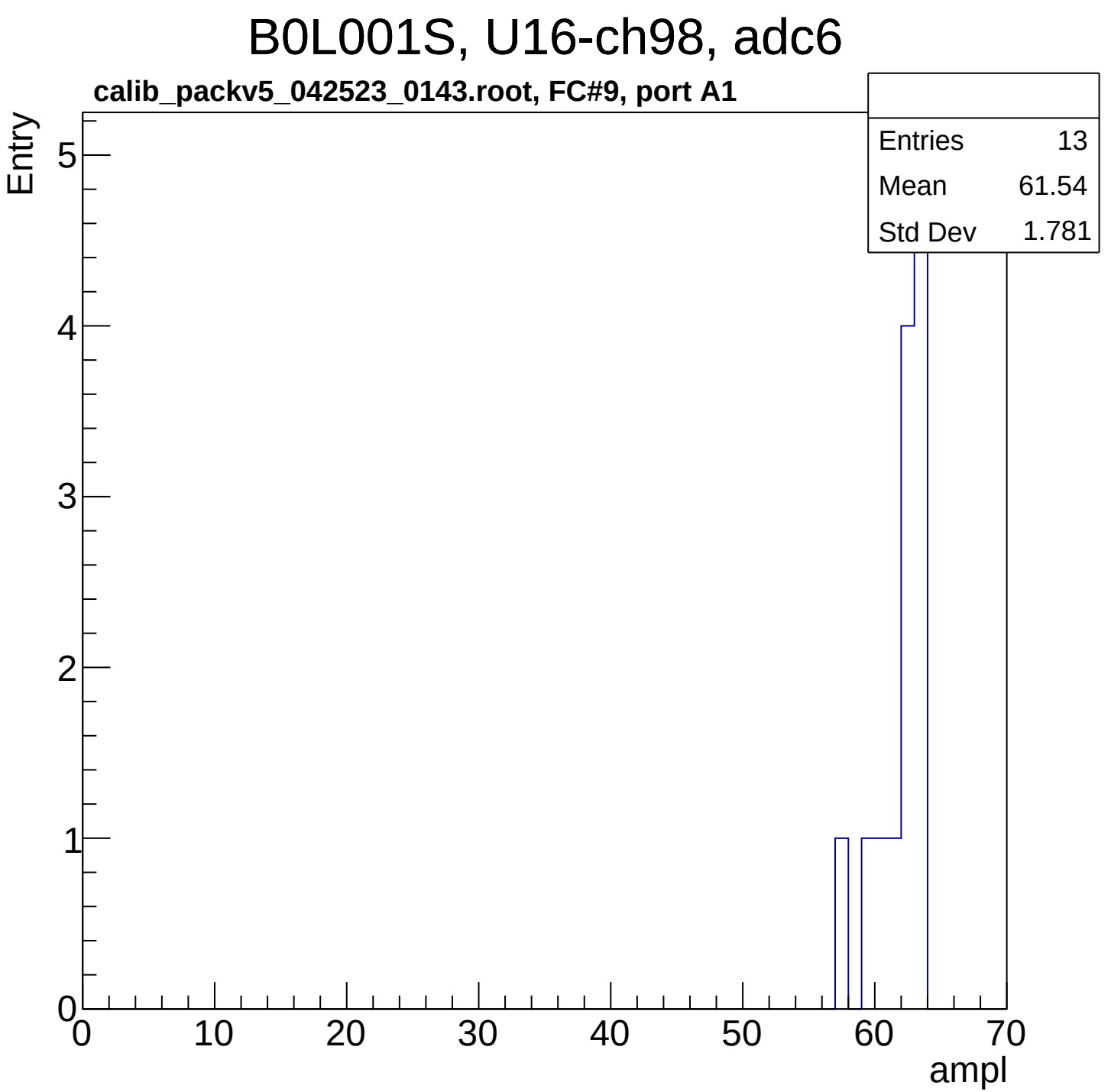
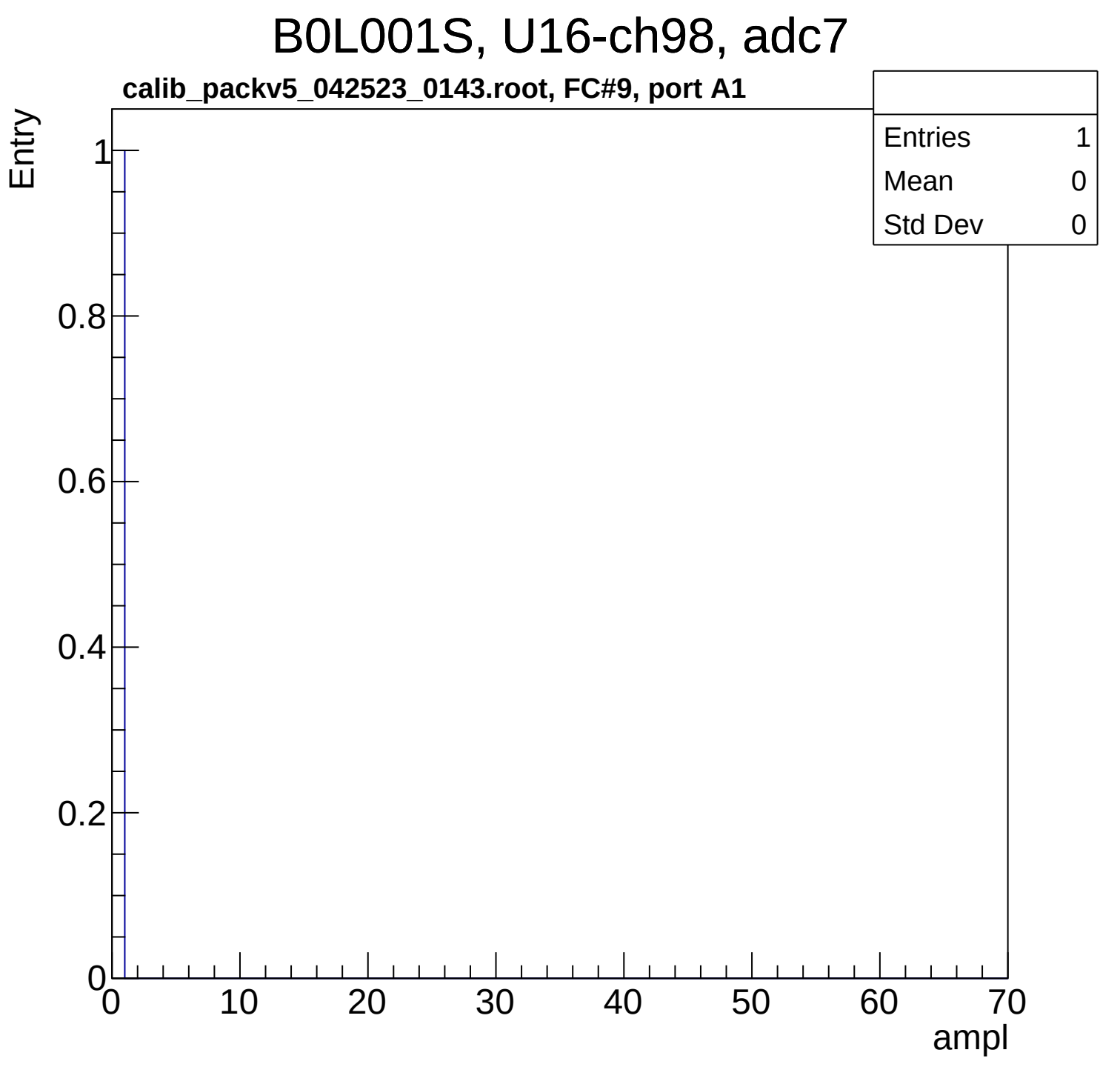

B0L001S, U16-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 61.54
Std Dev 1.781
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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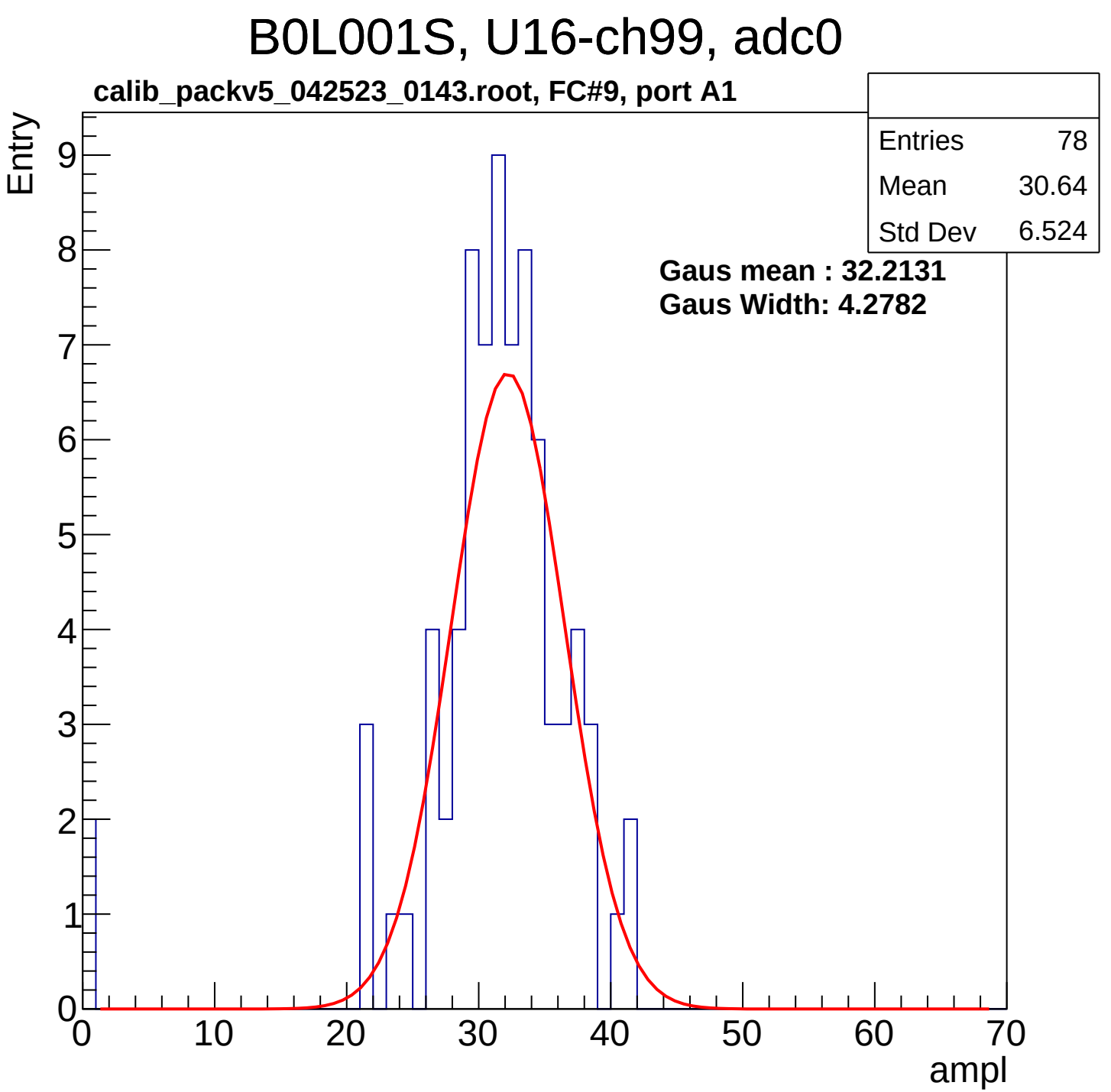
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.64
Std Dev 6.524
Gaus mean : 32.2131
Gaus Width: 4.2782
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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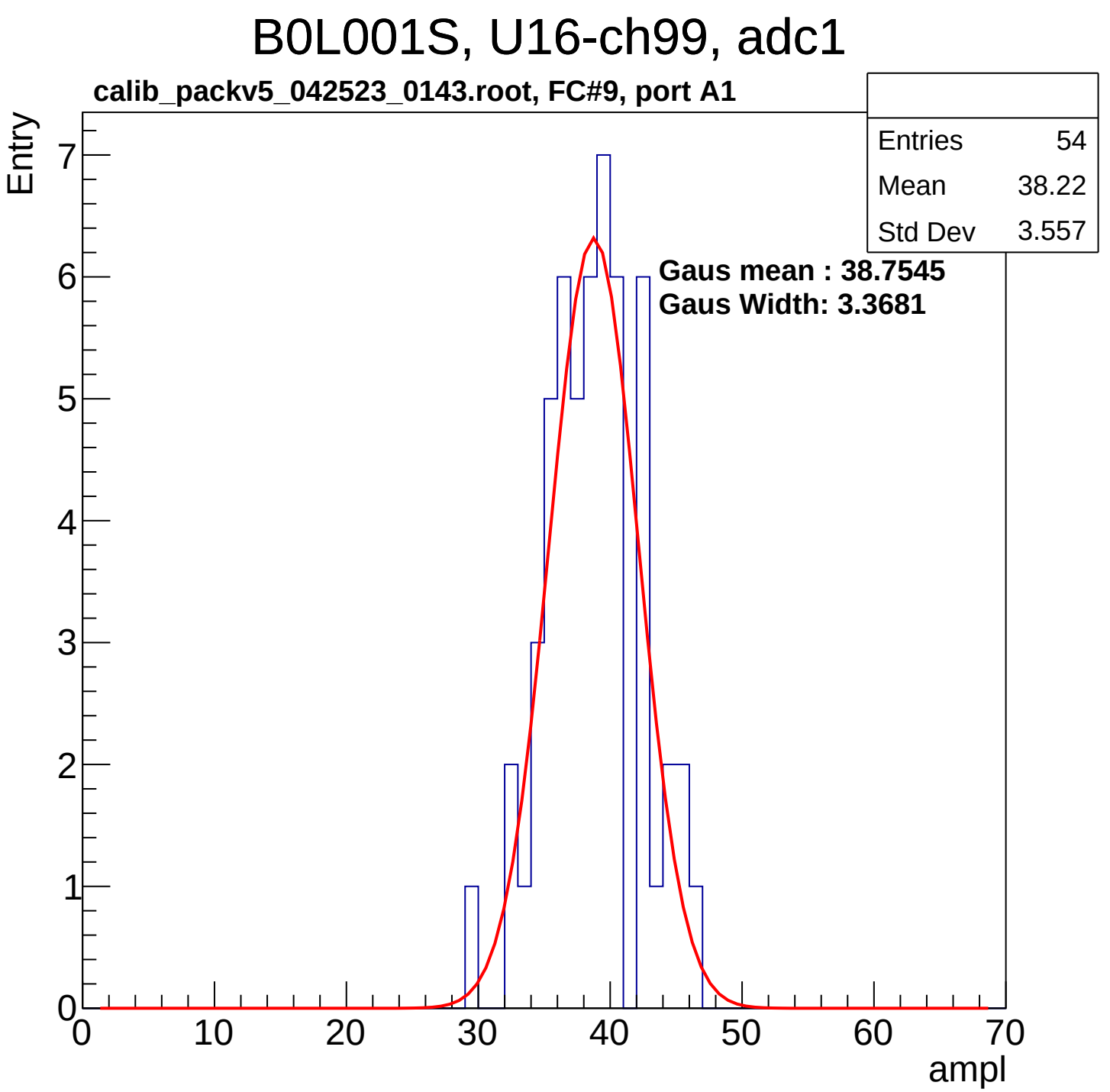
B0L001S, U16-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.22
Std Dev 3.557
Gaus mean : 38.7545
Gaus Width: 3.3681
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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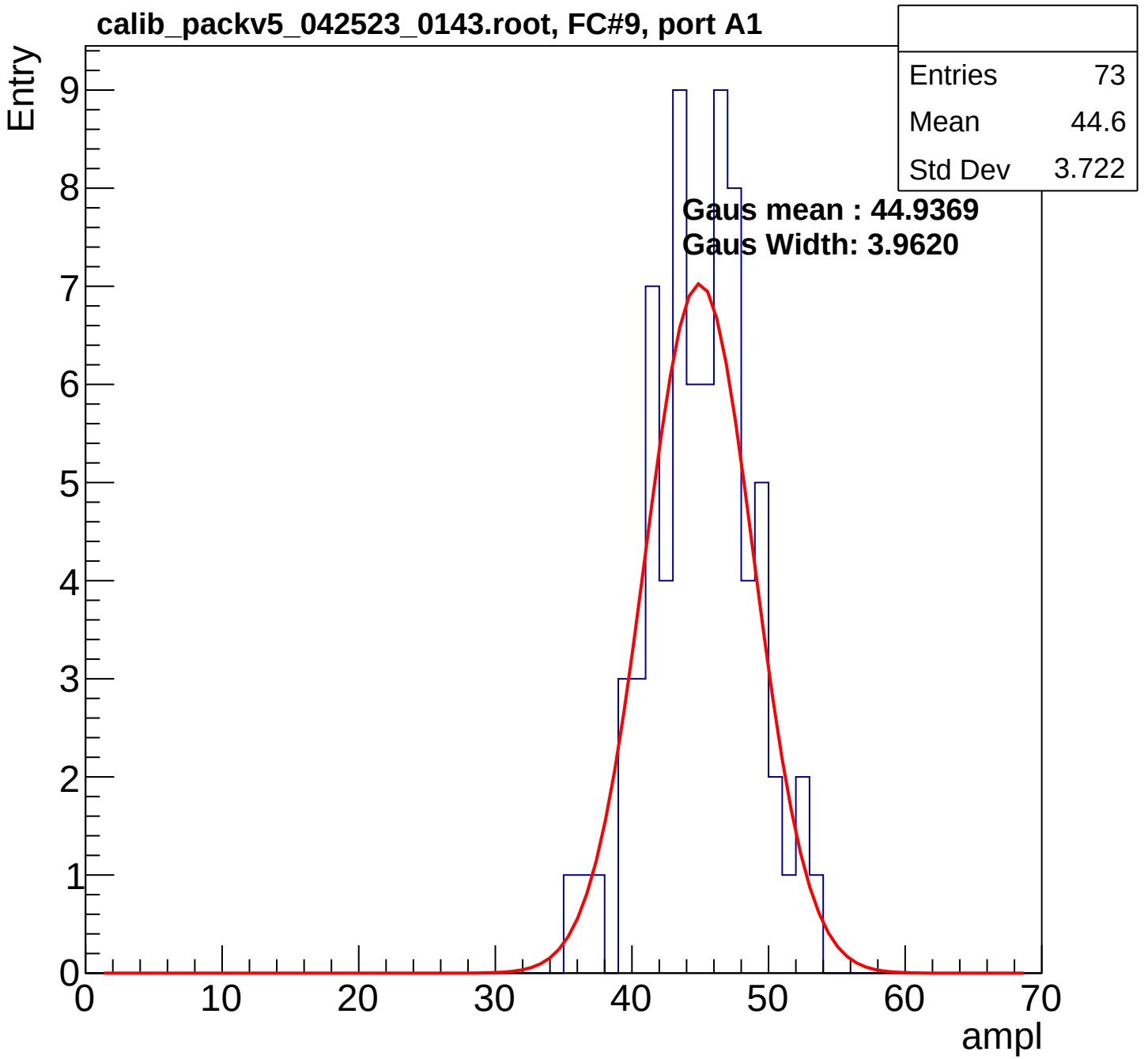
B0L001S, U16-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.6
Std Dev 3.722
Gaus mean : 44.9369
Gaus Width: 3.9620
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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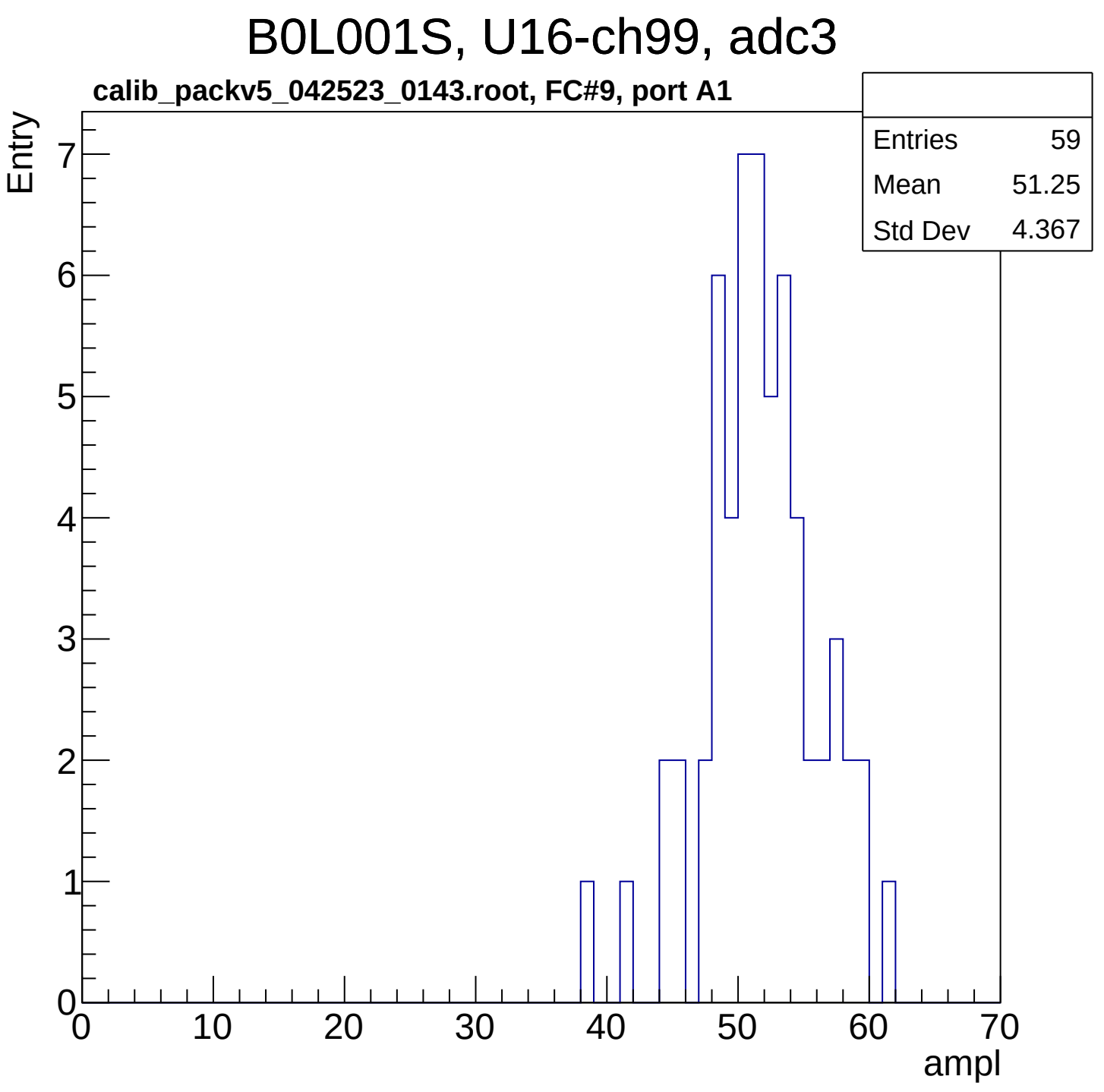
B0L001S, U16-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.25
Std Dev 4.367
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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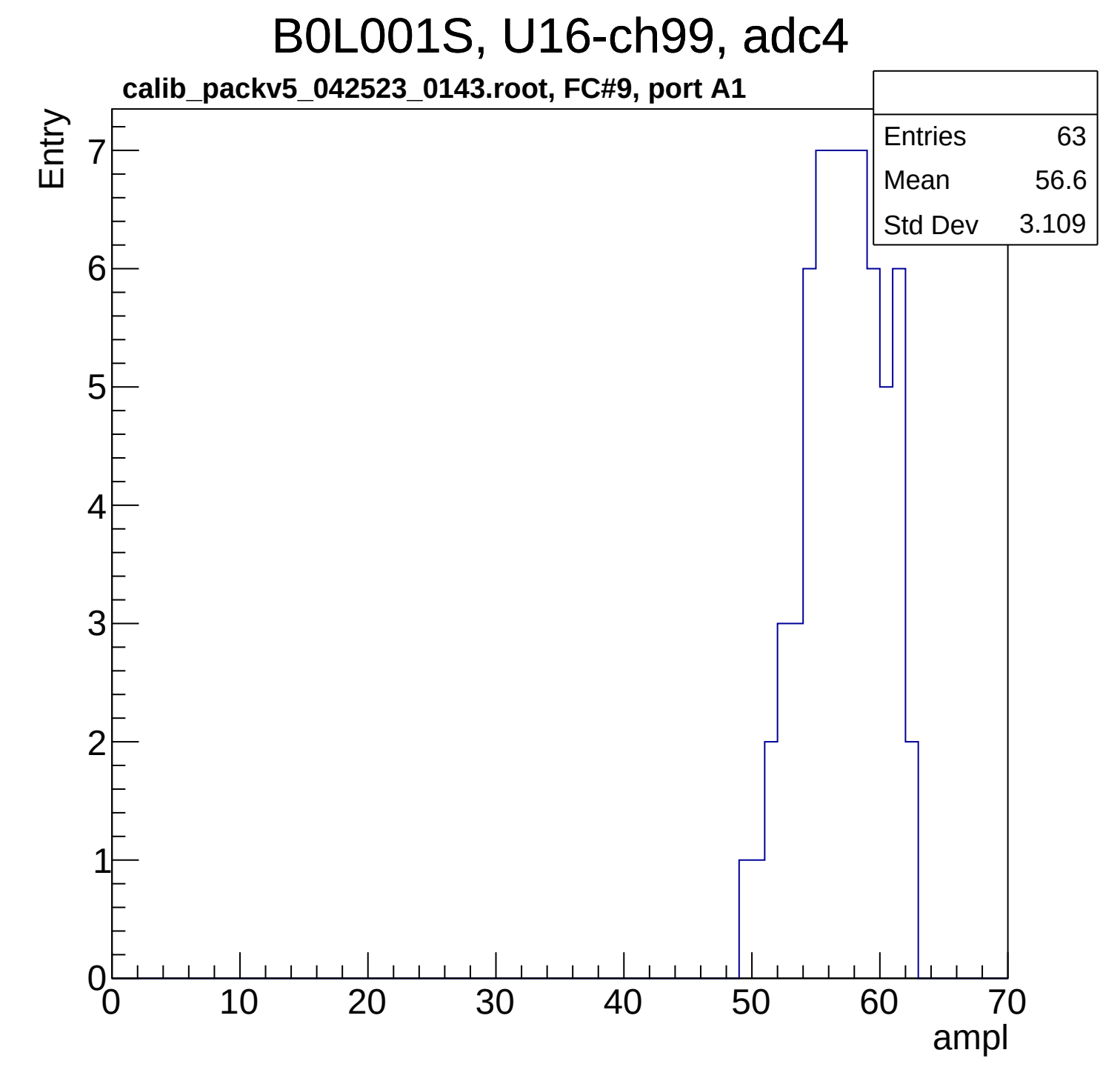
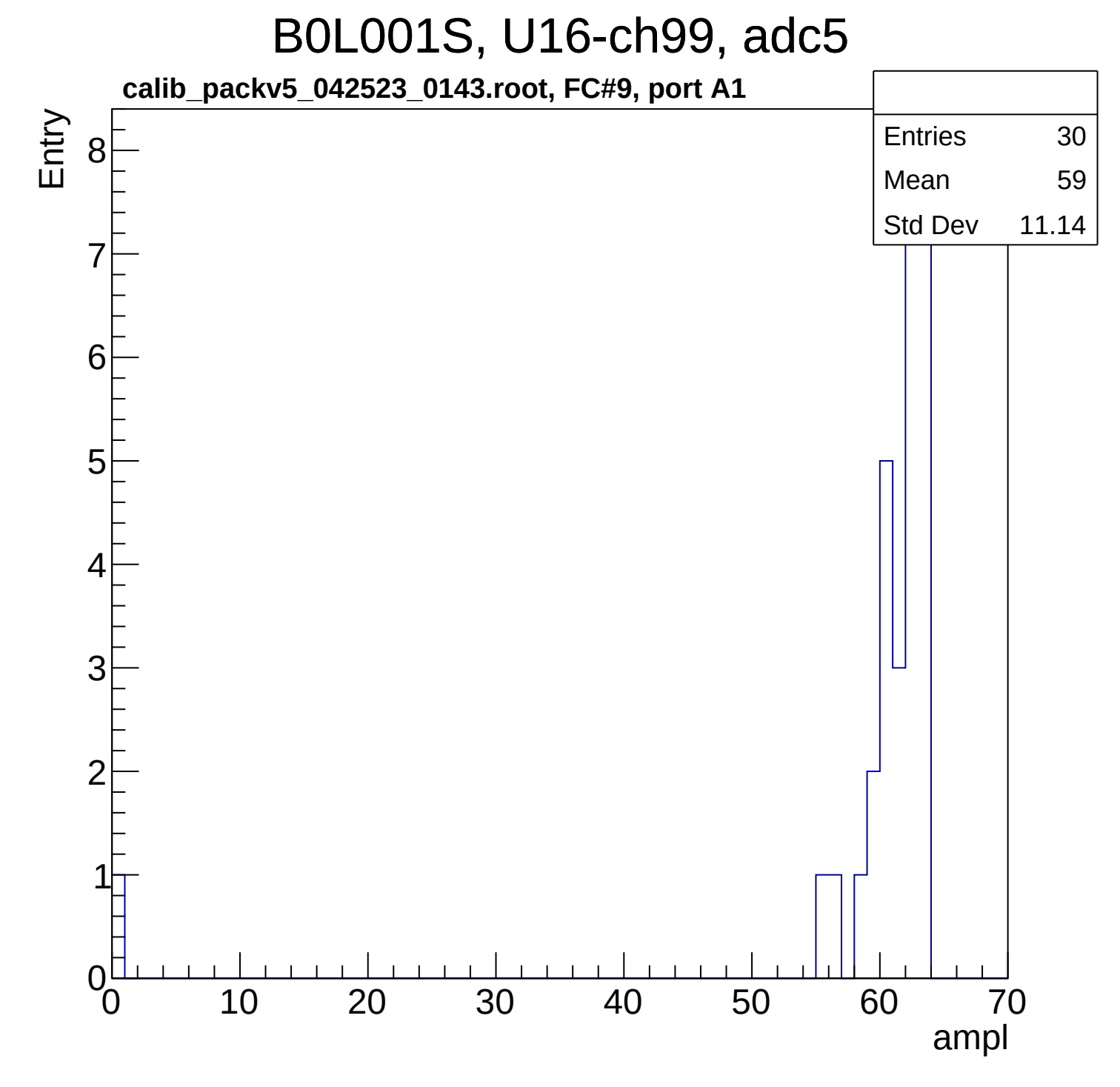

B0L001S, U16-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.6
Std Dev 3.109
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59
Std Dev 11.14
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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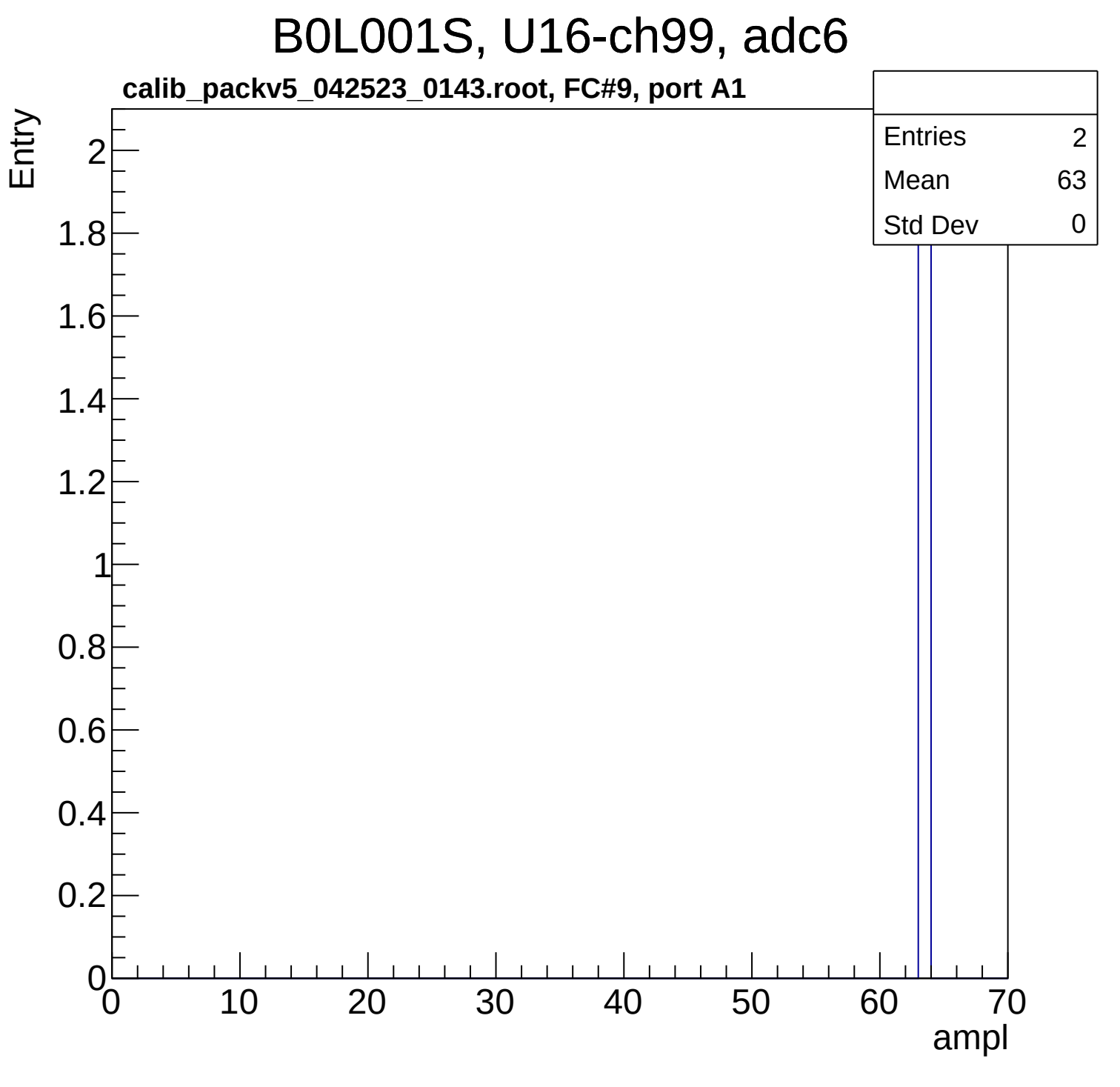
B0L001S, U16-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
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60
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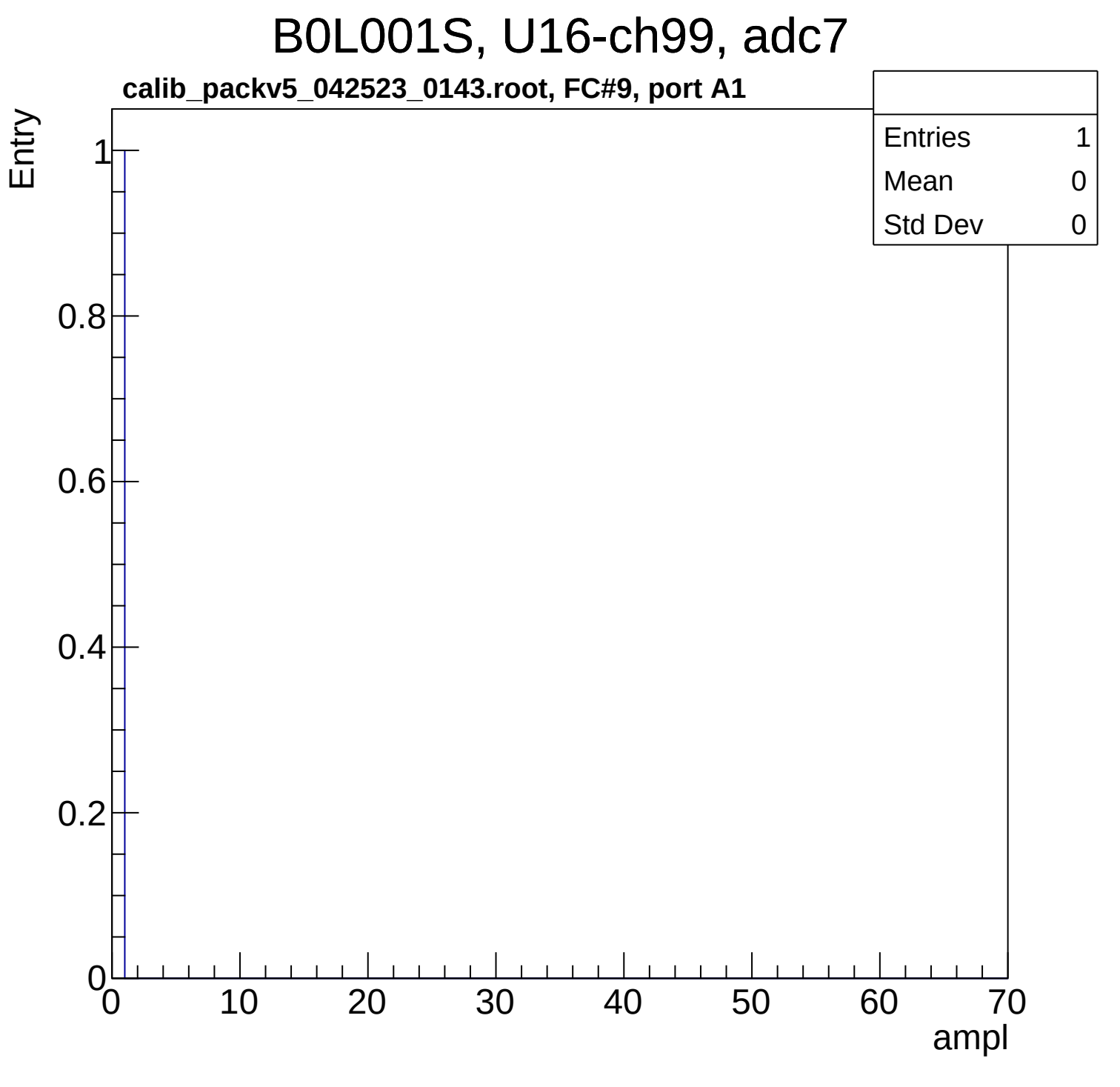
B0L001S, U16-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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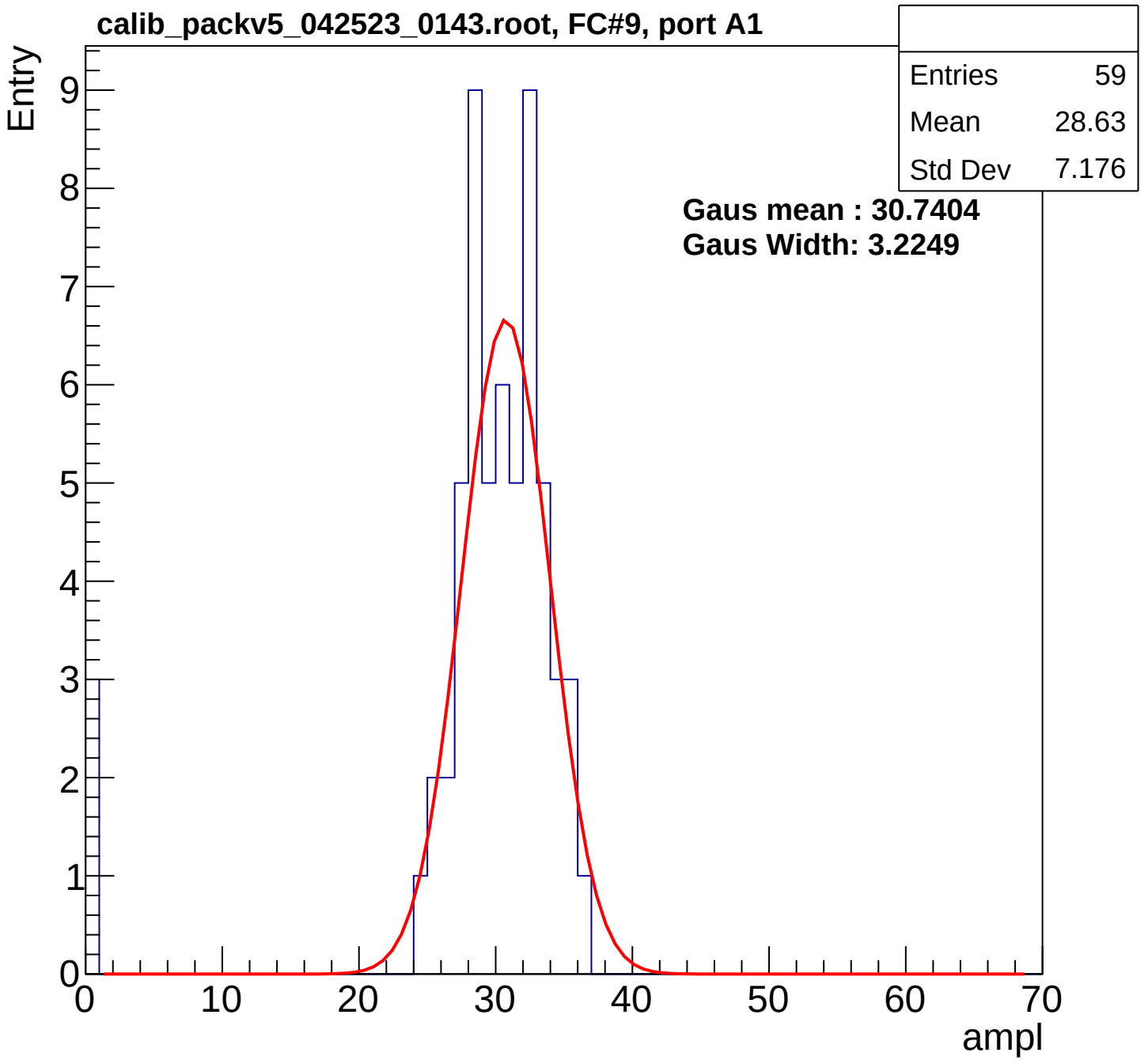
50
60
70
ampl


B0L001S, U16-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 28.63
Std Dev 7.176
Gaus mean : 30.7404
Gaus Width: 3.2249
Entry
ampl
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2
3
4
5
6
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8
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0
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60
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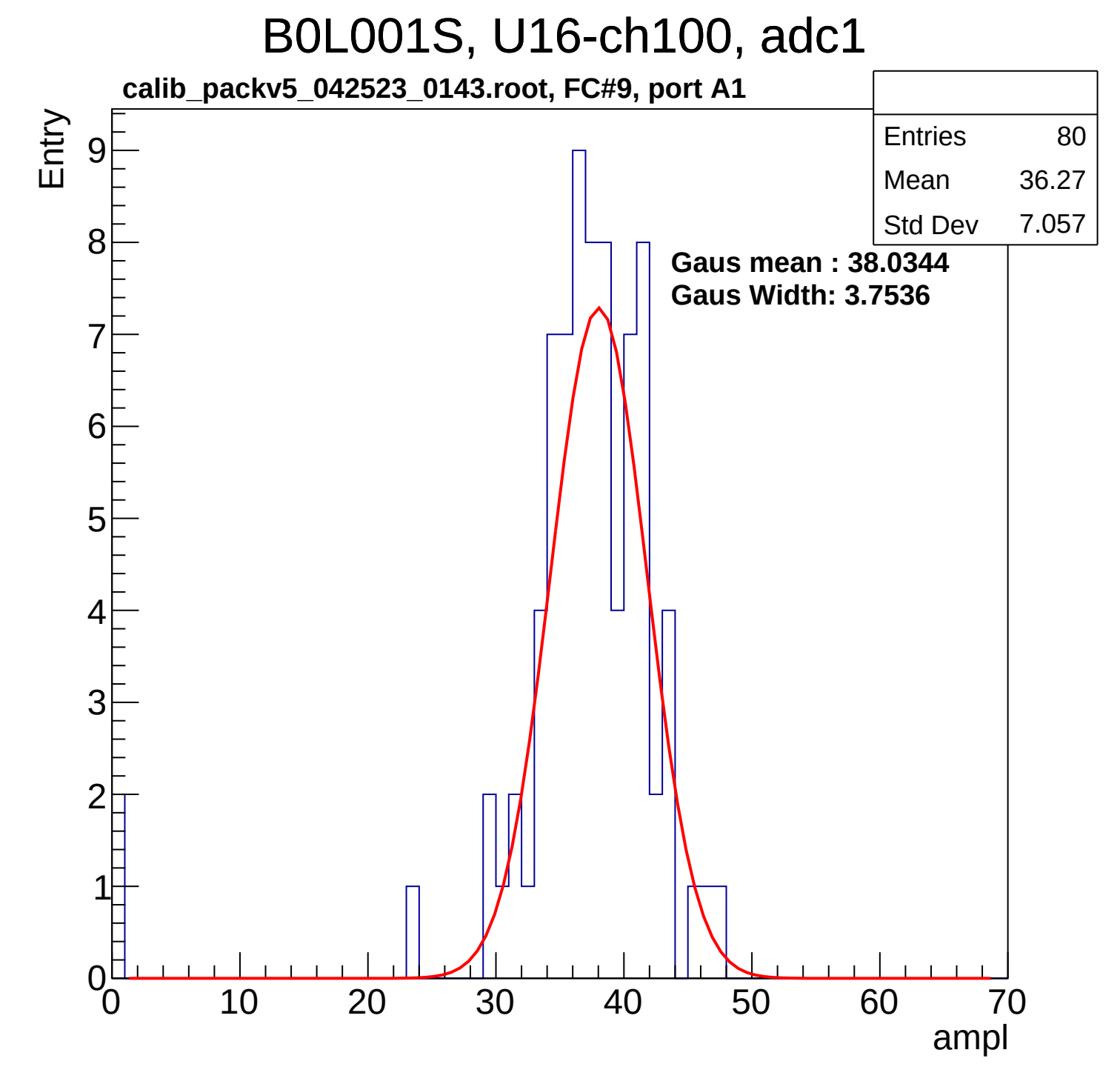
B0L001S, U16-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 36.27
Std Dev 7.057
Gaus mean : 38.0344
Gaus Width: 3.7536
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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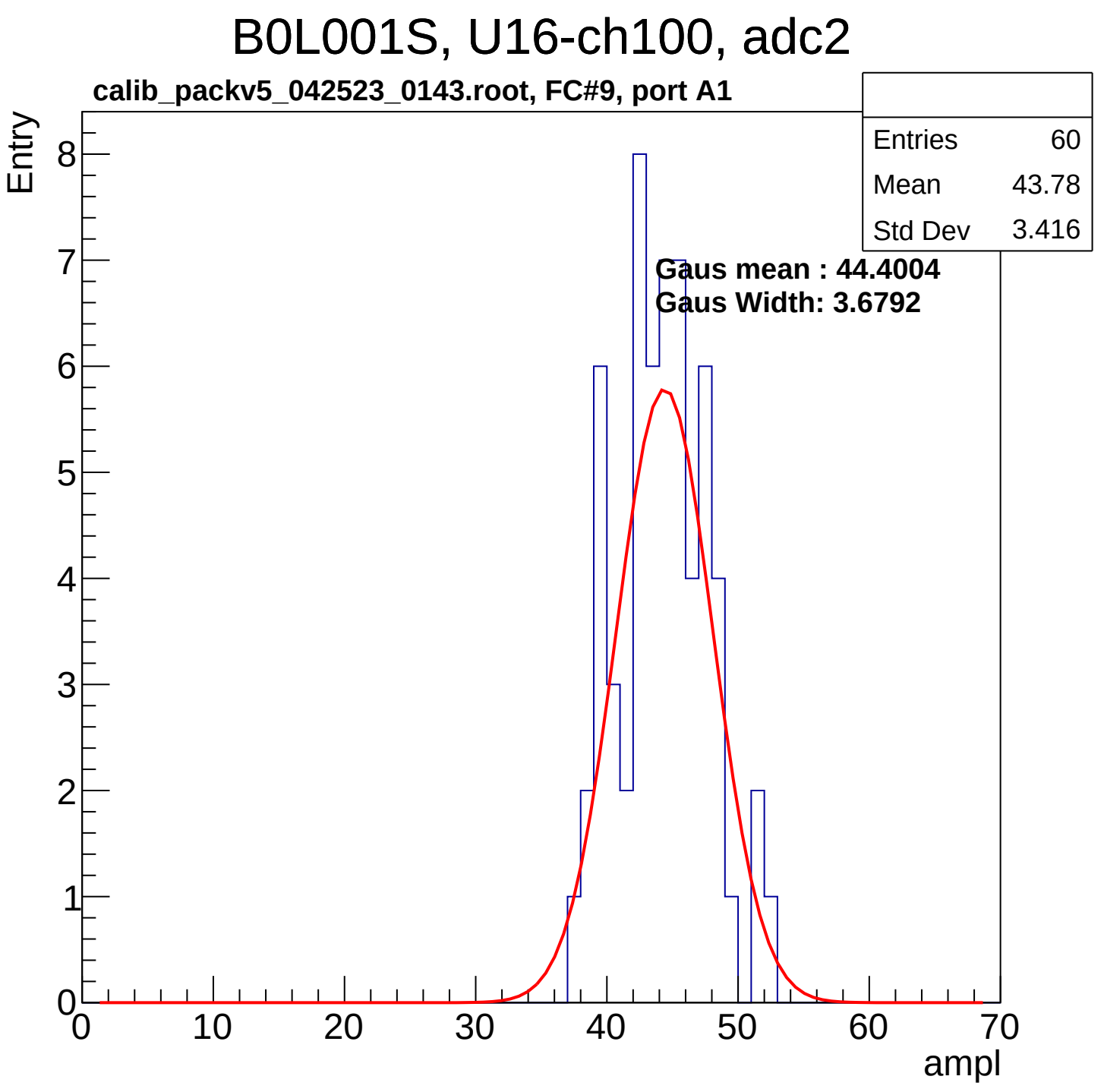
B0L001S, U16-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.78
Std Dev 3.416
Gaus mean : 44.4004
Gaus Width: 3.6792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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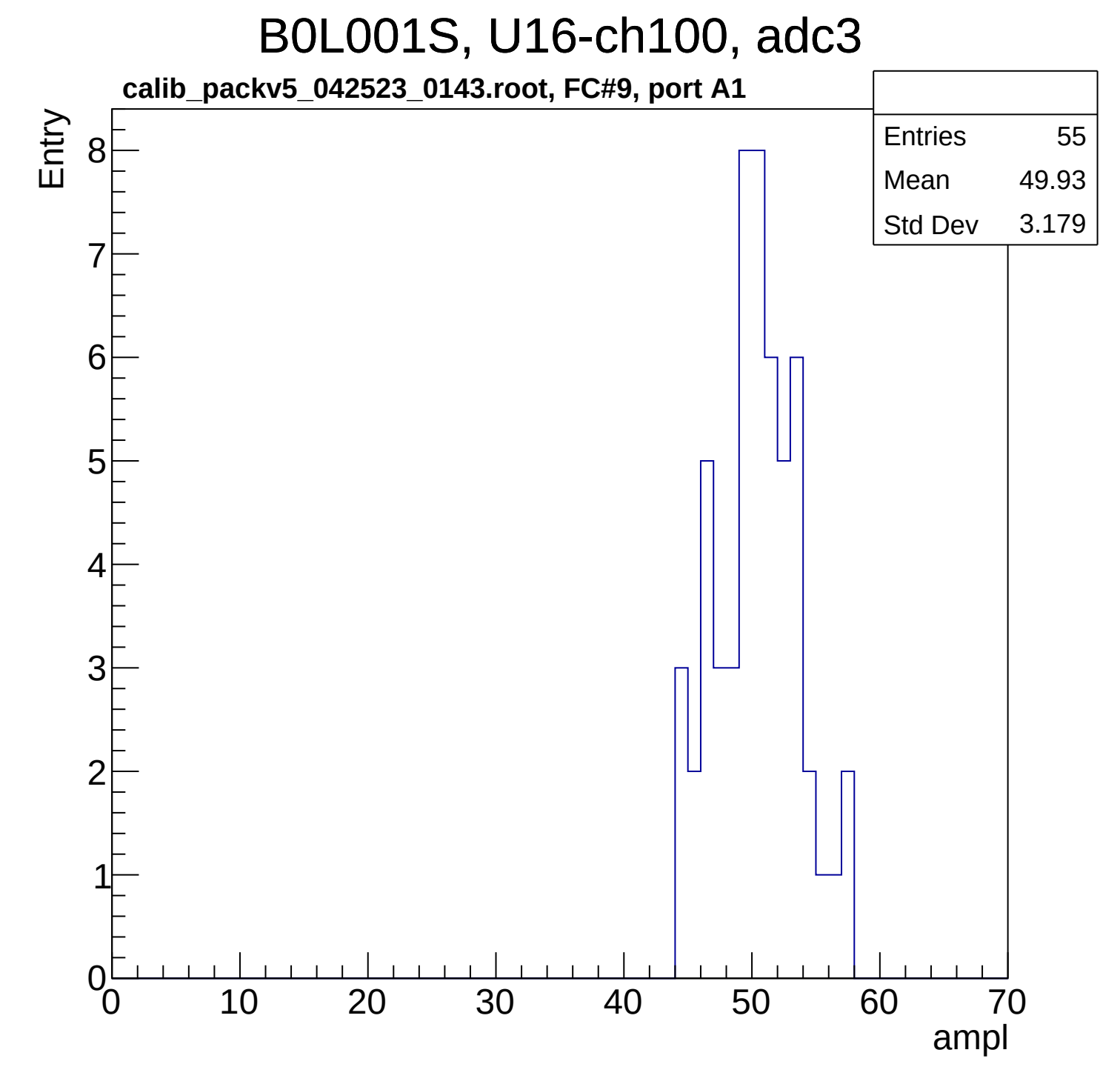
B0L001S, U16-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.93
Std Dev 3.179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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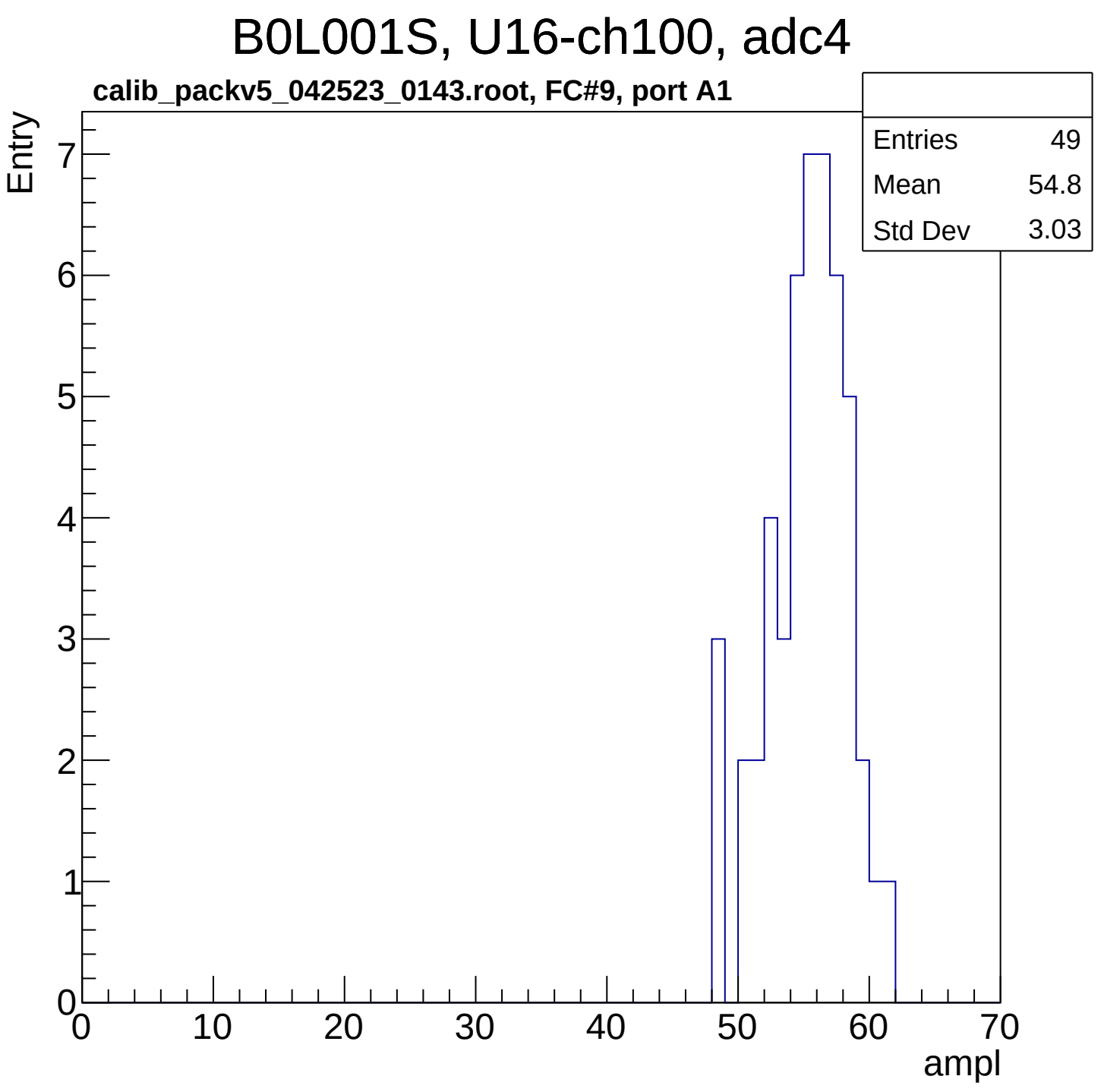
B0L001S, U16-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 54.8
Std Dev 3.03
Entry
ampl
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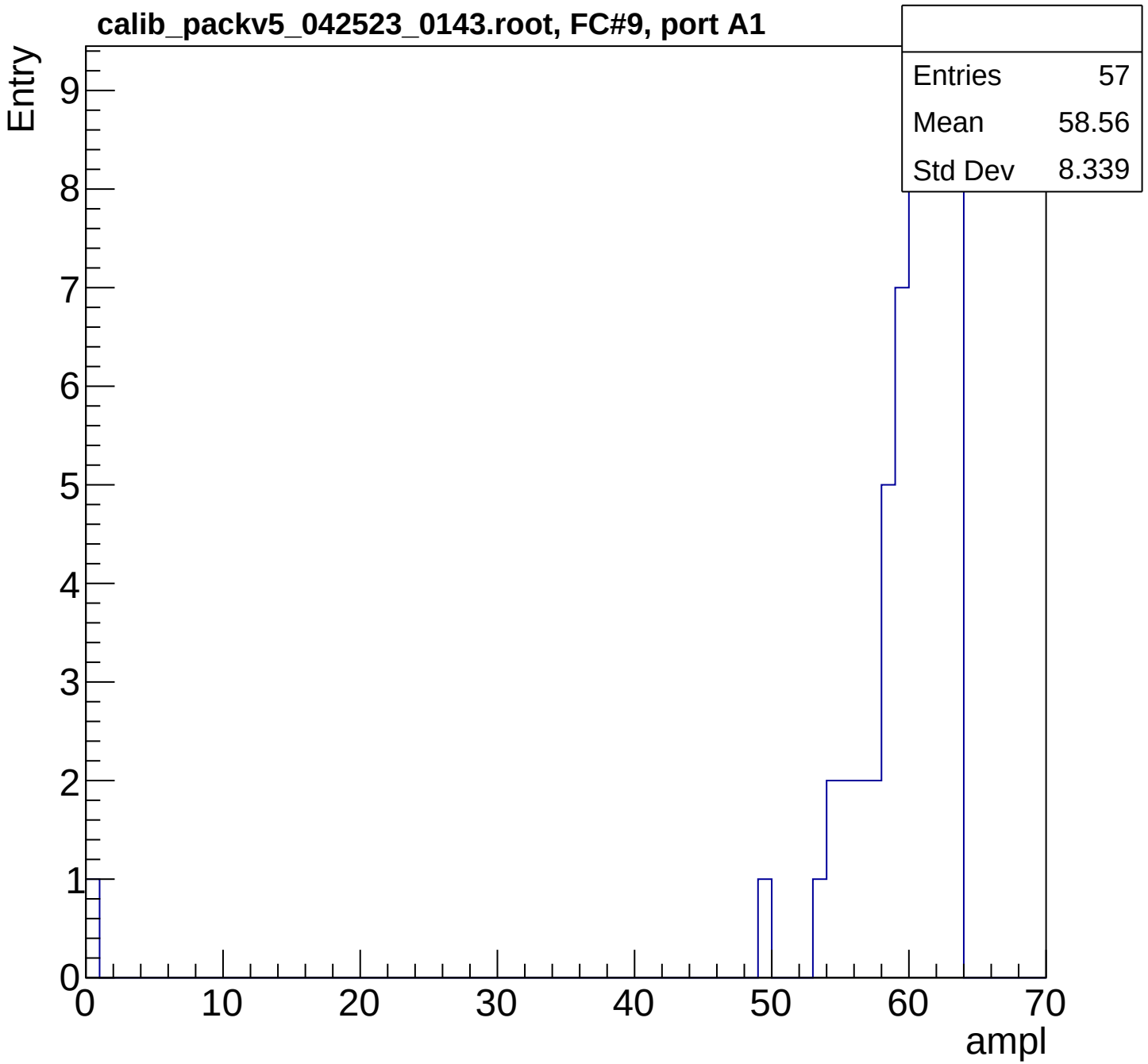
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.56
Std Dev 8.339
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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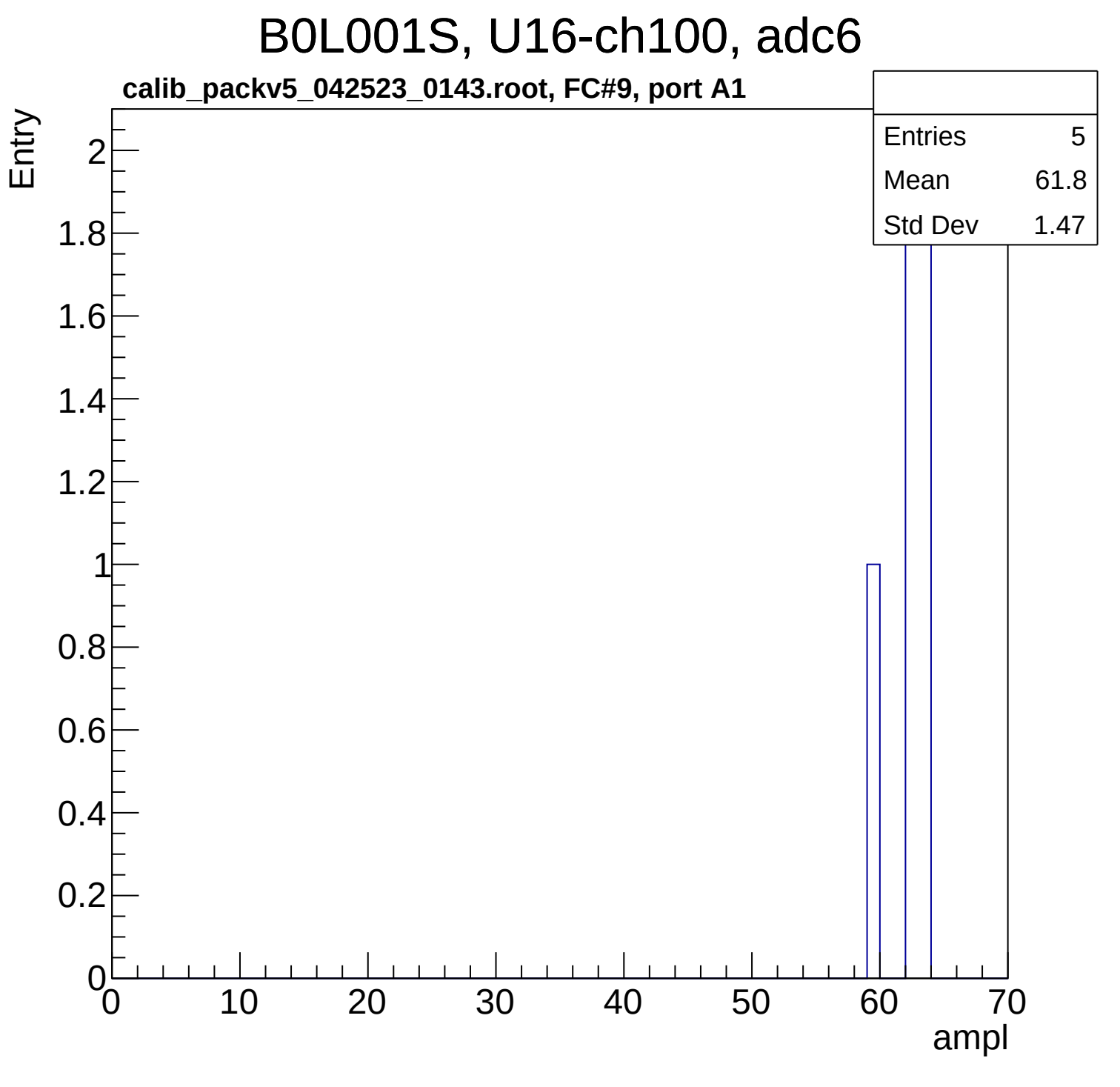
B0L001S, U16-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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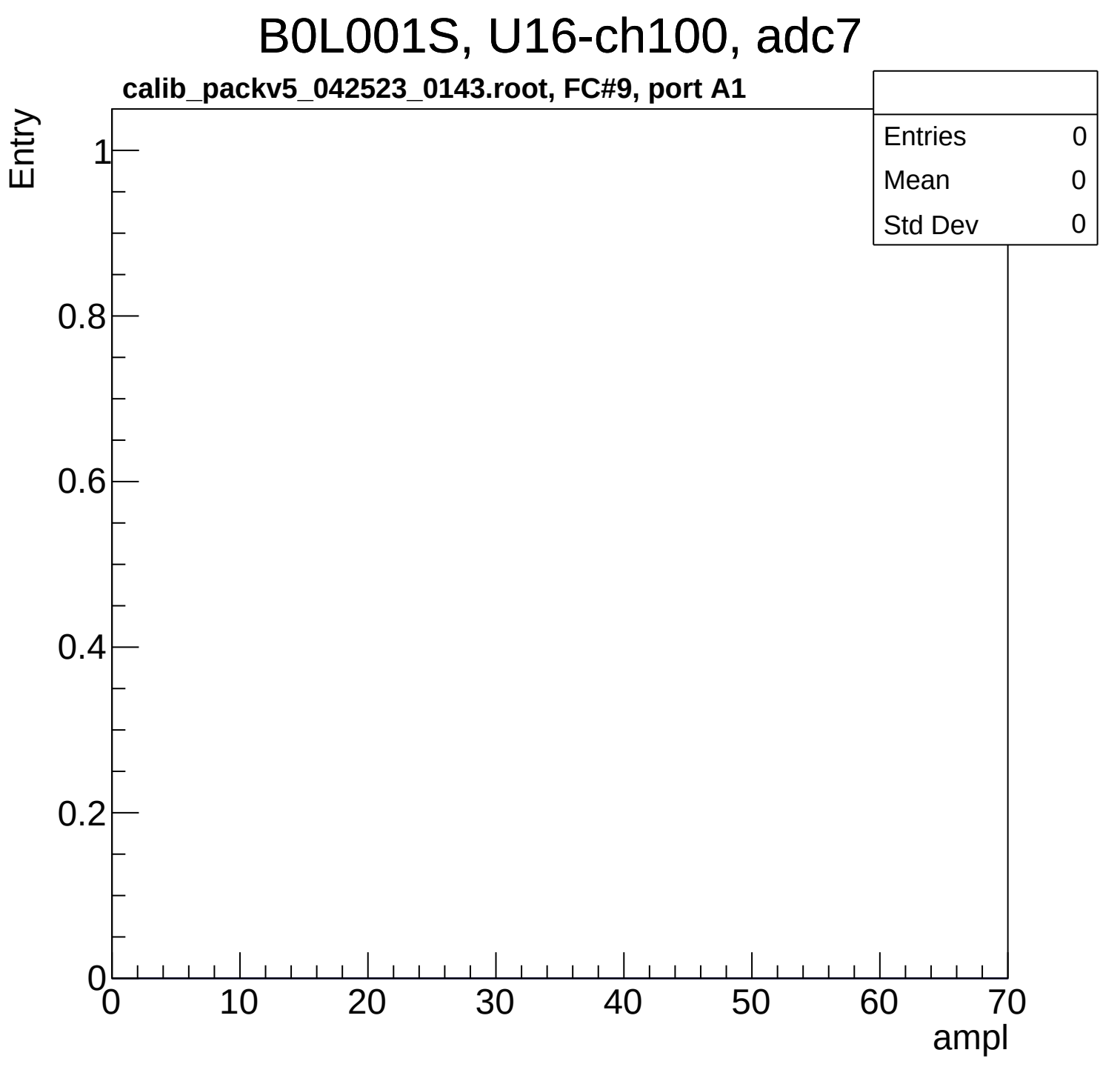
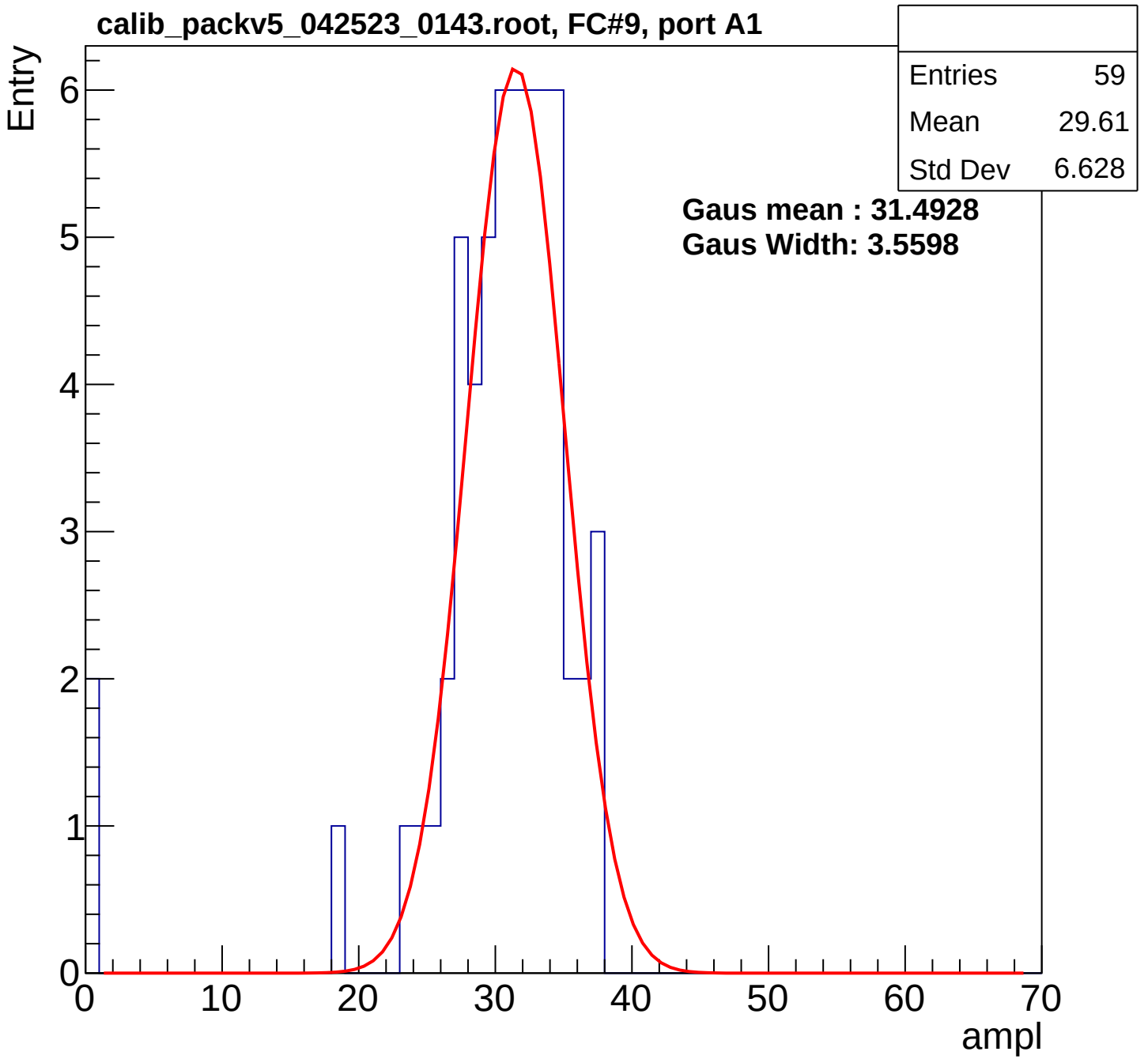
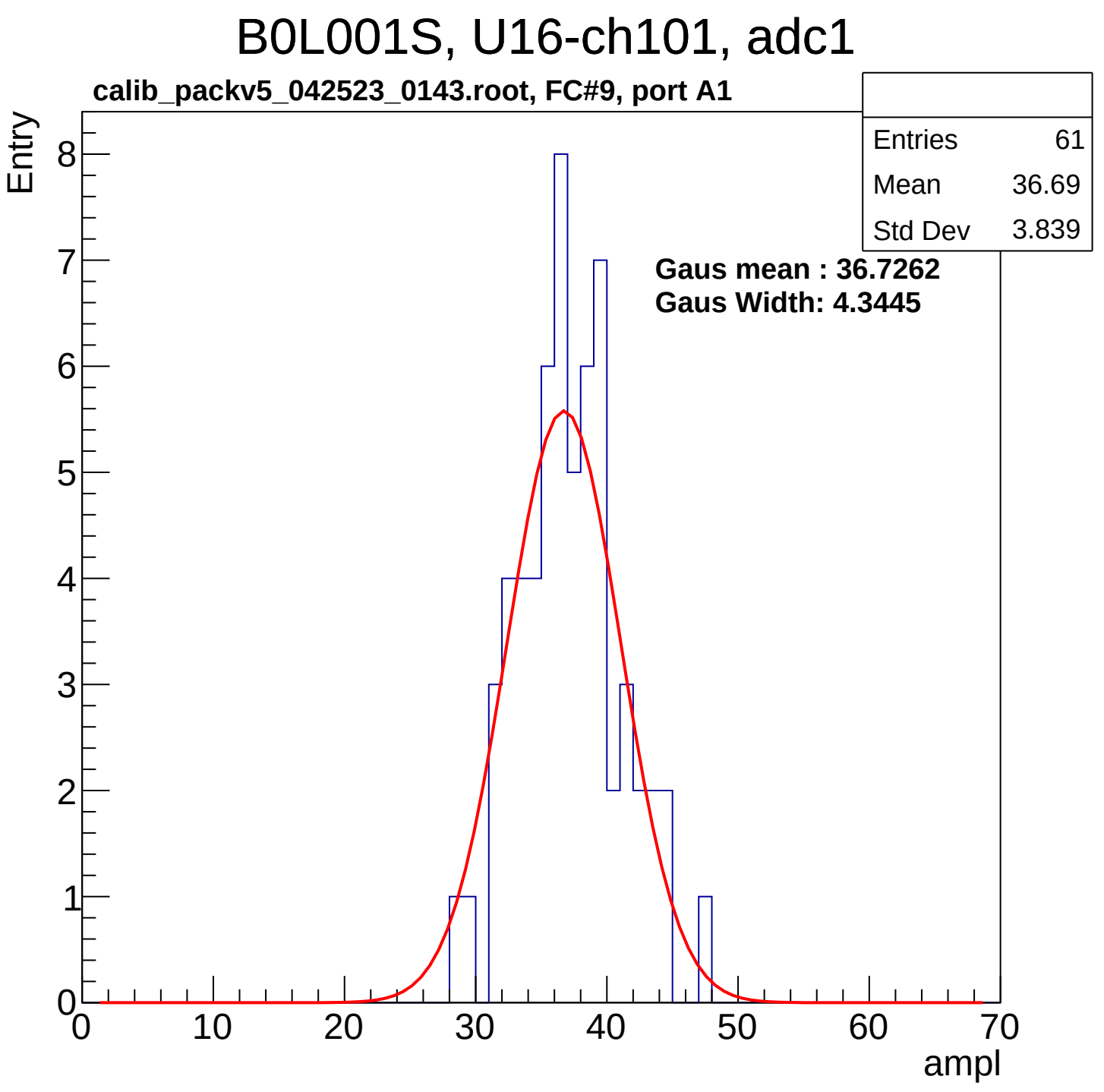
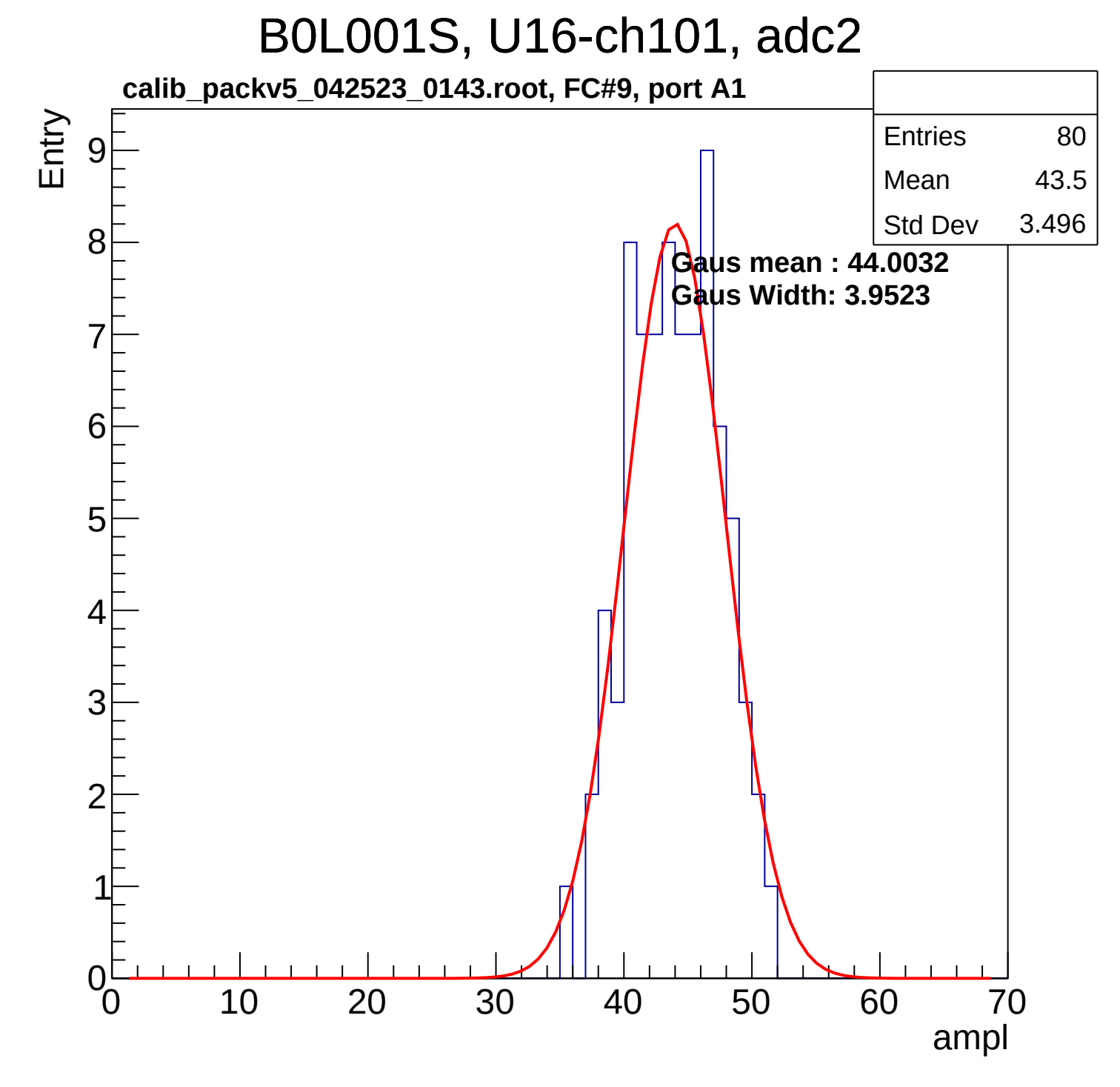

B0L001S, U16-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.61
Std Dev 6.628
Gaus mean : 31.4928
Gaus Width: 3.5598
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.69
Std Dev 3.839
Gaus mean : 36.7262
Gaus Width: 4.3445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.5
Std Dev 3.496
Gaus mean : 44.0032
Gaus Width: 3.9523
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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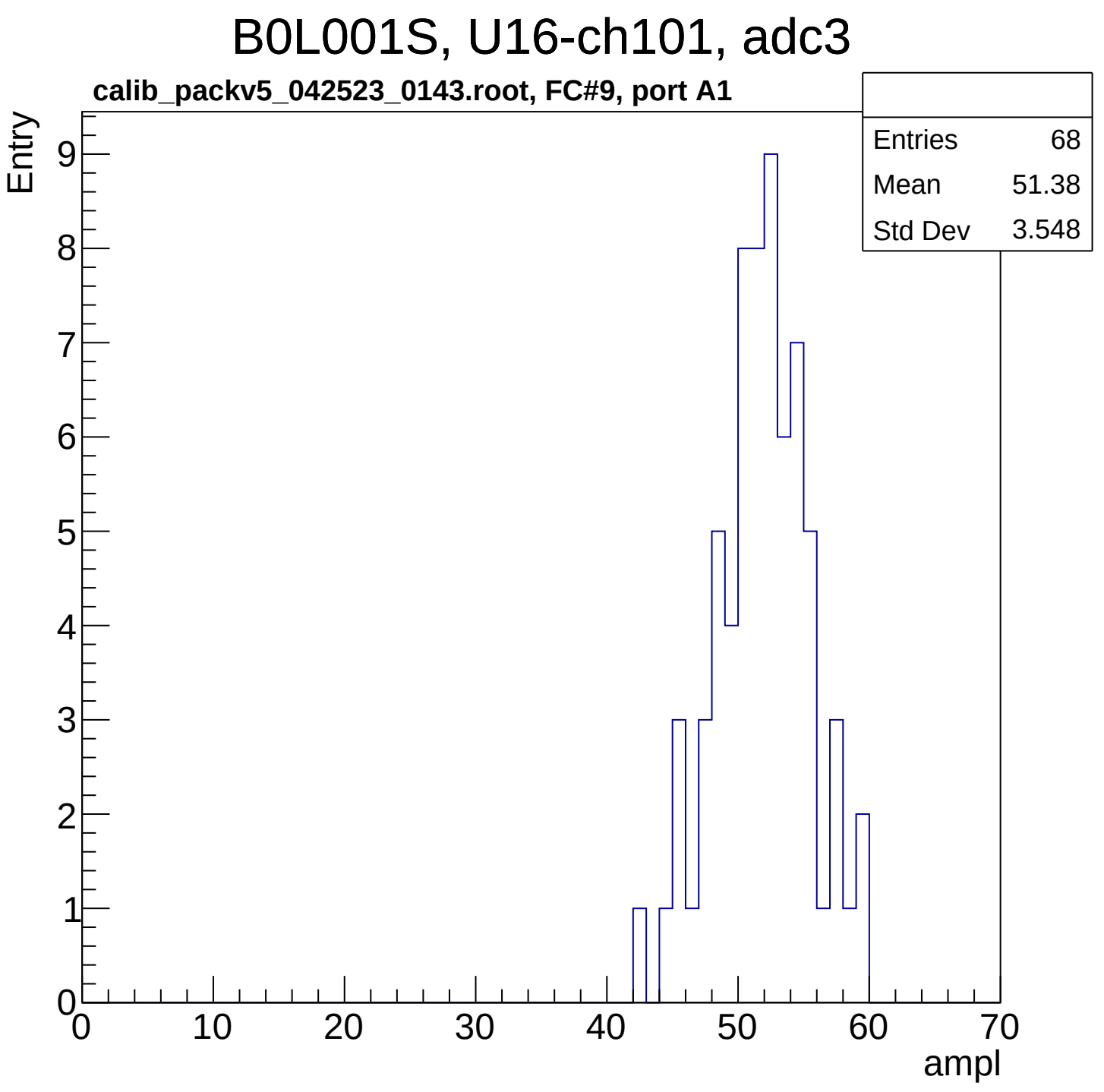
B0L001S, U16-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 51.38
Std Dev 3.548
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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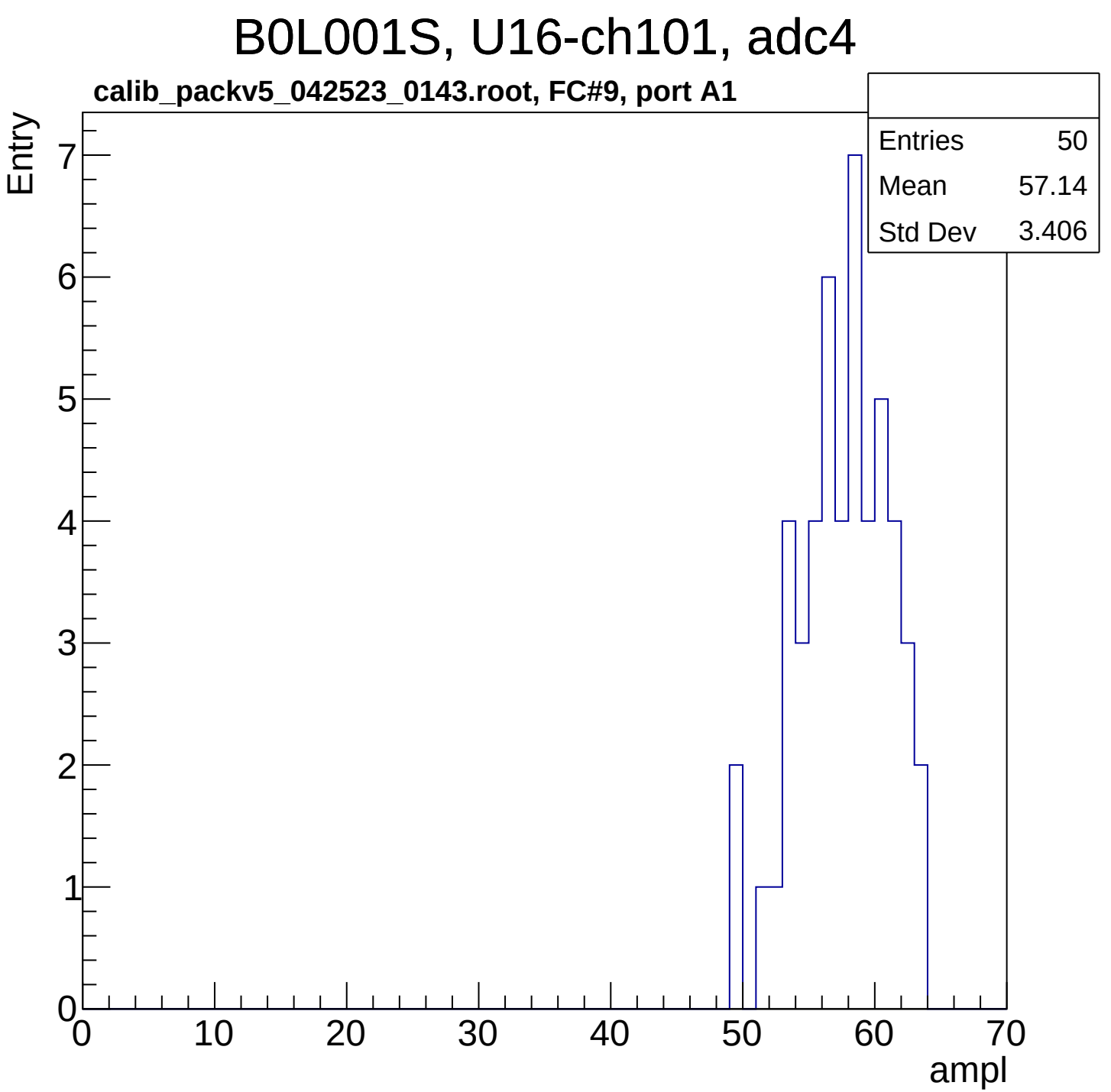
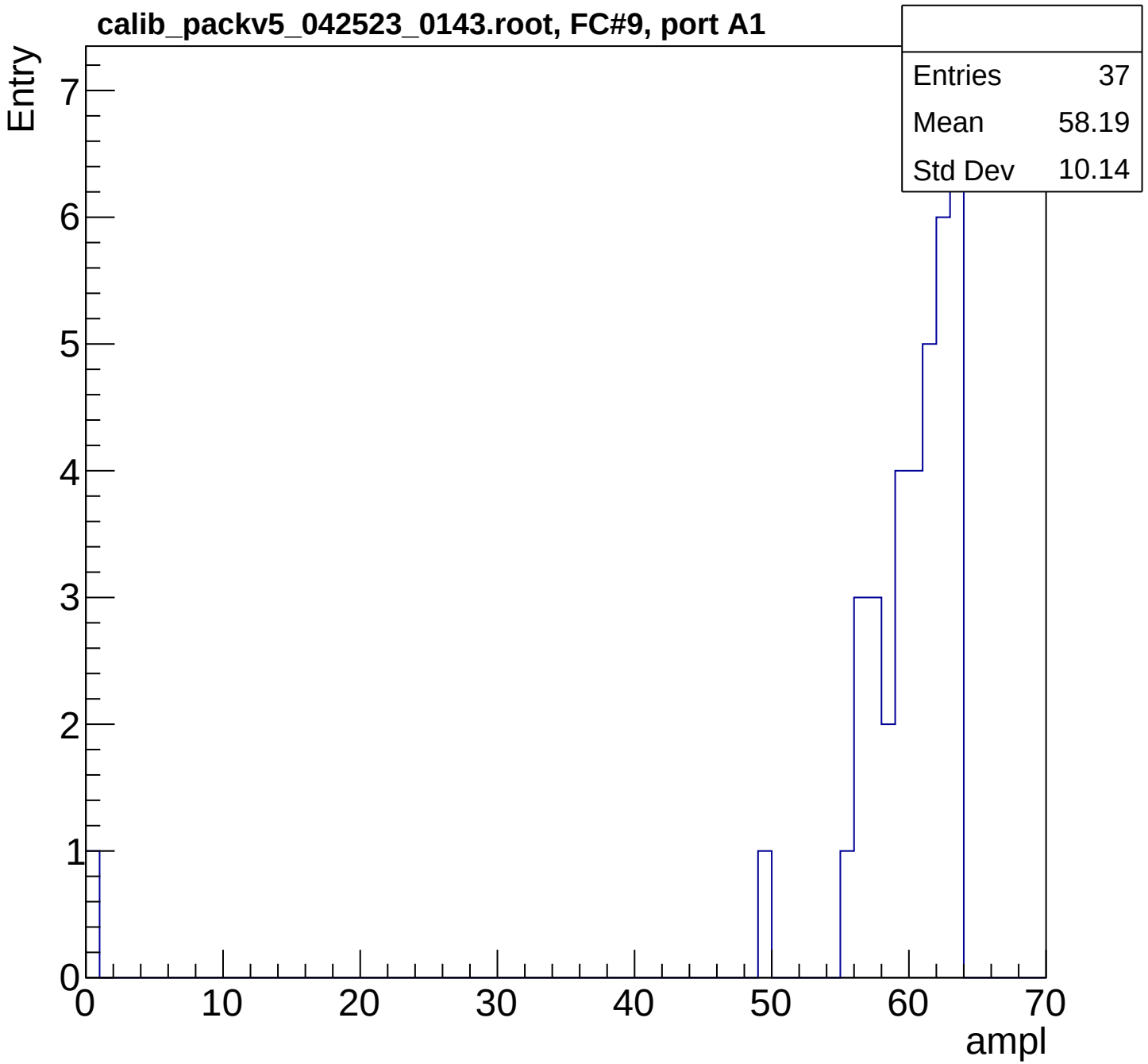
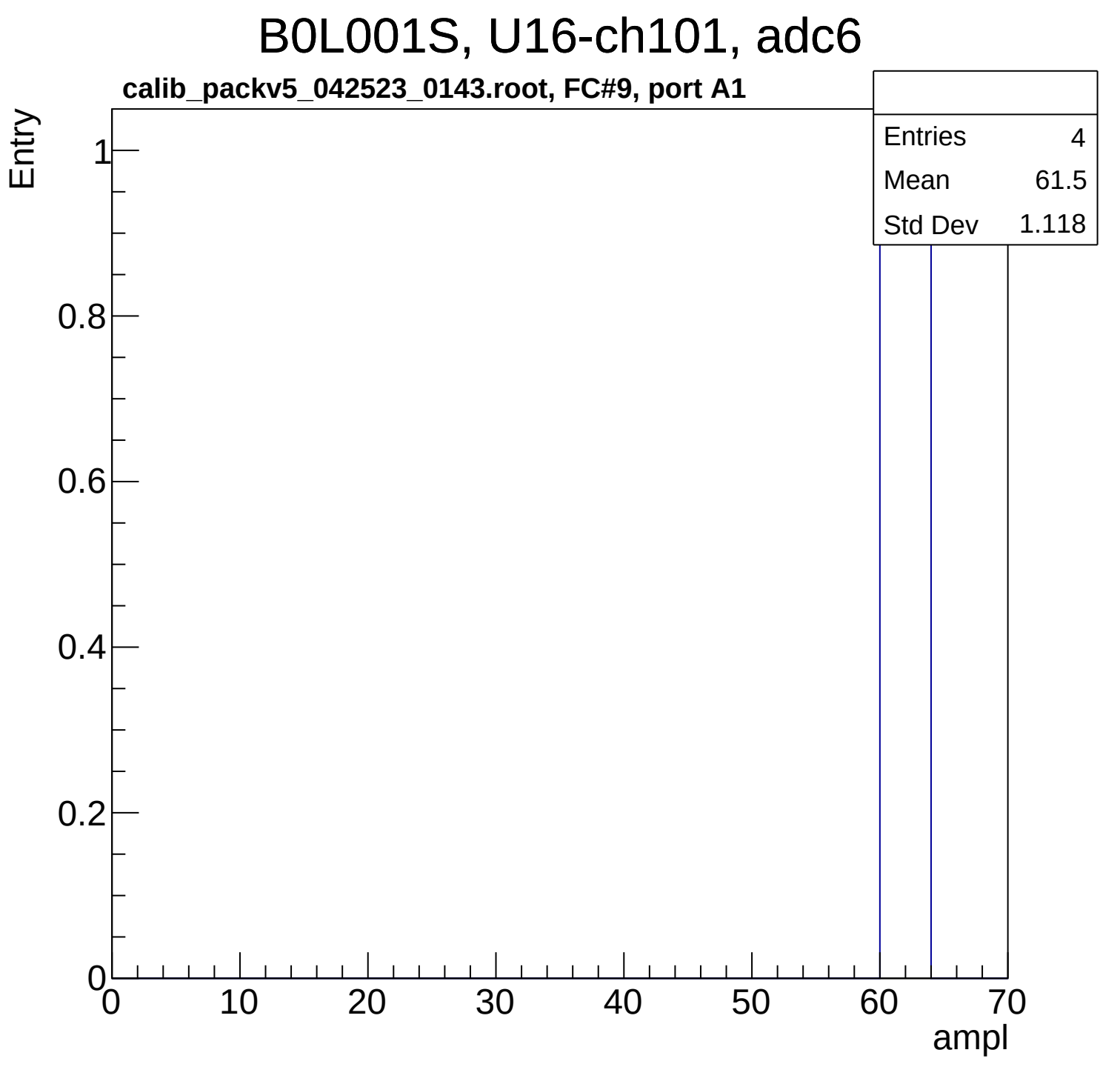
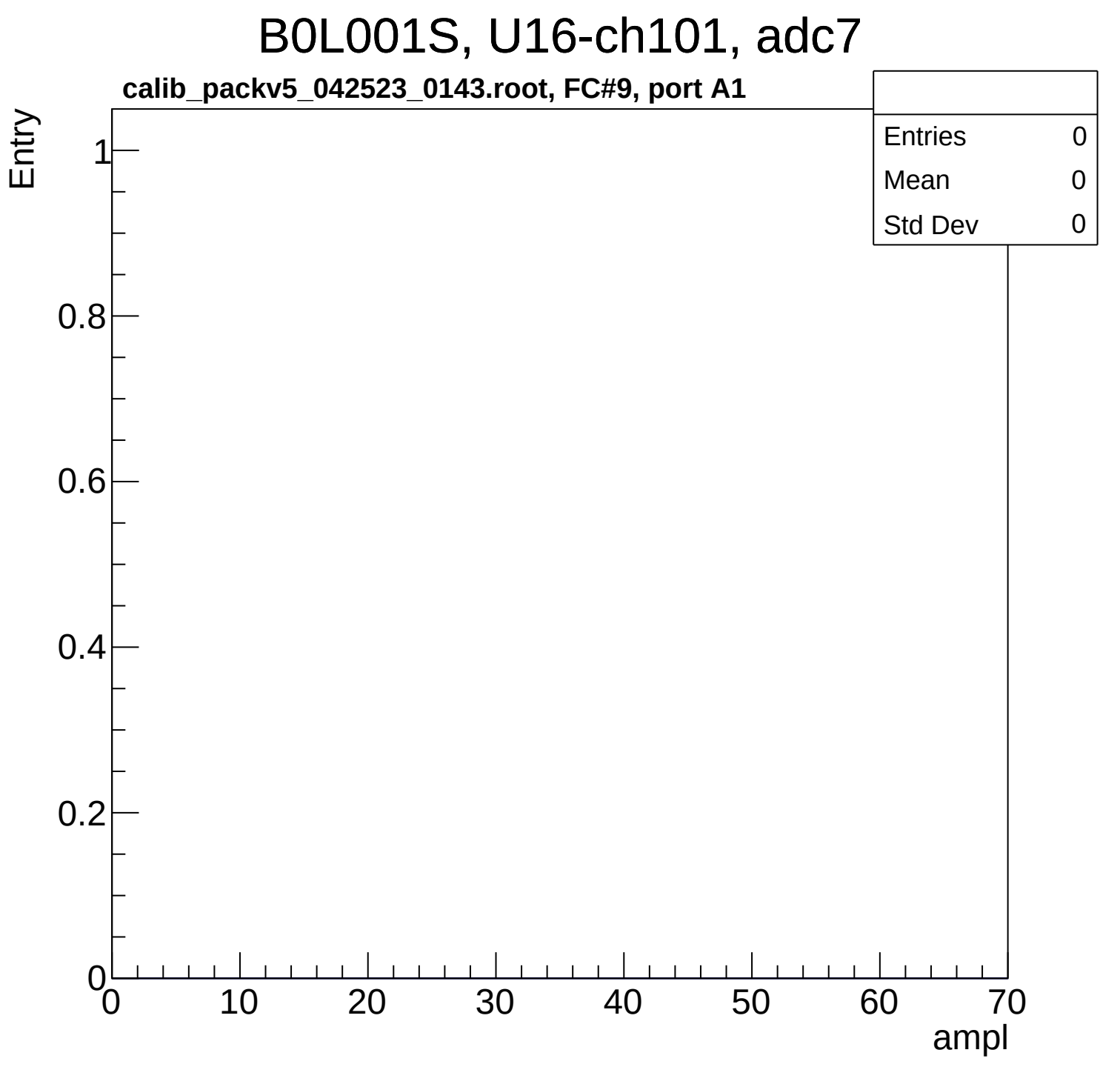
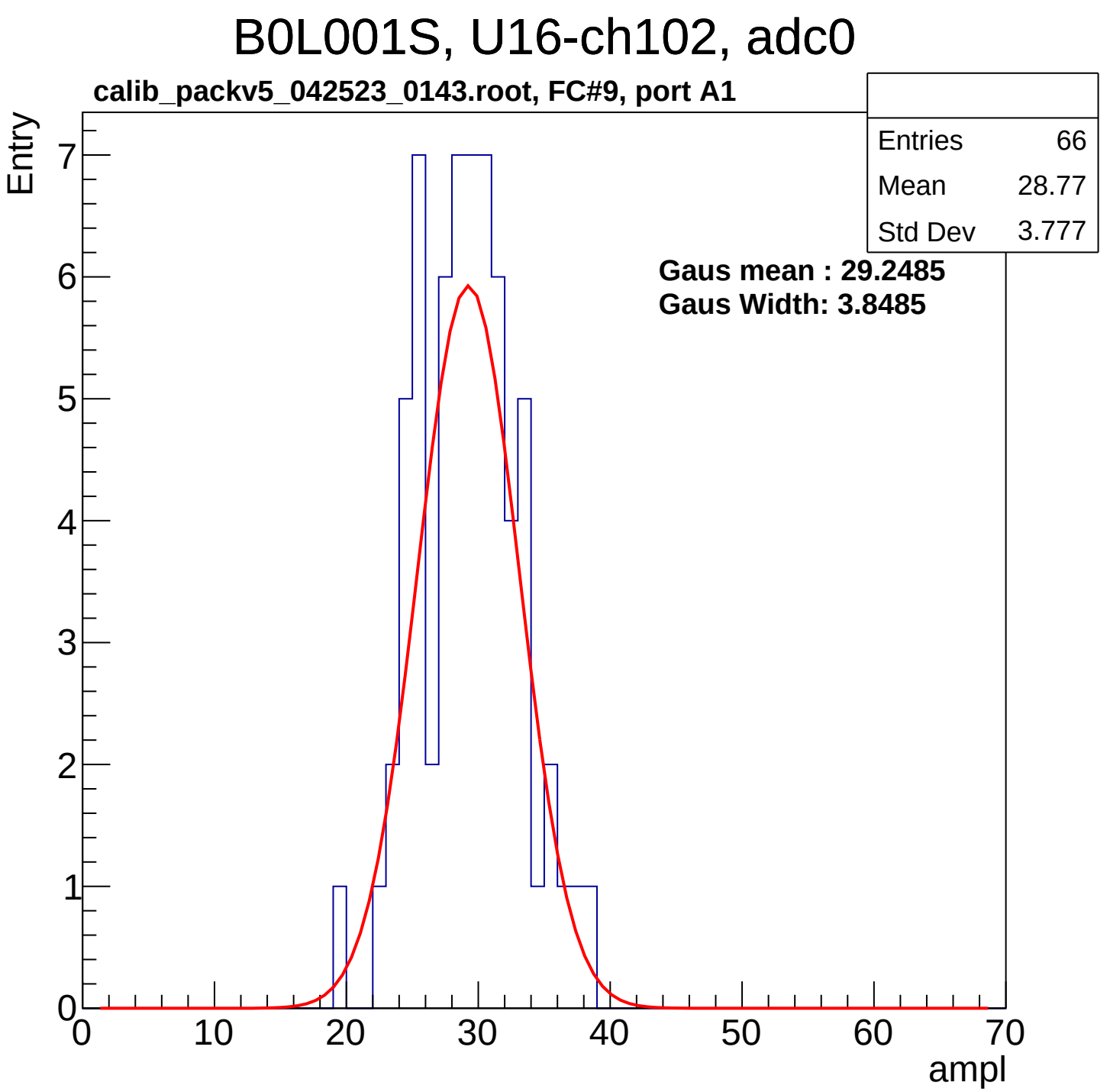
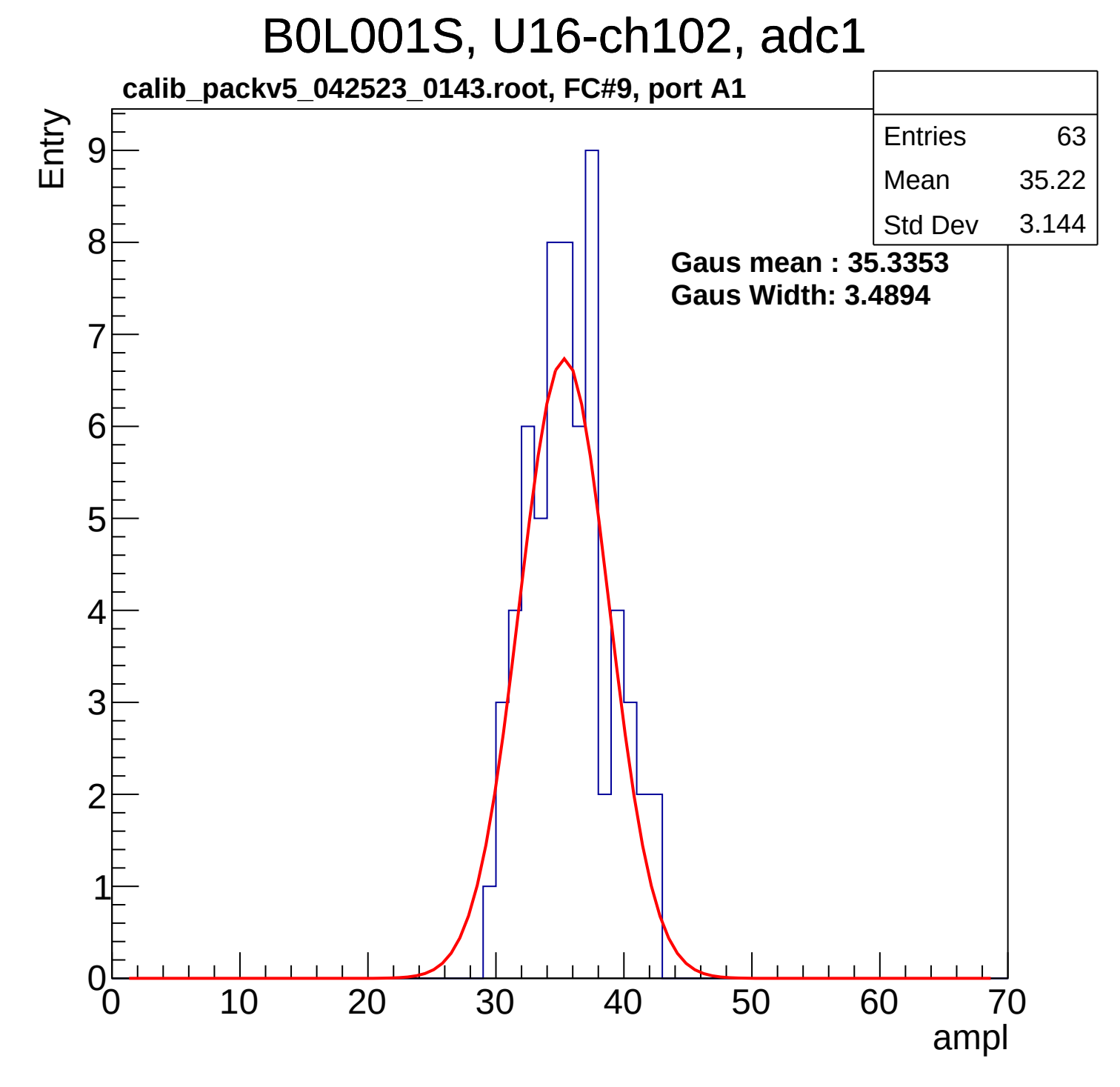

B0L001S, U16-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.14
Std Dev 3.406
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

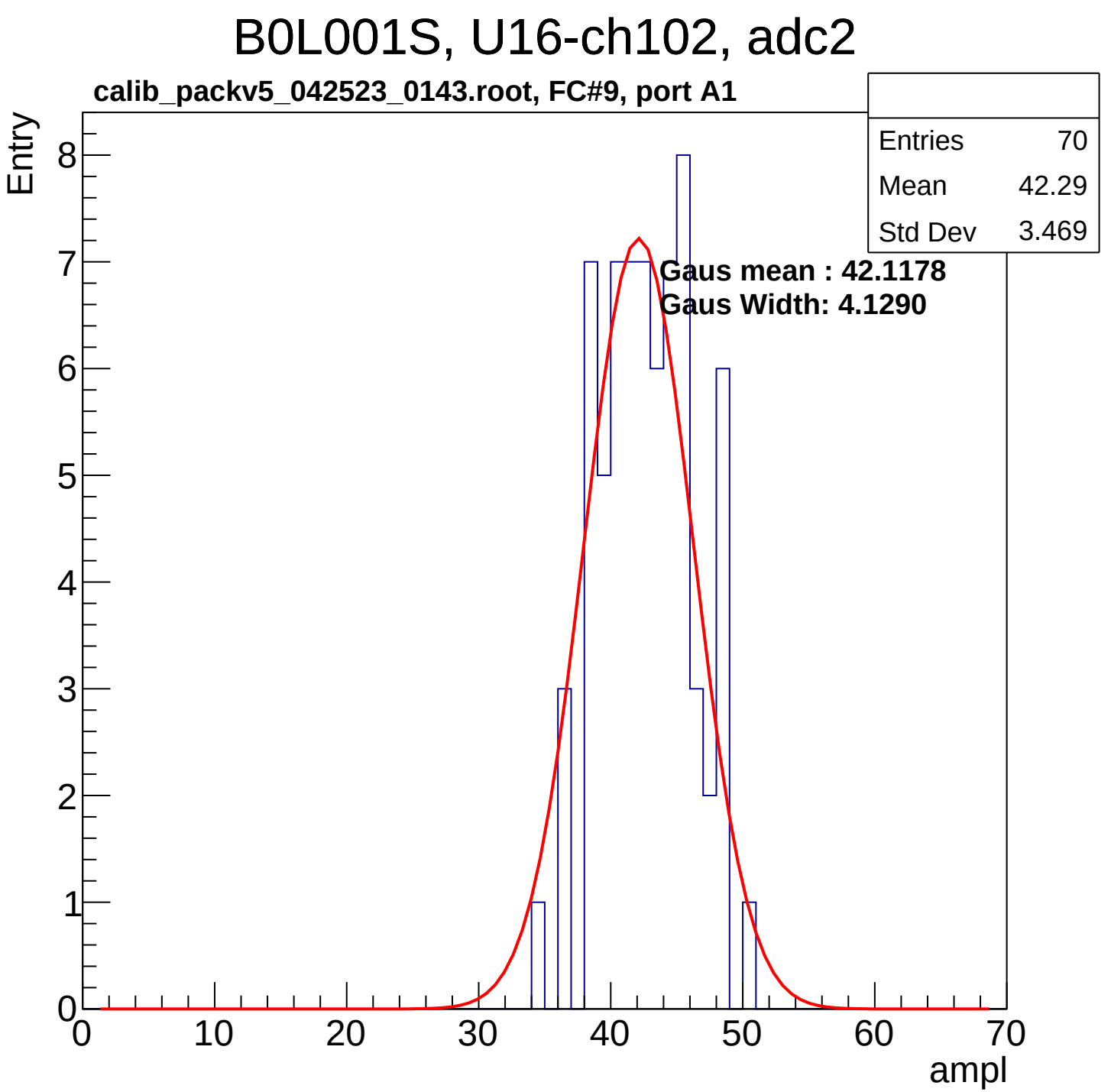

B0L001S, U16-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.19
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.77
Std Dev 3.777
Gaus mean : 29.2485
Gaus Width: 3.8485
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 35.22
Std Dev 3.144
Gaus mean : 35.3353
Gaus Width: 3.4894
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U16-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 42.29
Std Dev 3.469
Gaus mean : 42.1178
Gaus Width: 4.1290
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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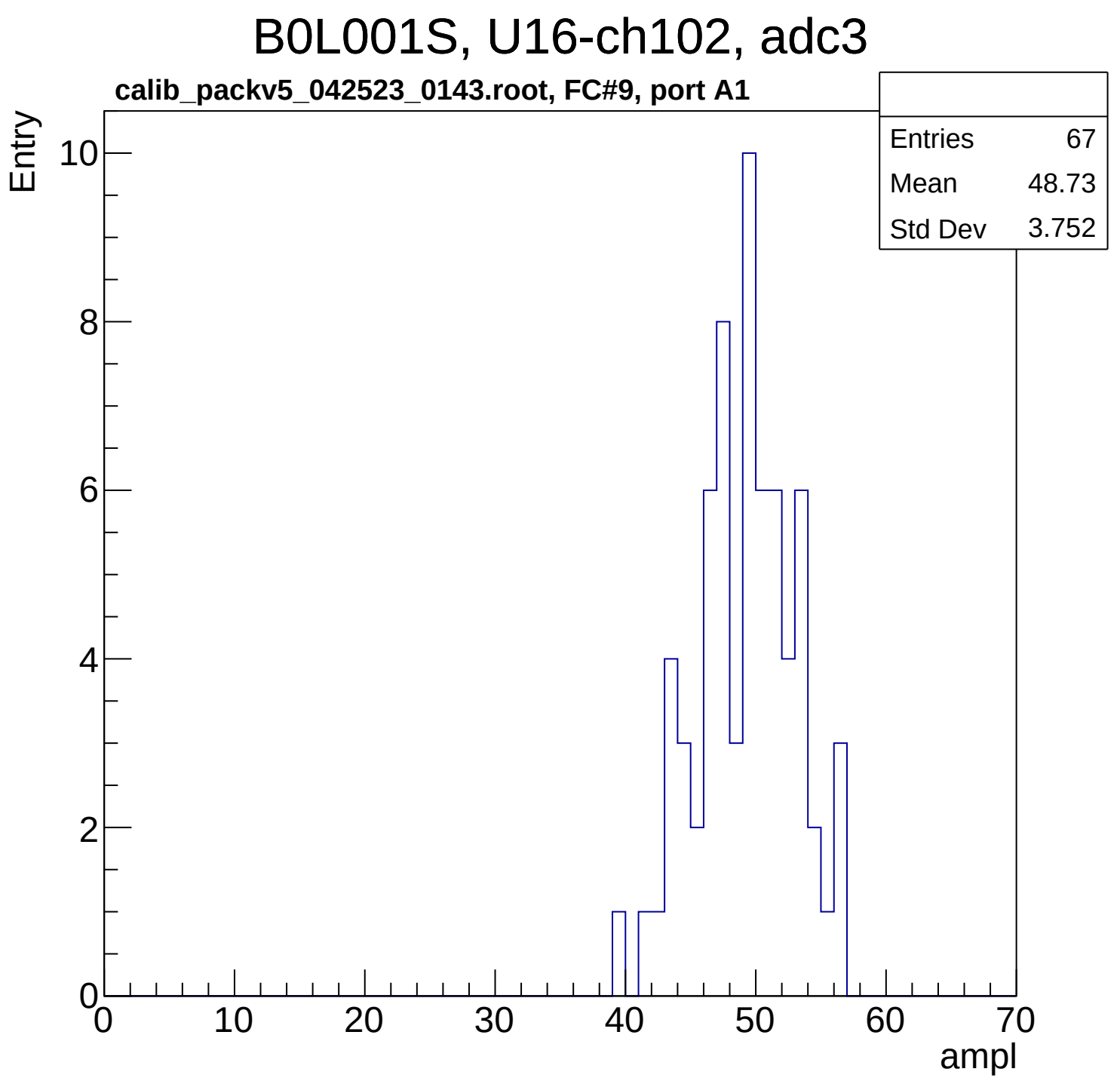
B0L001S, U16-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 48.73
Std Dev 3.752
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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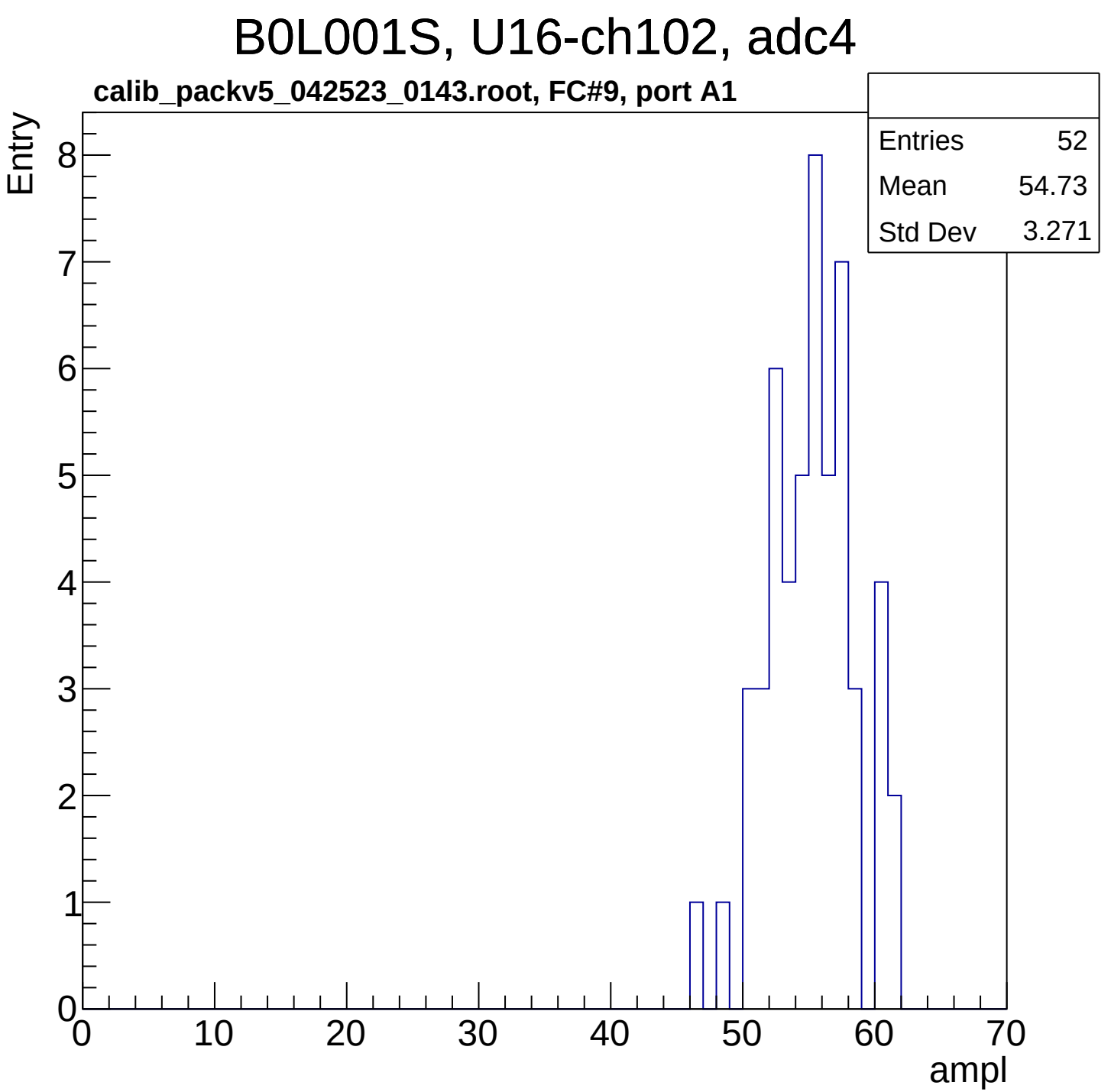
B0L001S, U16-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 54.73
Std Dev 3.271
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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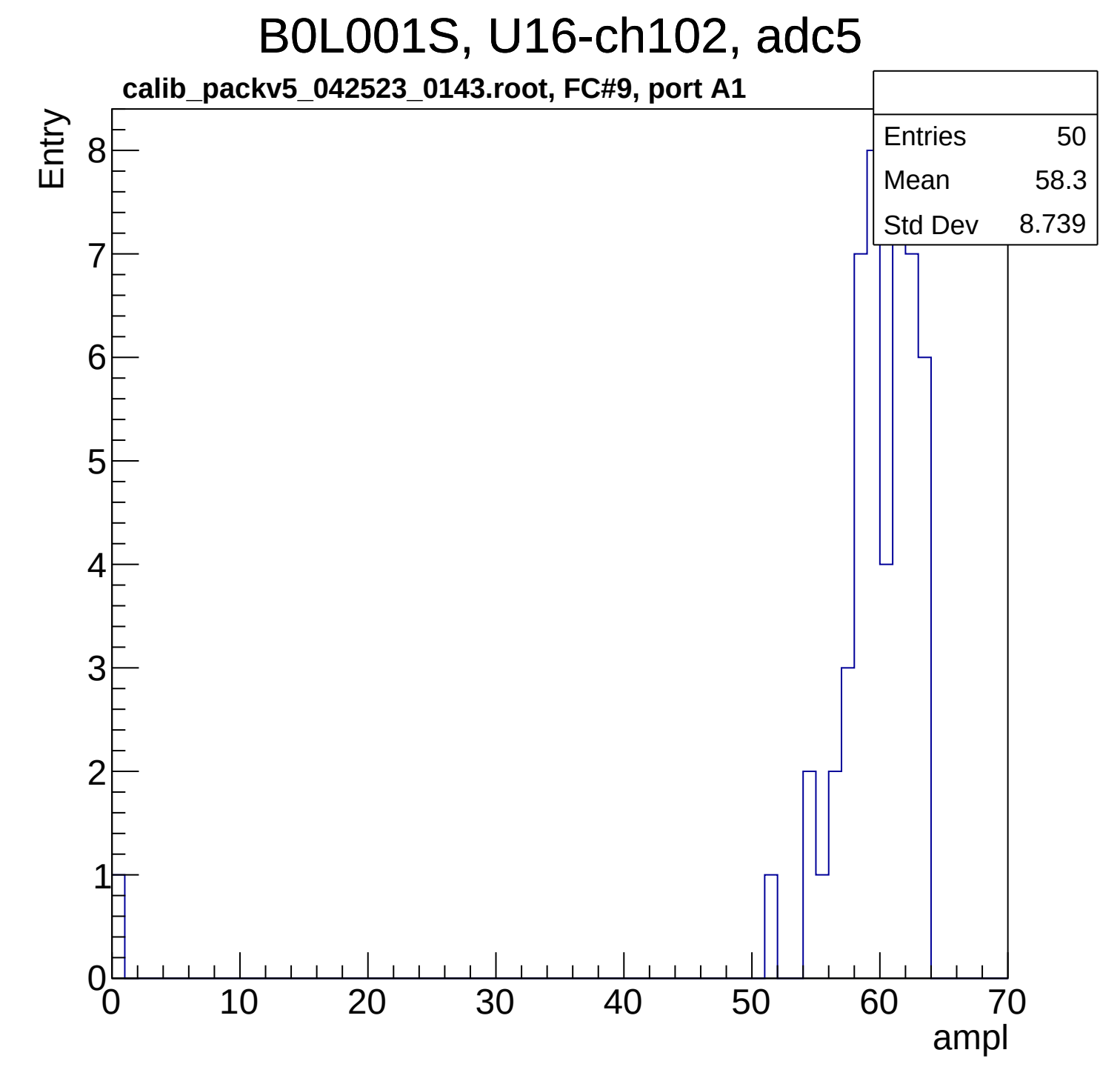
B0L001S, U16-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 58.3
Std Dev 8.739
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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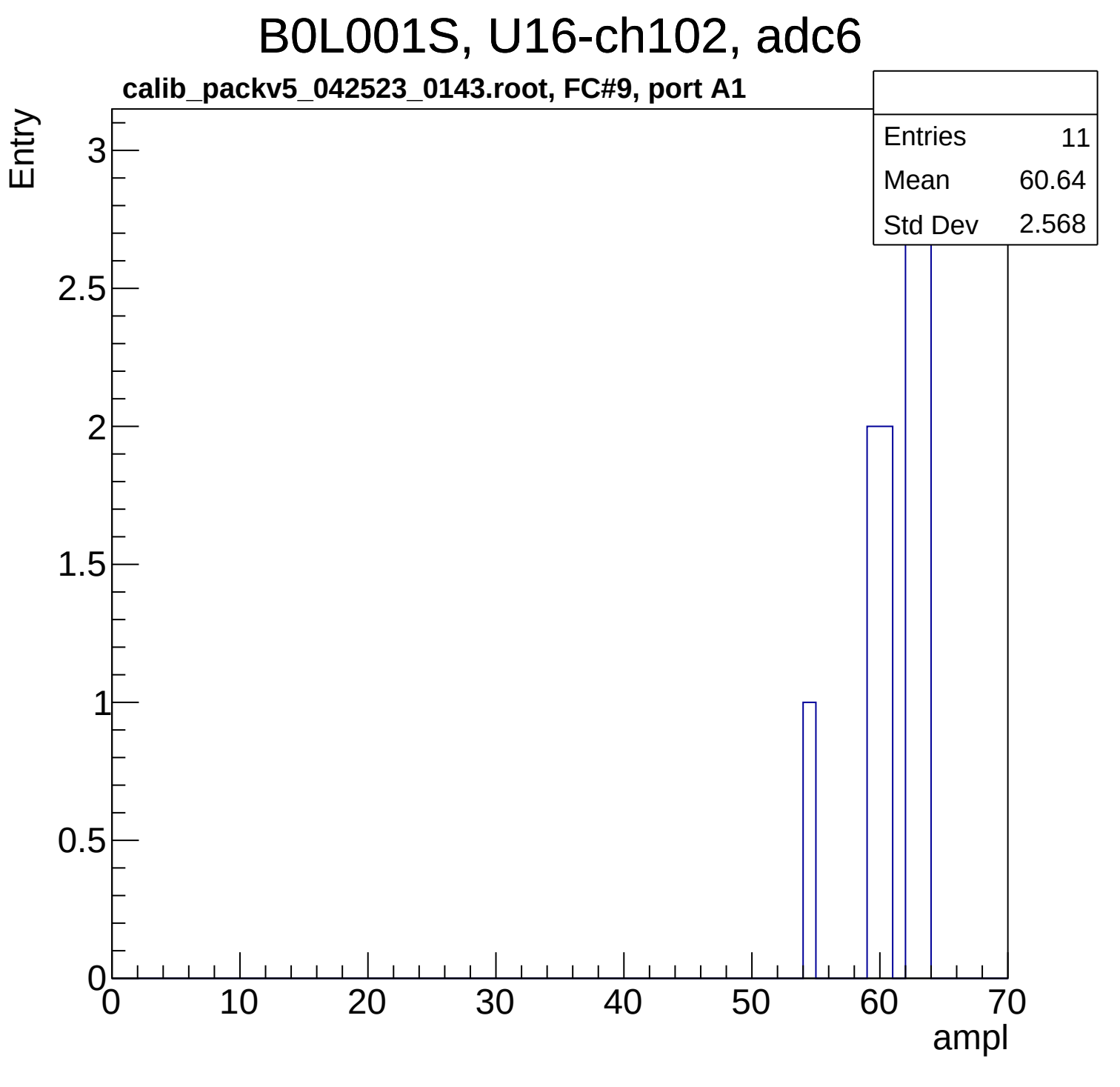
B0L001S, U16-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 60.64
Std Dev 2.568
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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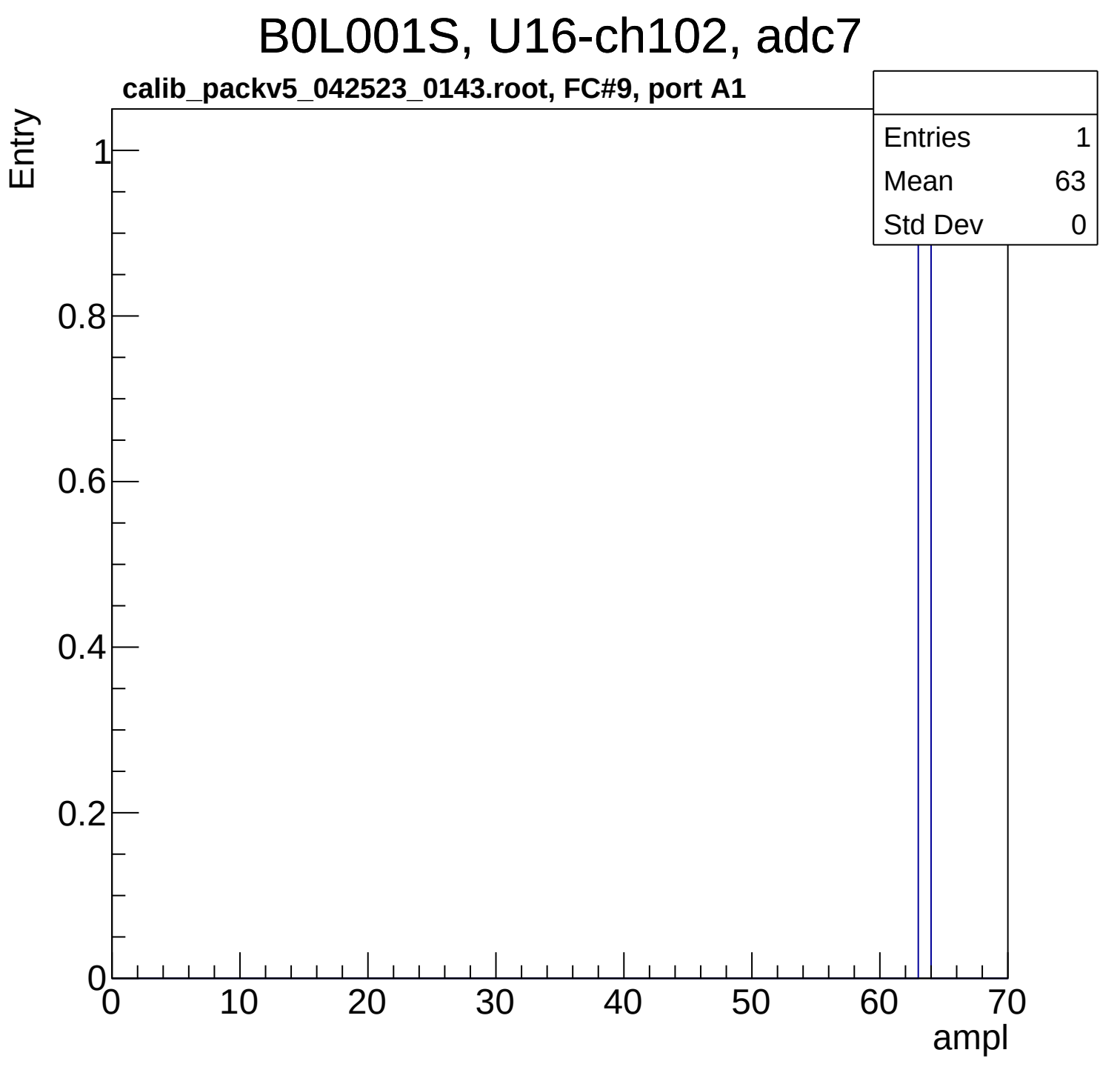
B0L001S, U16-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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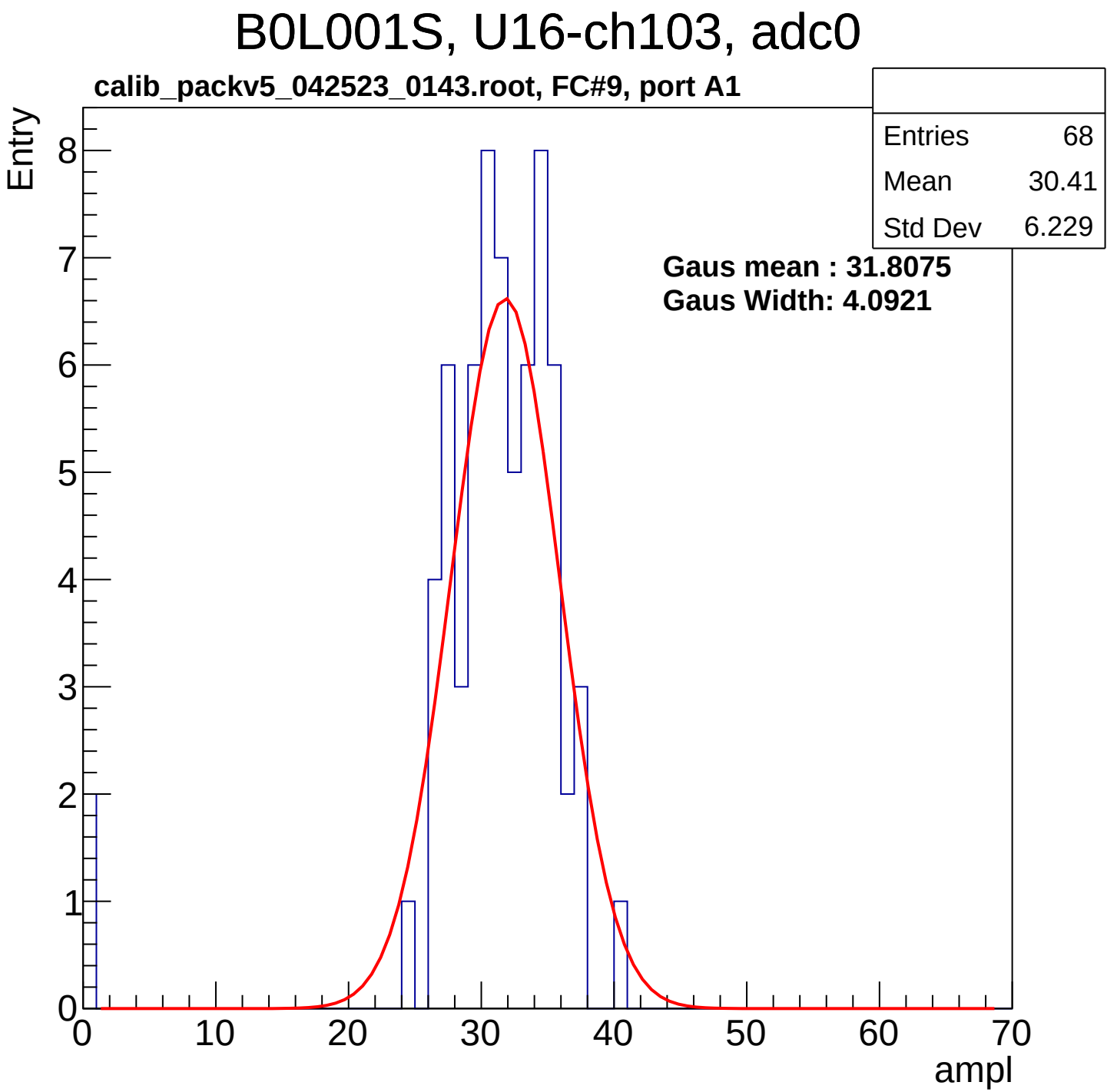
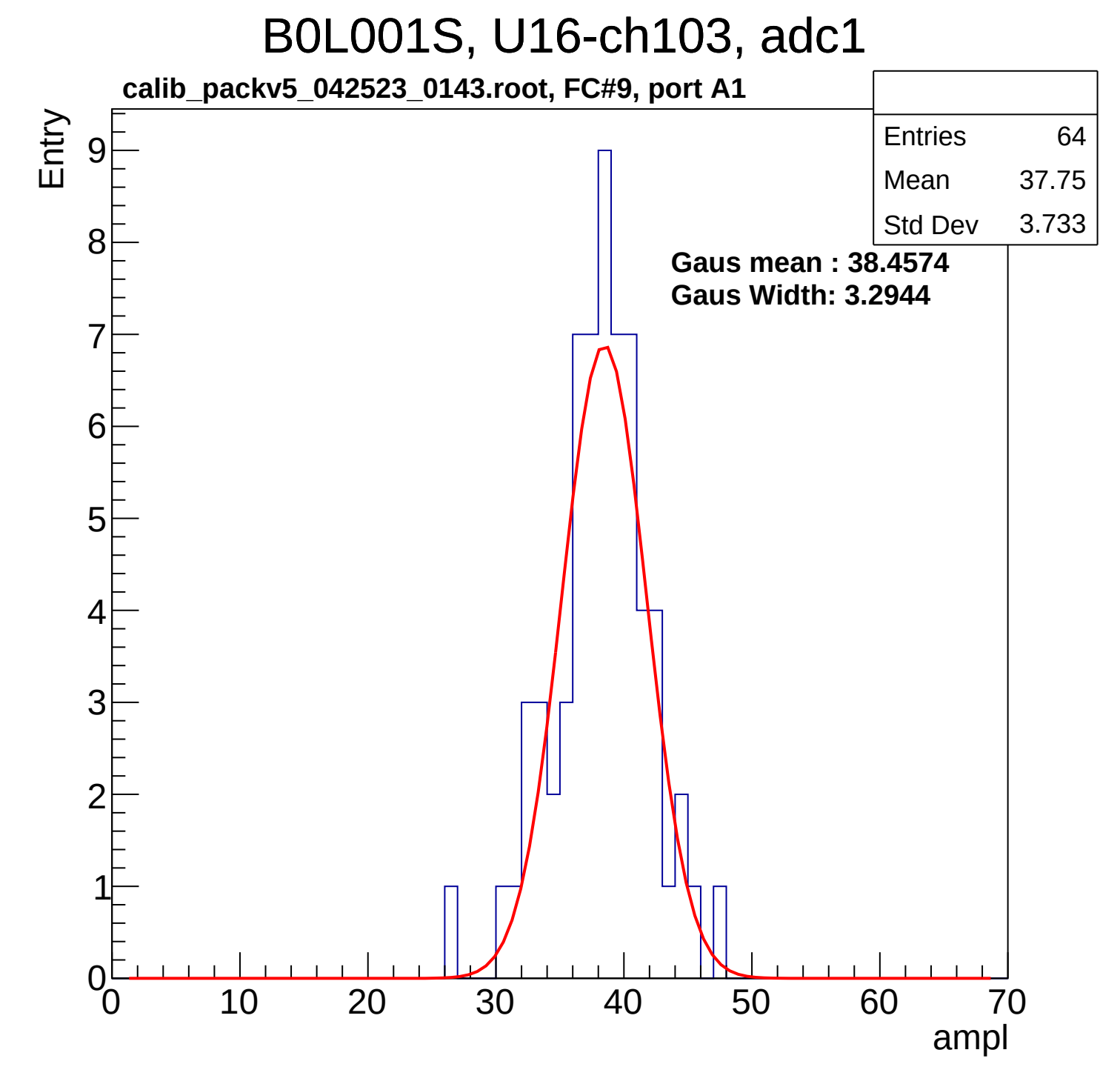
50
60
70
ampl


B0L001S, U16-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.41
Std Dev 6.229
Gaus mean : 31.8075
Gaus Width: 4.0921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.75
Std Dev 3.733
Gaus mean : 38.4574
Gaus Width: 3.2944
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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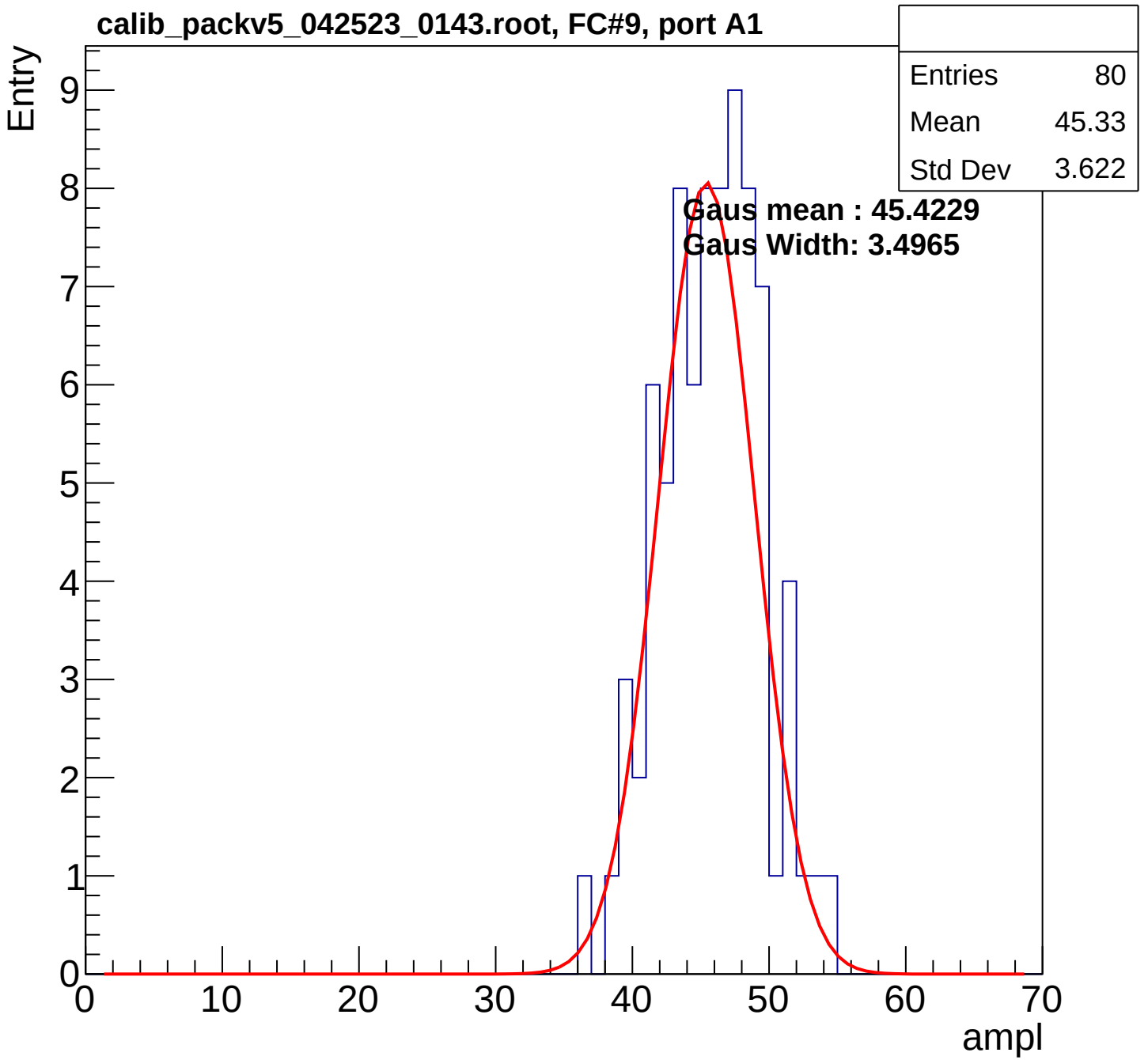
B0L001S, U16-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 45.33
Std Dev 3.622
Gaus mean : 45.4229
Gaus Width: 3.4965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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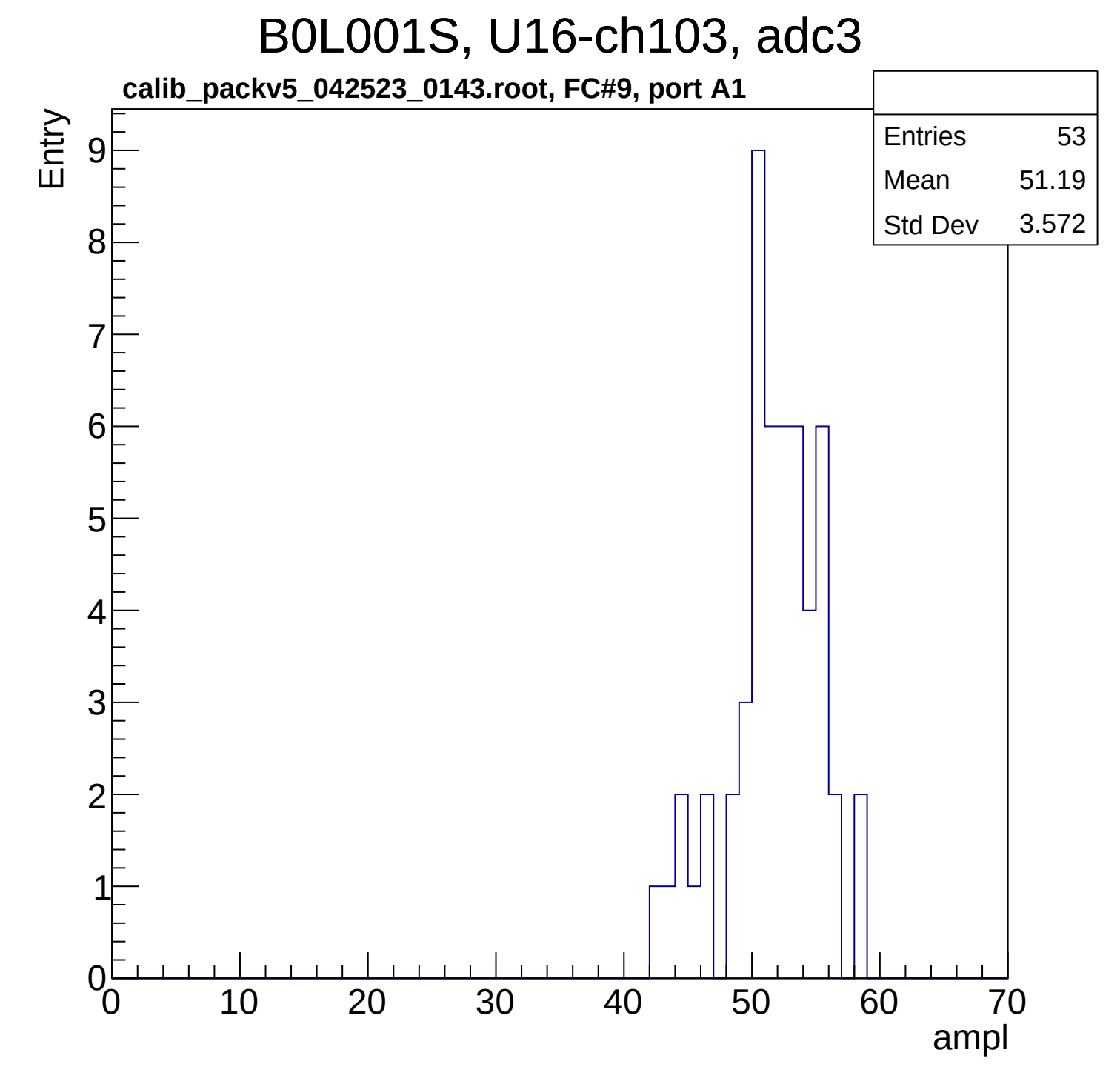
B0L001S, U16-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 51.19
Std Dev 3.572
Entry
ampl
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2
3
4
5
6
7
8
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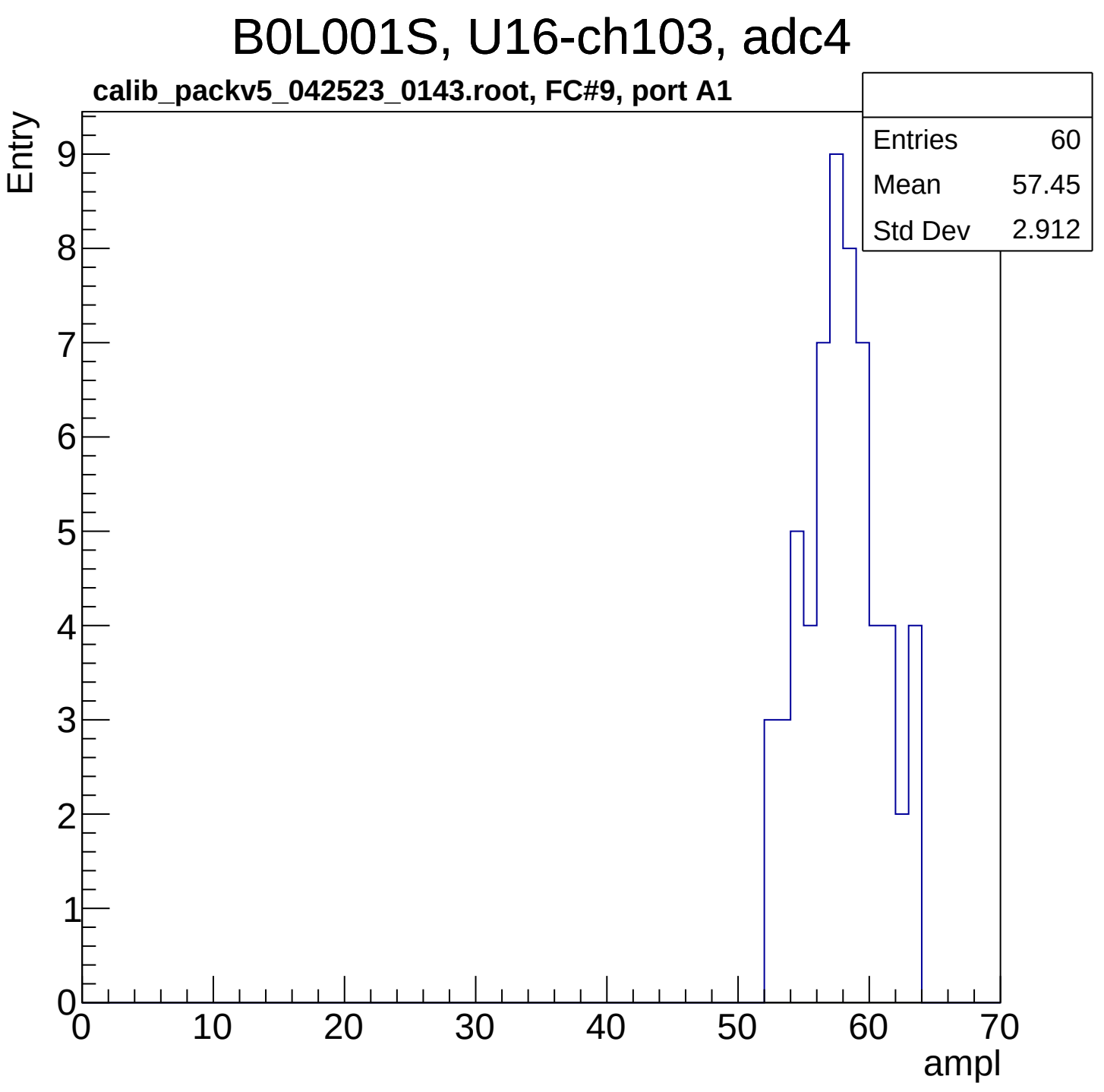
B0L001S, U16-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.45
Std Dev 2.912
Entry
ampl
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3
4
5
6
7
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9
0
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60
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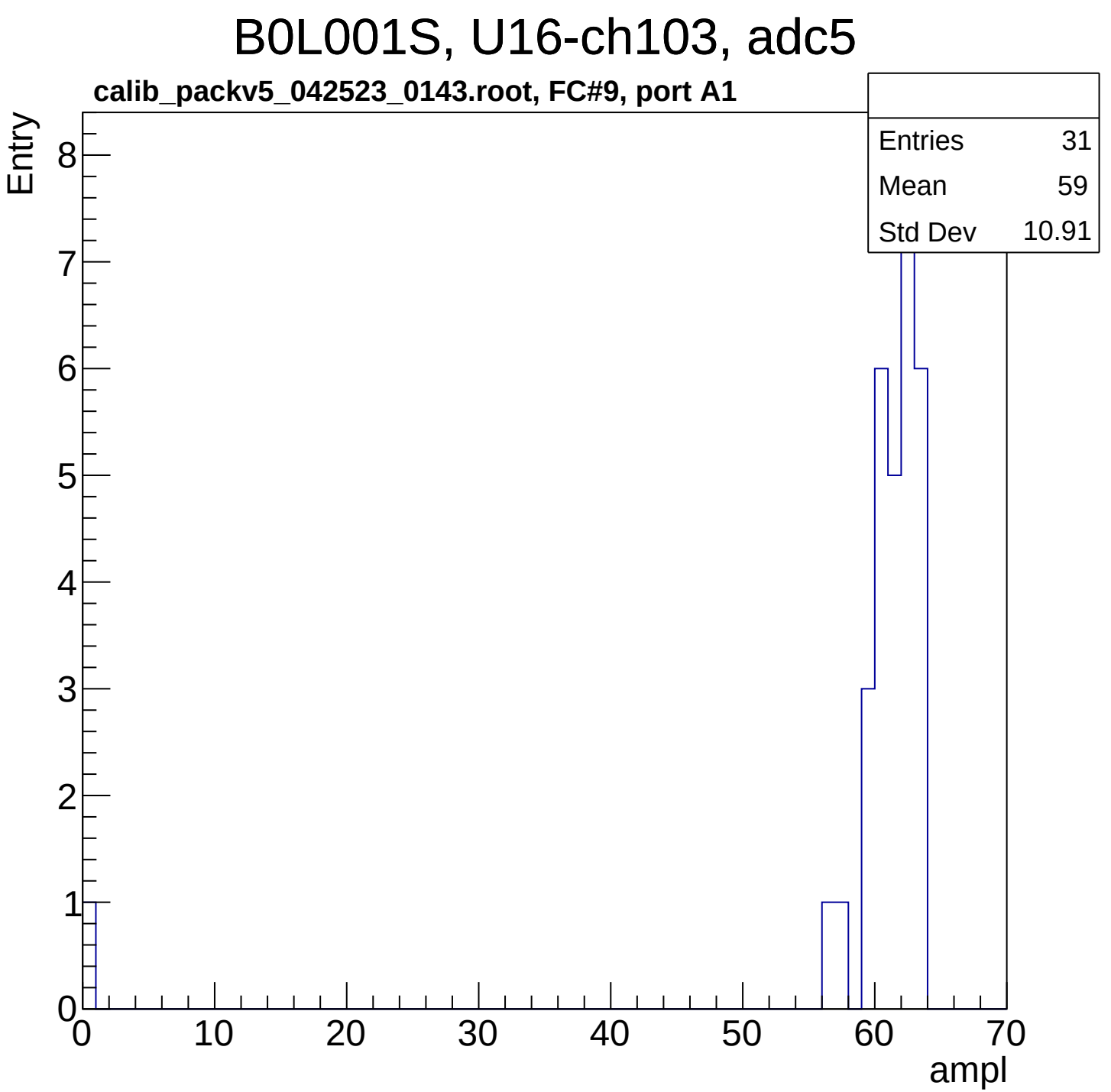
B0L001S, U16-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59
Std Dev 10.91
Entry
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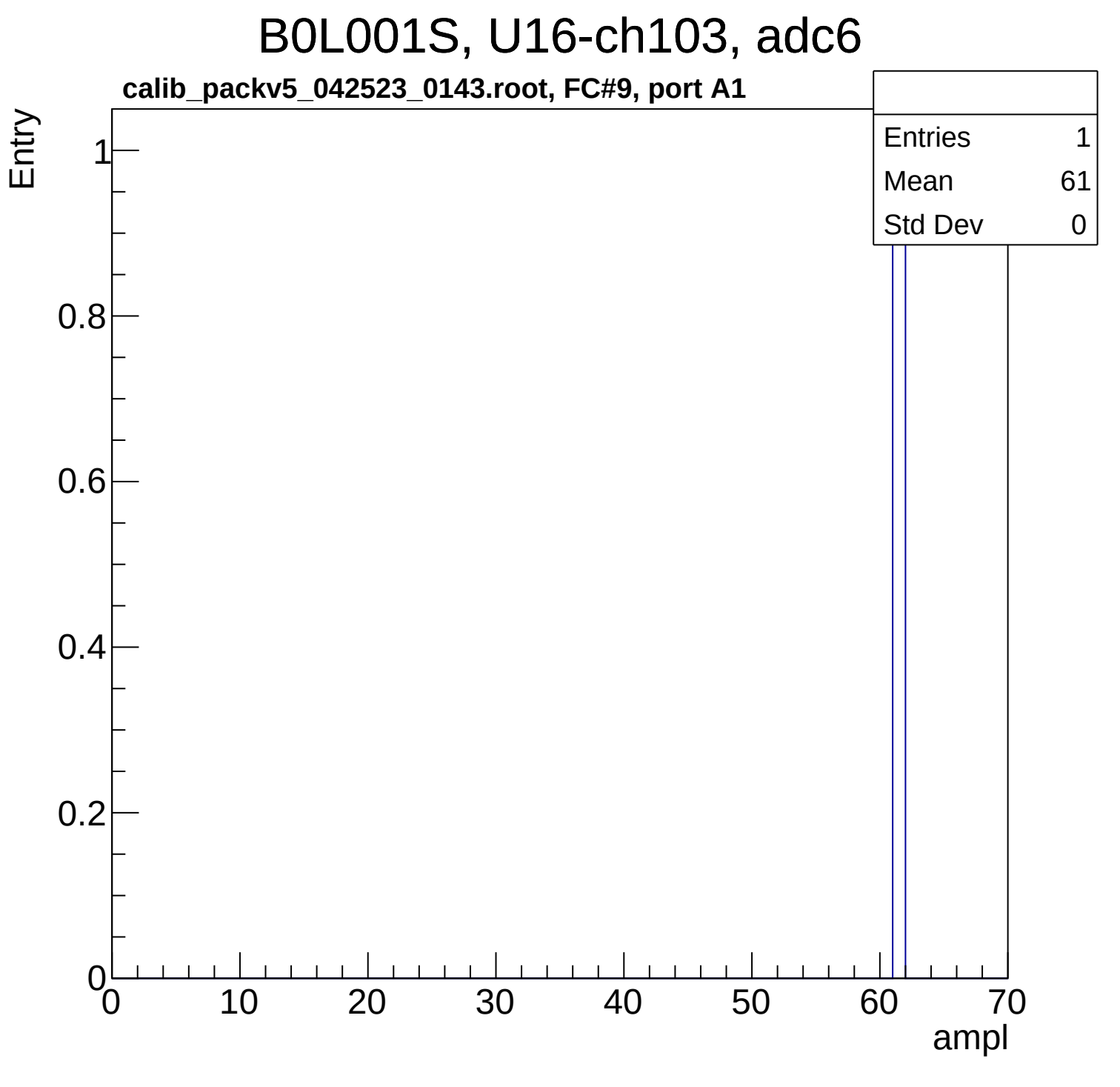
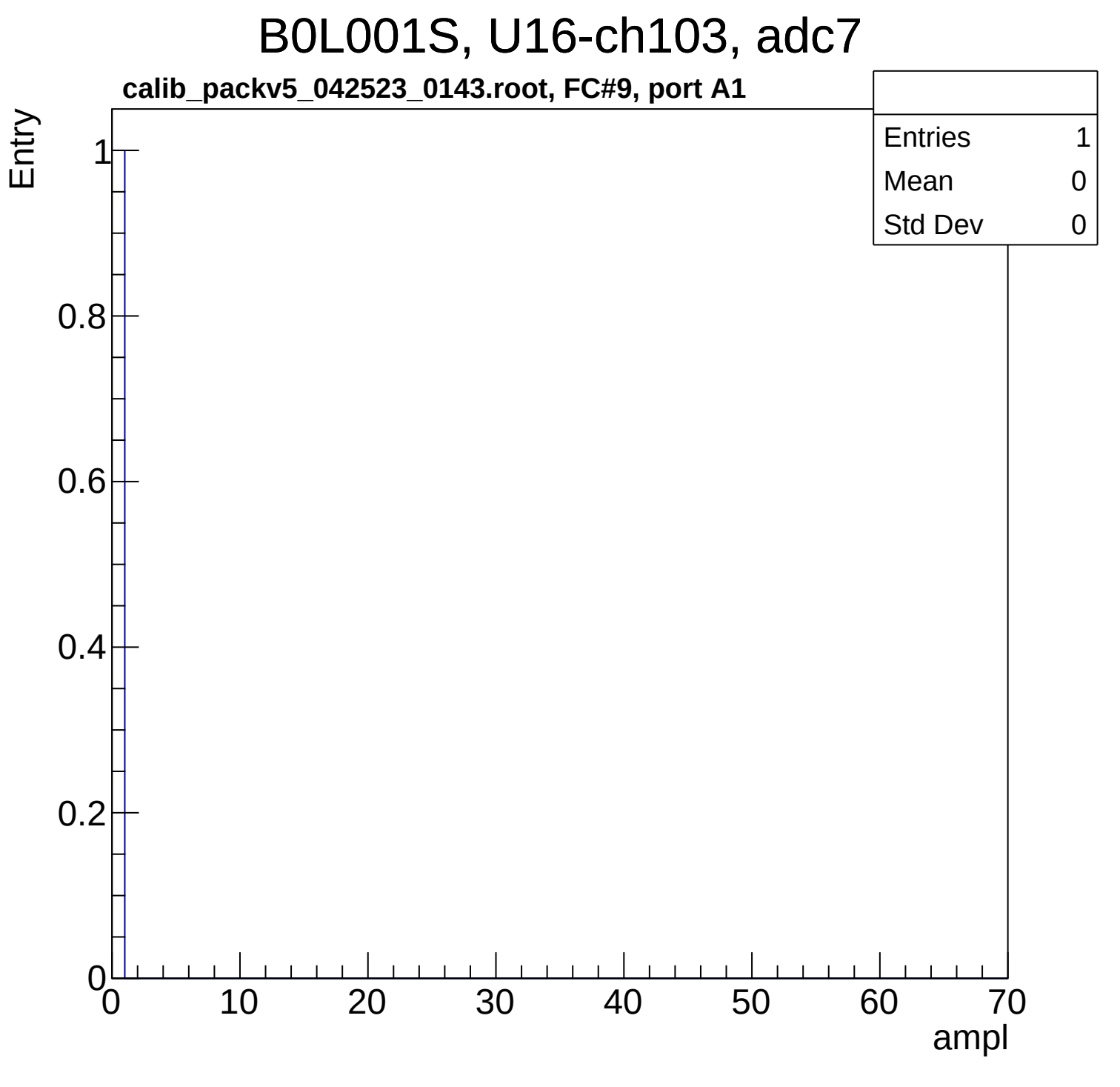
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
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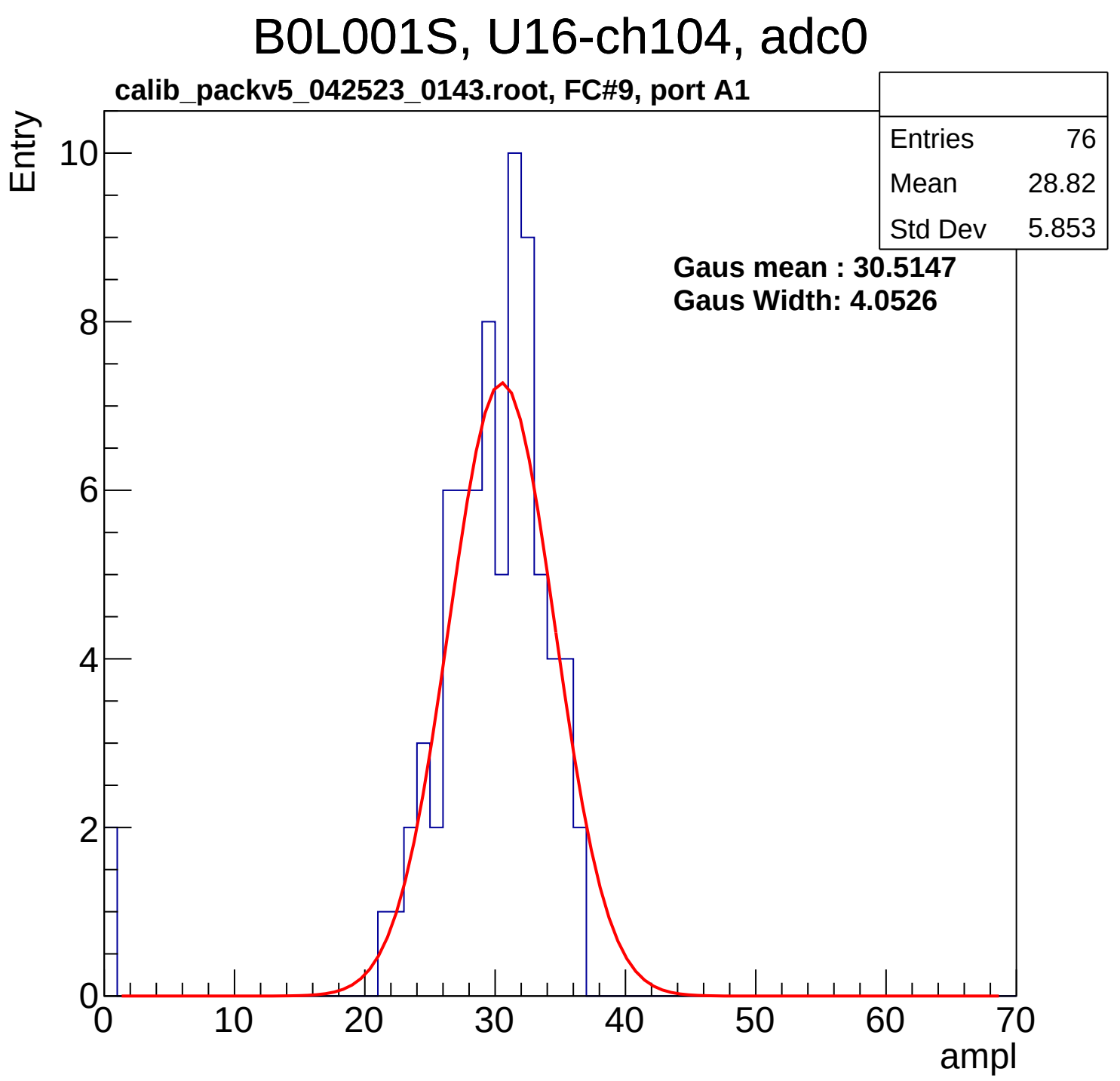
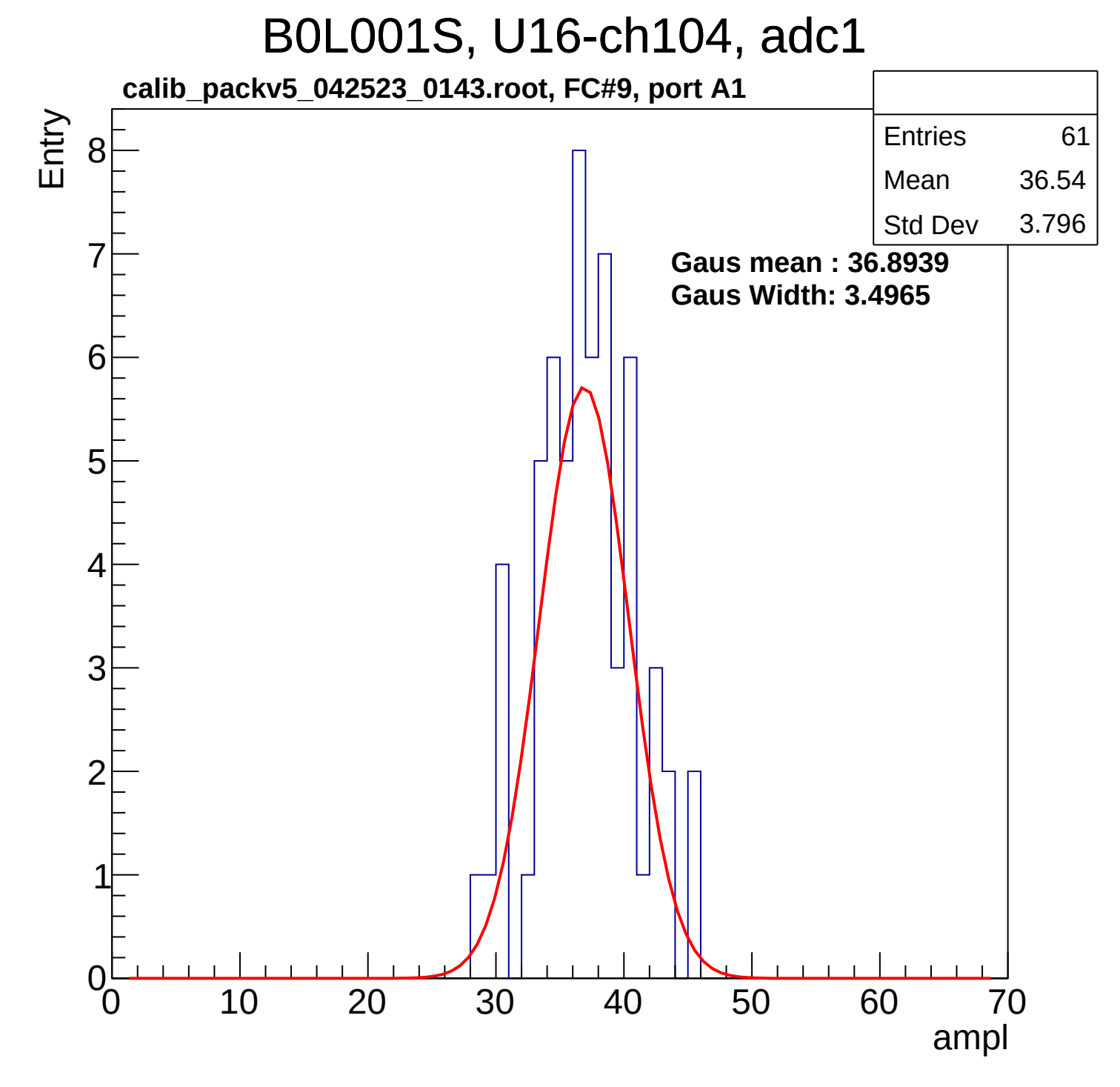
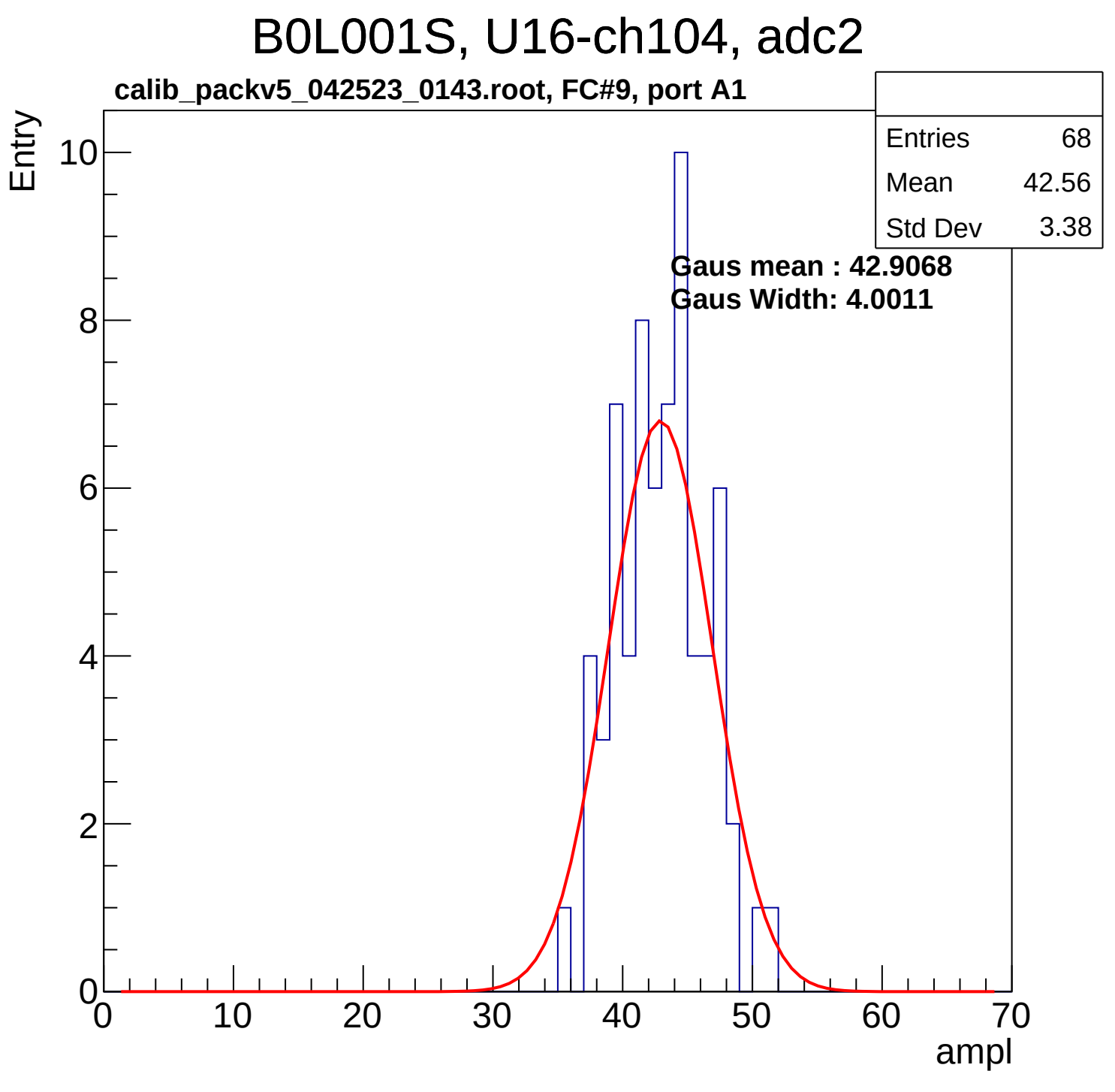
50
60
70
ampl


B0L001S, U16-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 28.82
Std Dev 5.853
Gaus mean : 30.5147
Gaus Width: 4.0526
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.54
Std Dev 3.796
Gaus mean : 36.8939
Gaus Width: 3.4965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 42.56
Std Dev 3.38
Gaus mean : 42.9068
Gaus Width: 4.0011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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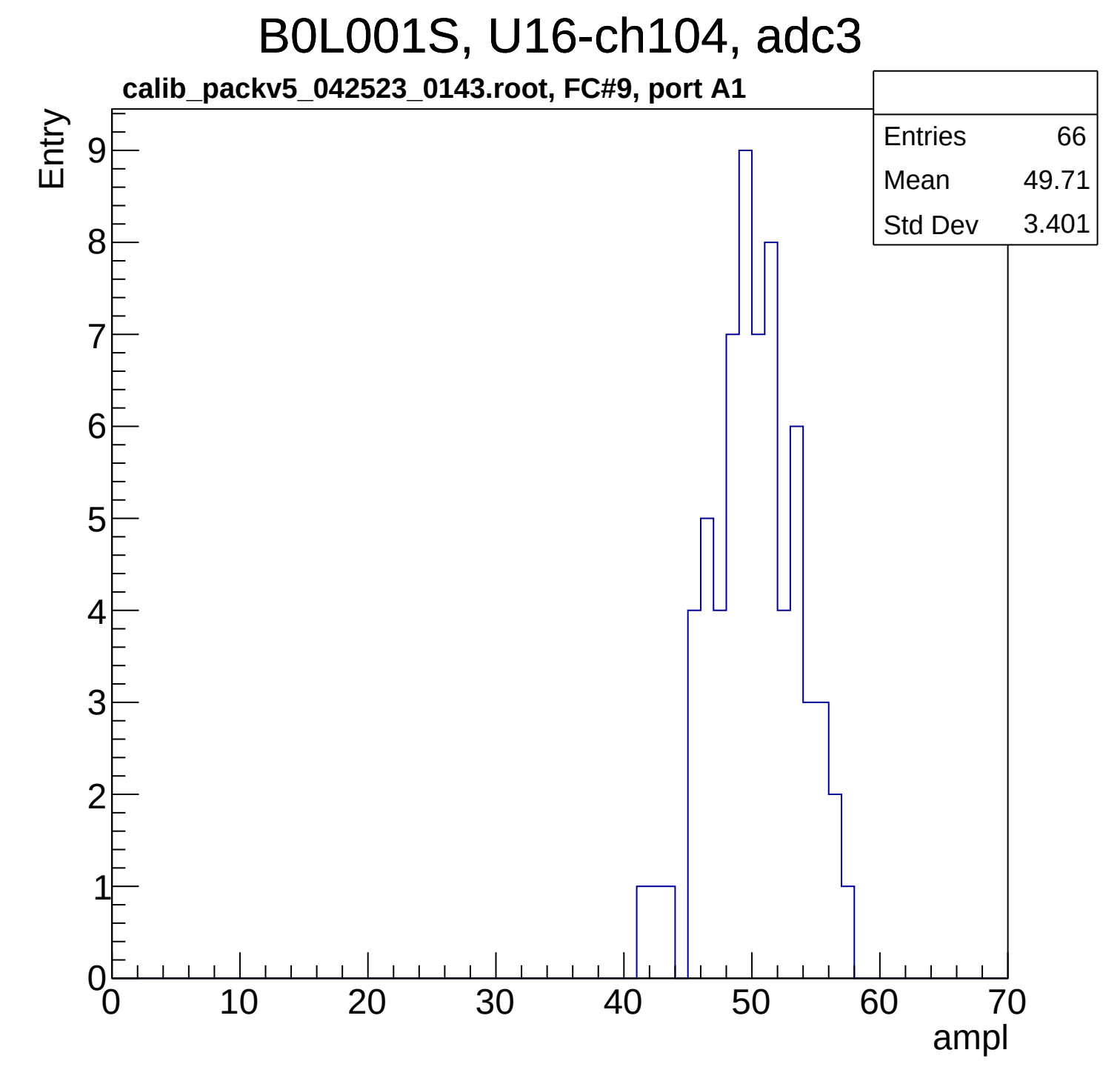
60
70


B0L001S, U16-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.71
Std Dev 3.401
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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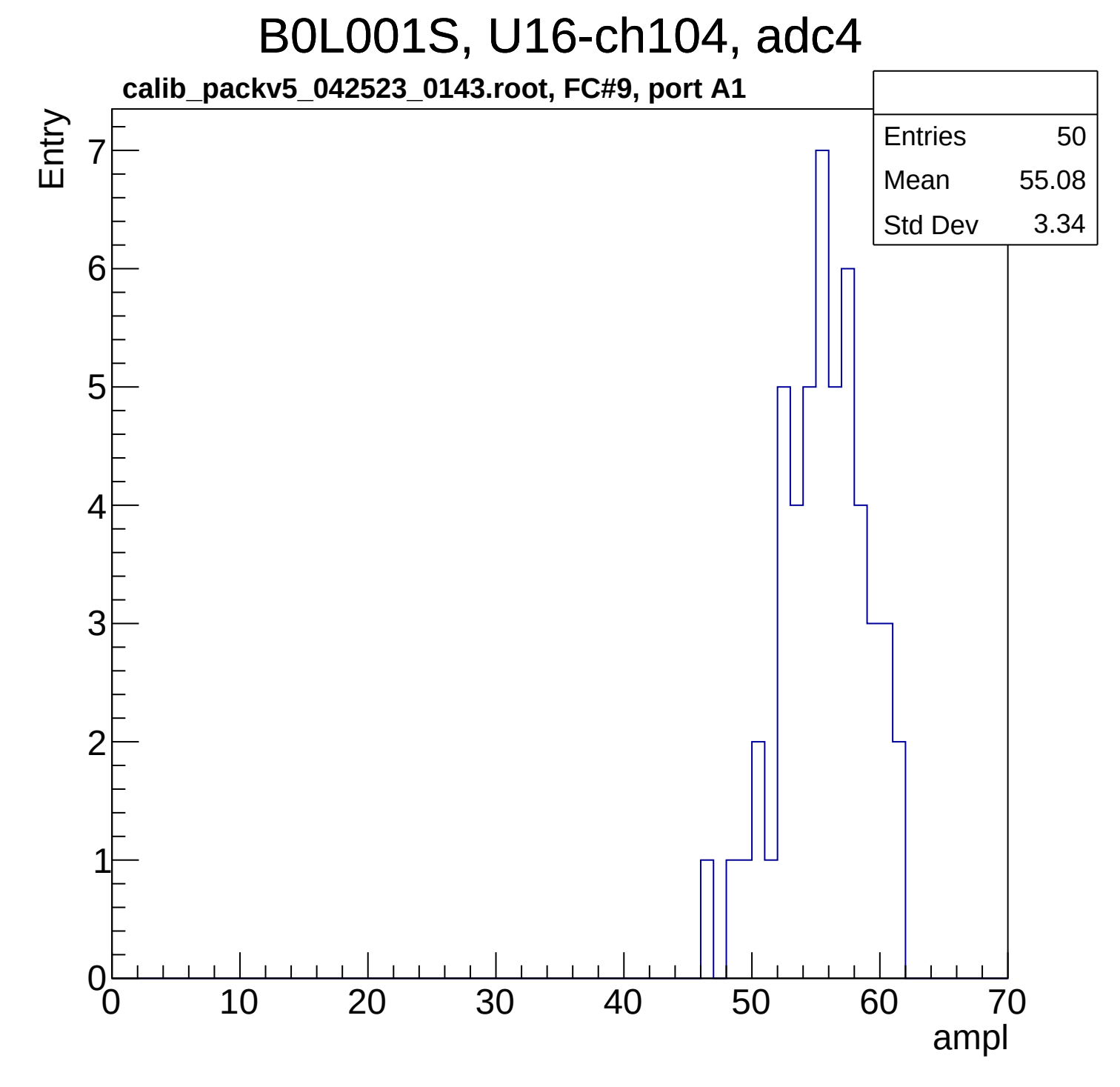
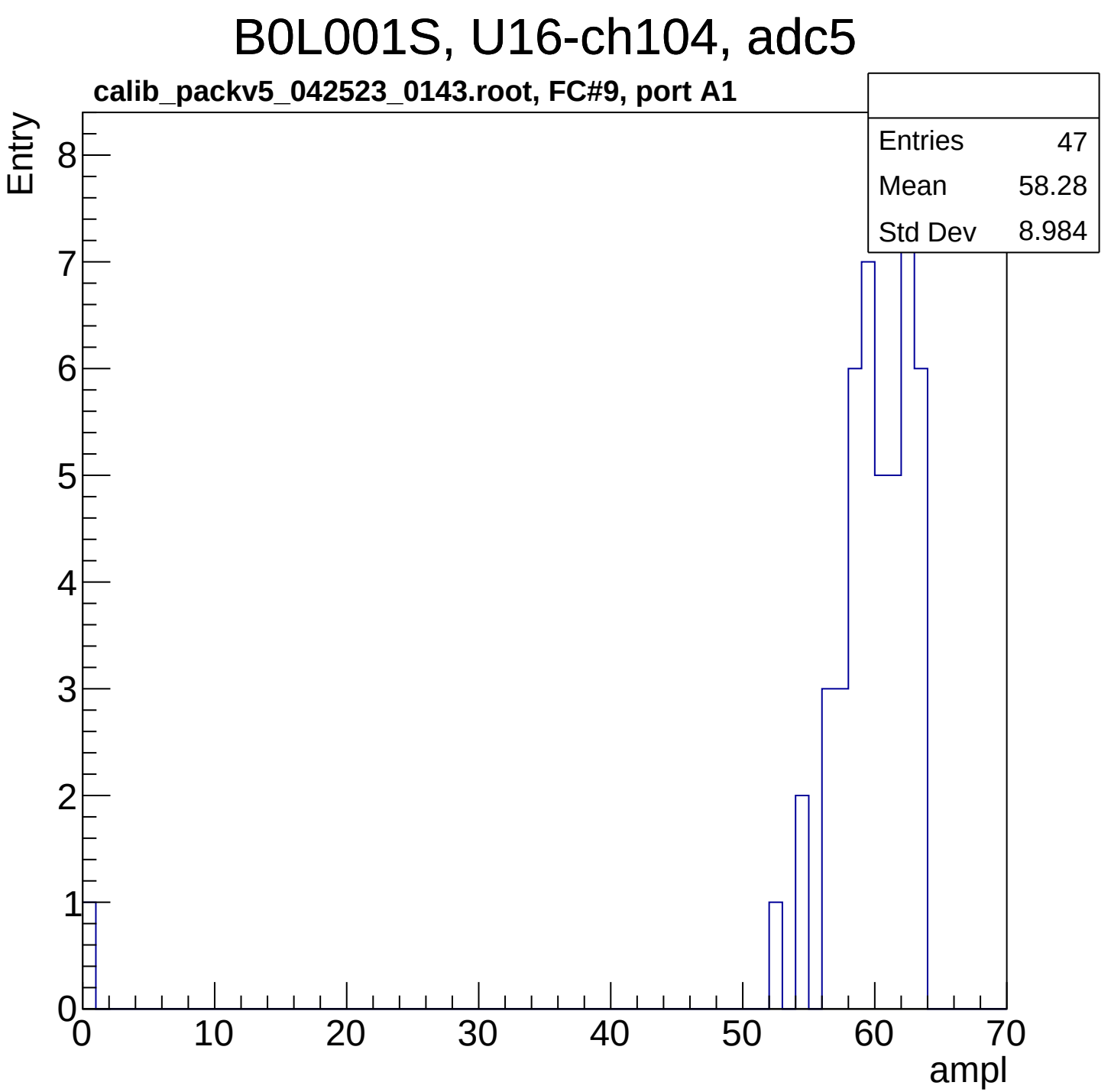
40
50
60
70


B0L001S, U16-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 55.08
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.28
Std Dev 8.984
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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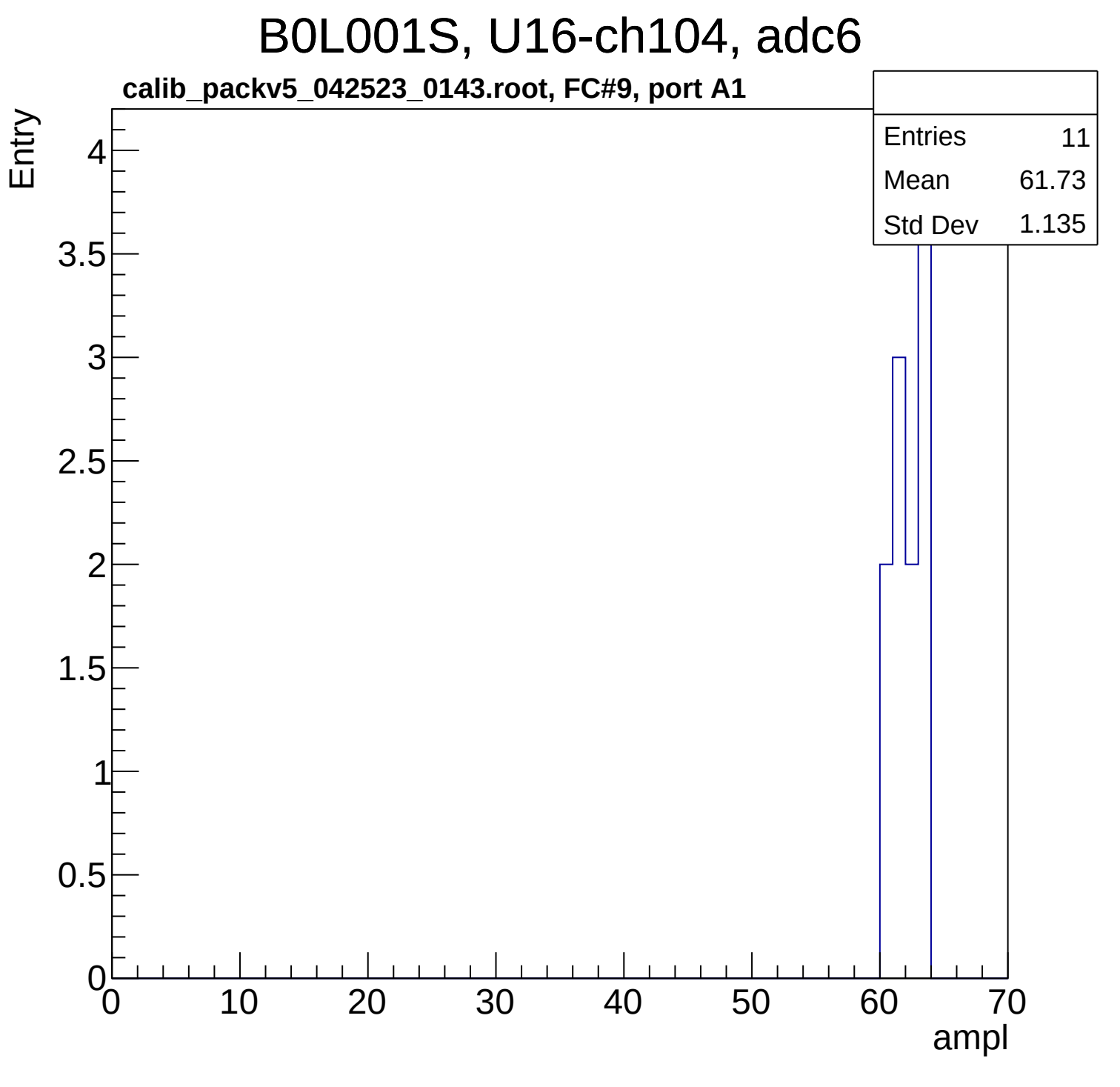
B0L001S, U16-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.73
Std Dev 1.135
Entry
ampl
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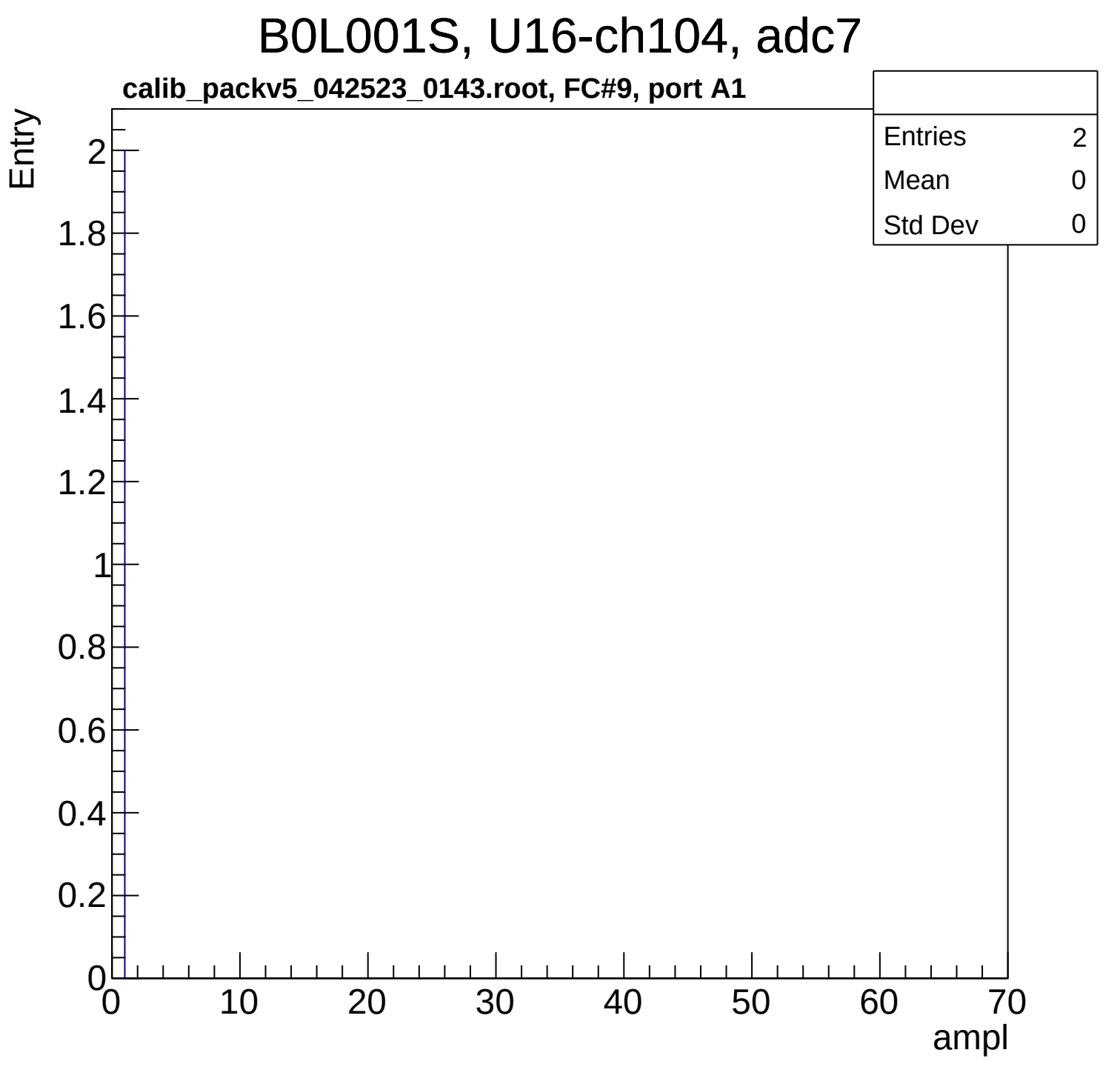
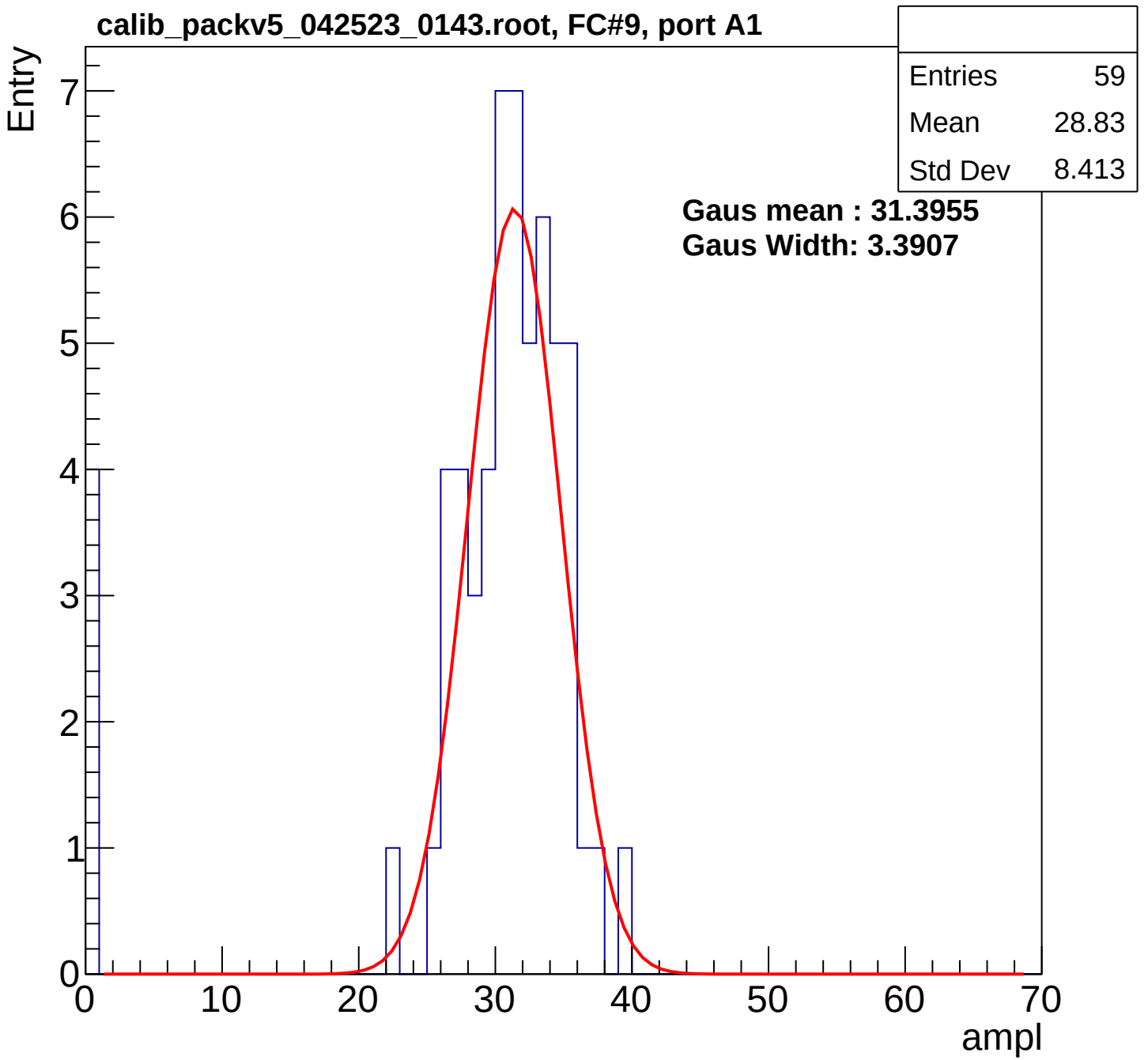
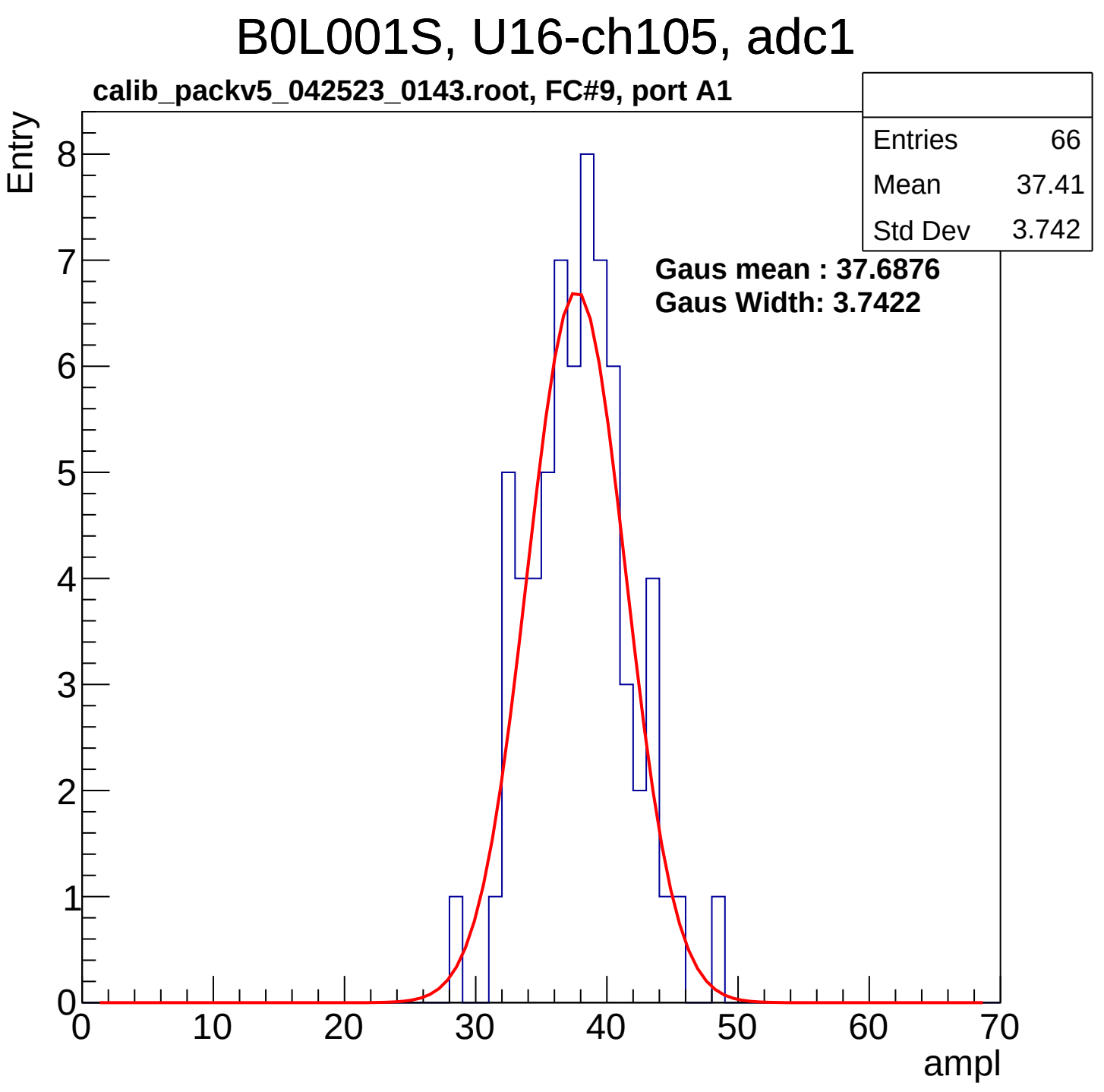
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 28.83
Std Dev 8.413
Gaus mean : 31.3955
Gaus Width: 3.3907
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.41
Std Dev 3.742
Gaus mean : 37.6876
Gaus Width: 3.7422
Entry
ampl
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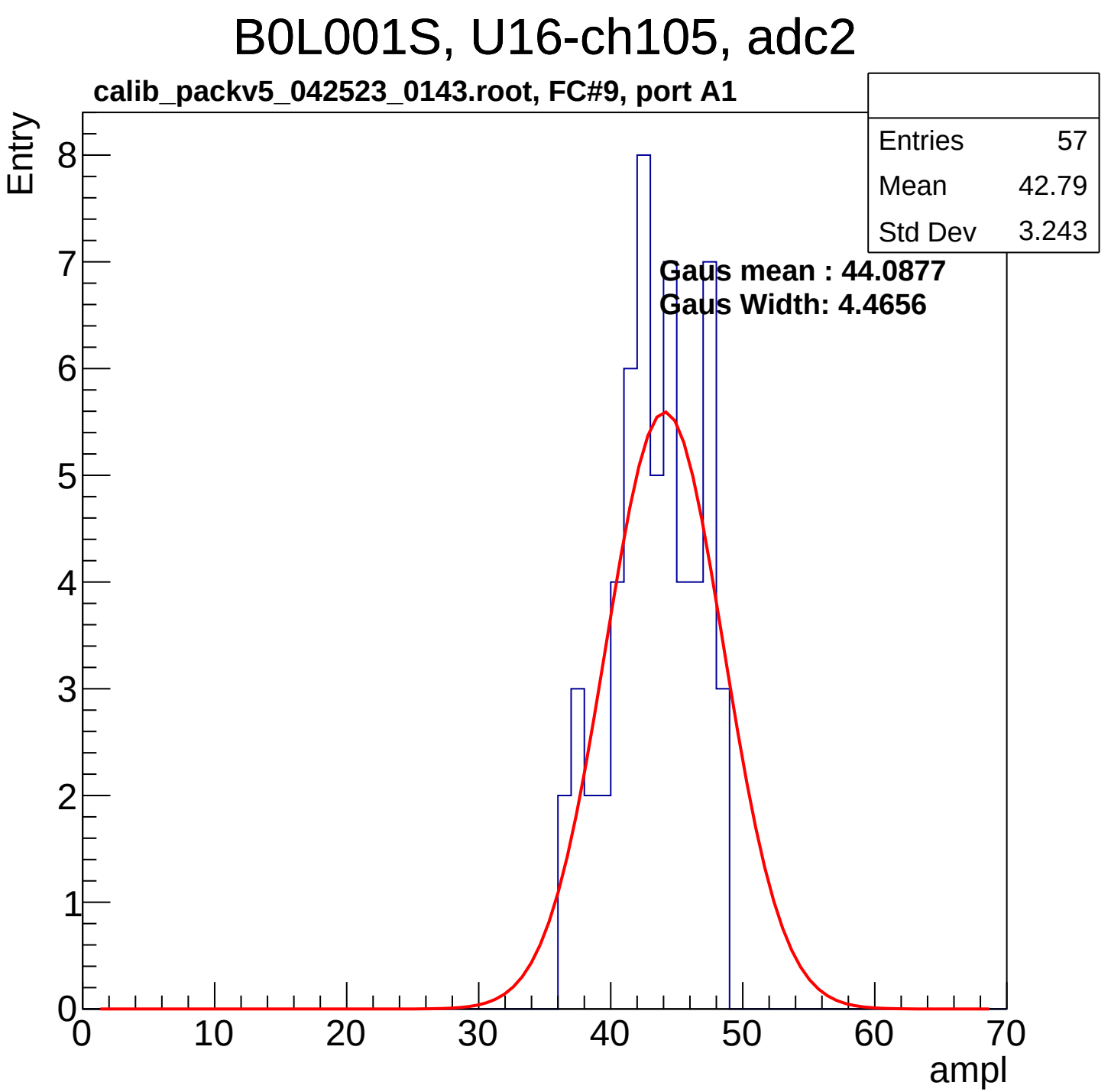
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.79
Std Dev 3.243
Gaus mean : 44.0877
Gaus Width: 4.4656
Entry
ampl
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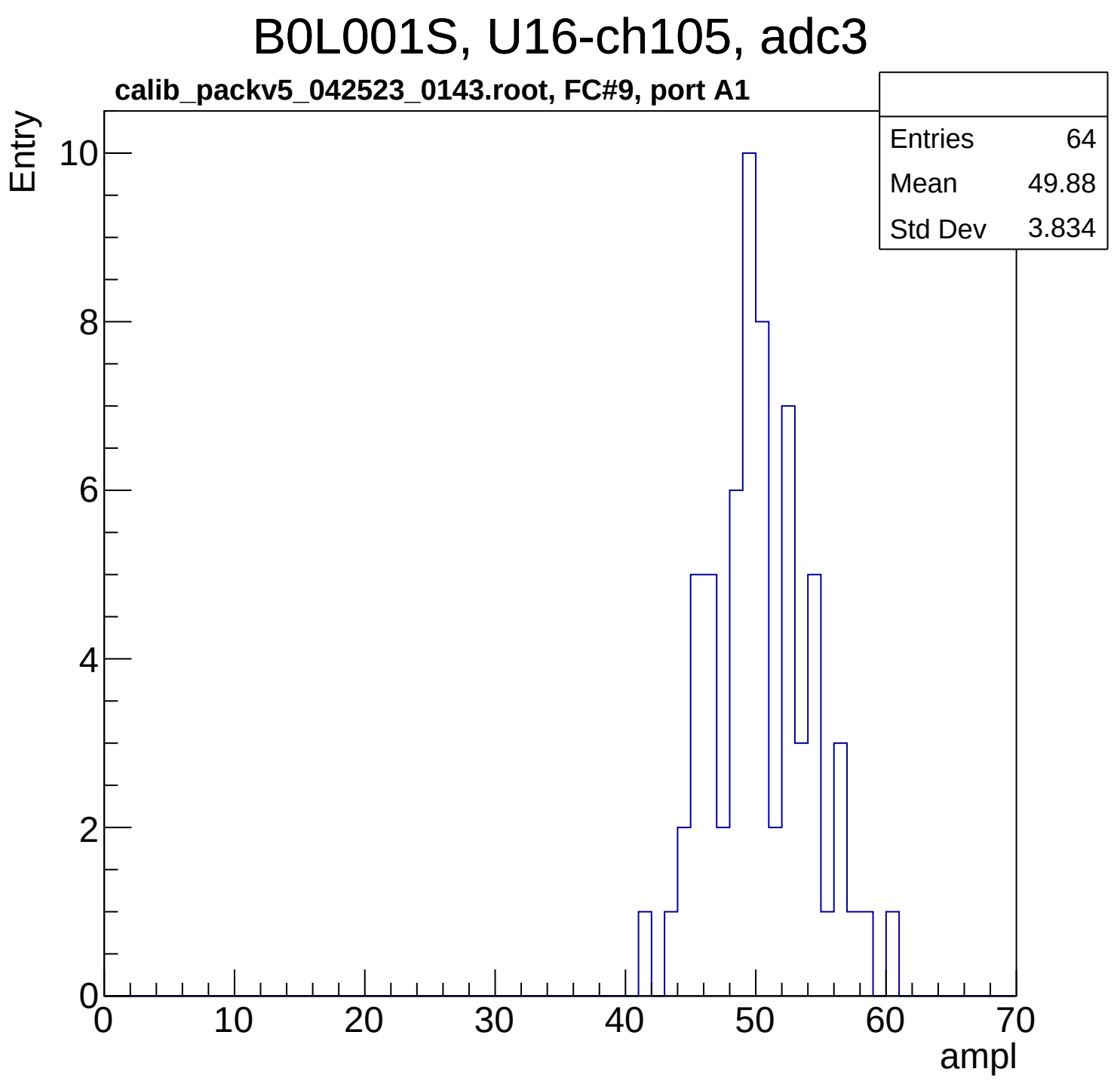
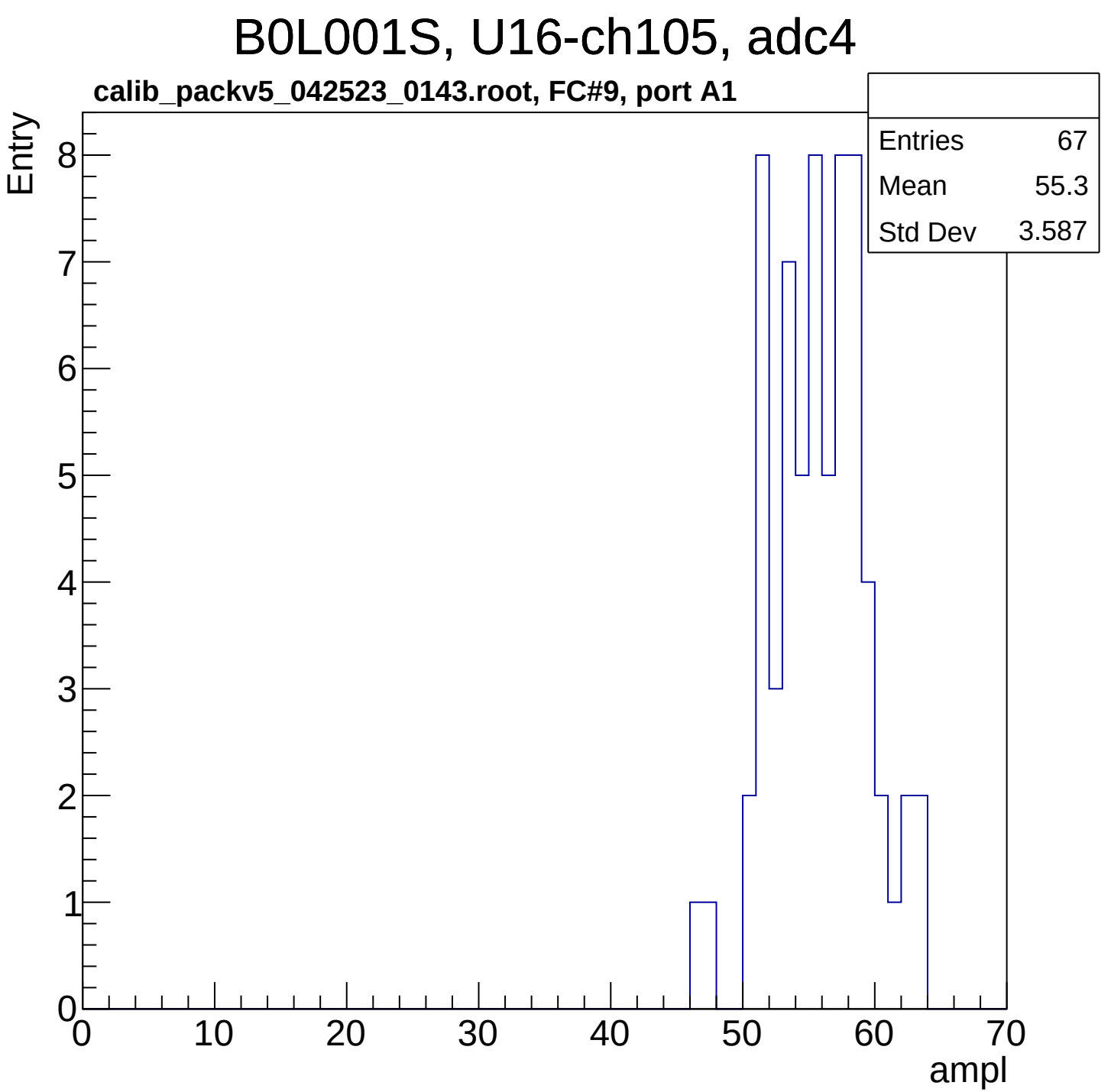
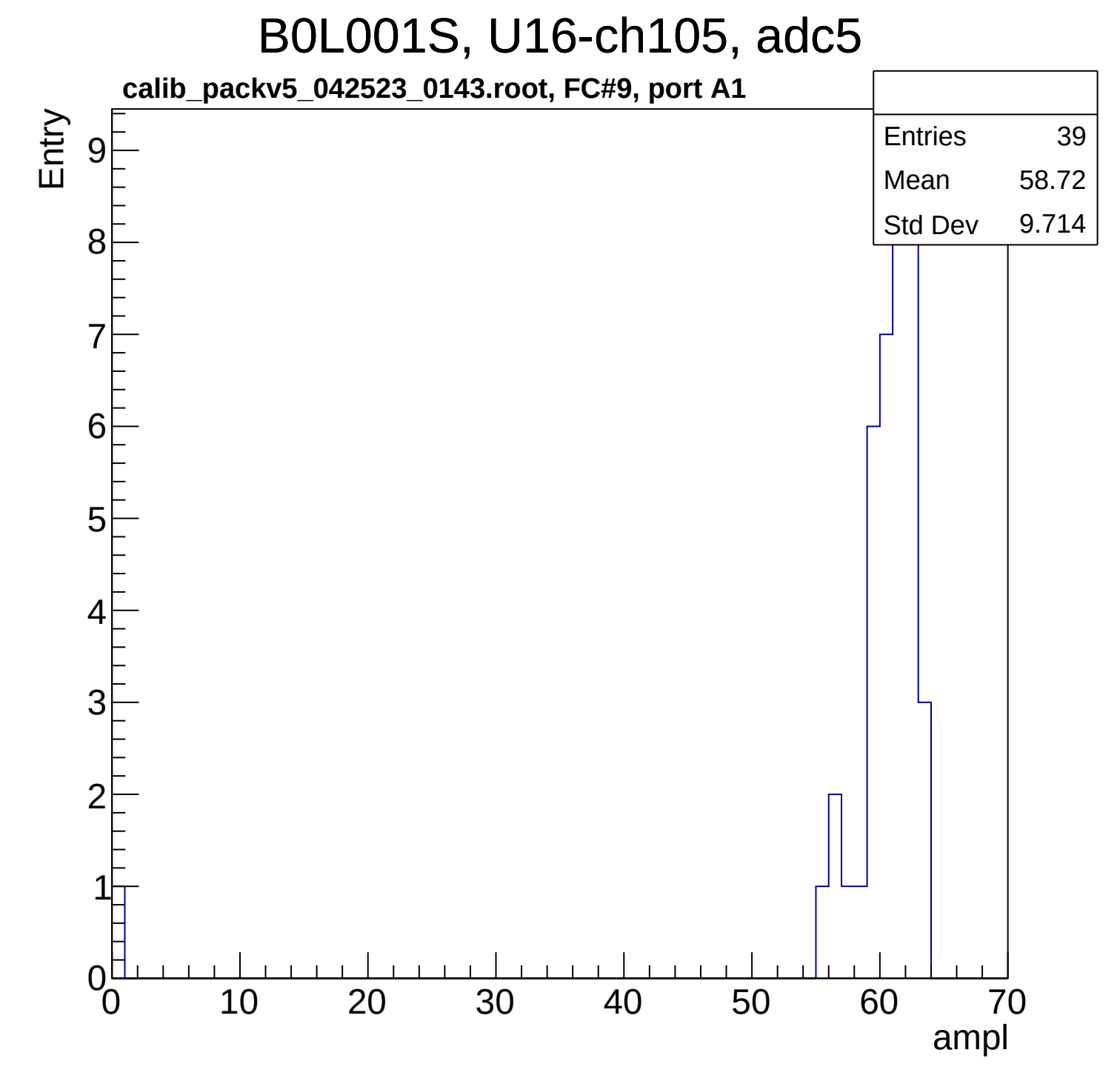
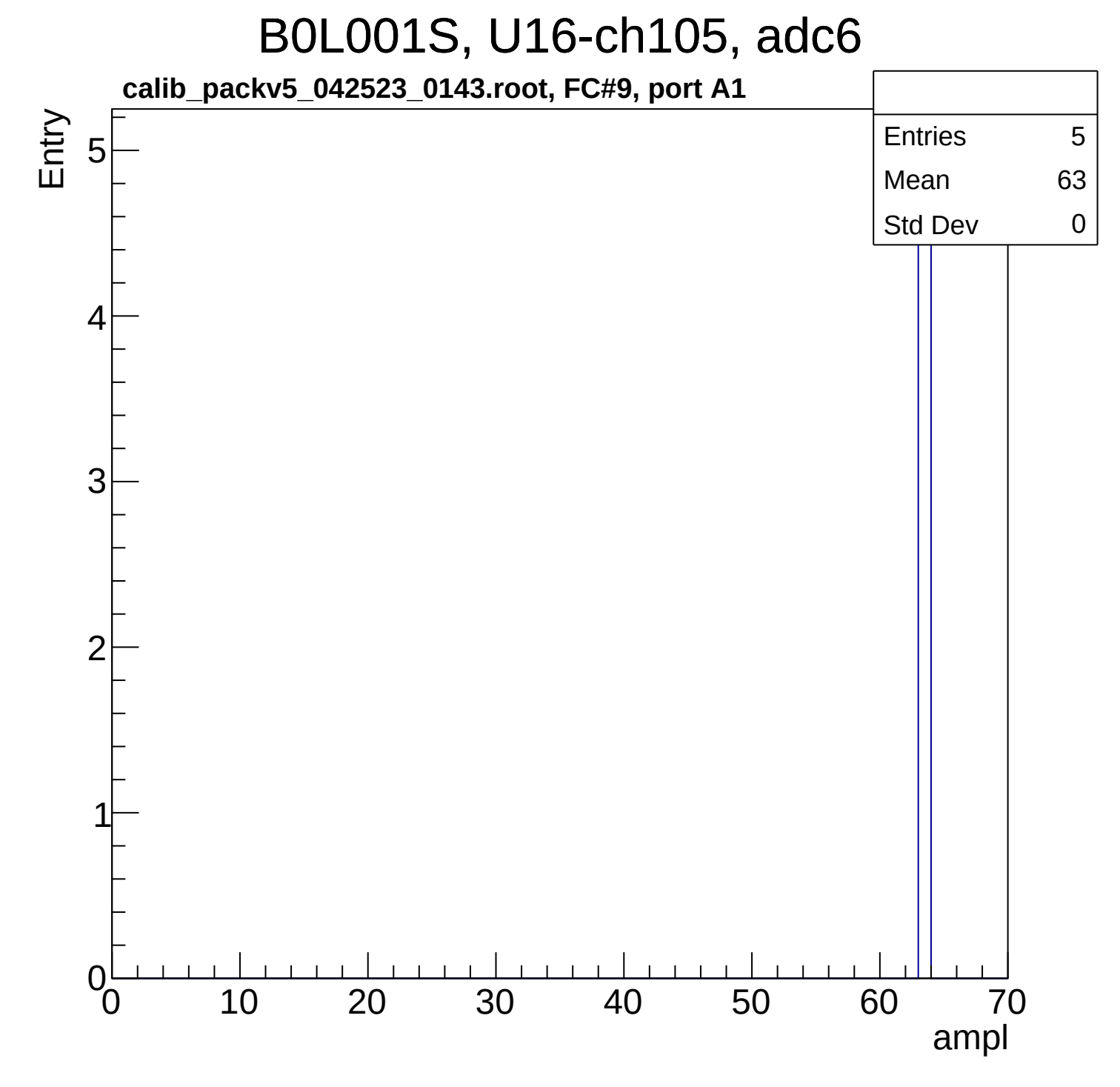
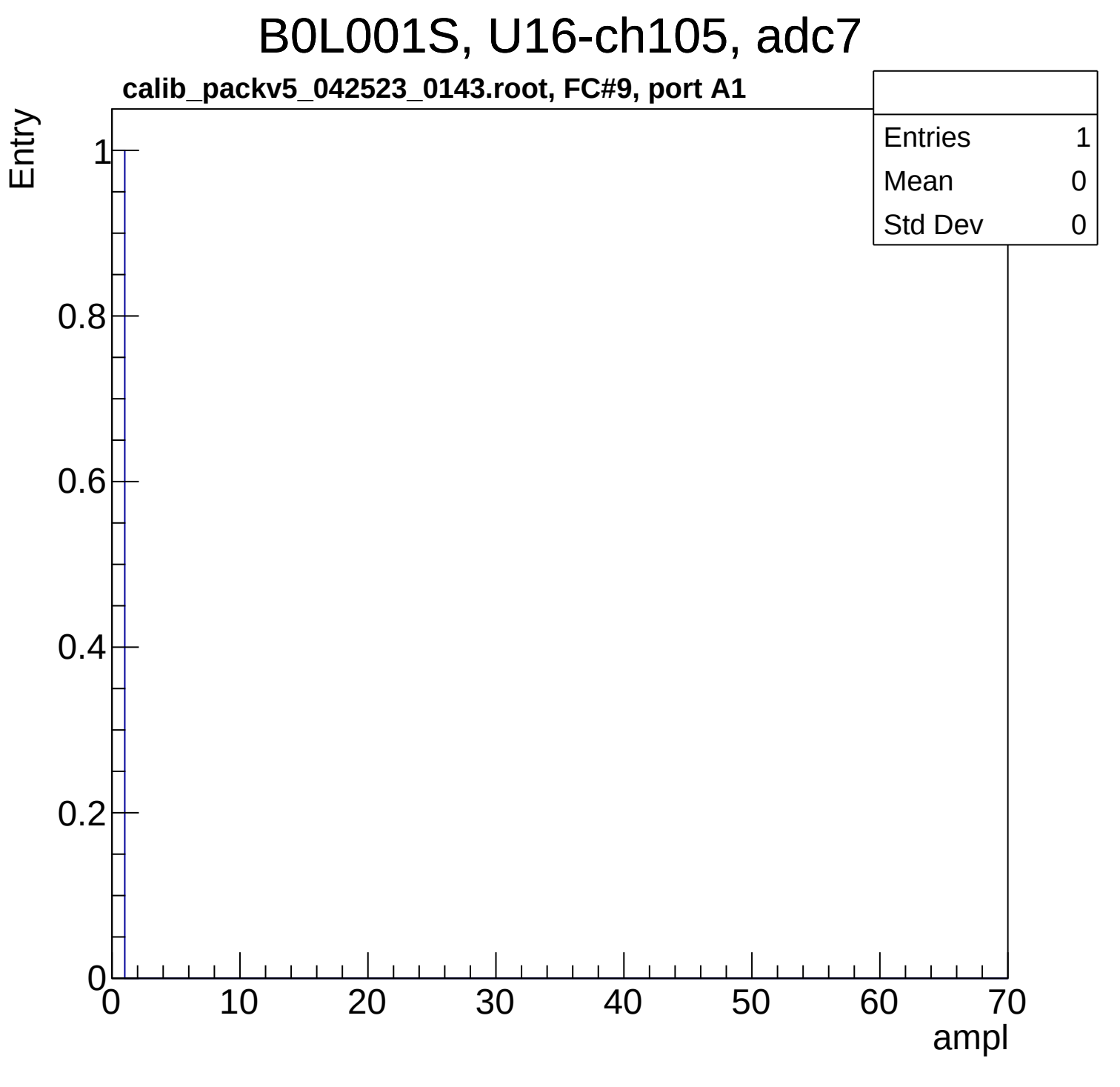
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

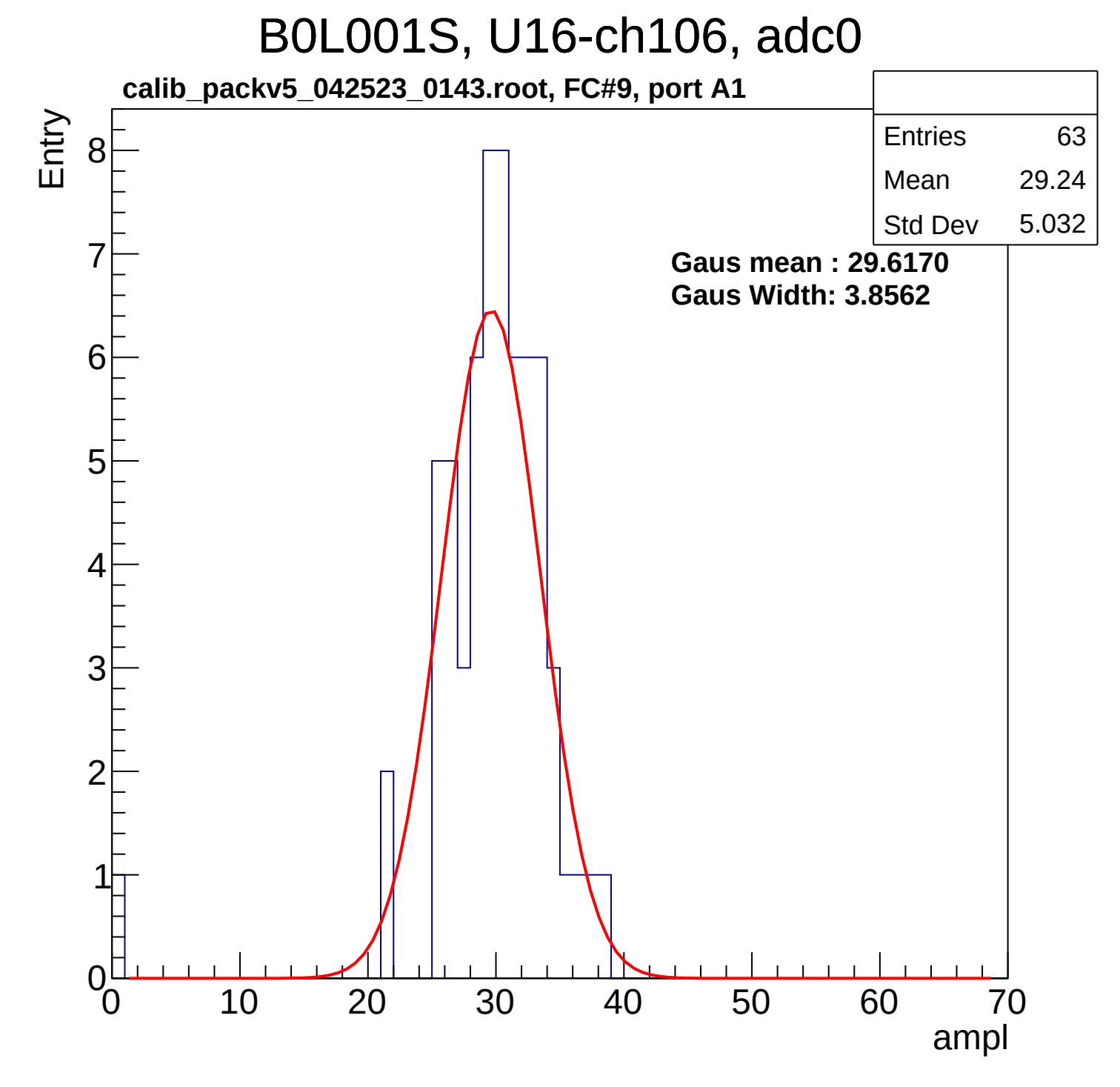

B0L001S, U16-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.88
Std Dev 3.834
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.3
Std Dev 3.587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.72
Std Dev 9.714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 63
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.24
Std Dev 5.032
Gaus mean : 29.6170
Gaus Width: 3.8562
Entry
ampl
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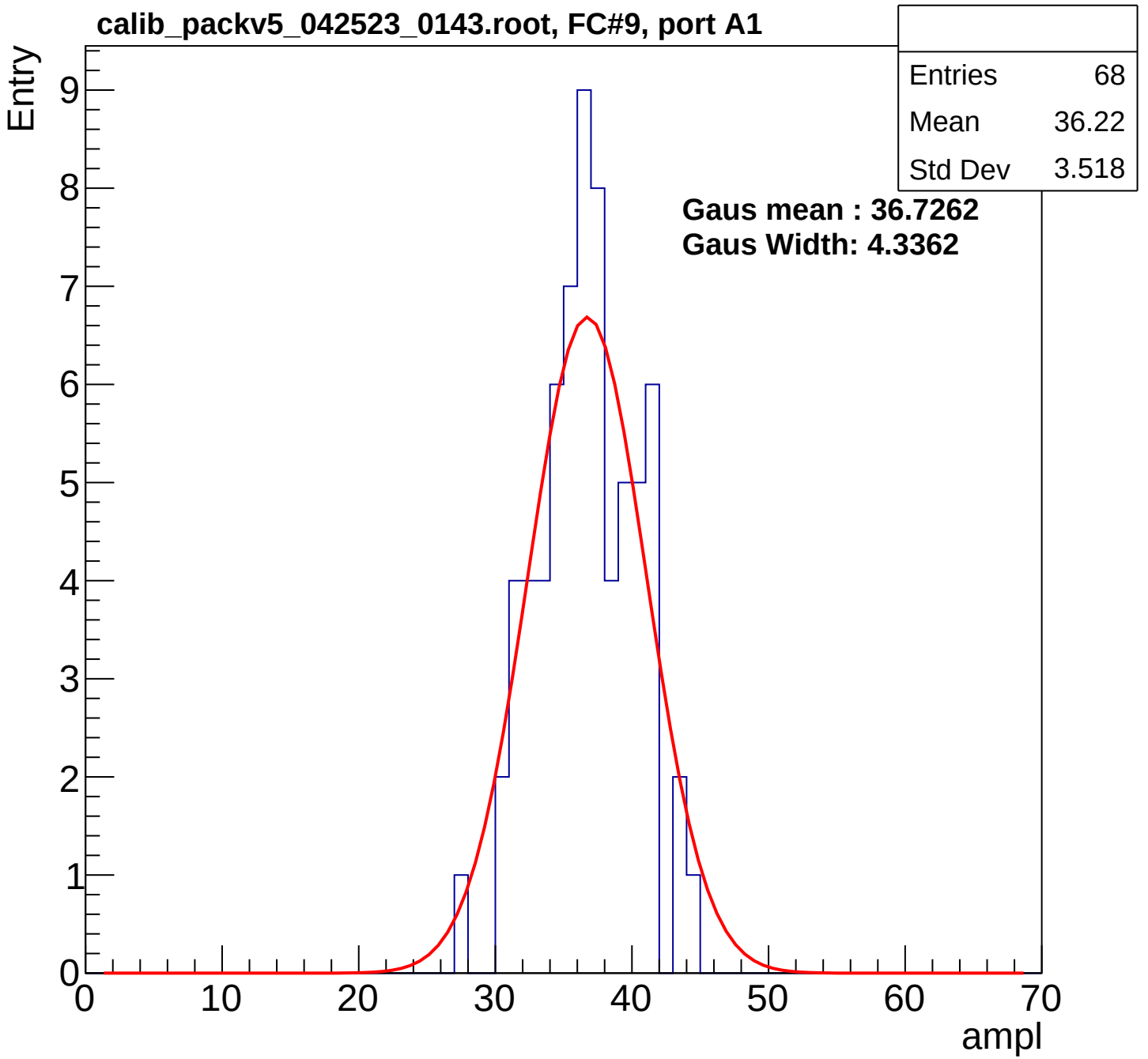
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.22
Std Dev 3.518
Gaus mean : 36.7262
Gaus Width: 4.3362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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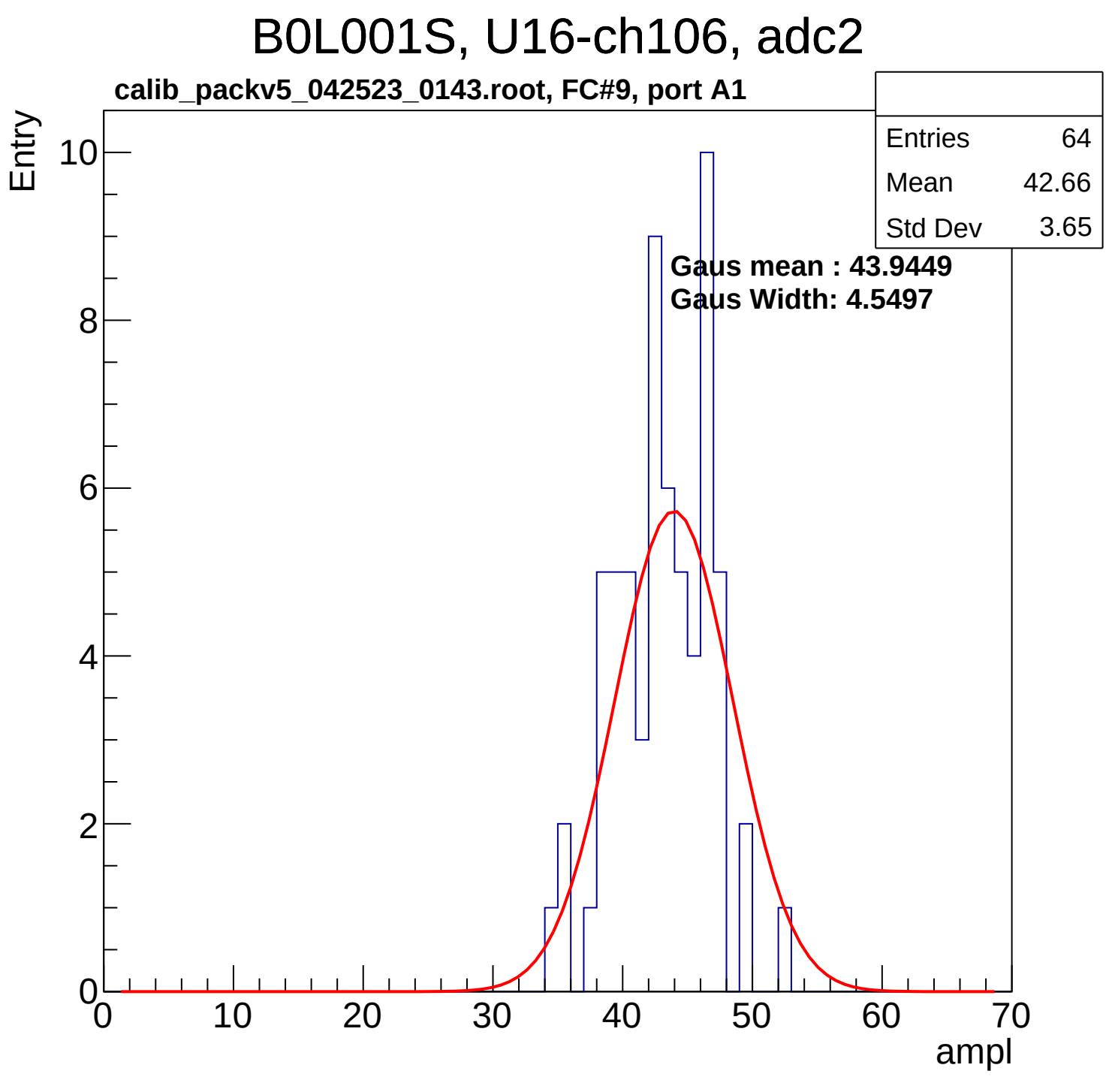
60
70


B0L001S, U16-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 42.66
Std Dev 3.65
Gaus mean : 43.9449
Gaus Width: 4.5497
Entry
ampl
0
2
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8
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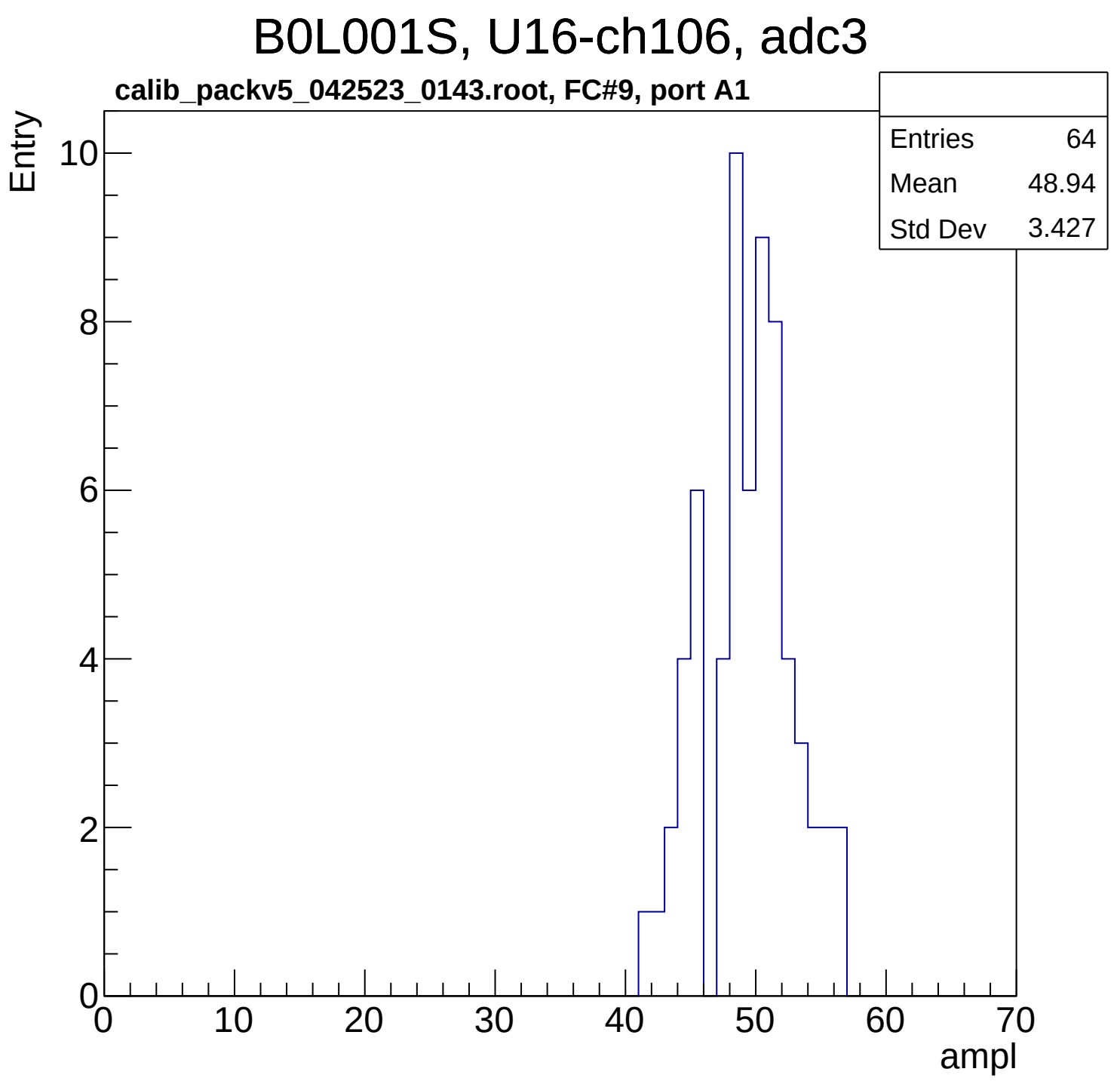
0
10
20
30
40
50
60
70


B0L001S, U16-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.94
Std Dev 3.427
Entry
ampl
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2
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0
10
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40
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60
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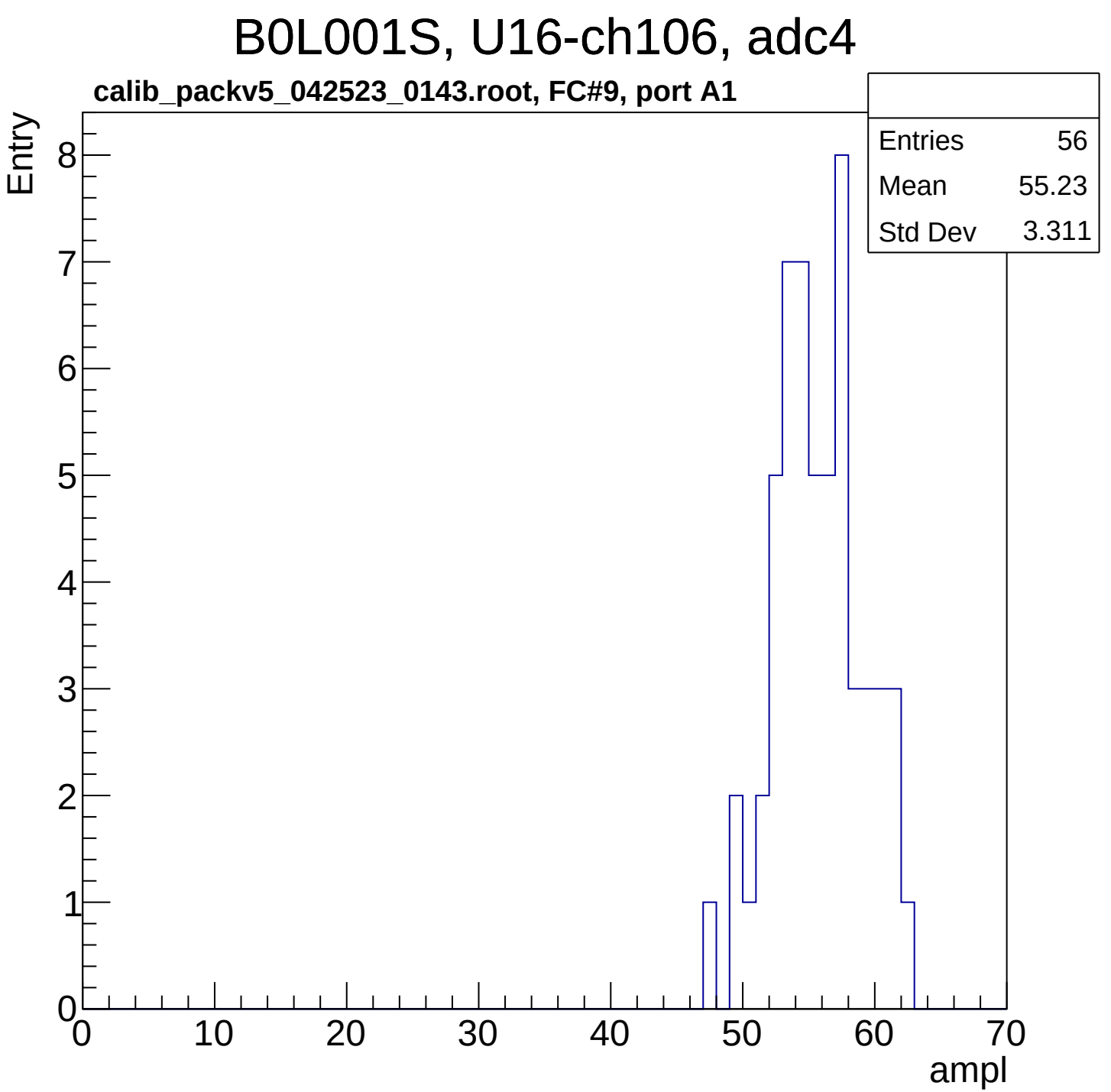
B0L001S, U16-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.23
Std Dev 3.311
Entry
ampl
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1
2
3
4
5
6
7
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20
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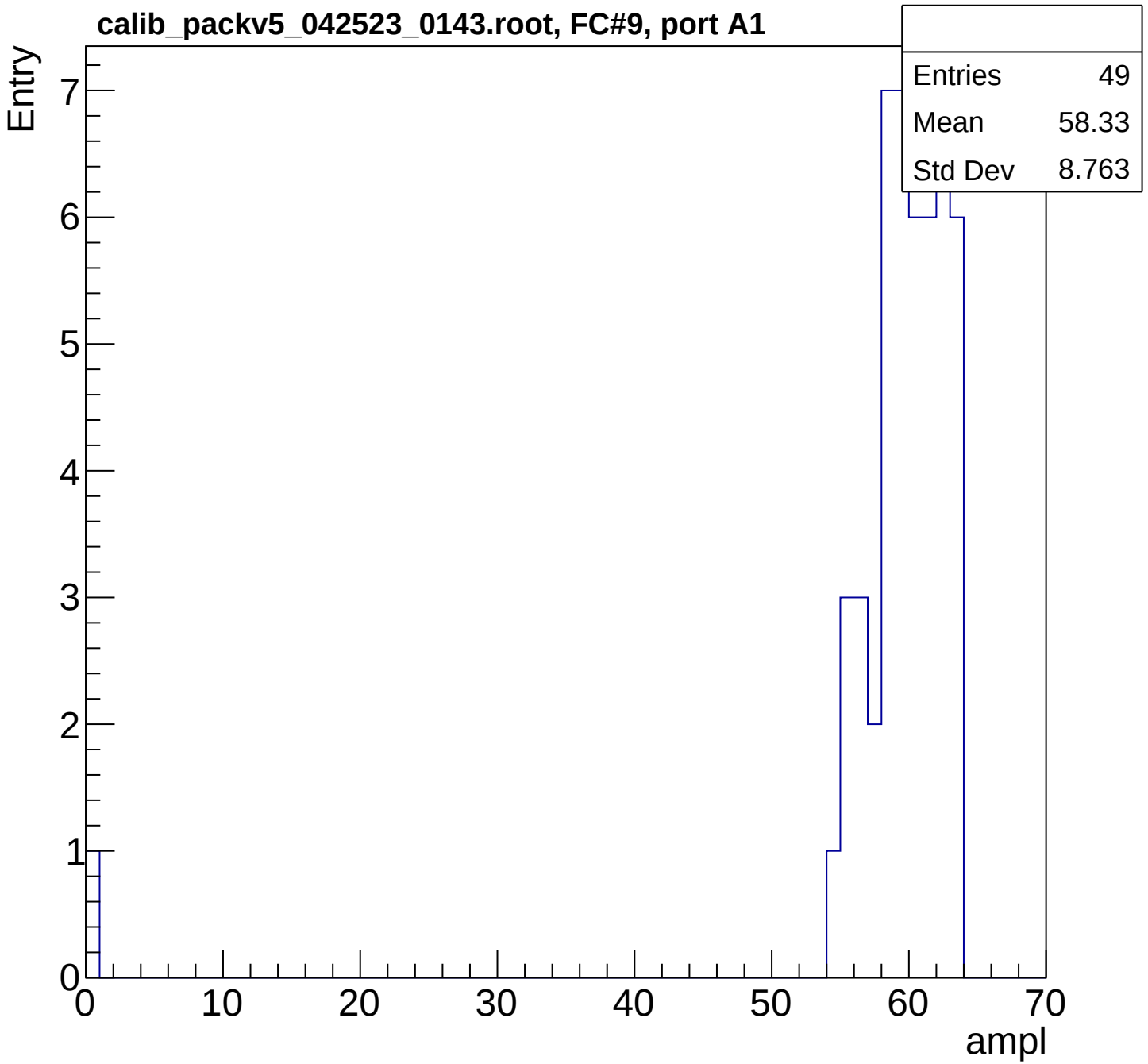
60
70


B0L001S, U16-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.33
Std Dev 8.763
Entry
ampl
0
1
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3
4
5
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10
20
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40
50
60
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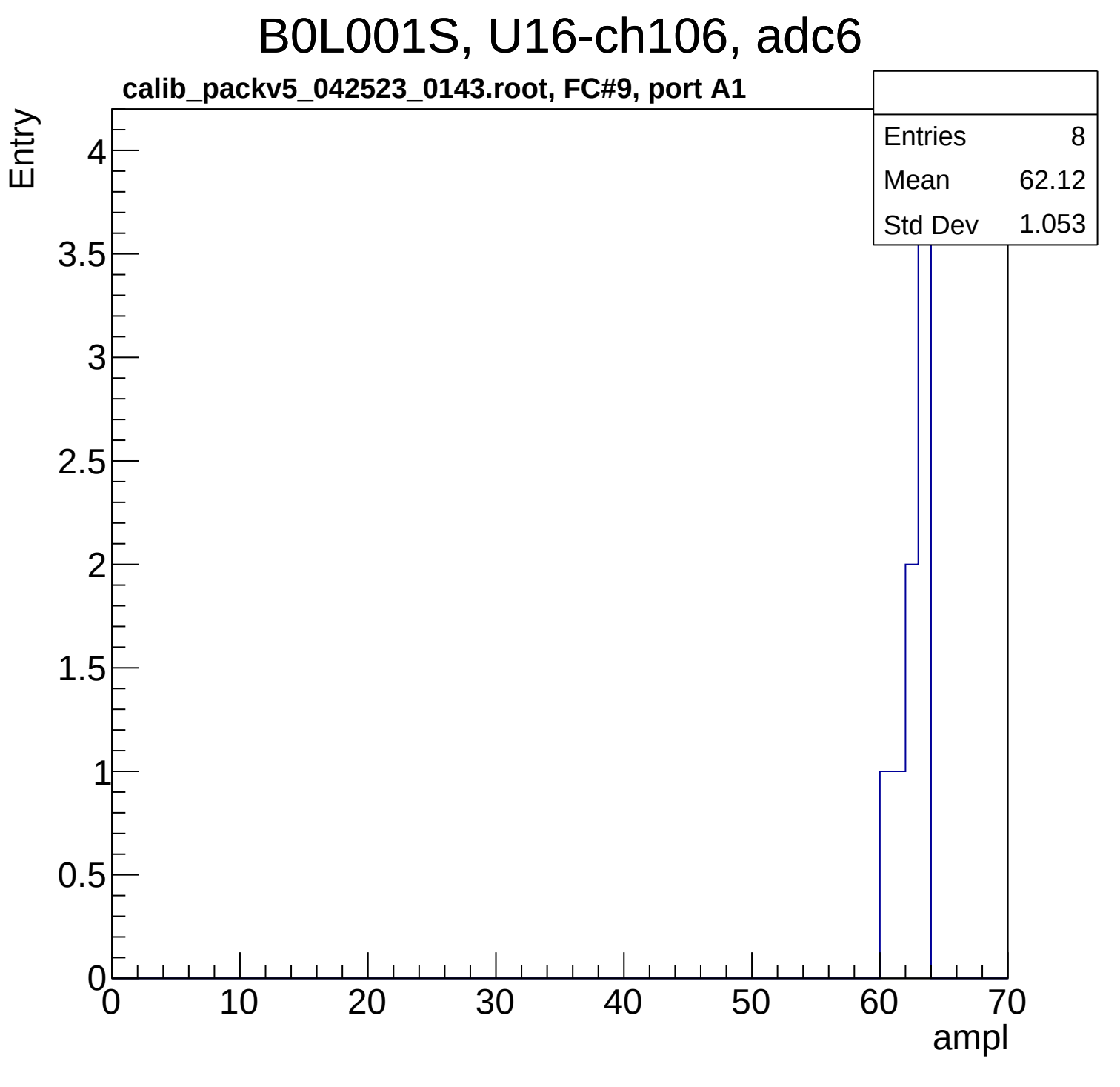
B0L001S, U16-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
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1.5
2
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3
3.5
4
0
10
20
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60
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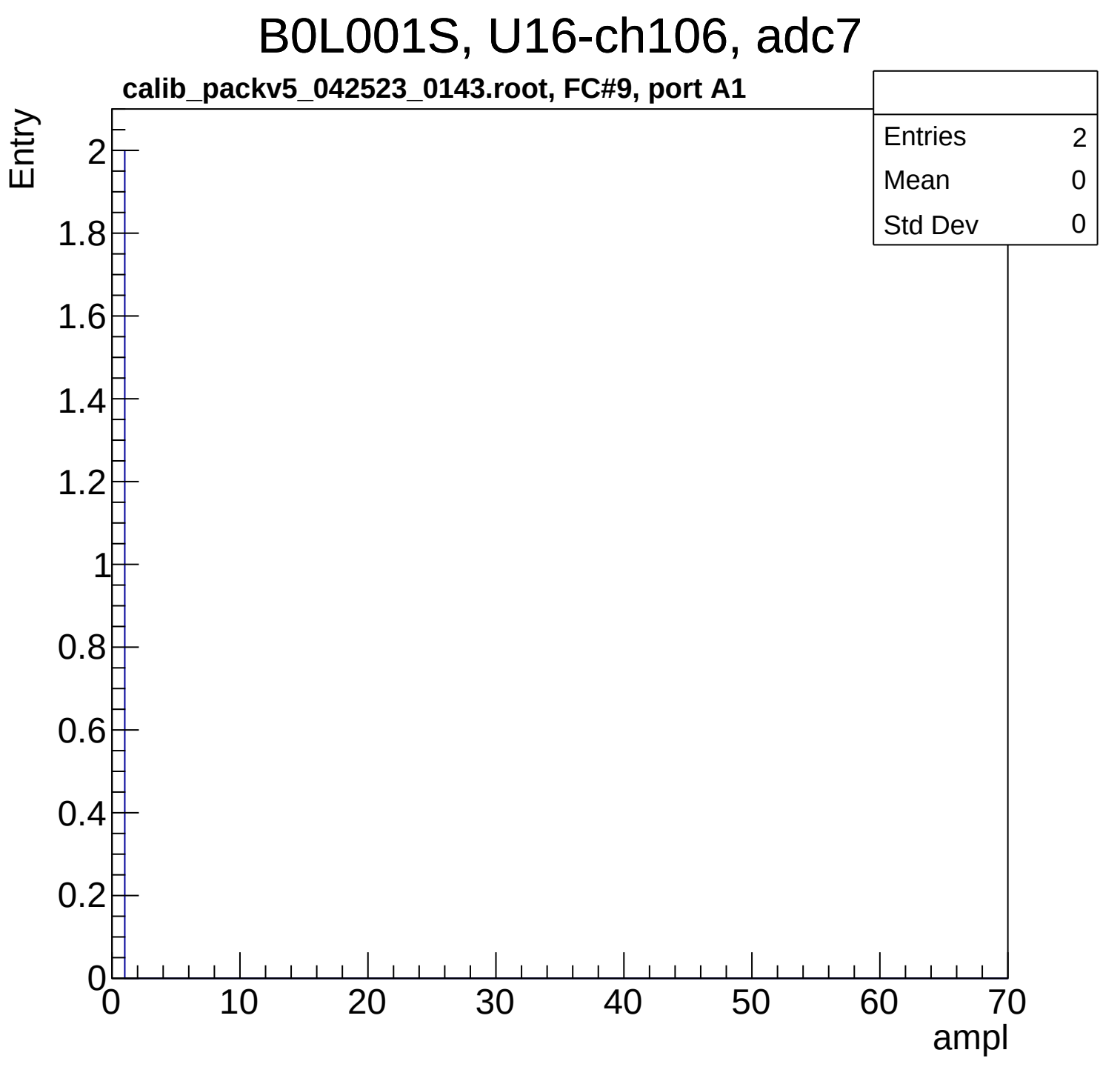
B0L001S, U16-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
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10
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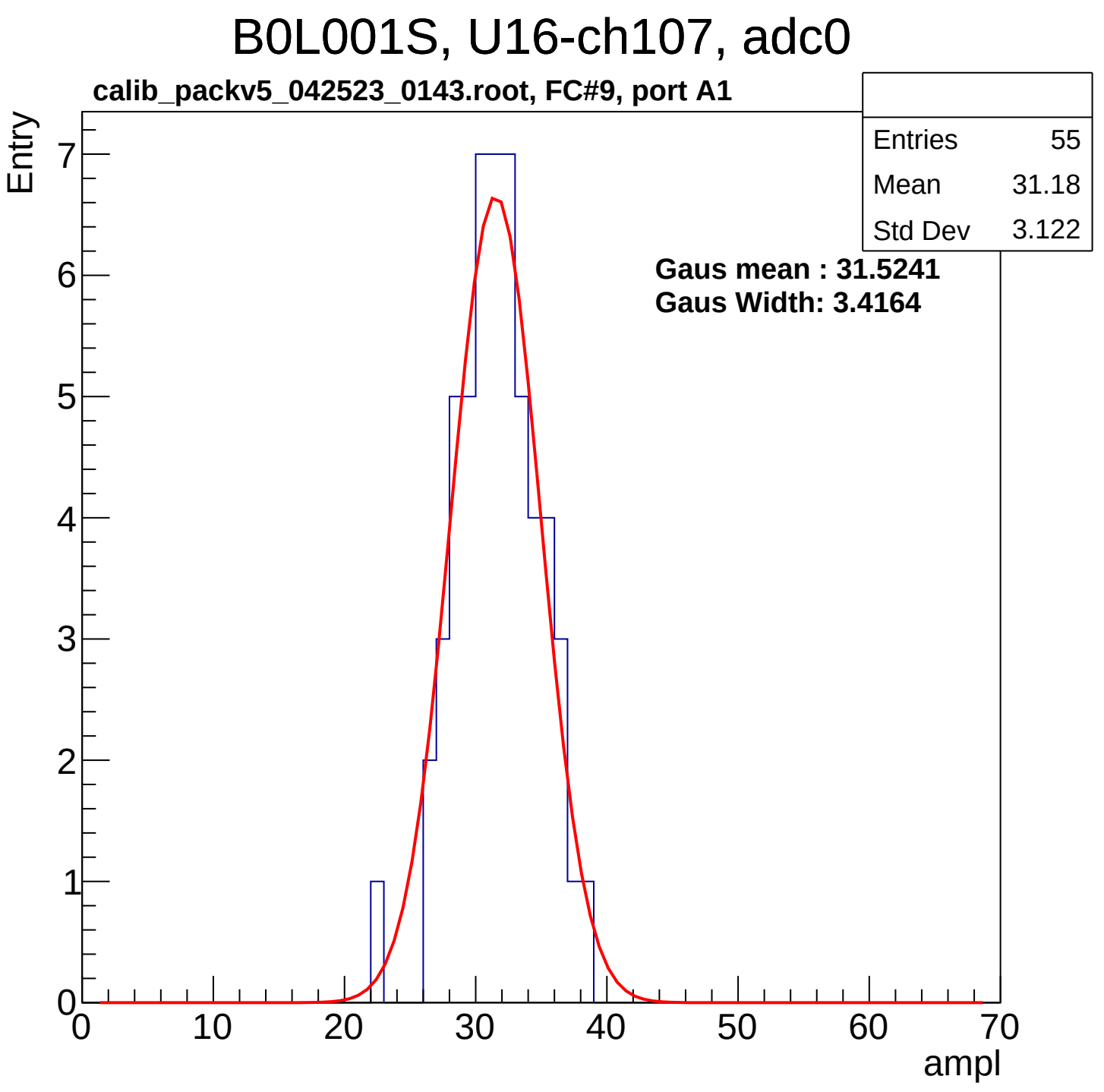
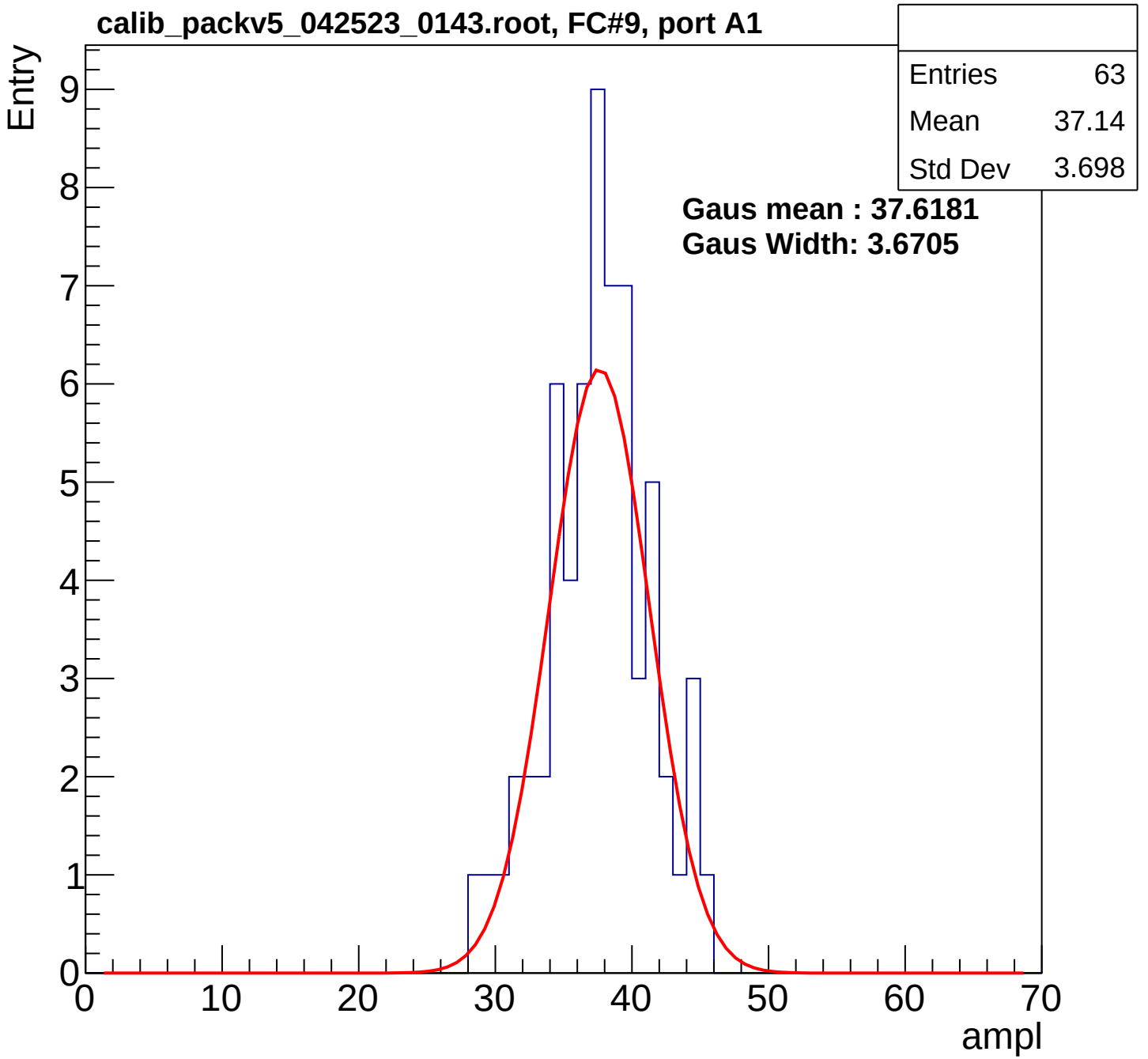
50
60
70
ampl


B0L001S, U16-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 31.18
Std Dev 3.122
Gaus mean : 31.5241
Gaus Width: 3.4164
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.14
Std Dev 3.698
Gaus mean : 37.6181
Gaus Width: 3.6705
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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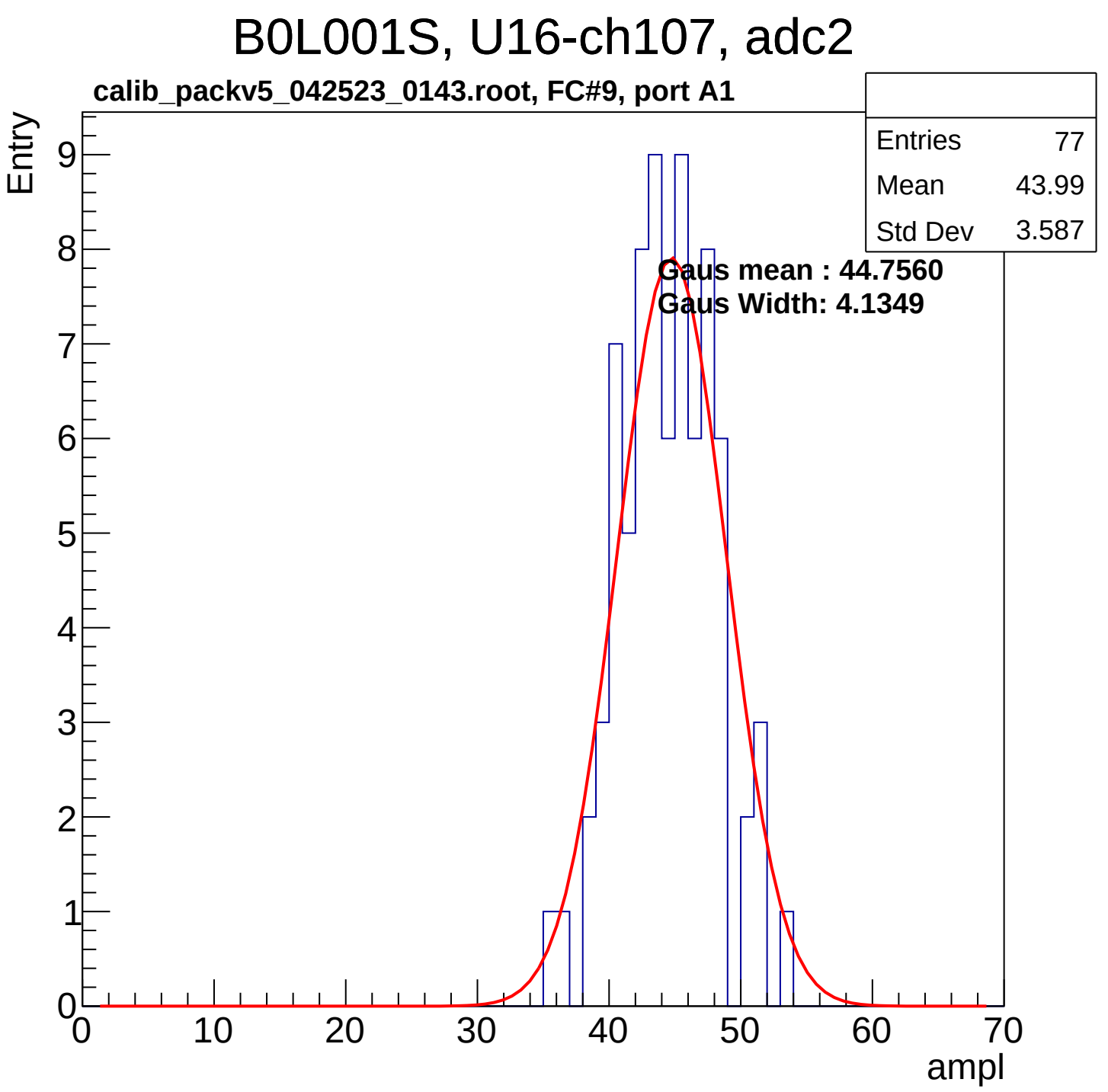
B0L001S, U16-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 43.99
Std Dev 3.587
Gaus mean : 44.7560
Gaus Width: 4.1349
Entry
ampl
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2
3
4
5
6
7
8
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0
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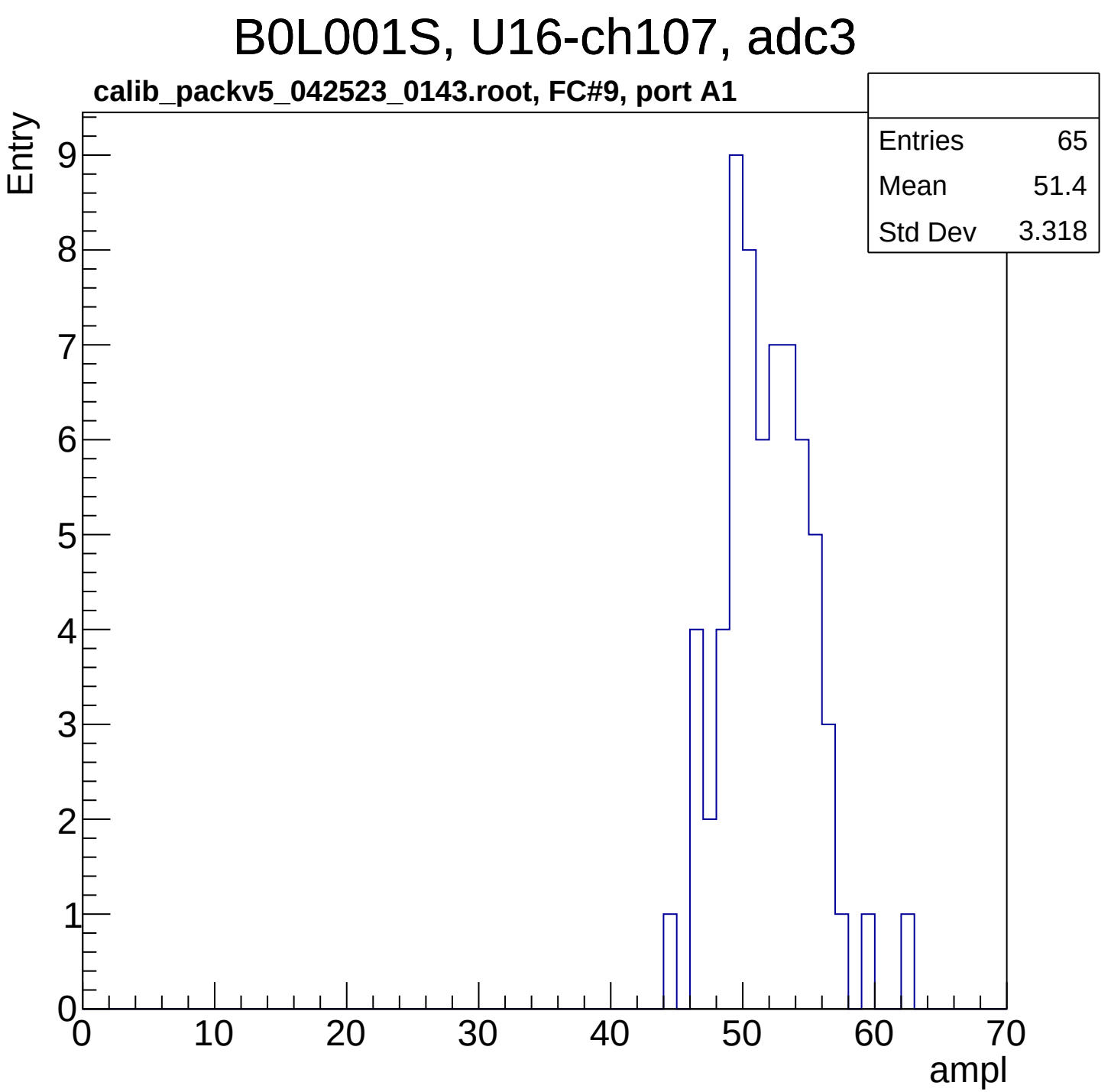
B0L001S, U16-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 51.4
Std Dev 3.318
Entry
ampl
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3
4
5
6
7
8
9
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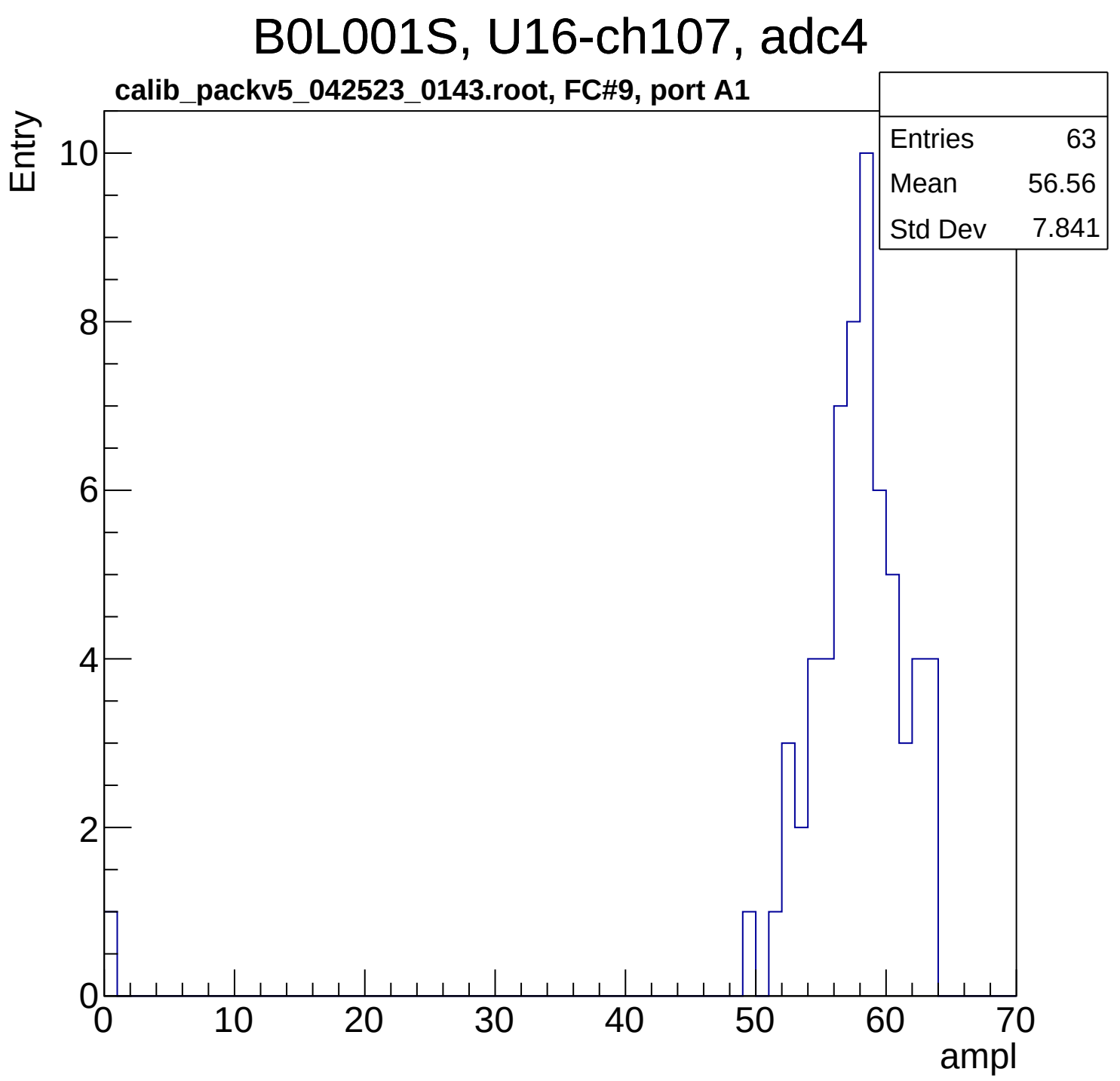
B0L001S, U16-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.56
Std Dev 7.841
Entry
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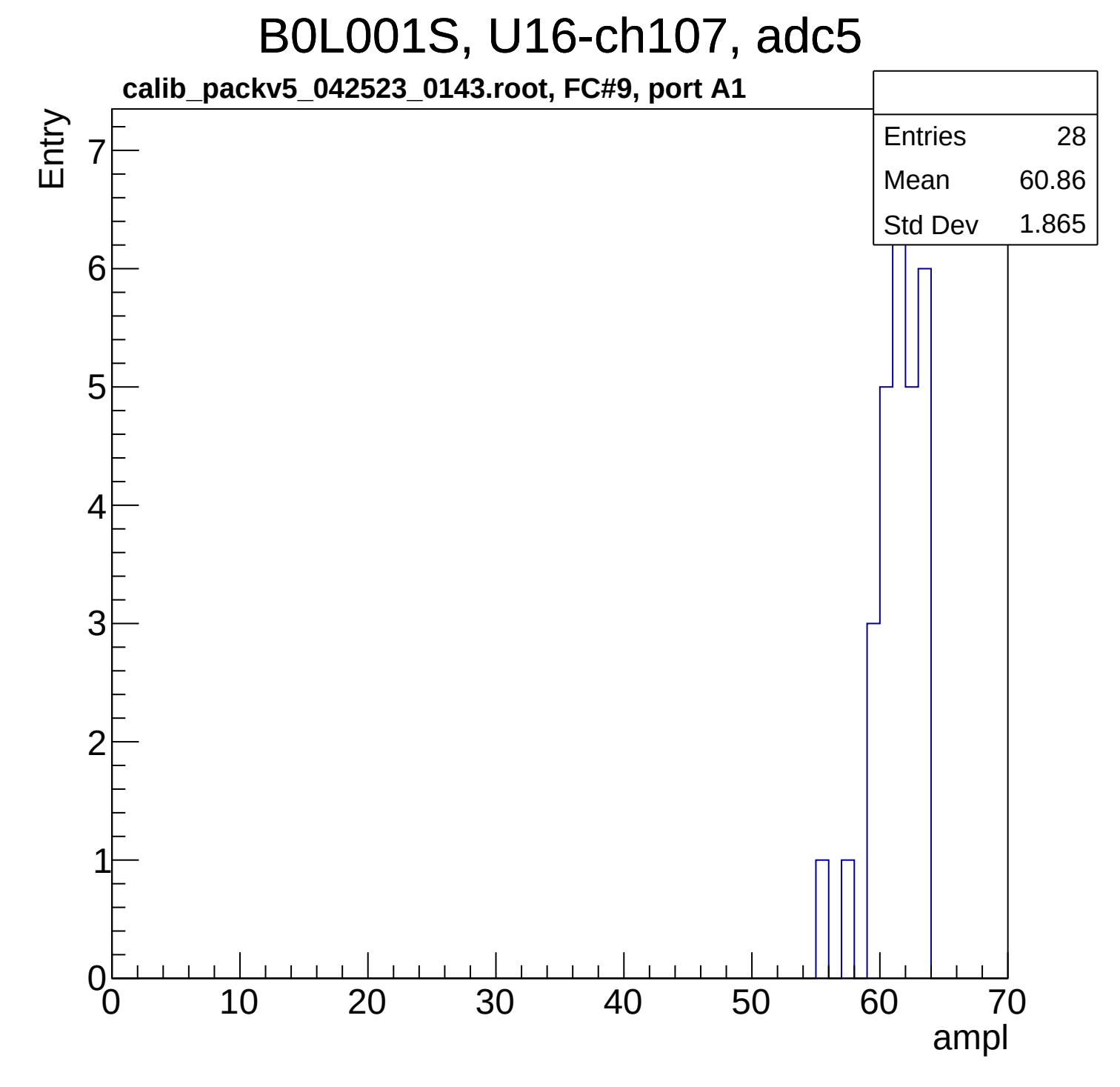
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.86
Std Dev 1.865
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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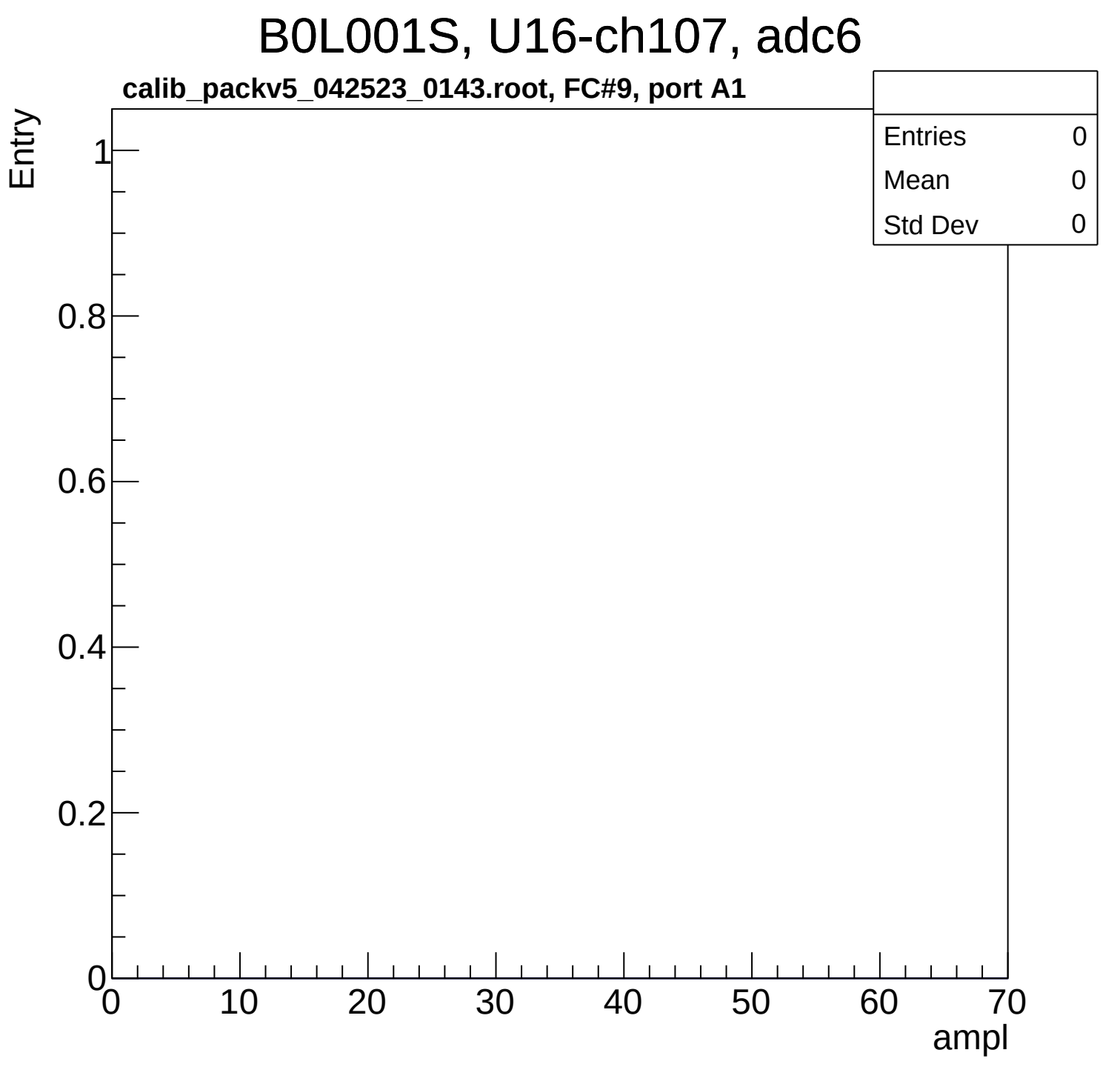
B0L001S, U16-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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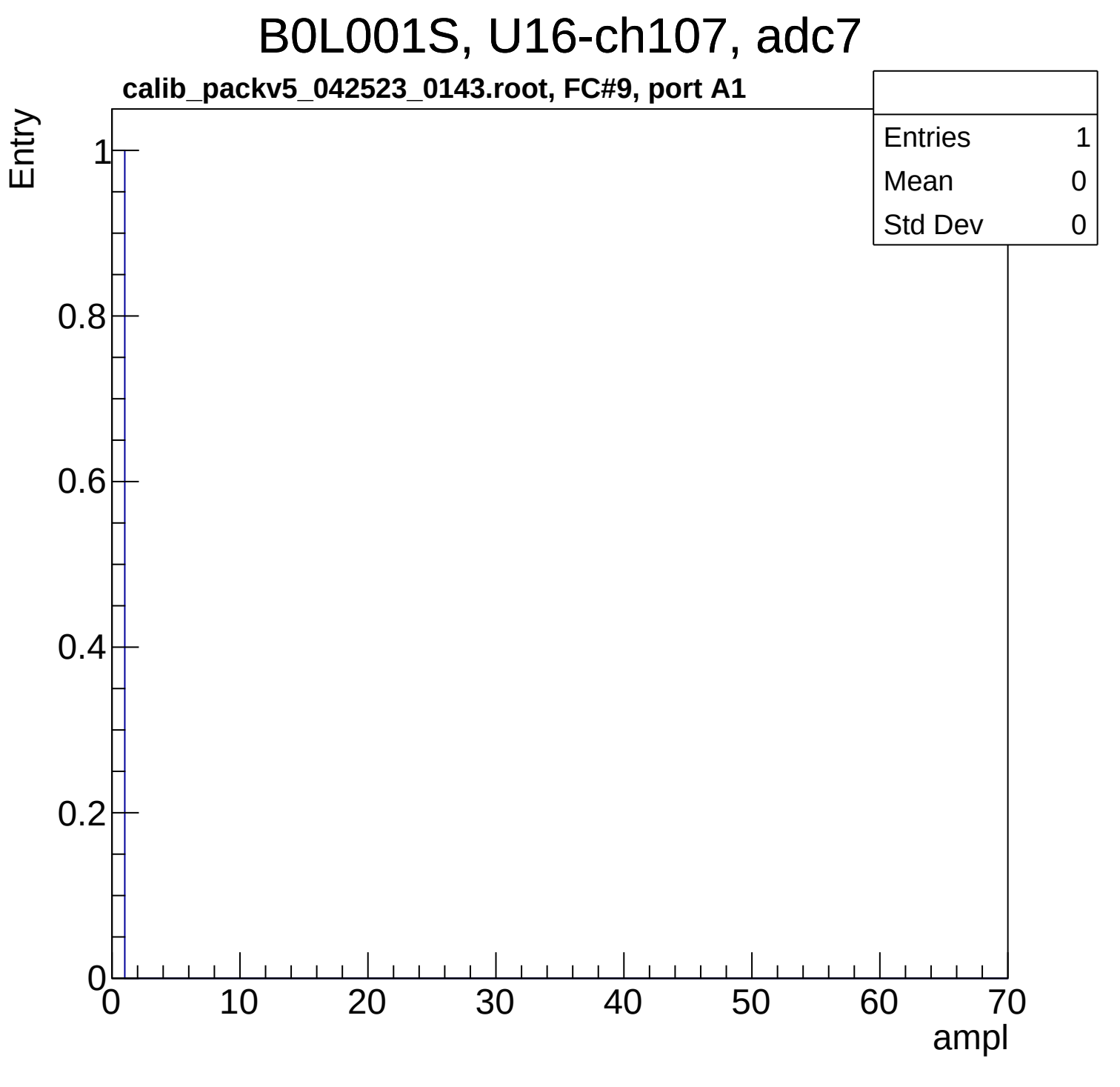
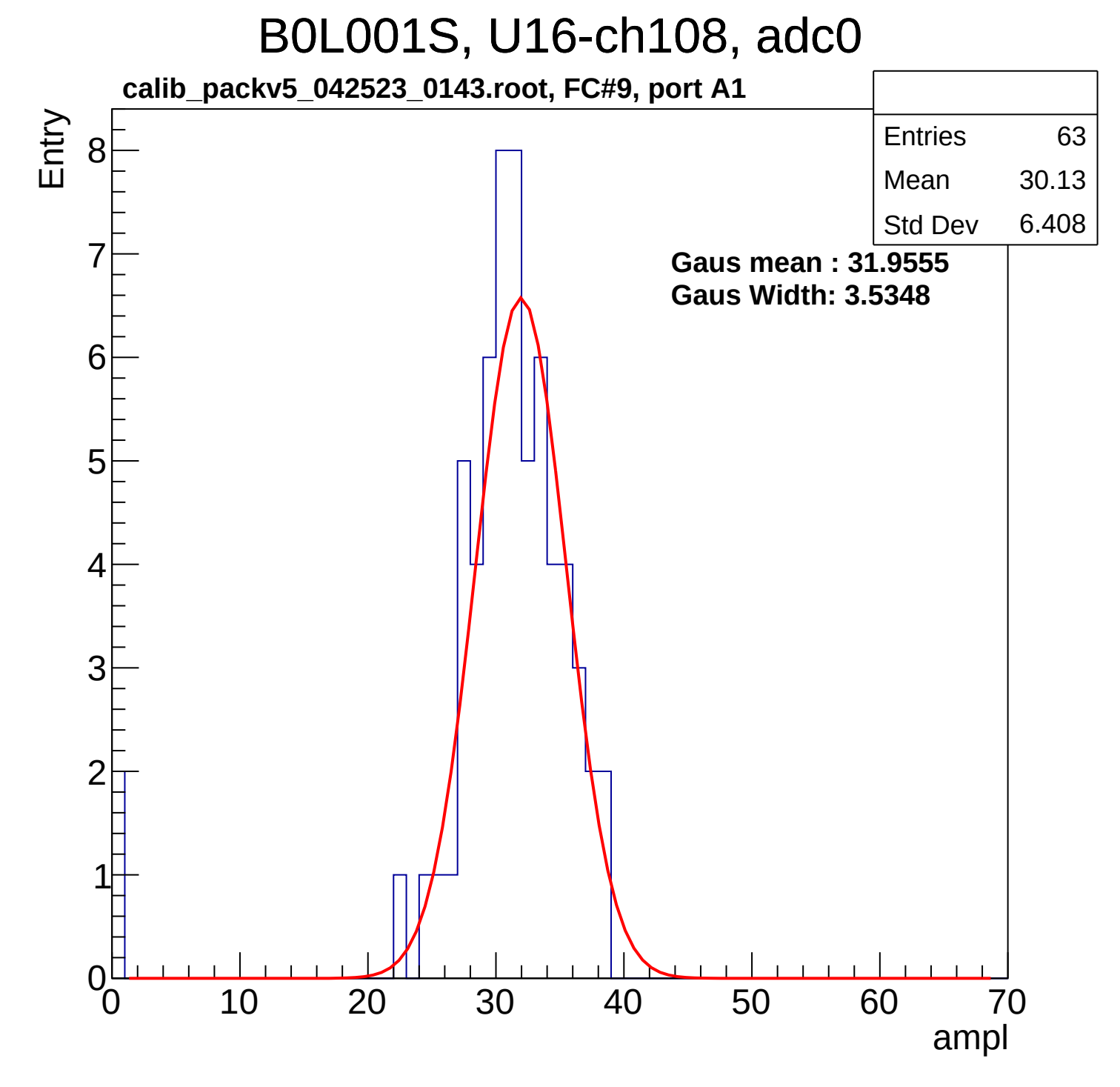
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.13
Std Dev 6.408
Gaus mean : 31.9555
Gaus Width: 3.5348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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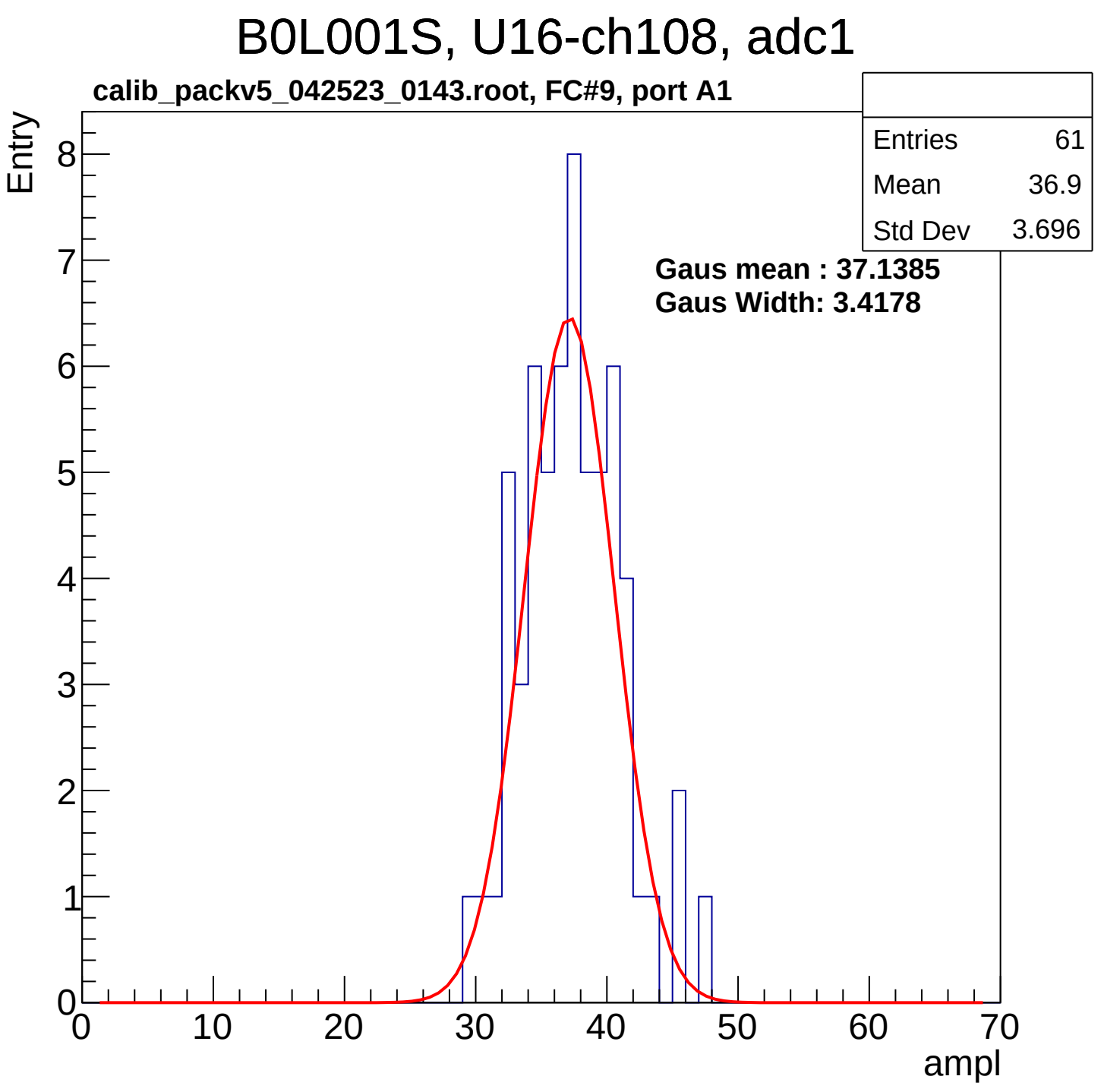
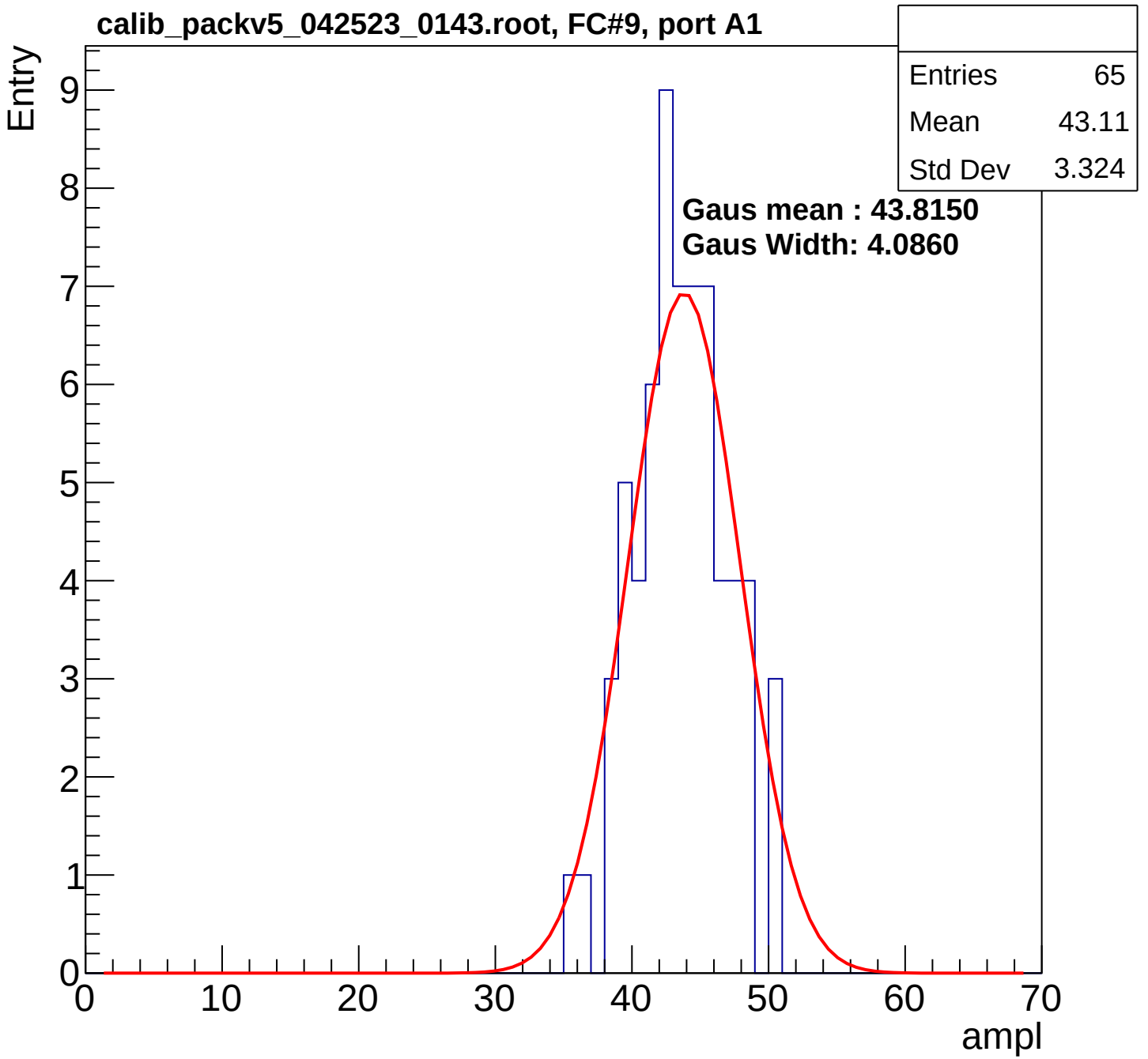

B0L001S, U16-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.9
Std Dev 3.696
Gaus mean : 37.1385
Gaus Width: 3.4178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.11
Std Dev 3.324
Gaus mean : 43.8150
Gaus Width: 4.0860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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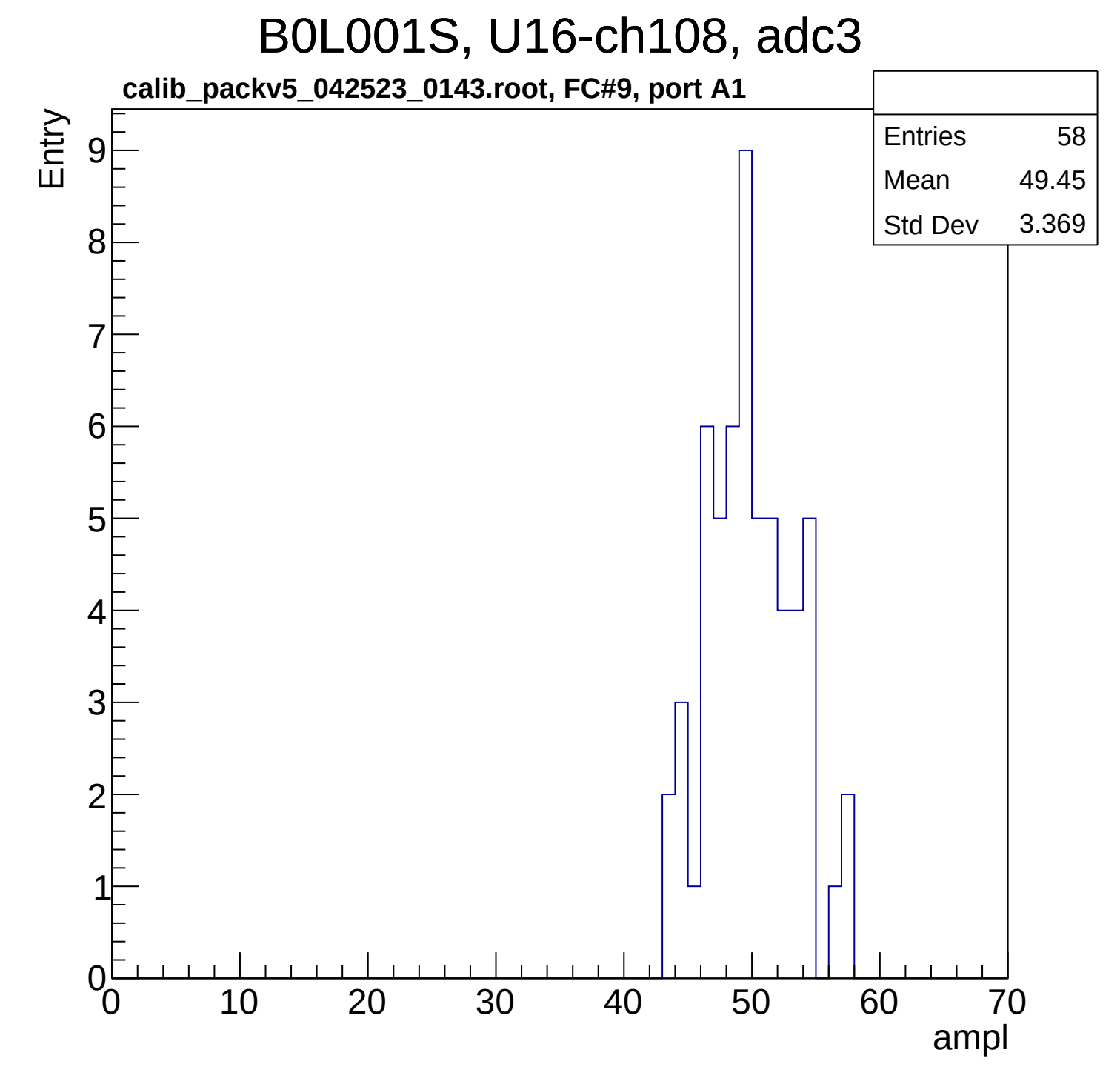
B0L001S, U16-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.45
Std Dev 3.369
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
50
60
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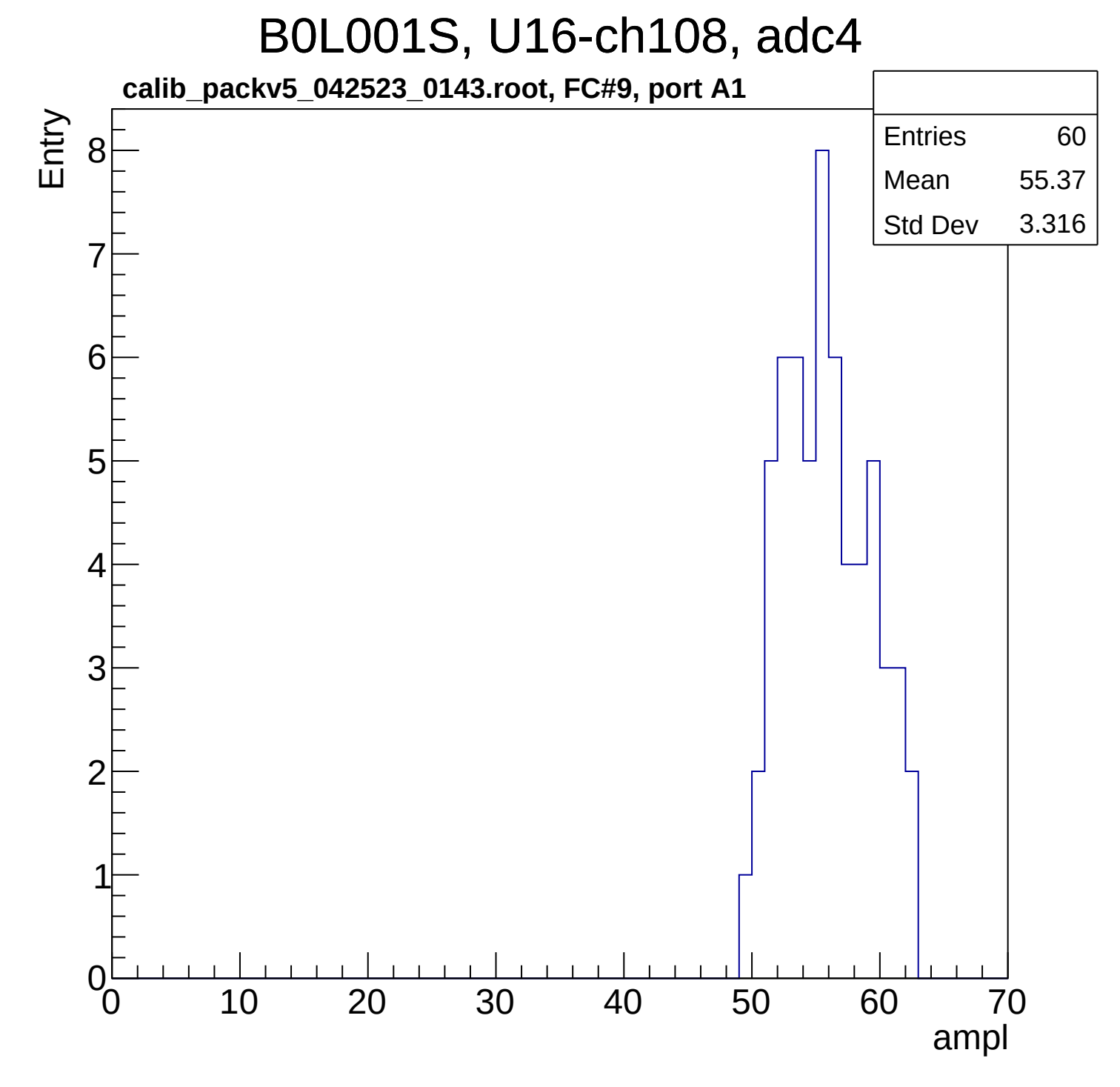
B0L001S, U16-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.37
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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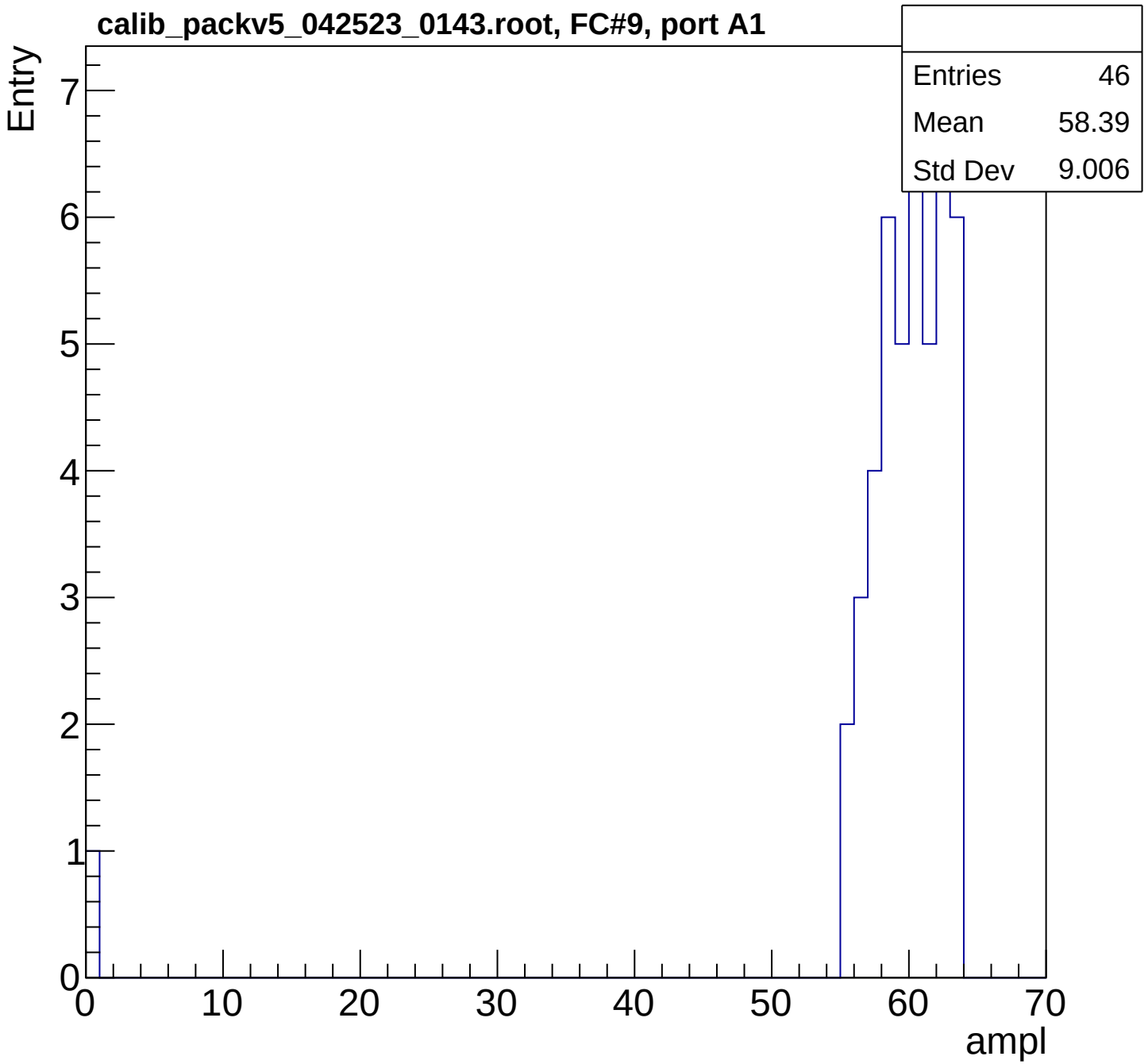
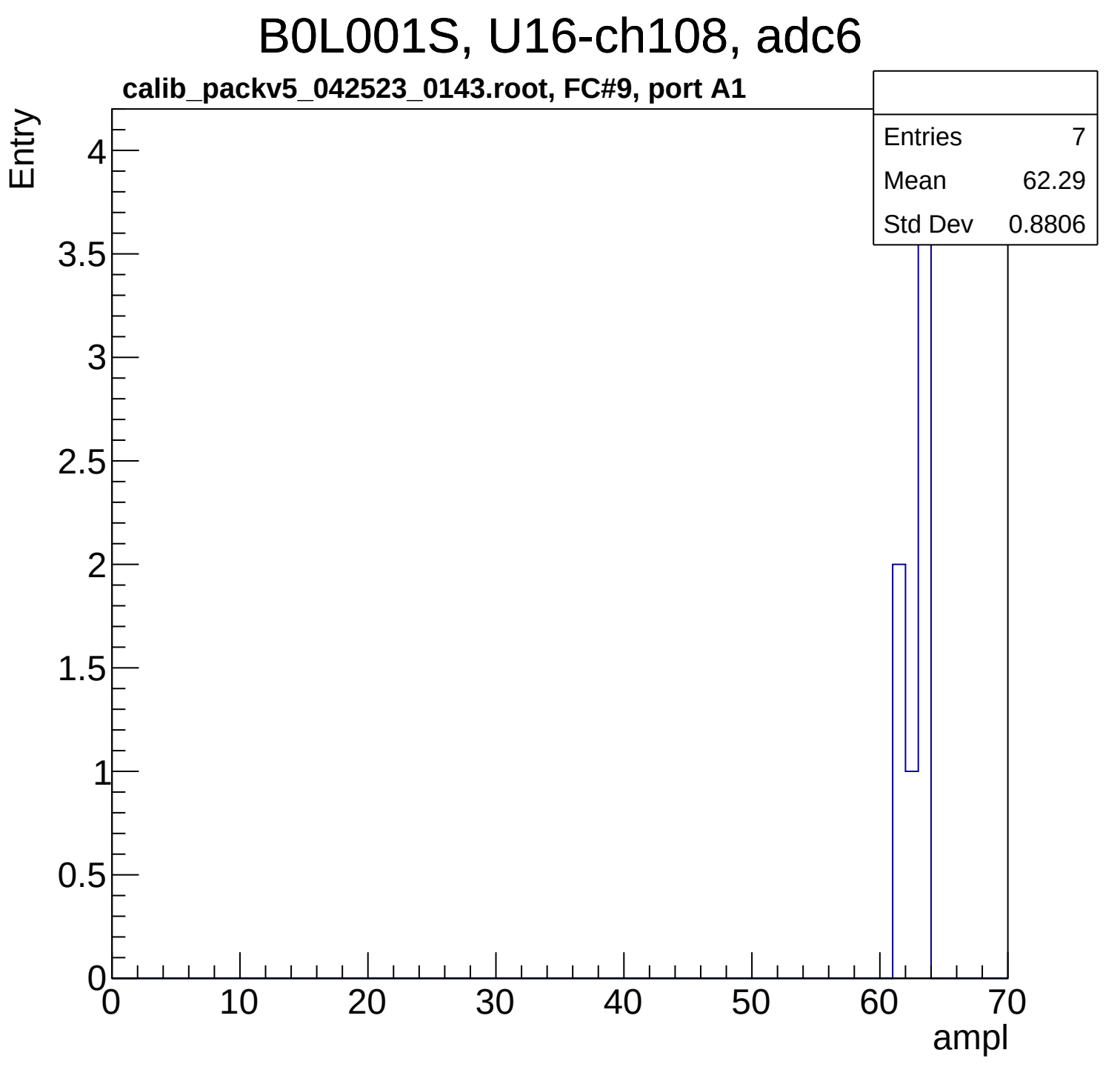
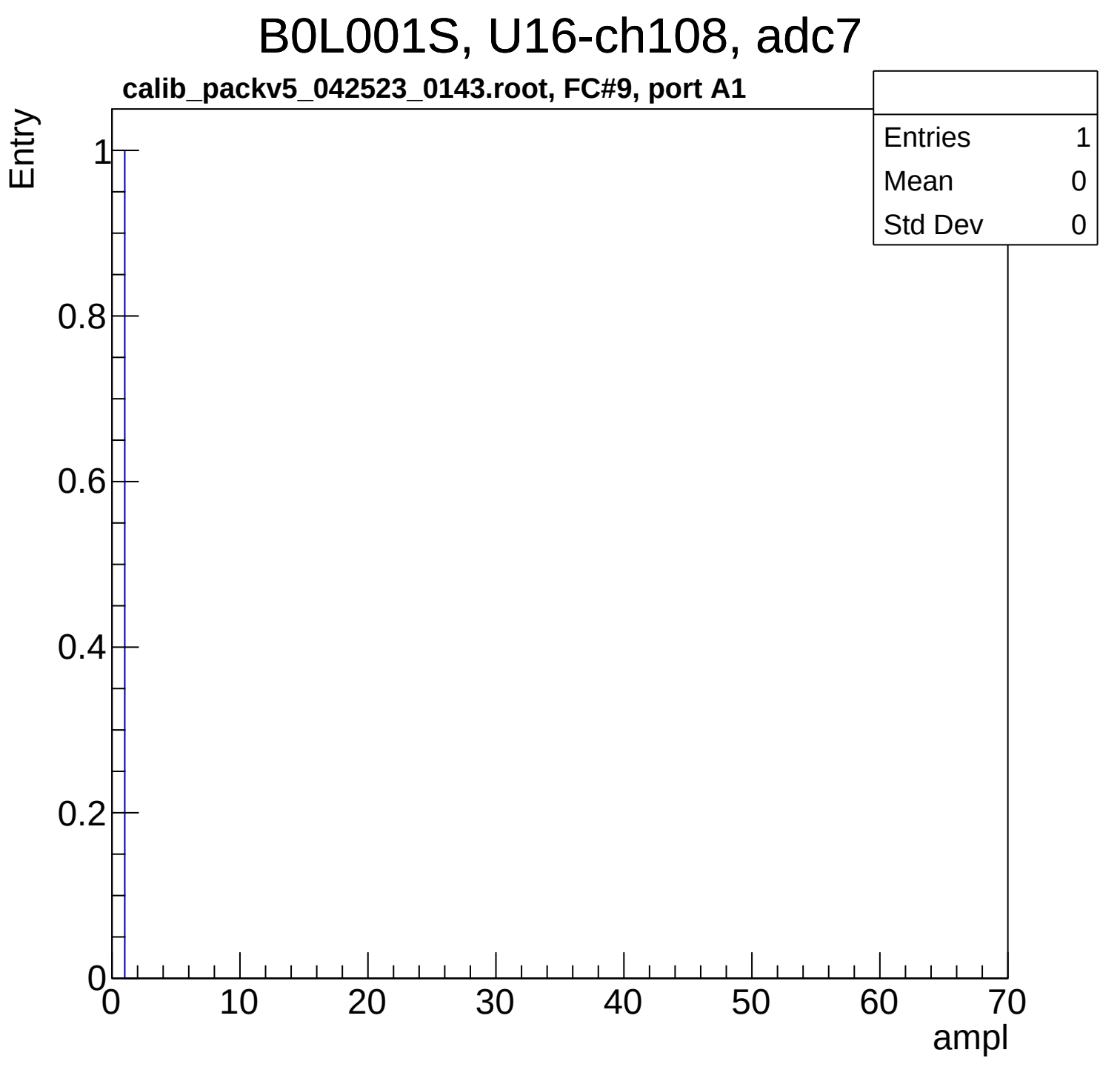
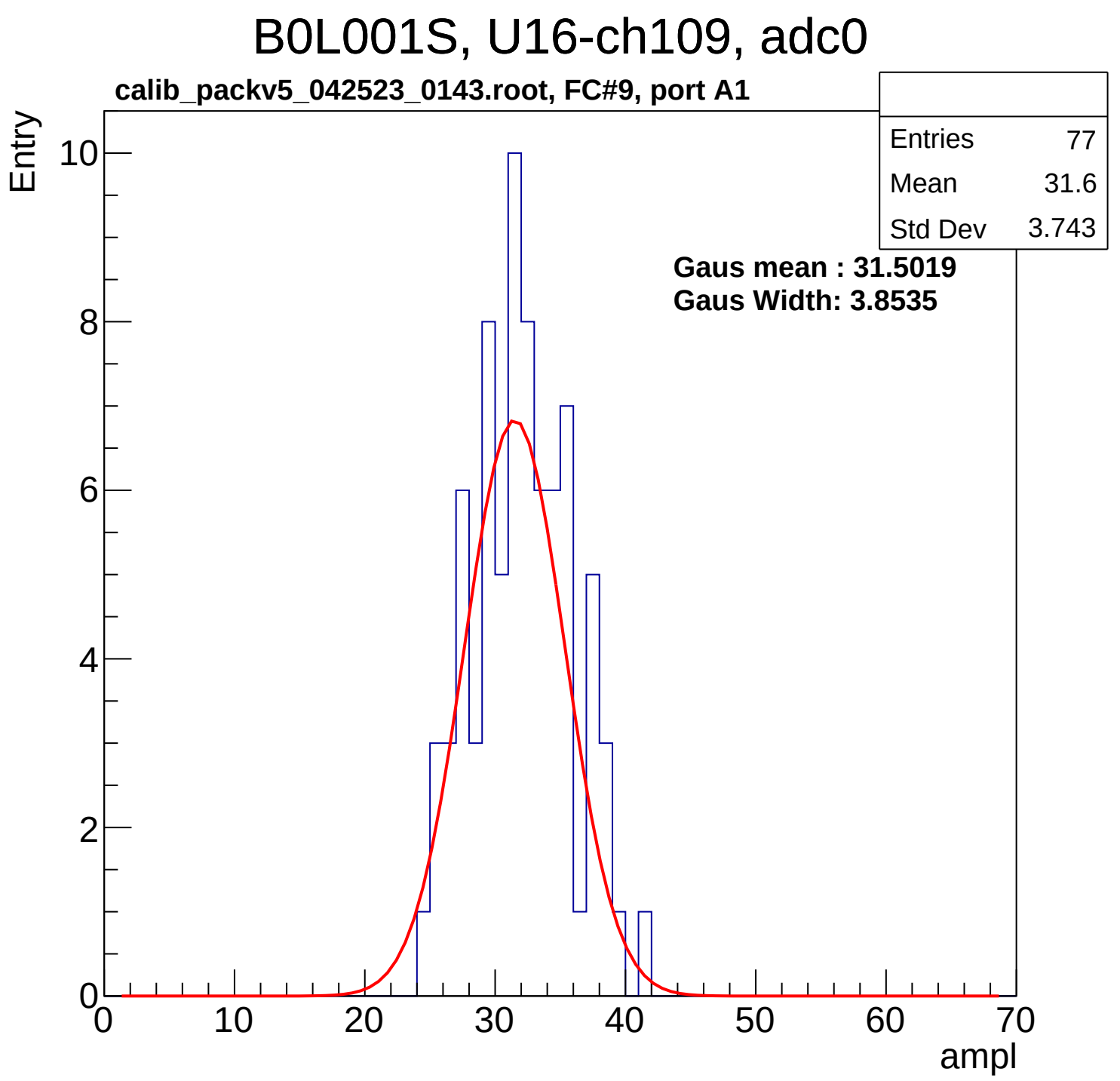
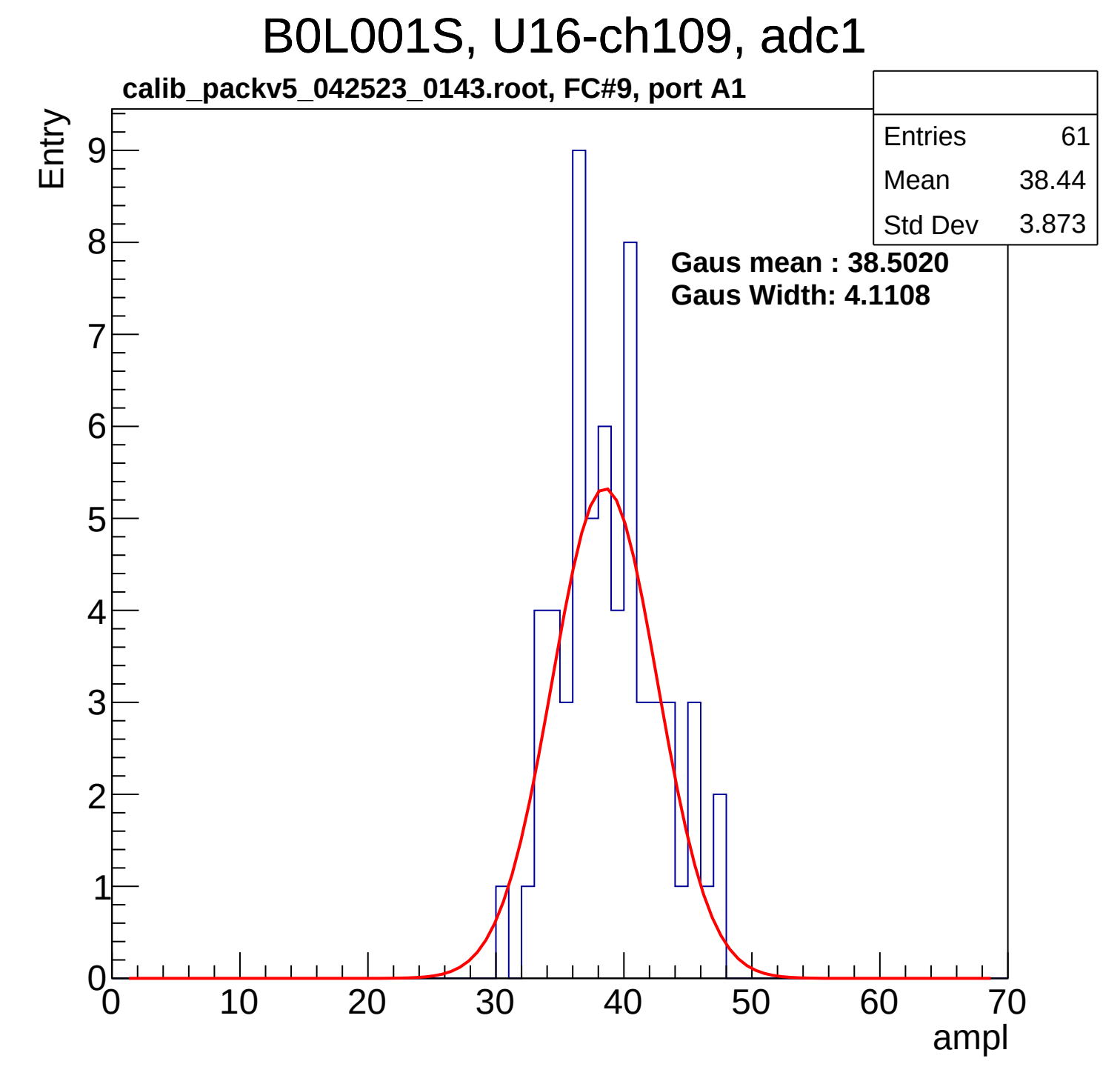
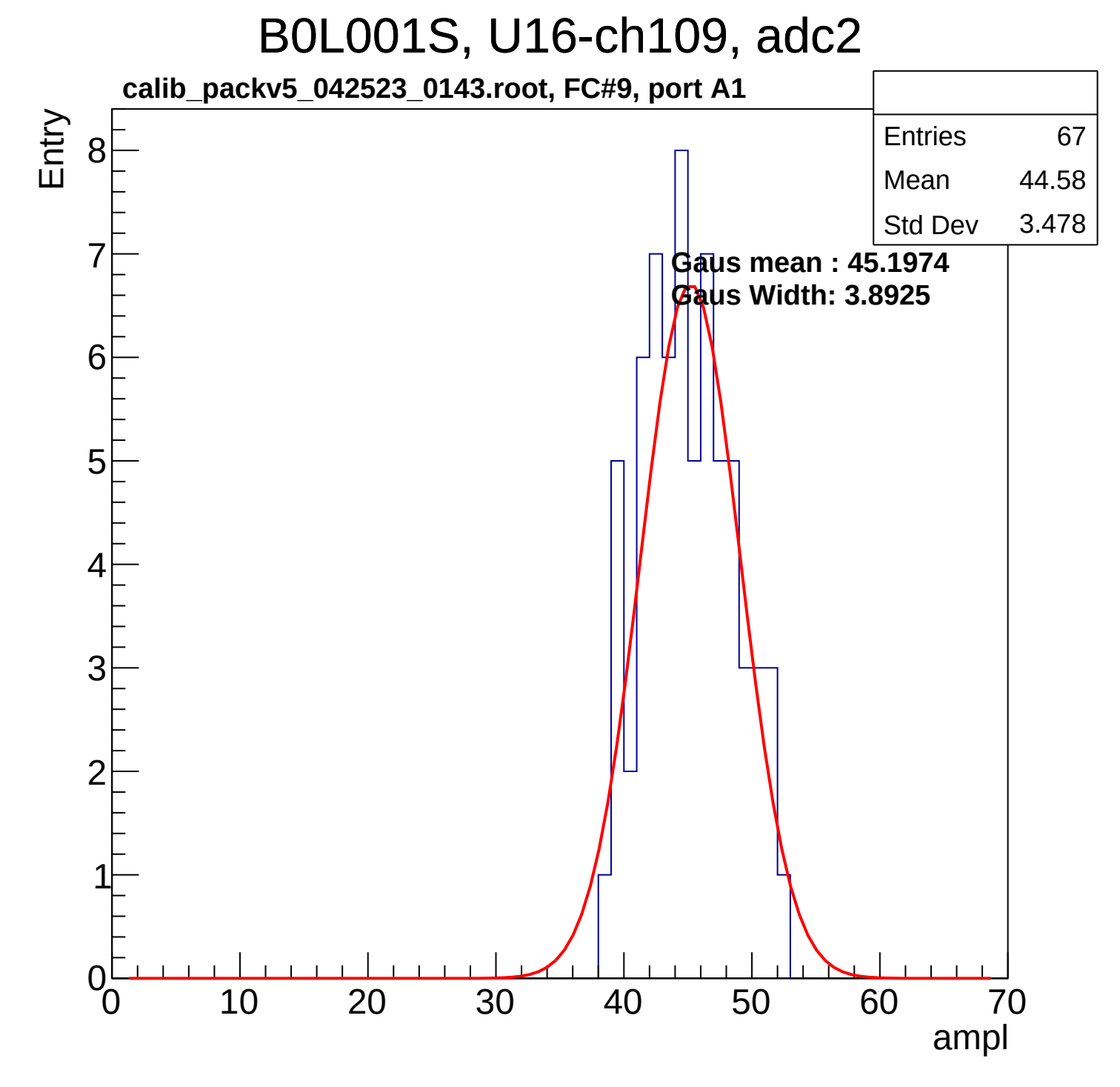

B0L001S, U16-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.39
Std Dev 9.006
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62.29
Std Dev 0.8806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.6
Std Dev 3.743
Gaus mean : 31.5019
Gaus Width: 3.8535
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 38.44
Std Dev 3.873
Gaus mean : 38.5020
Gaus Width: 4.1108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.58
Std Dev 3.478
Gaus mean : 45.1974
Gaus Width: 3.8925
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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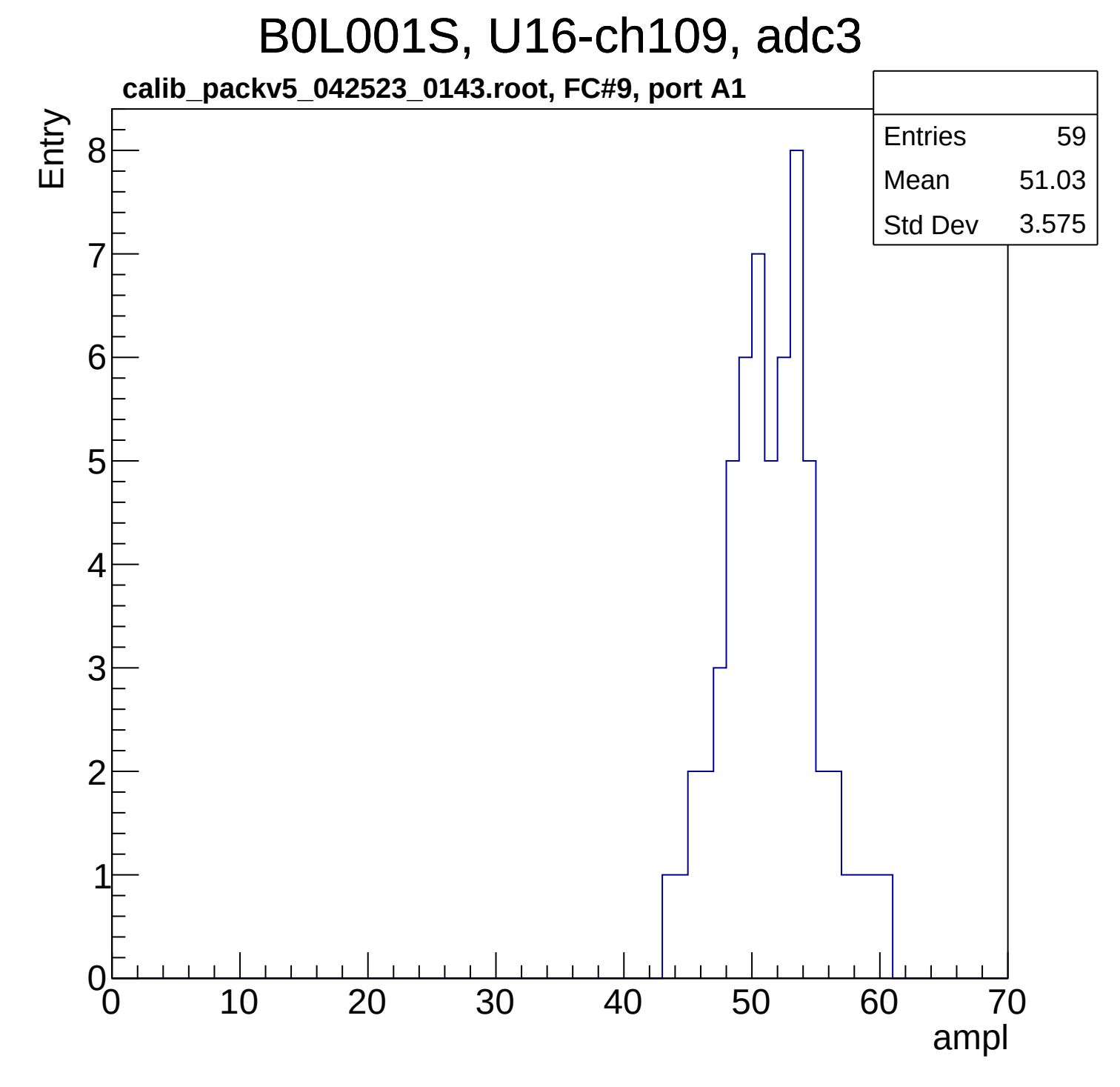
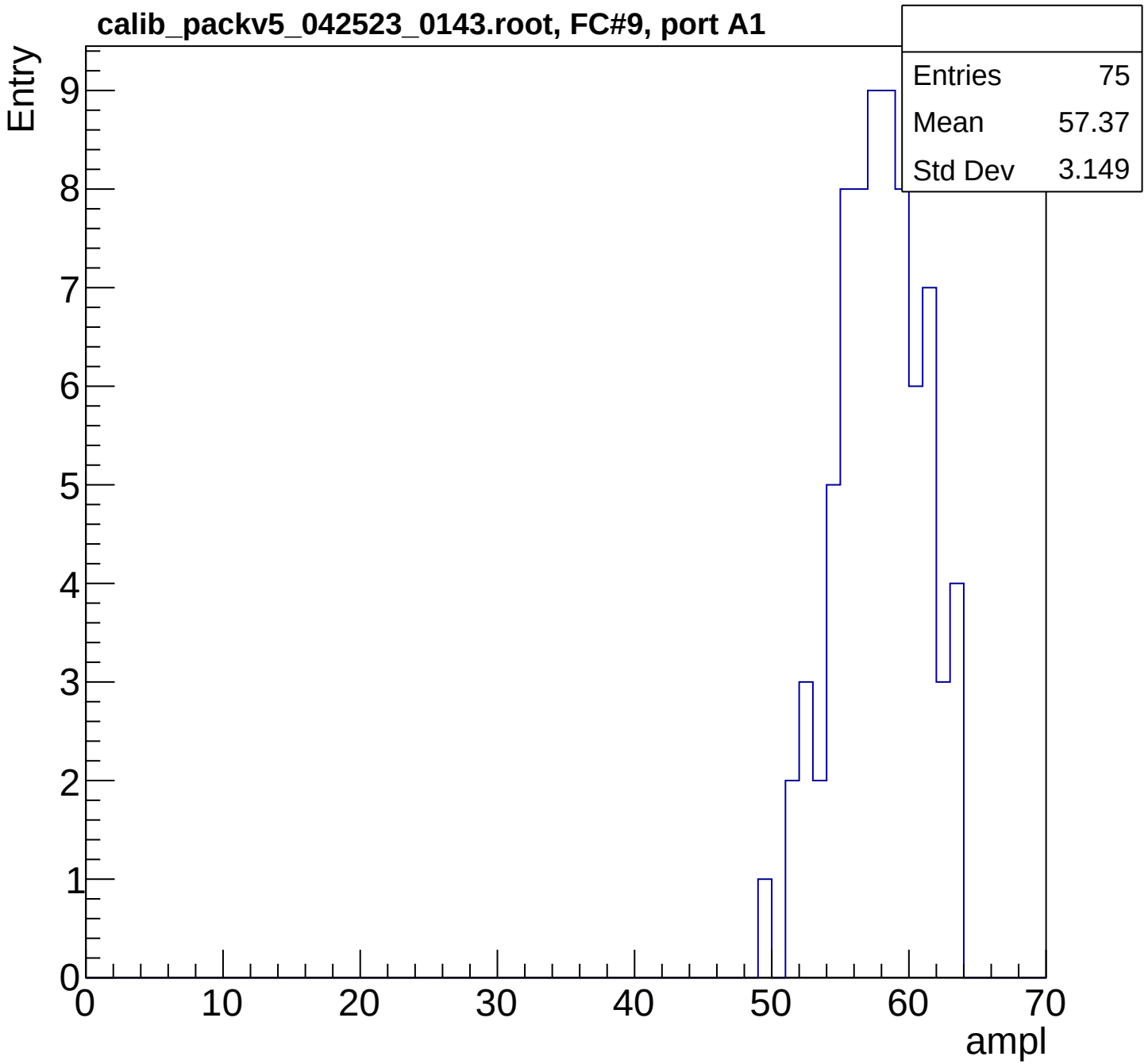
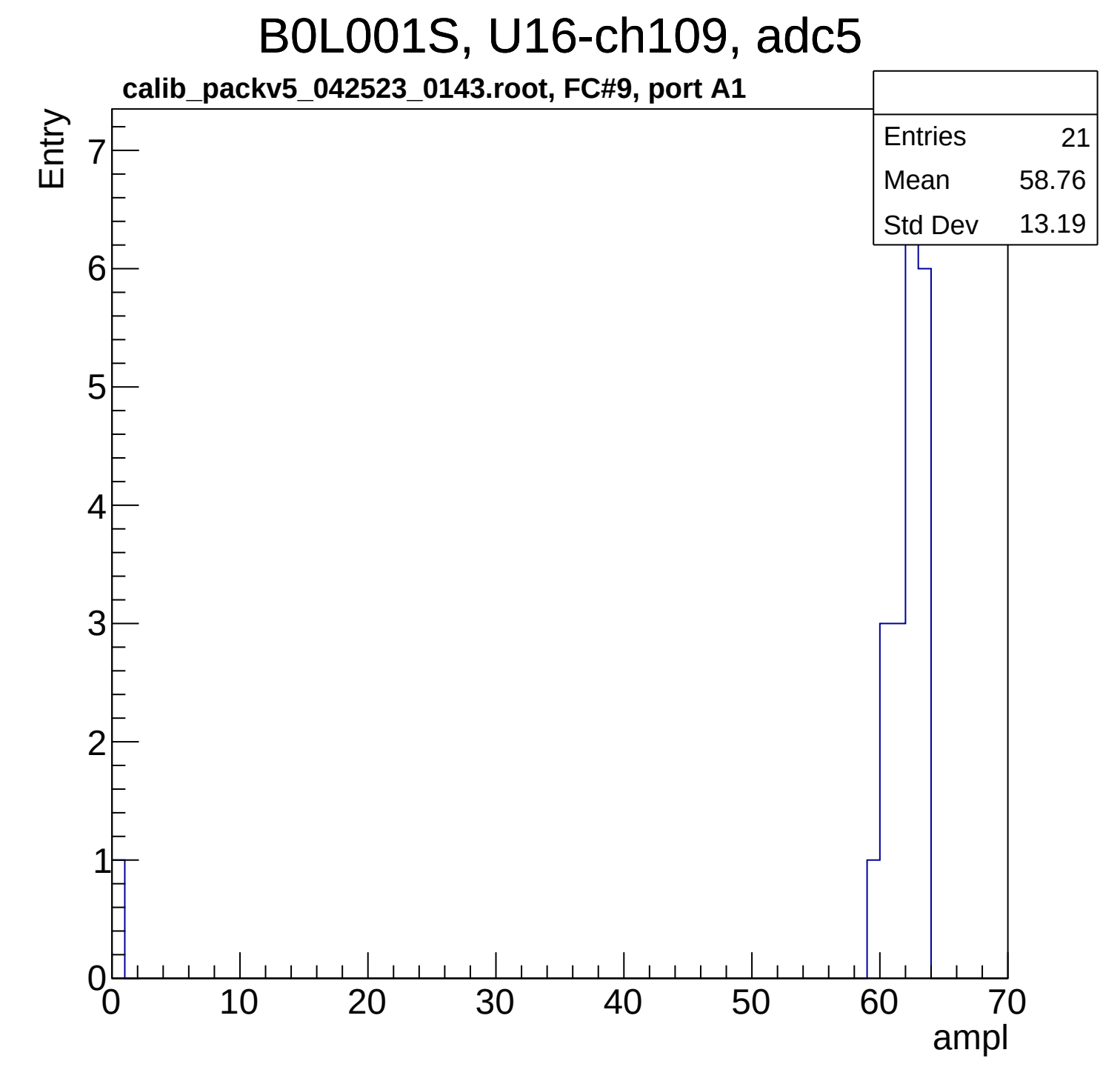
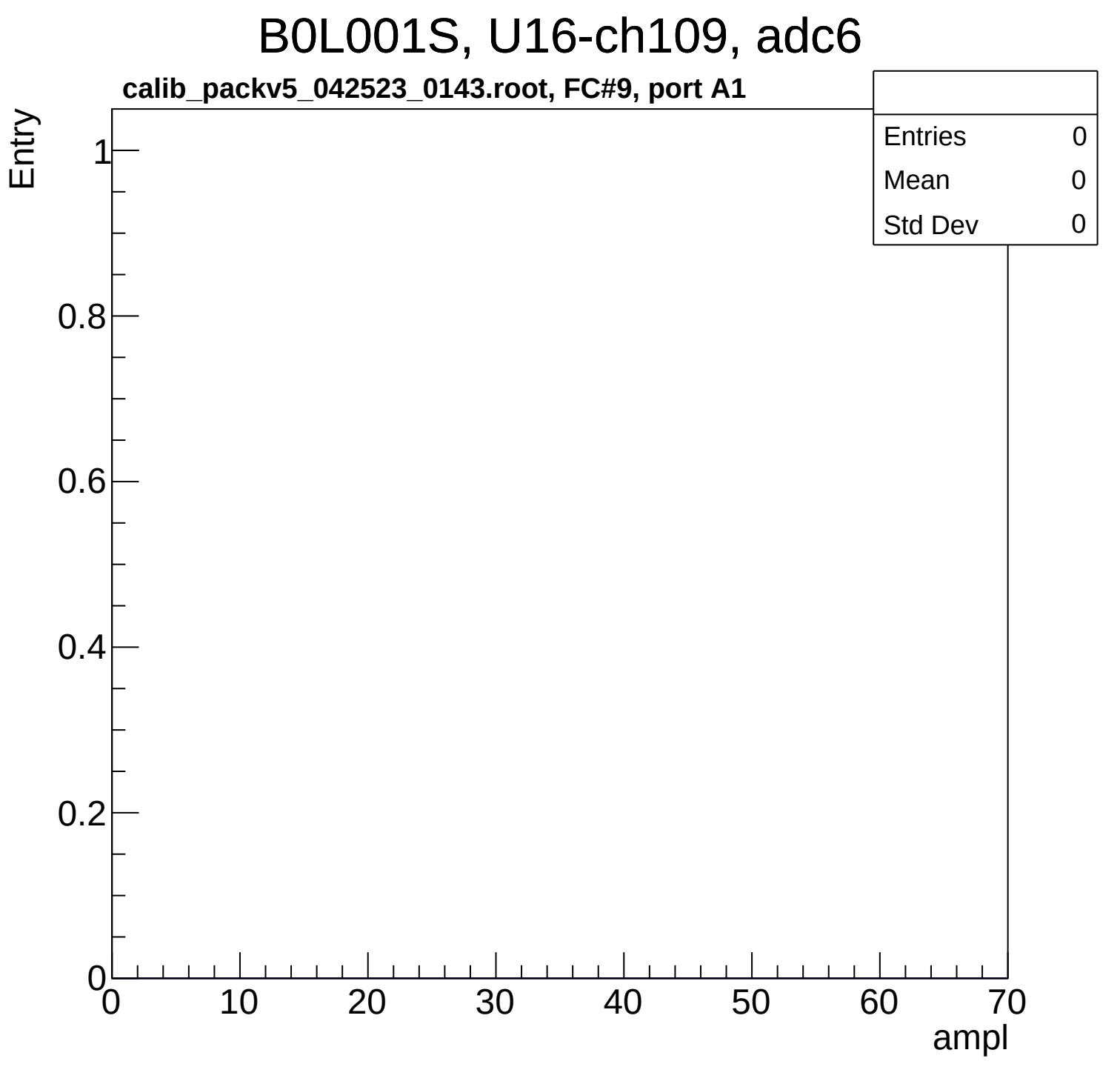
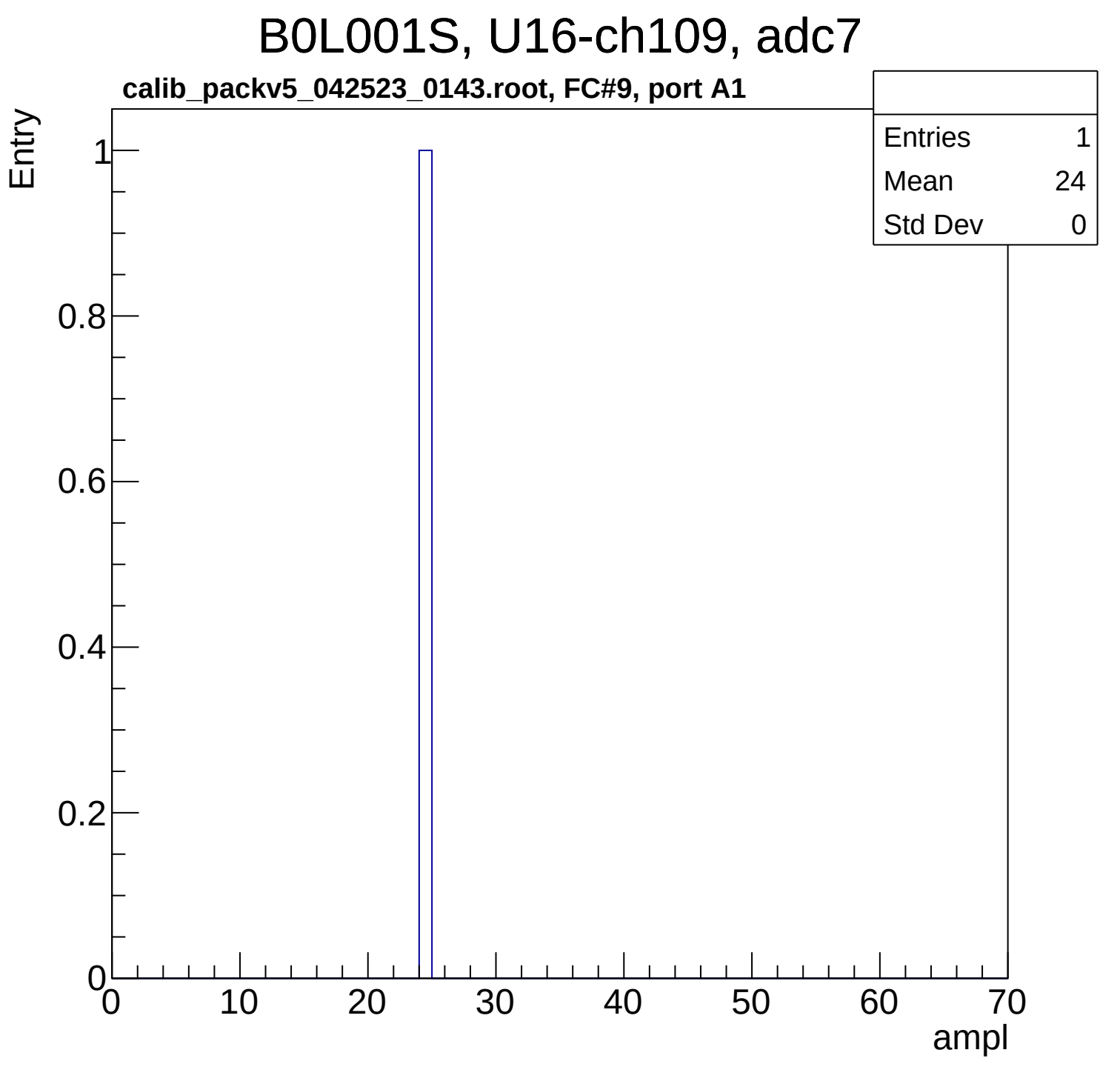

B0L001S, U16-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.03
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 57.37
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.76
Std Dev 13.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
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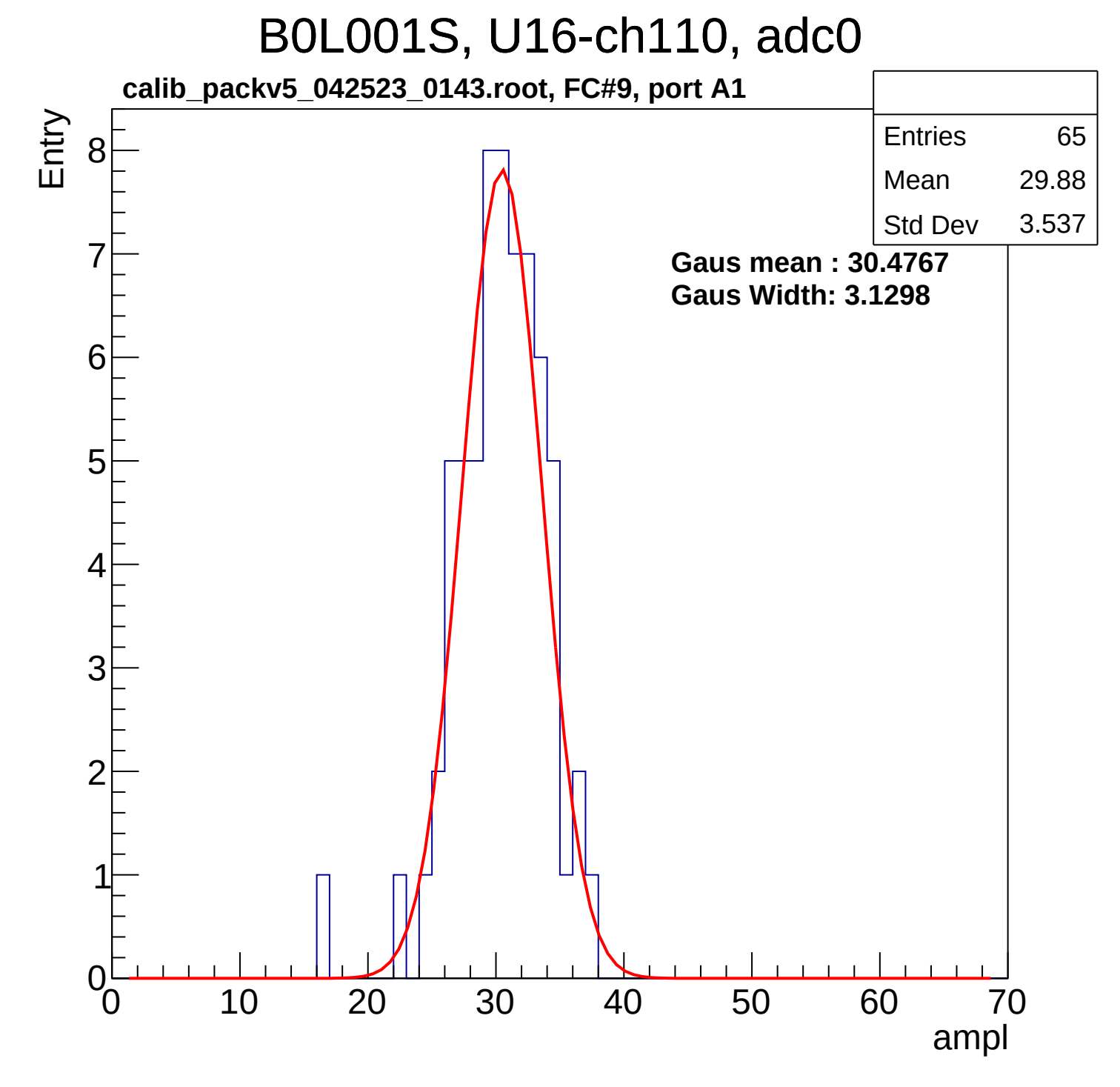
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.88
Std Dev 3.537
Gaus mean : 30.4767
Gaus Width: 3.1298
Entry
ampl
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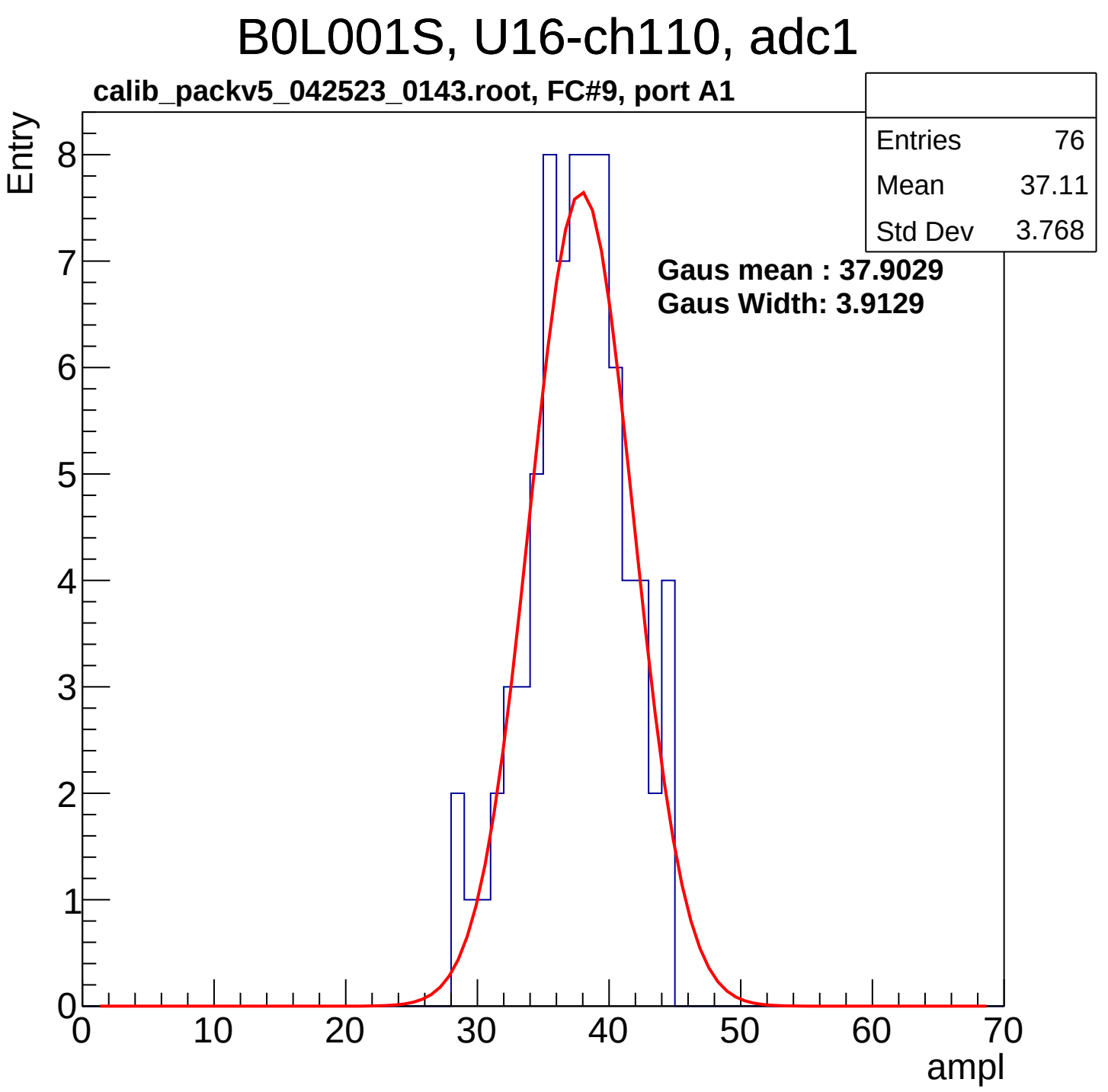
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.11
Std Dev 3.768
Gaus mean : 37.9029
Gaus Width: 3.9129
Entry
ampl
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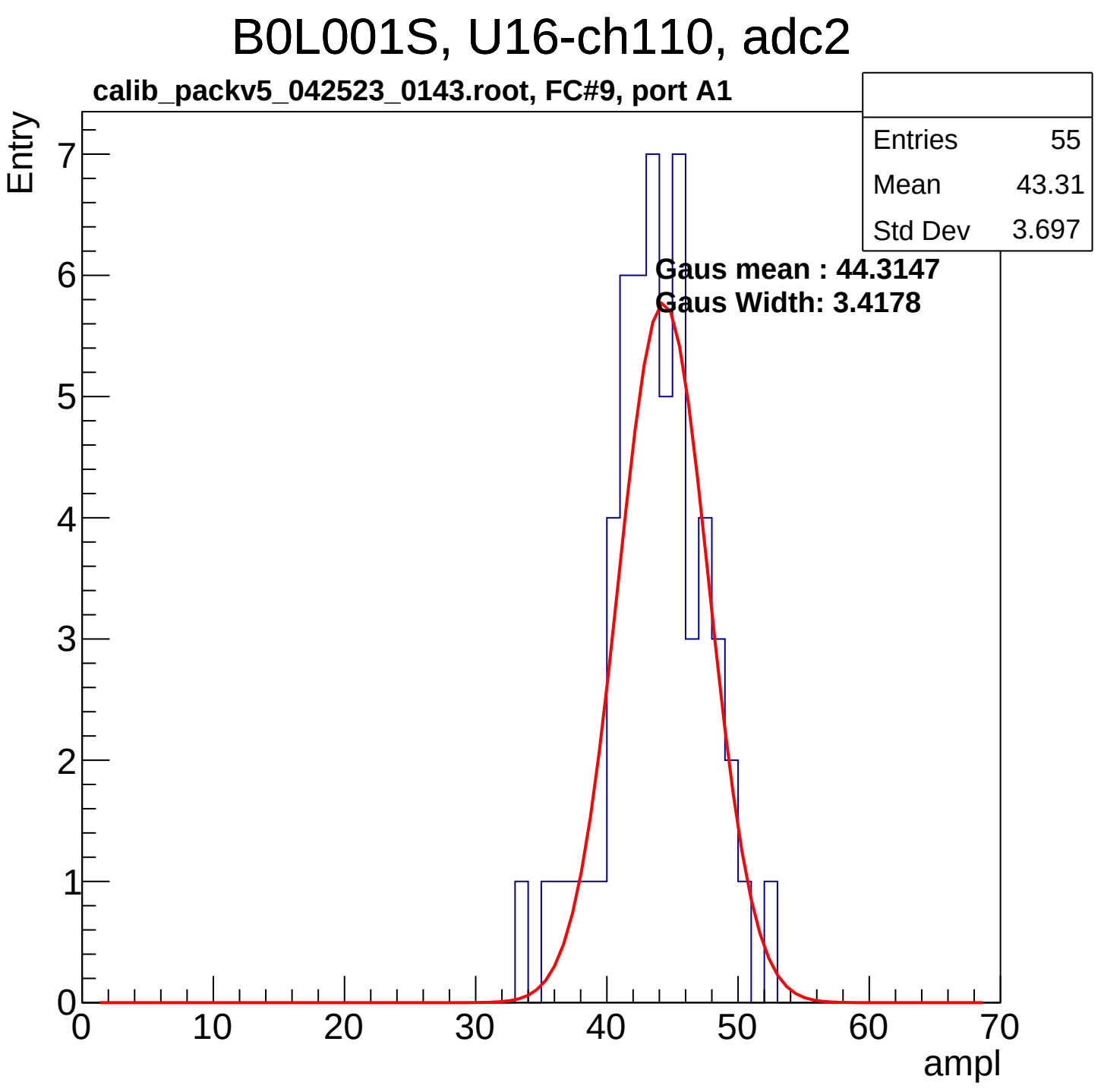
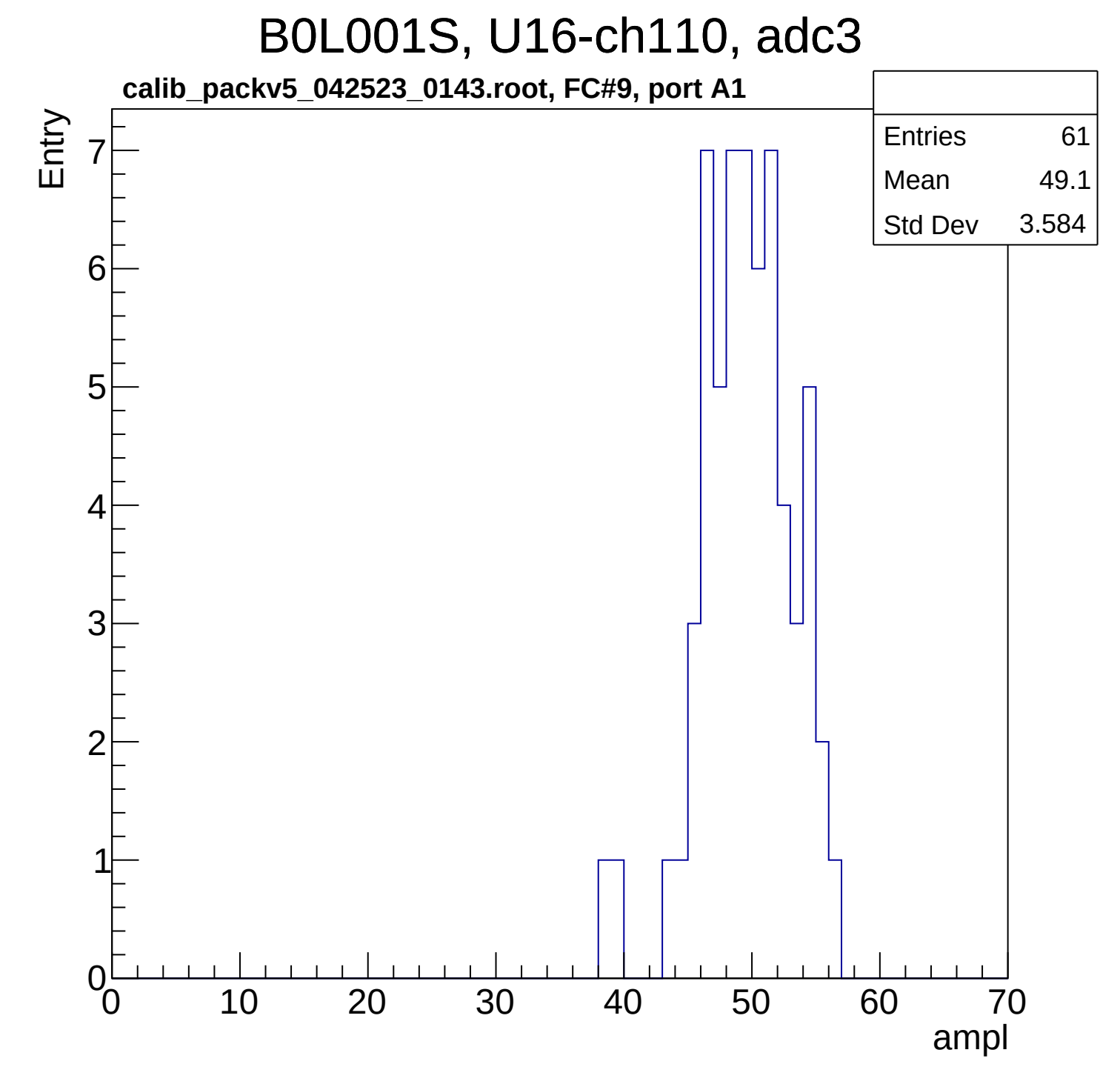
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 43.31
Std Dev 3.697
Gaus mean : 44.3147
Gaus Width: 3.4178
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.1
Std Dev 3.584
Entry
ampl
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1
2
3
4
5
6
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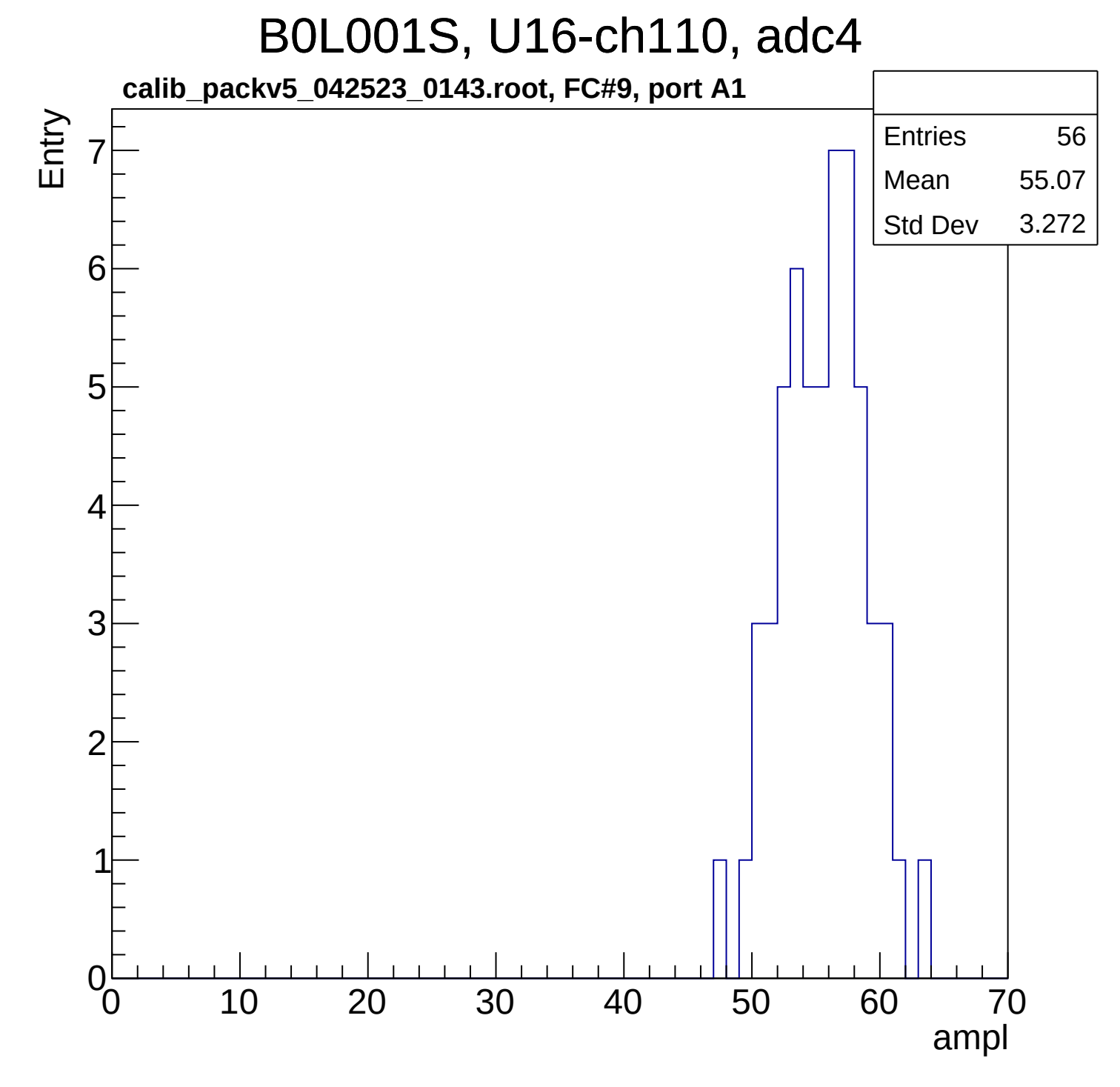
0
10
20
30
40
50
60
70


B0L001S, U16-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.07
Std Dev 3.272
Entry
ampl
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3
4
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6
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0
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40
50
60
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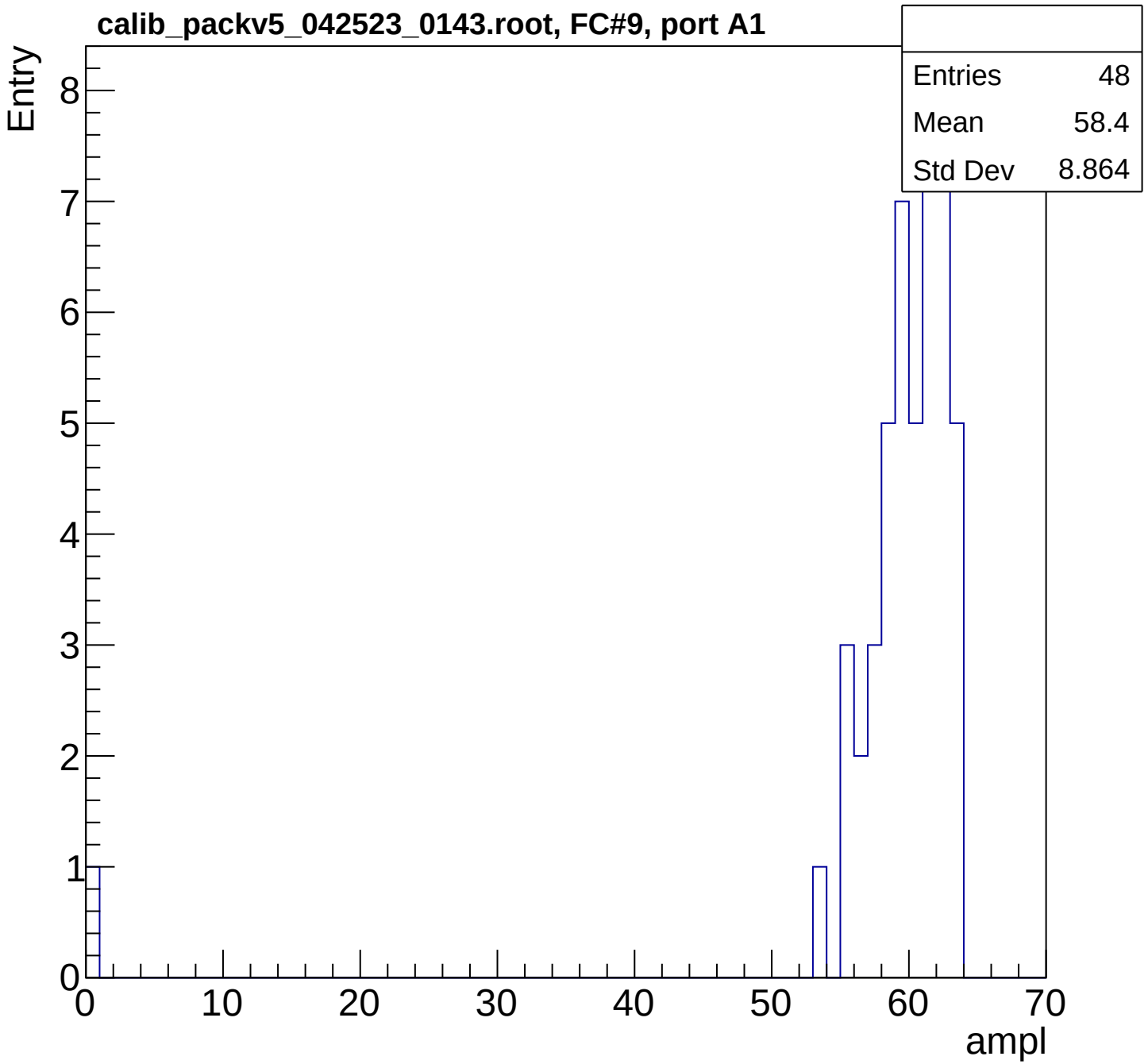
B0L001S, U16-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.4
Std Dev 8.864
Entry
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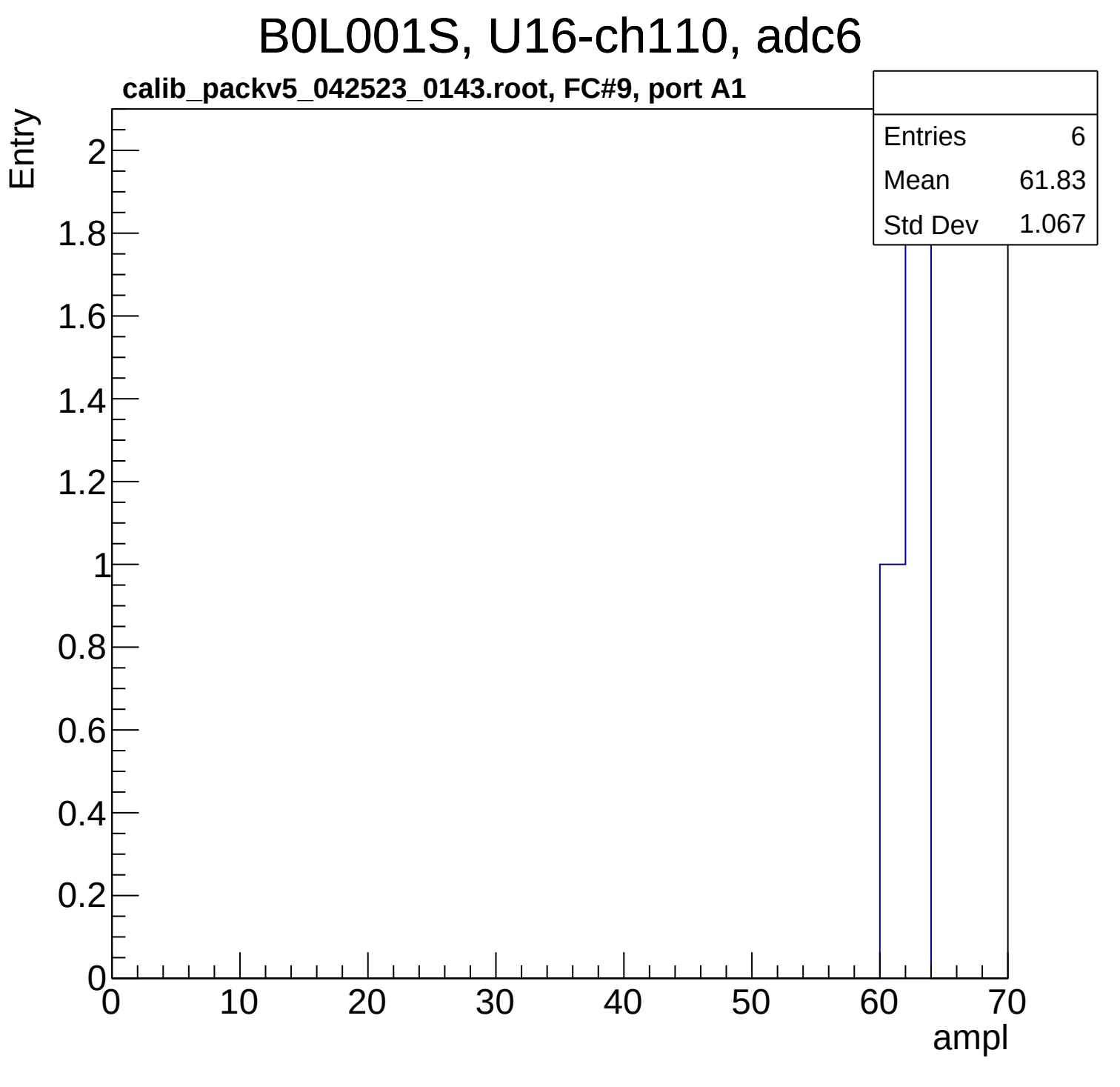
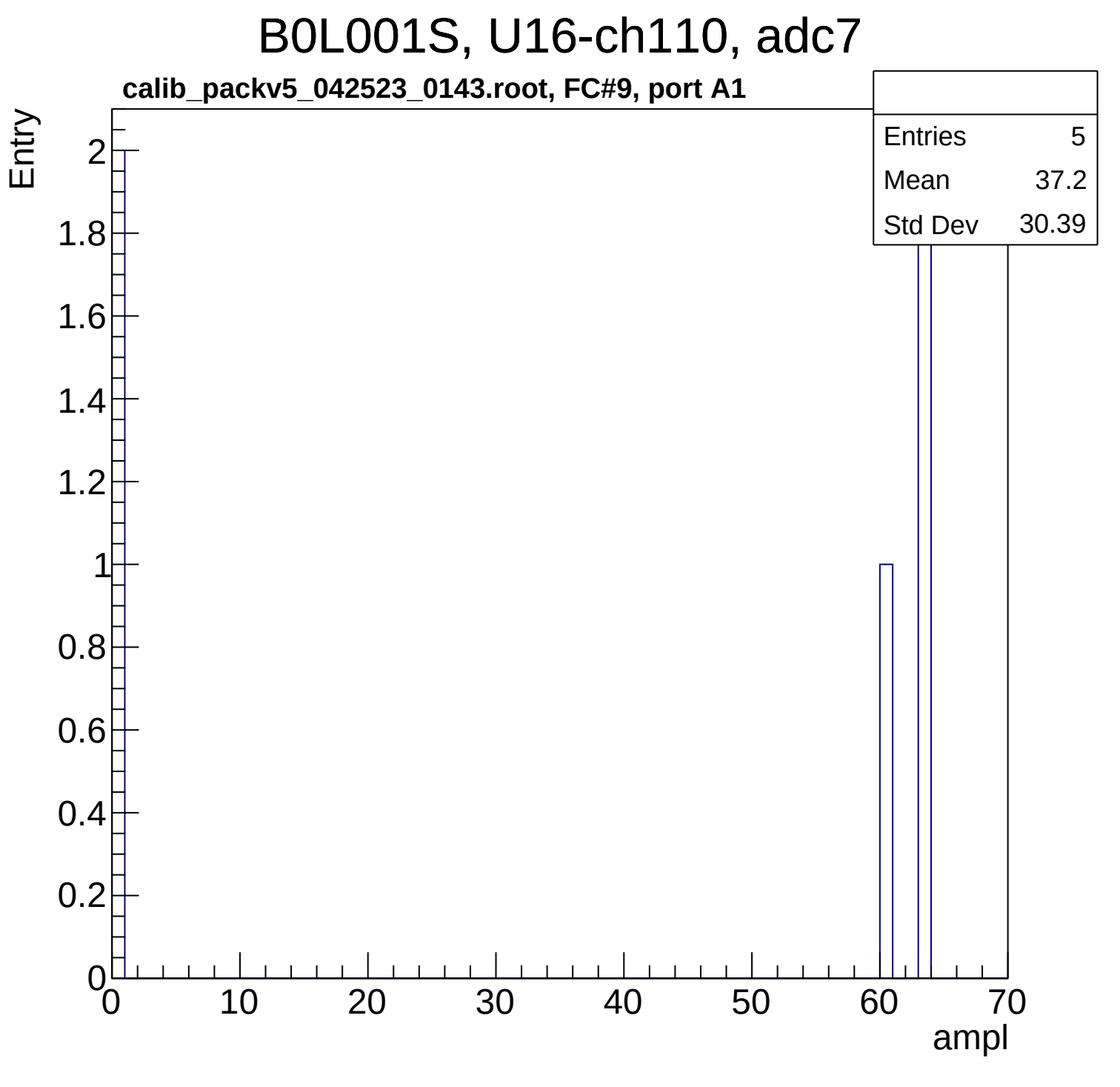
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 37.2
Std Dev 30.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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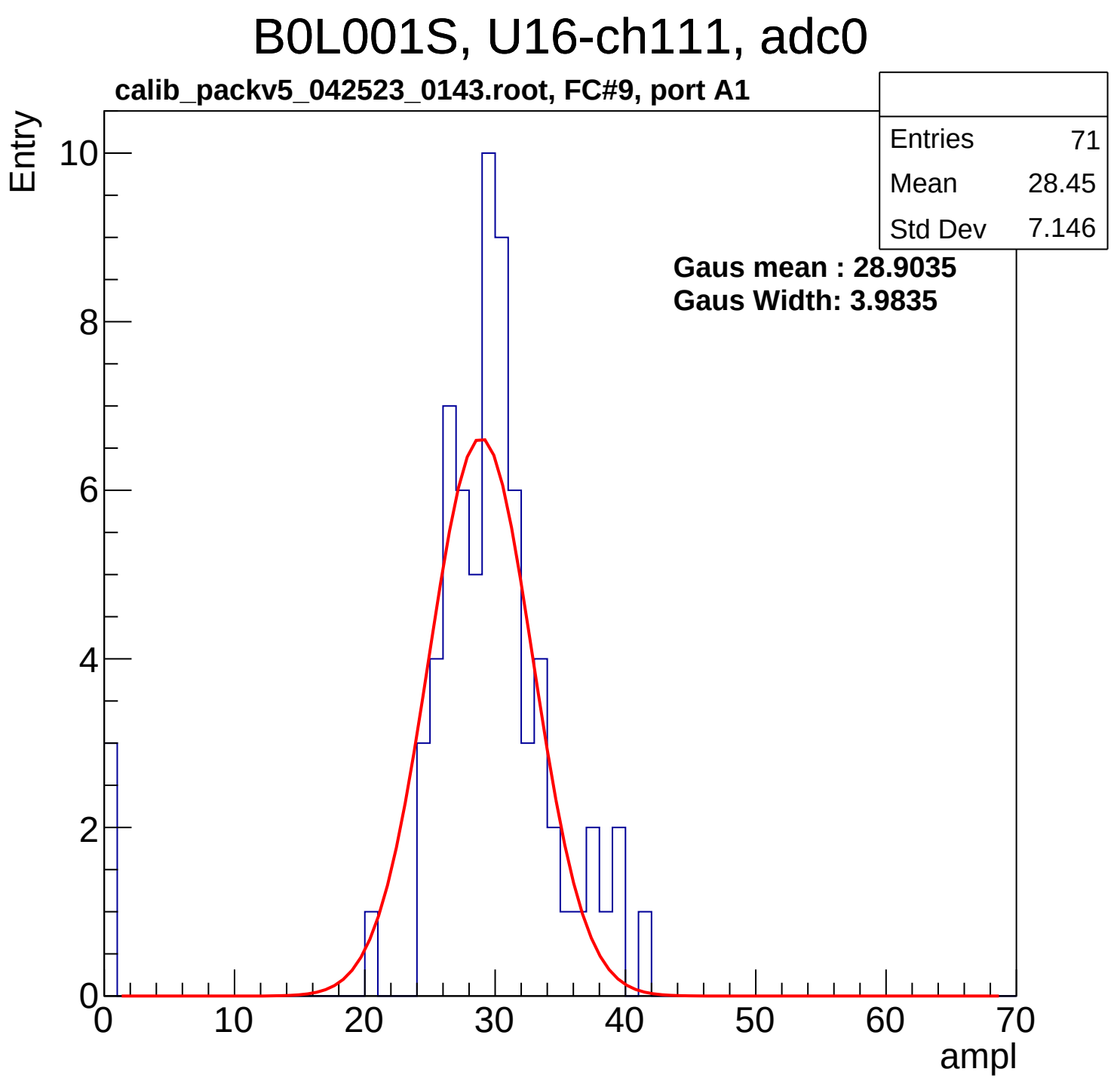
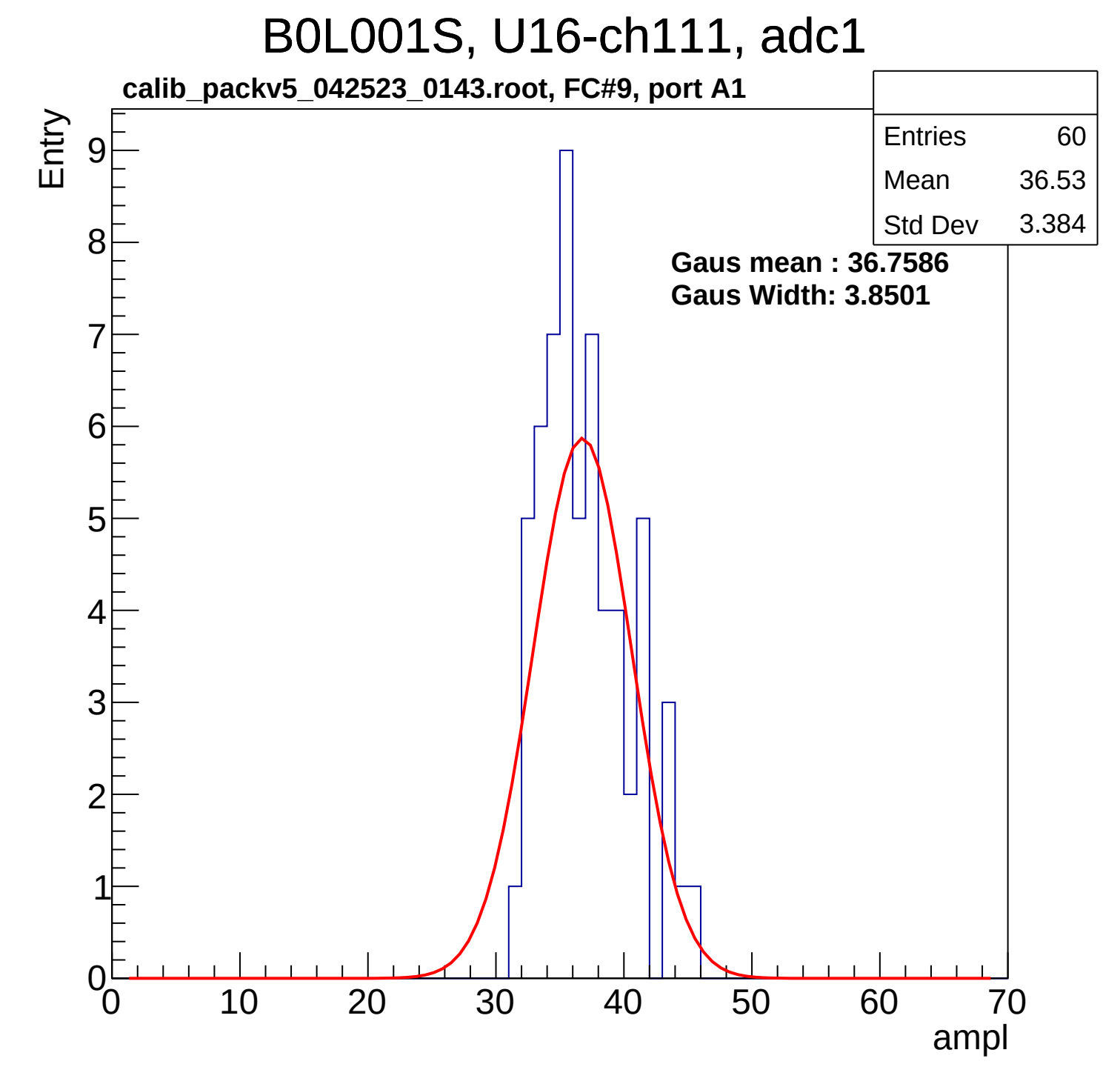

B0L001S, U16-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.45
Std Dev 7.146
Gaus mean : 28.9035
Gaus Width: 3.9835
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.53
Std Dev 3.384
Gaus mean : 36.7586
Gaus Width: 3.8501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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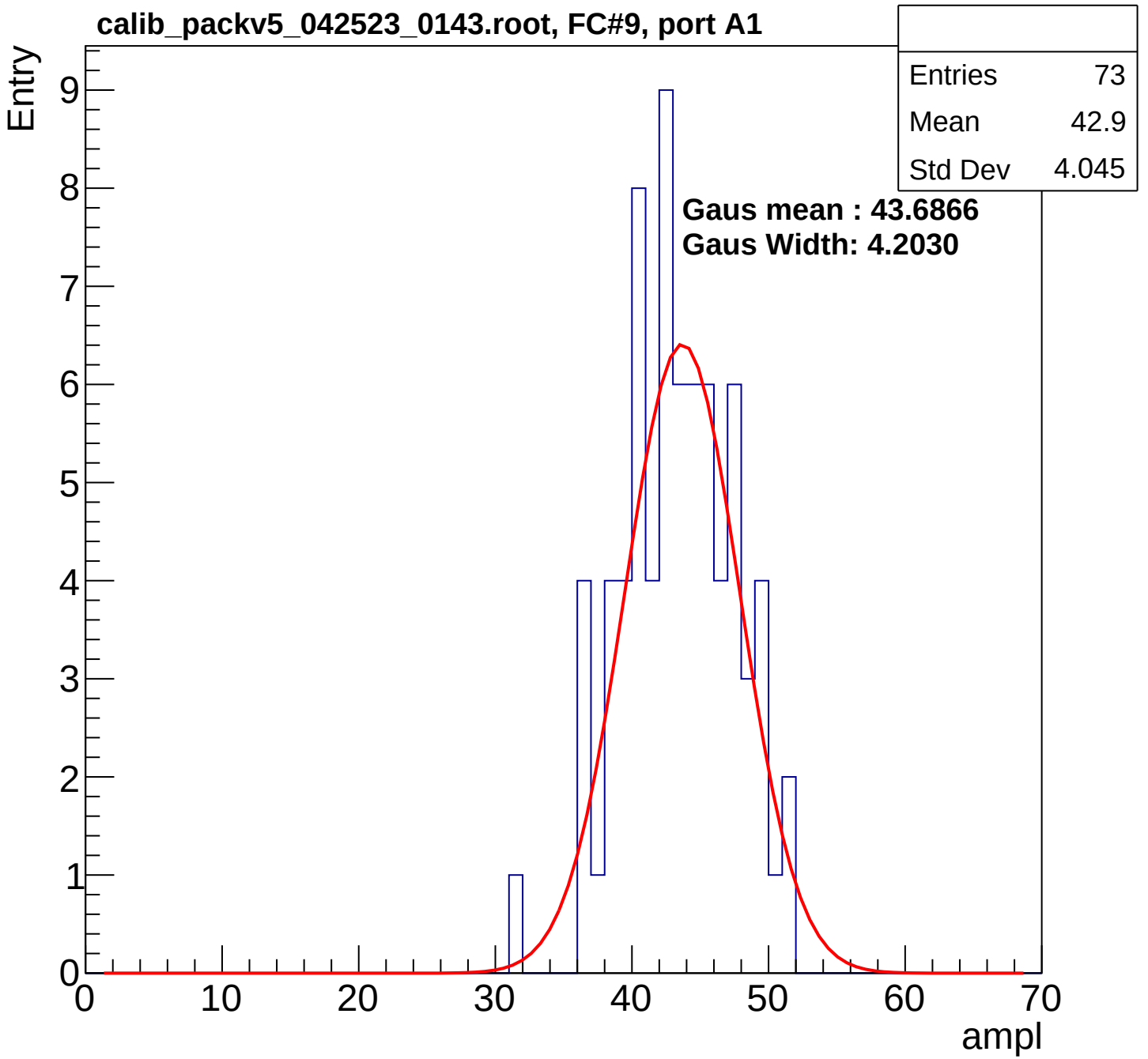
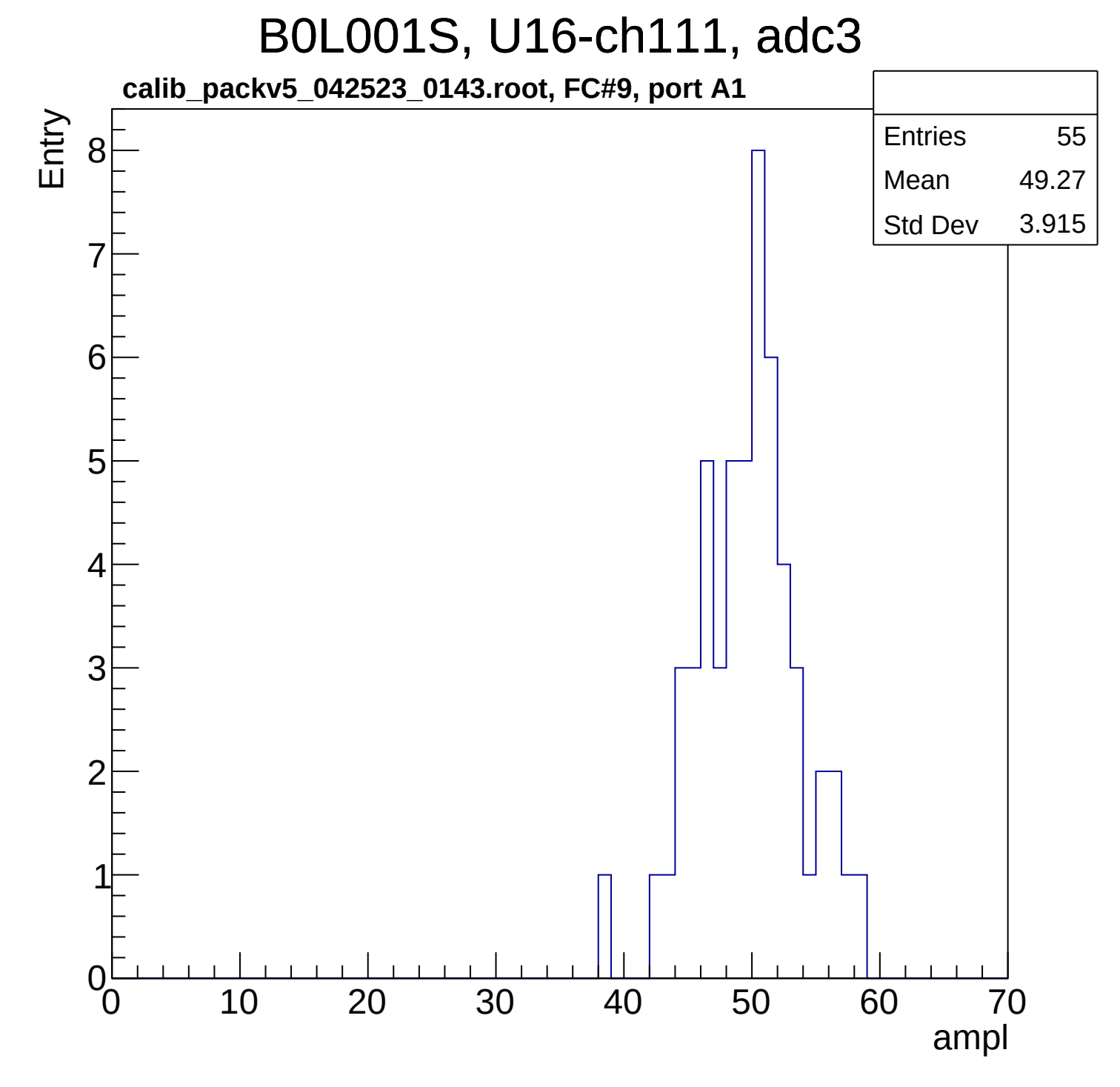

B0L001S, U16-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.9
Std Dev 4.045
Gaus mean : 43.6866
Gaus Width: 4.2030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.27
Std Dev 3.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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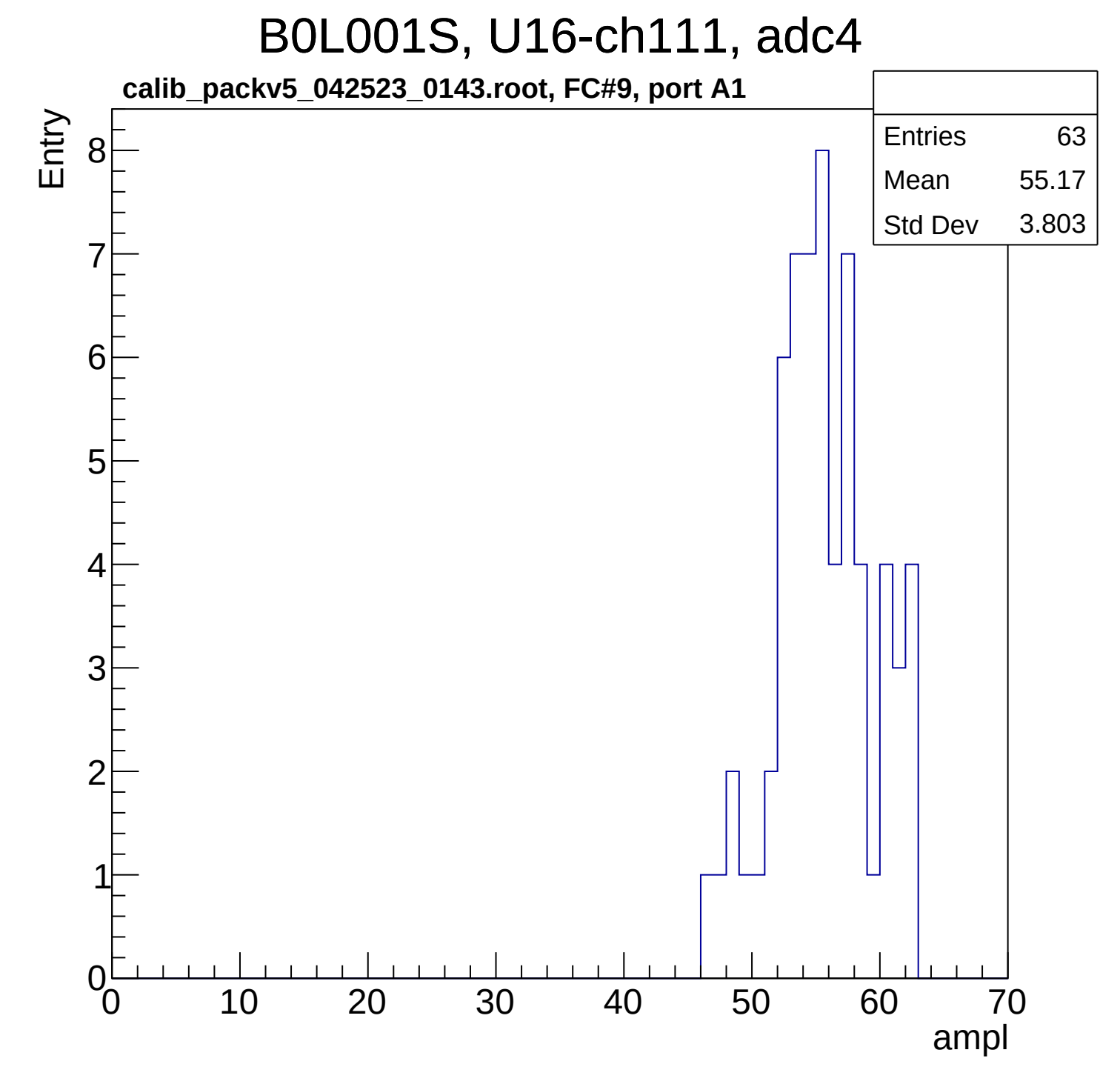
B0L001S, U16-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.17
Std Dev 3.803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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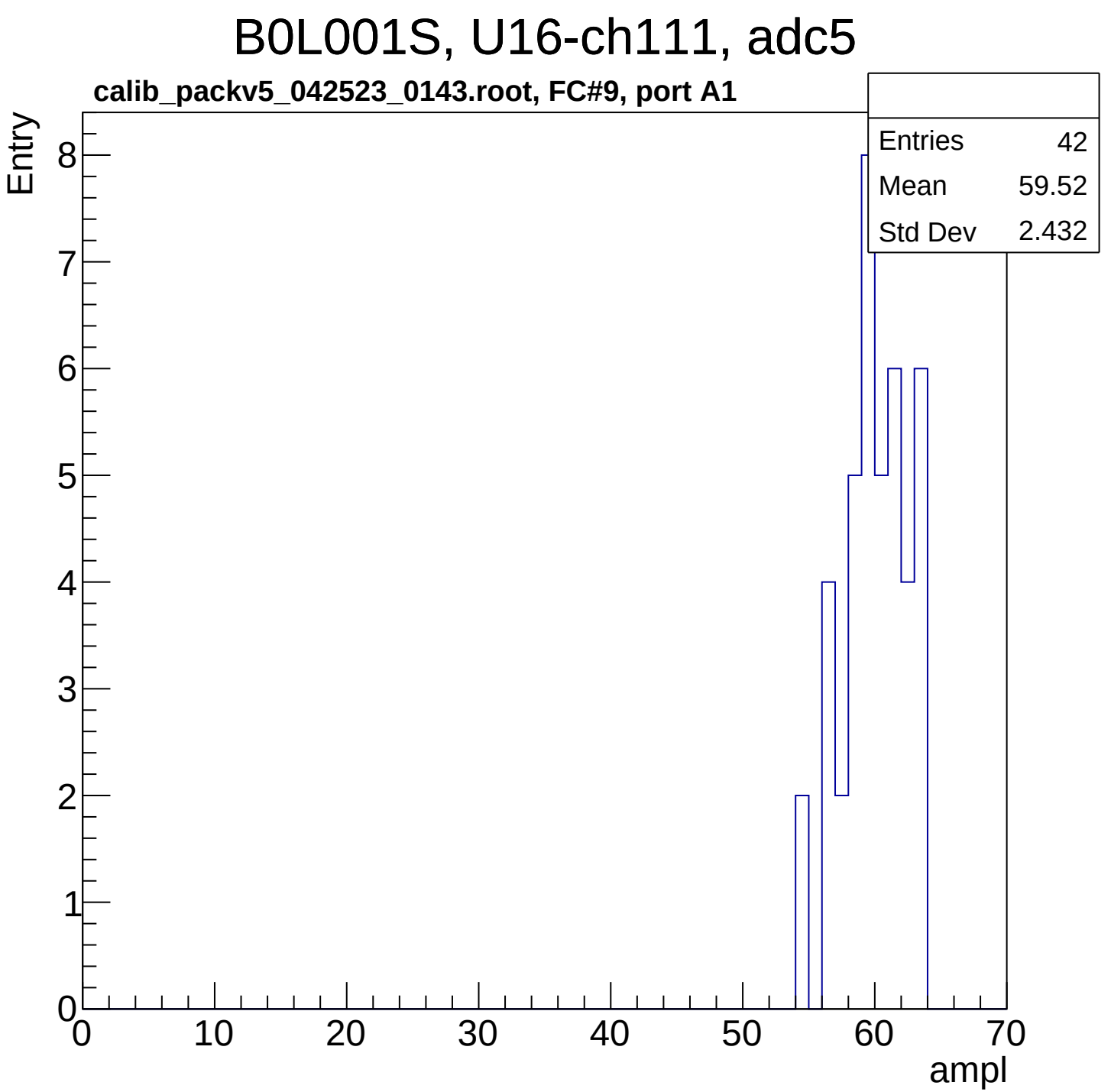
40
50
60
70


B0L001S, U16-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.52
Std Dev 2.432
Entry
ampl
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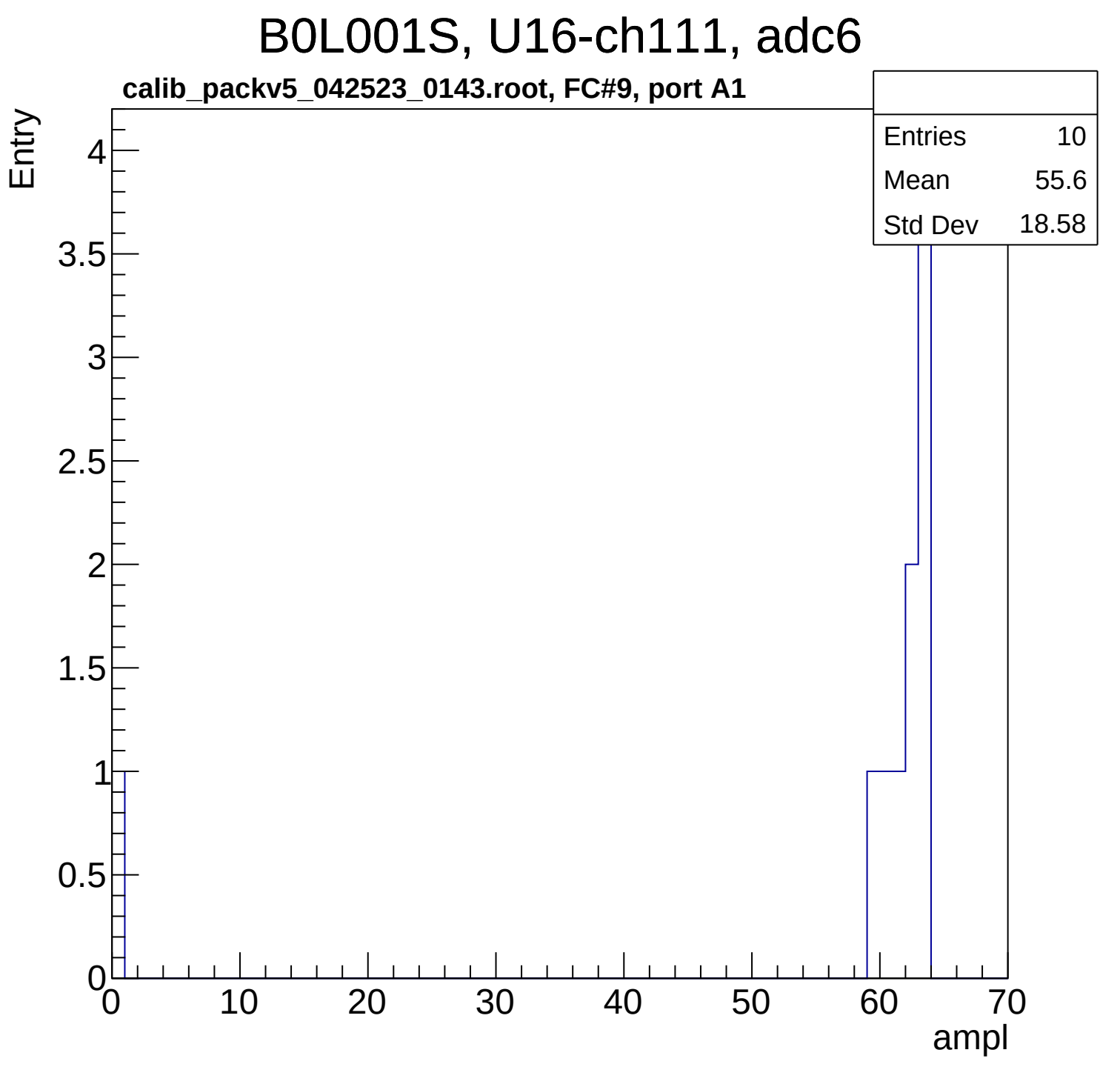
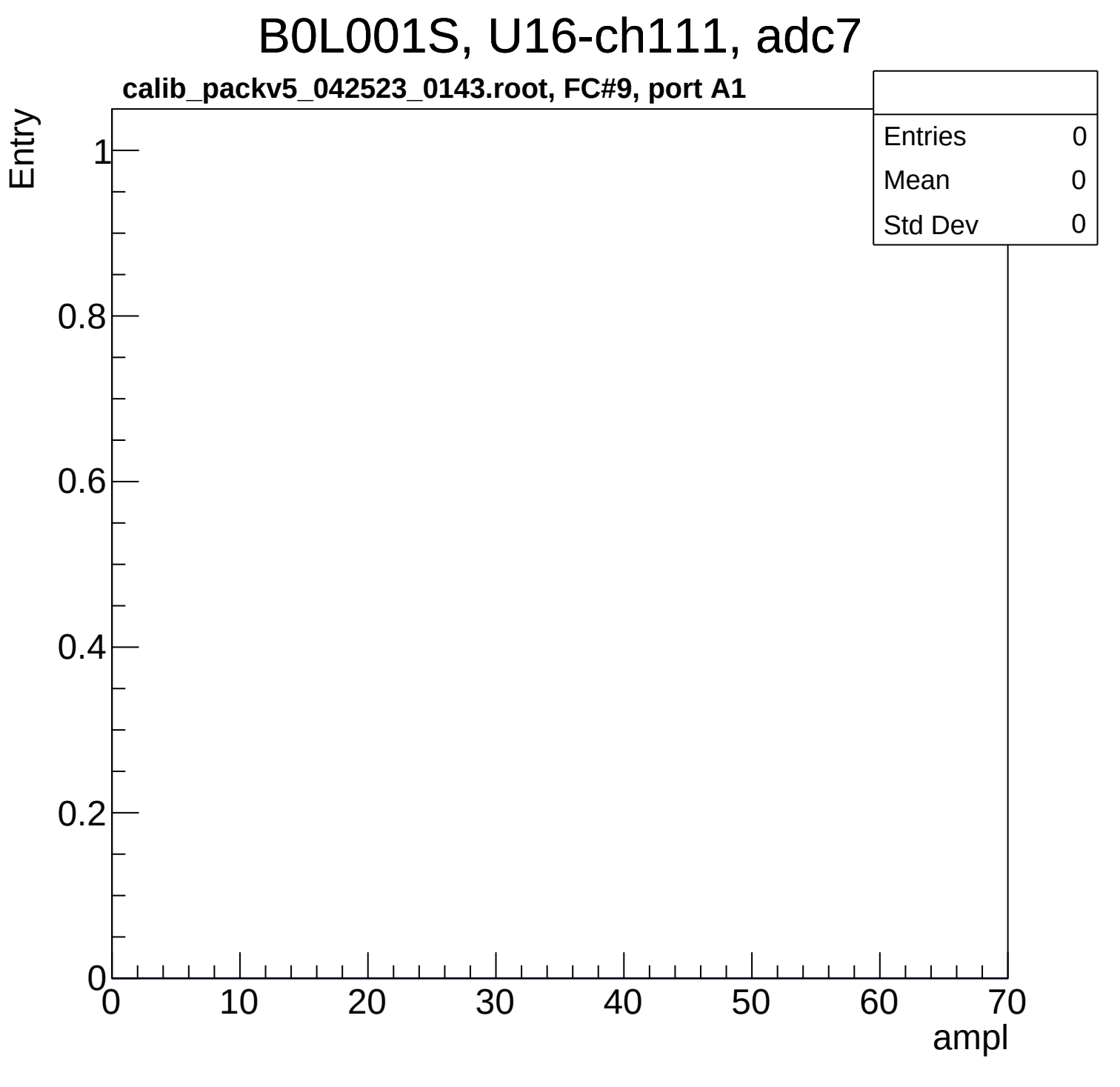
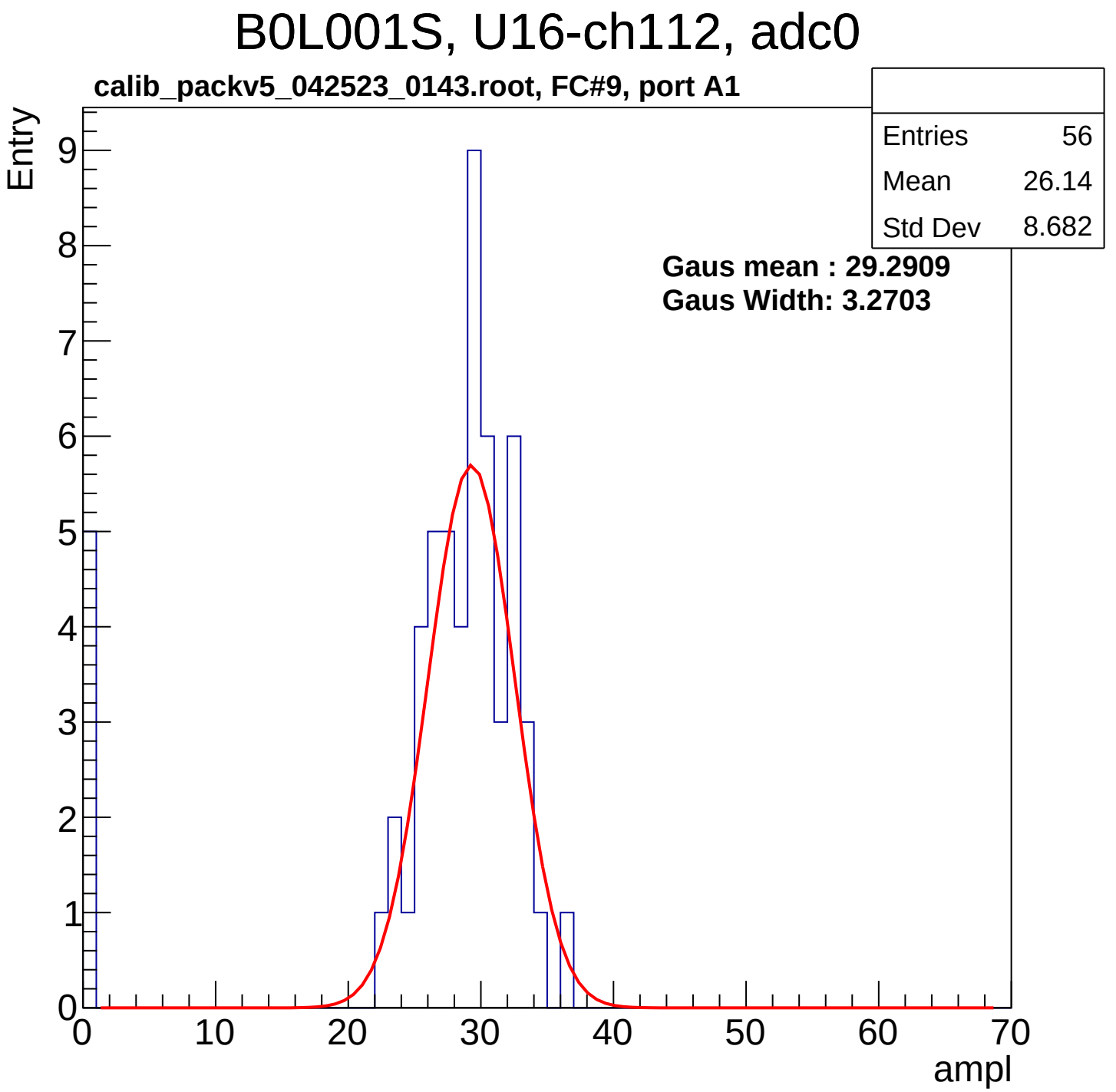
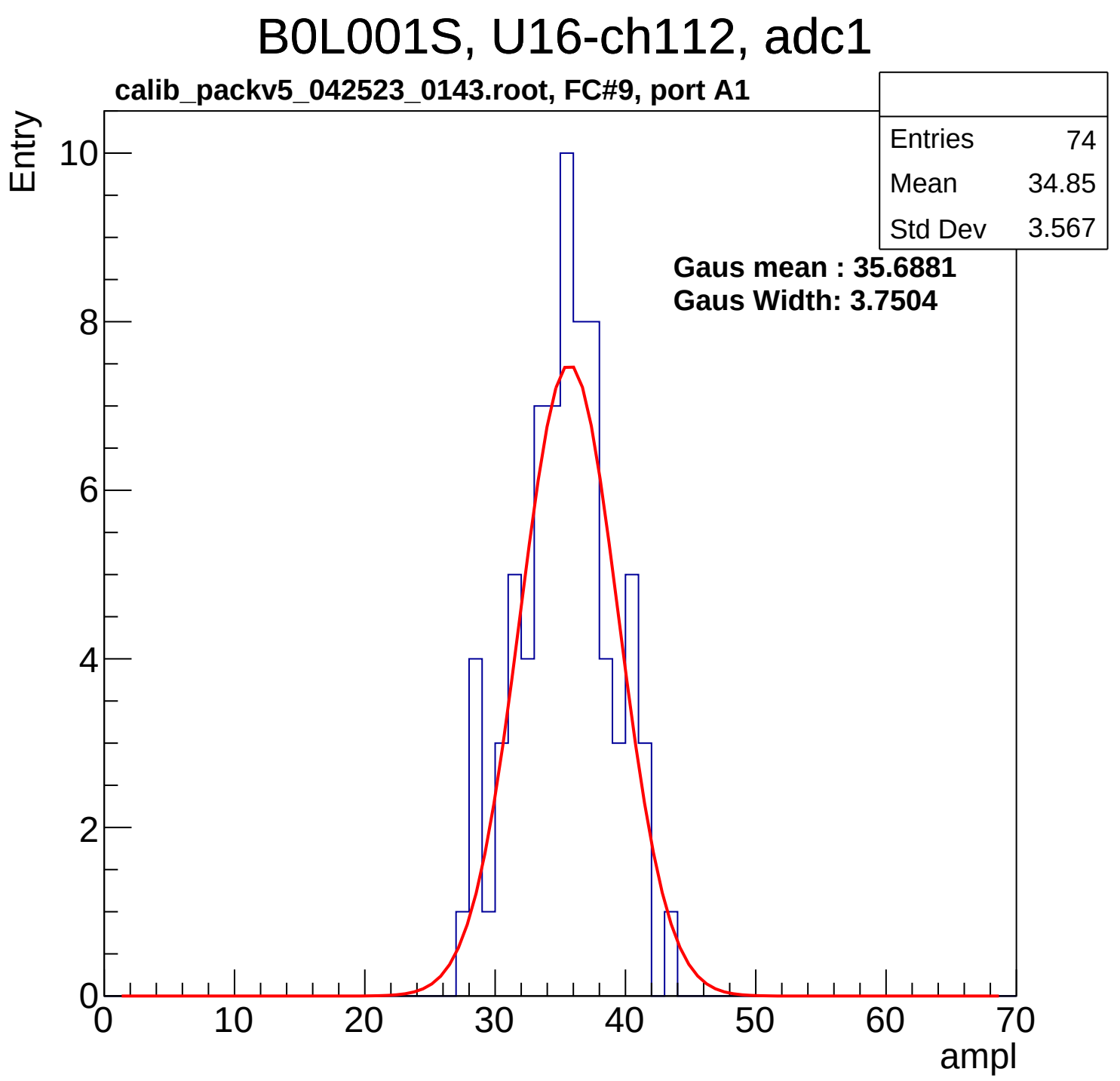
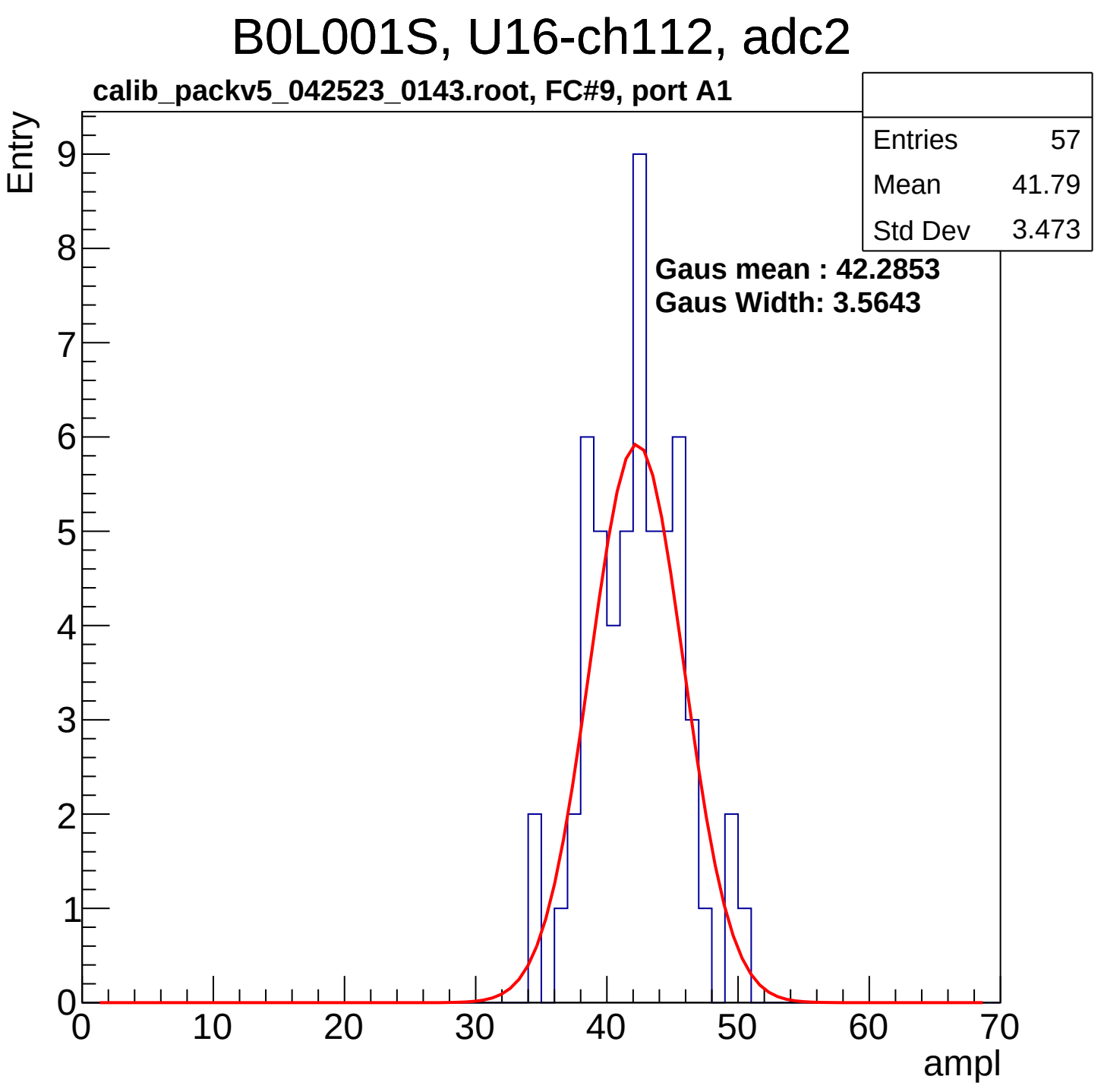
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

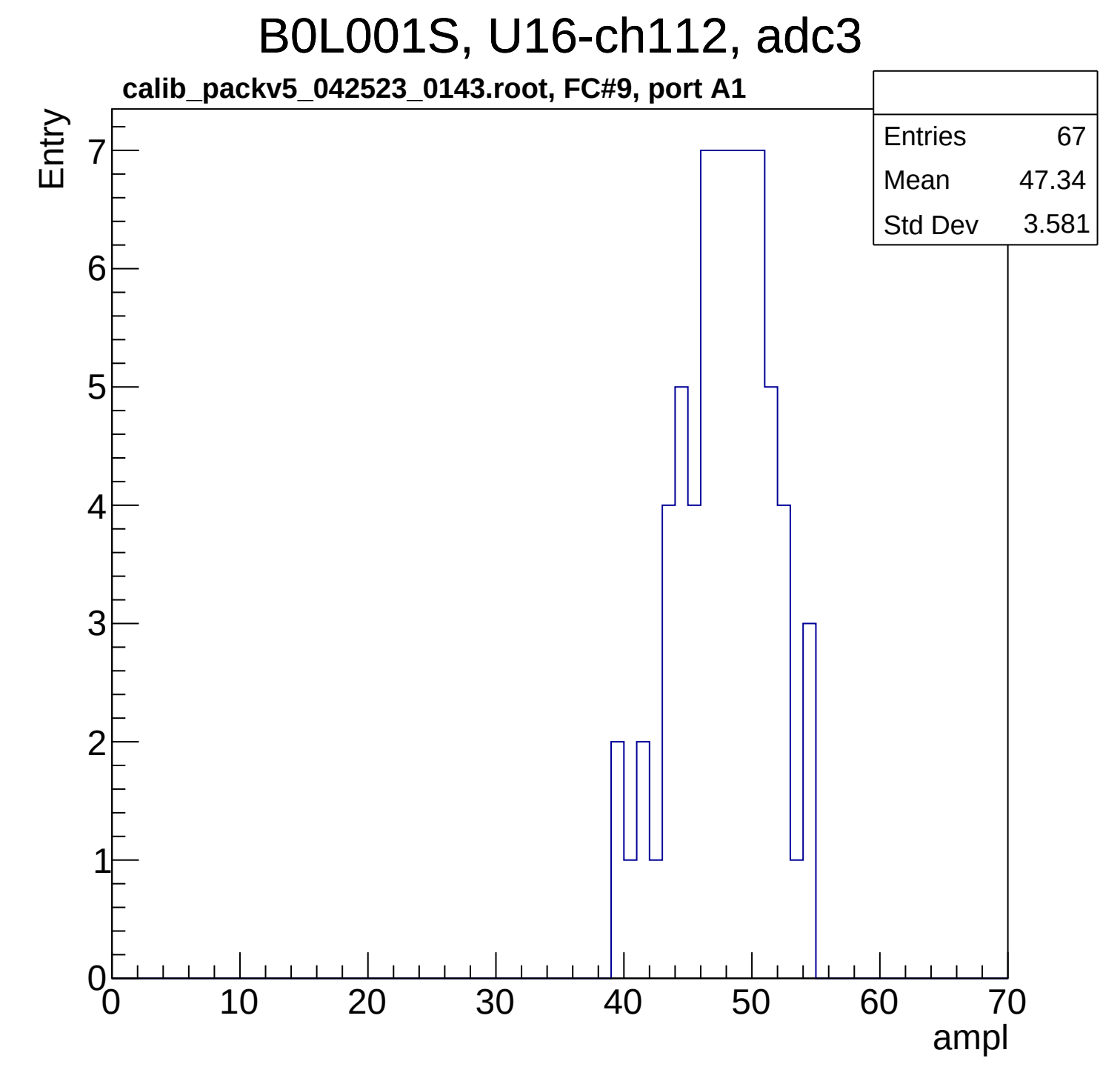

B0L001S, U16-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 55.6
Std Dev 18.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

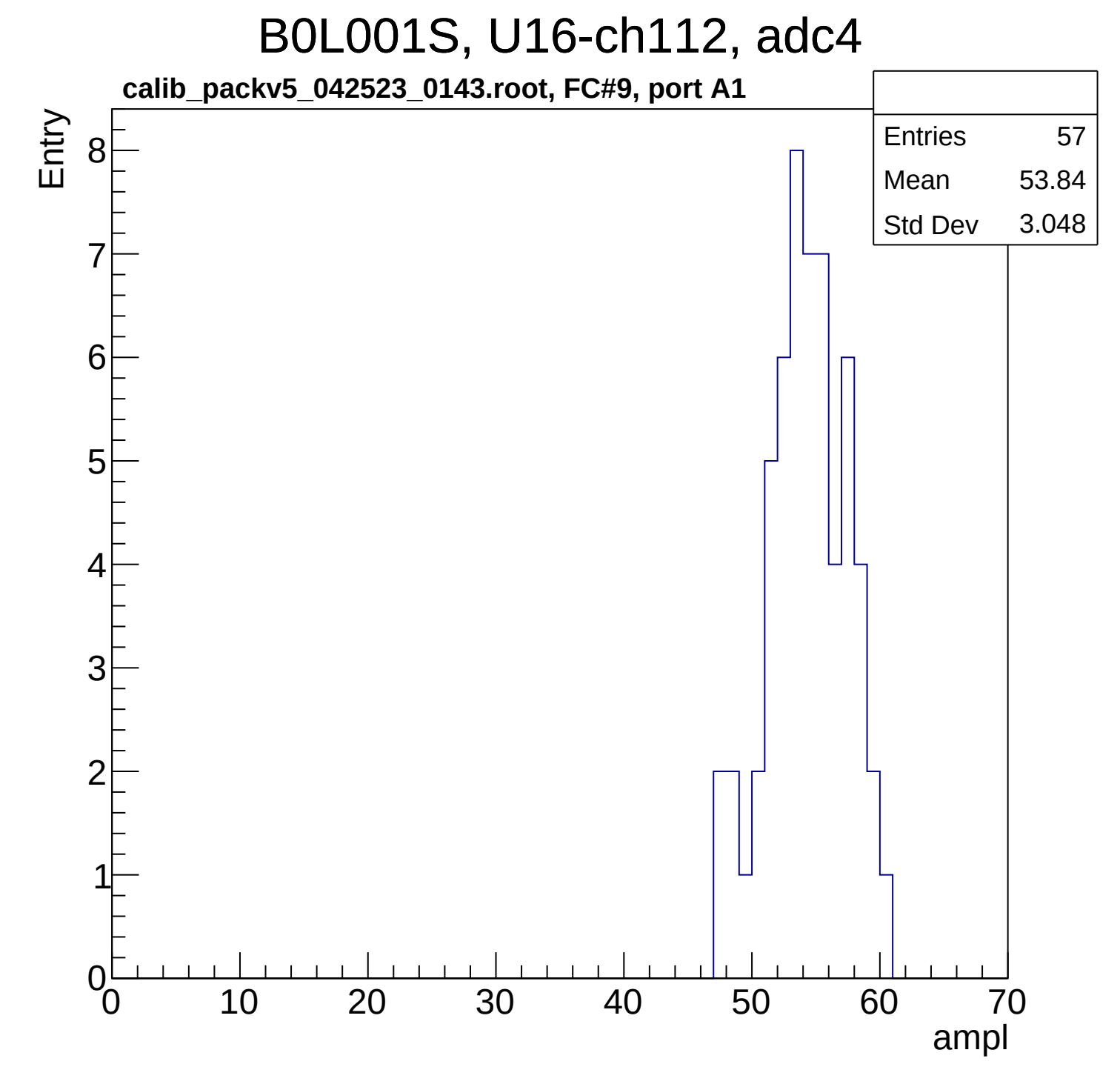

B0L001S, U16-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

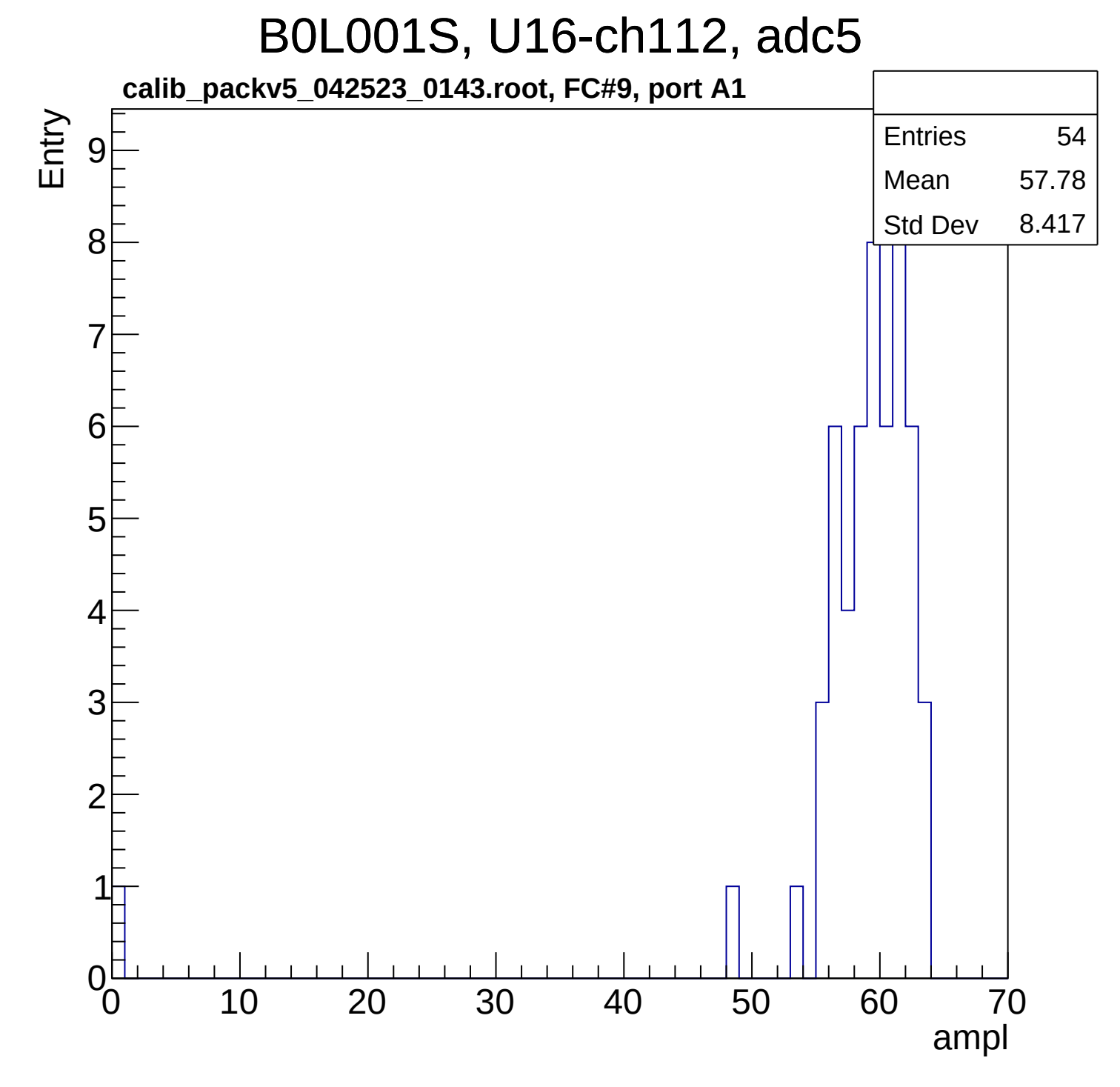

B0L001S, U16-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 26.14
Std Dev 8.682
Gaus mean : 29.2909
Gaus Width: 3.2703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

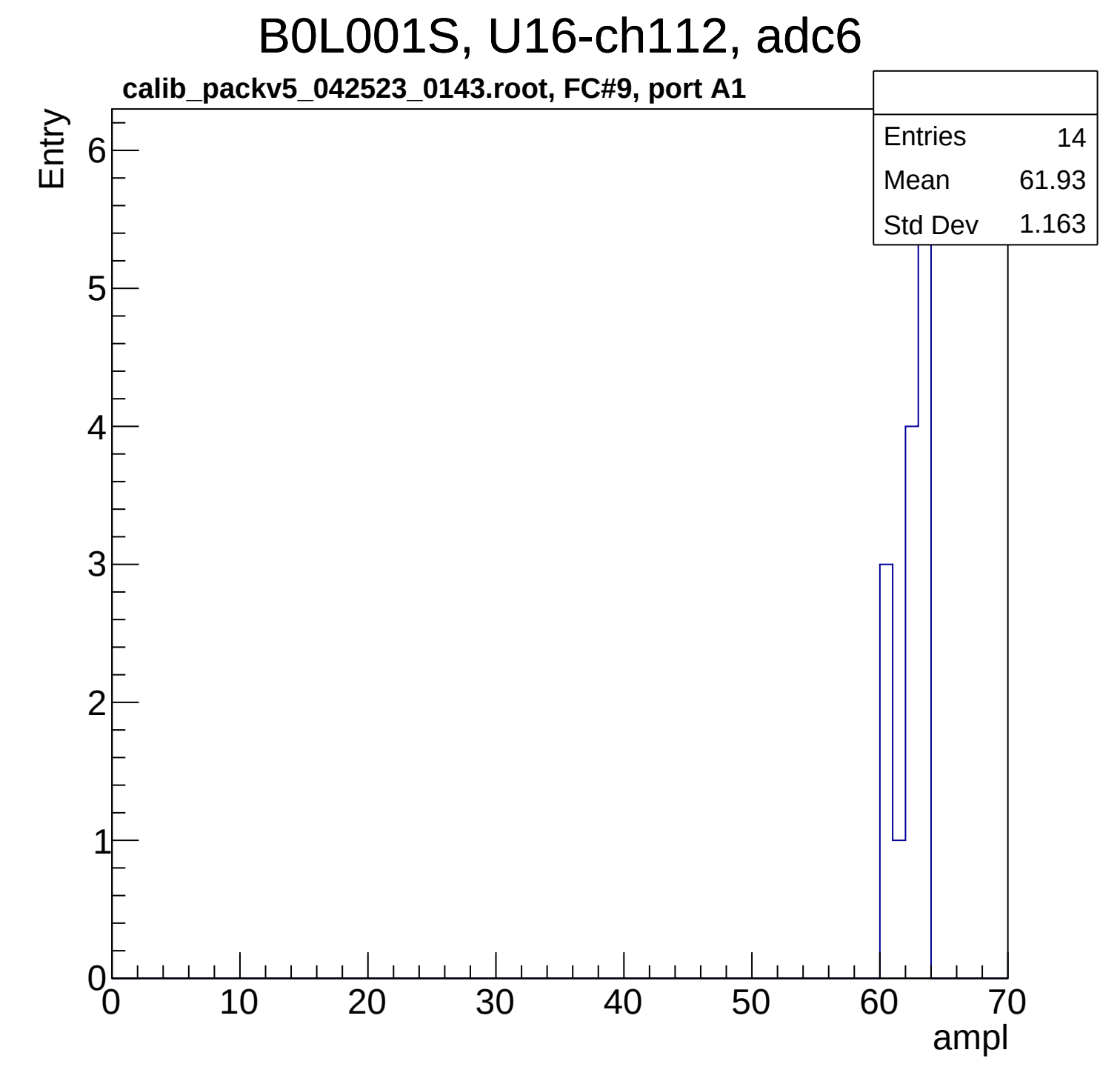

B0L001S, U16-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 34.85
Std Dev 3.567
Gaus mean : 35.6881
Gaus Width: 3.7504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

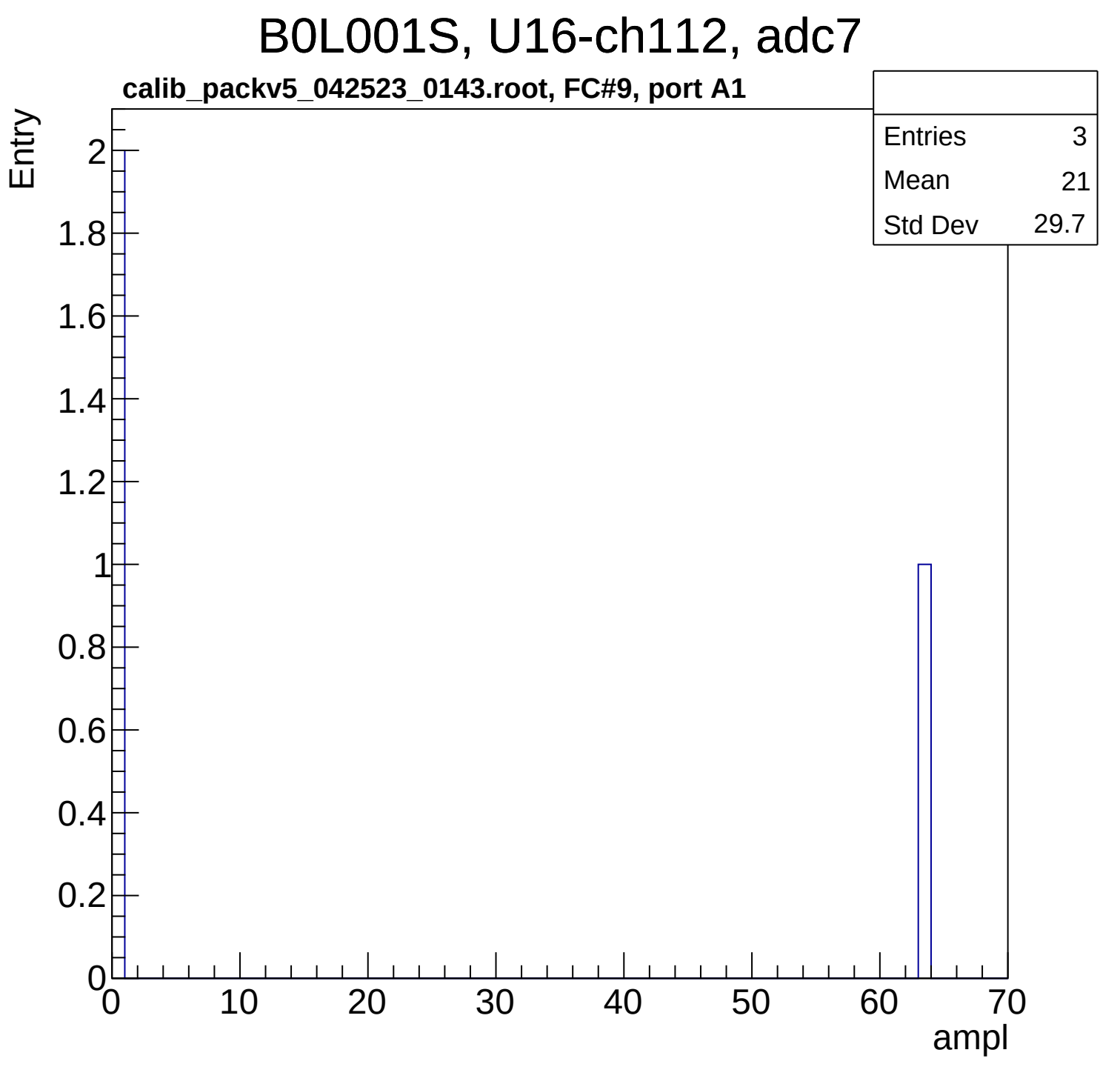

B0L001S, U16-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 41.79
Std Dev 3.473
Gaus mean : 42.2853
Gaus Width: 3.5643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

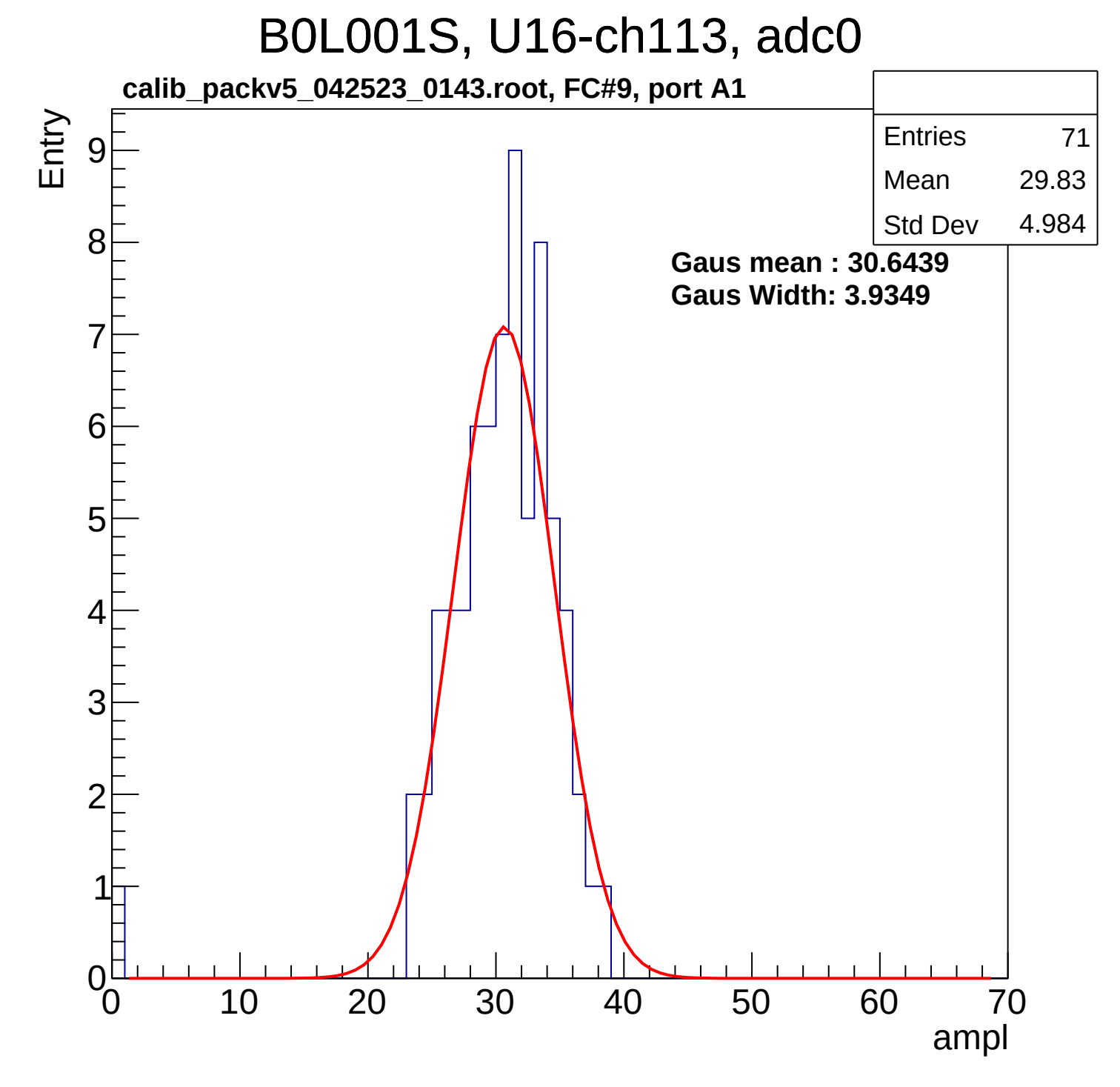

B0L001S, U16-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 47.34
Std Dev 3.581
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

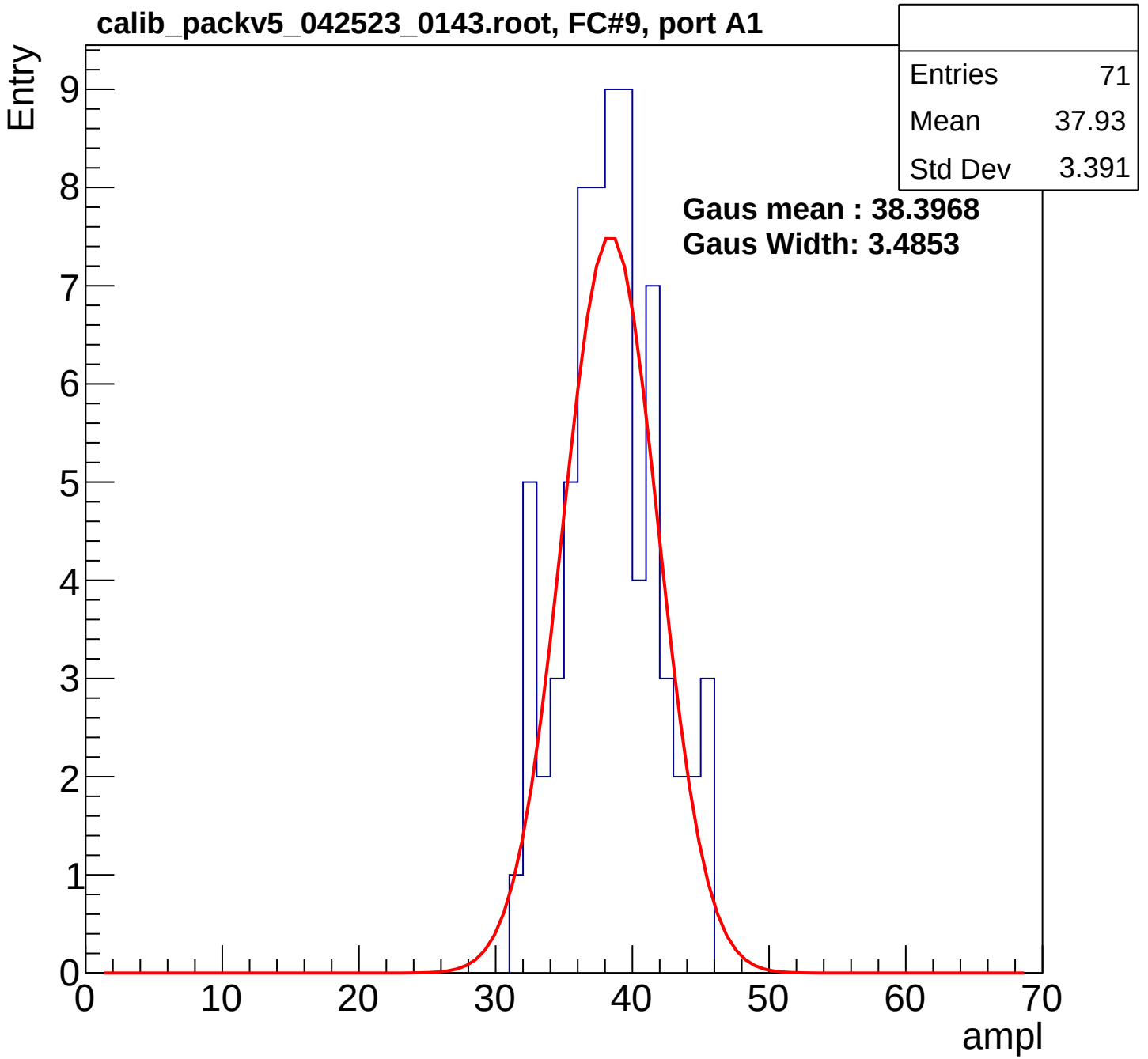

B0L001S, U16-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 53.84
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.78
Std Dev 8.417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 61.93
Std Dev 1.163
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.83
Std Dev 4.984
Gaus mean : 30.6439
Gaus Width: 3.9349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.93
Std Dev 3.391
Gaus mean : 38.3968
Gaus Width: 3.4853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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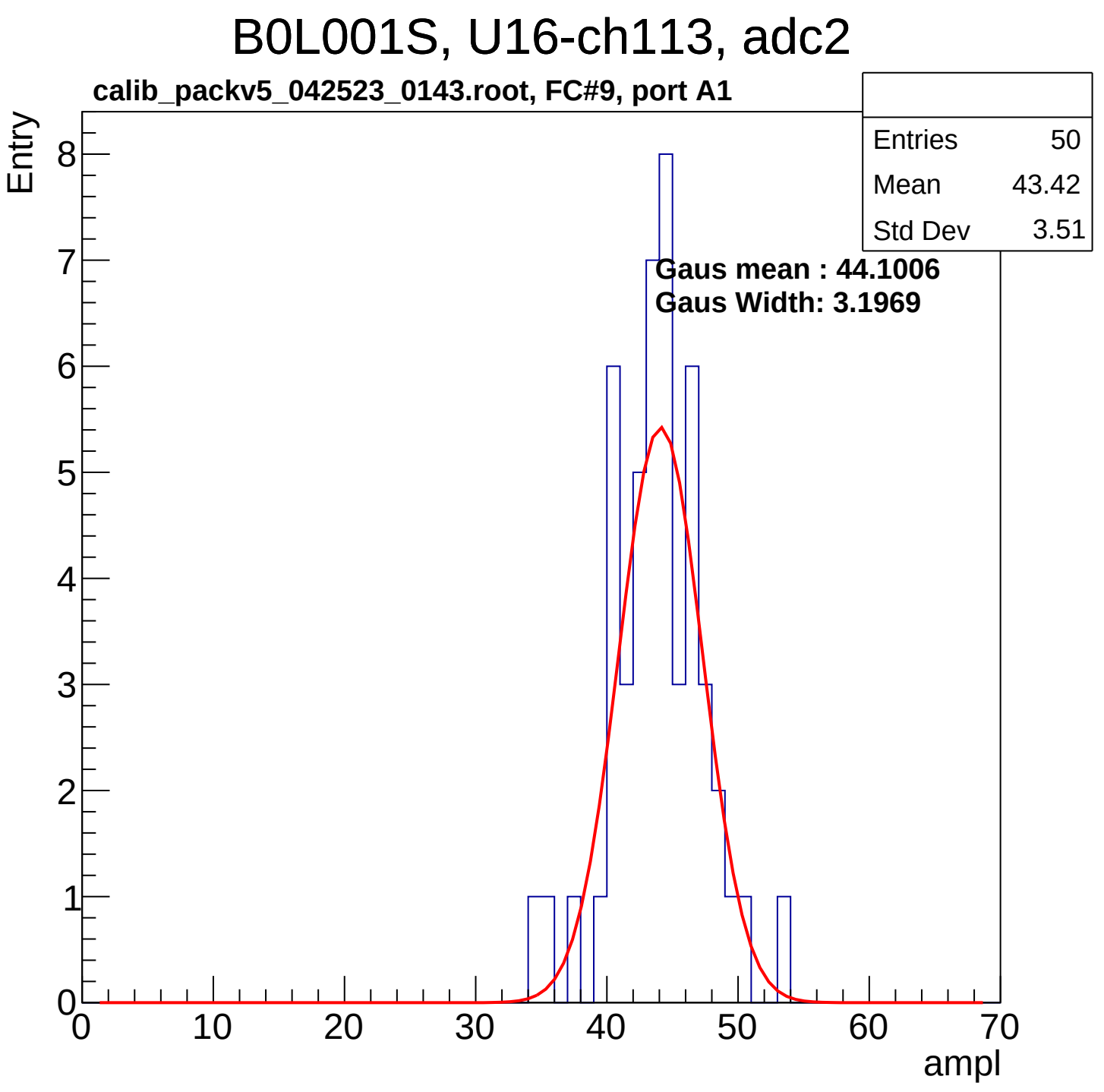
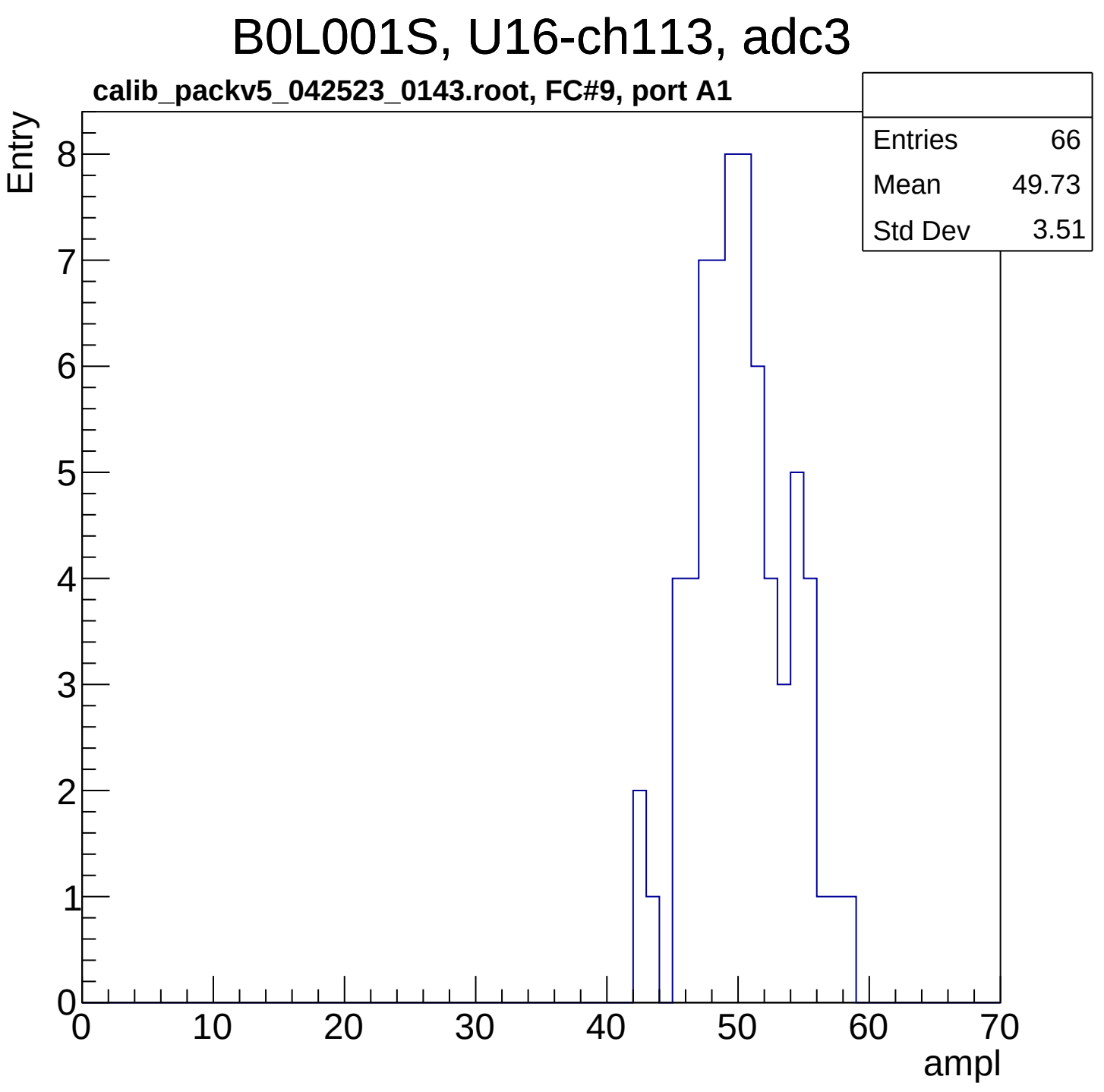
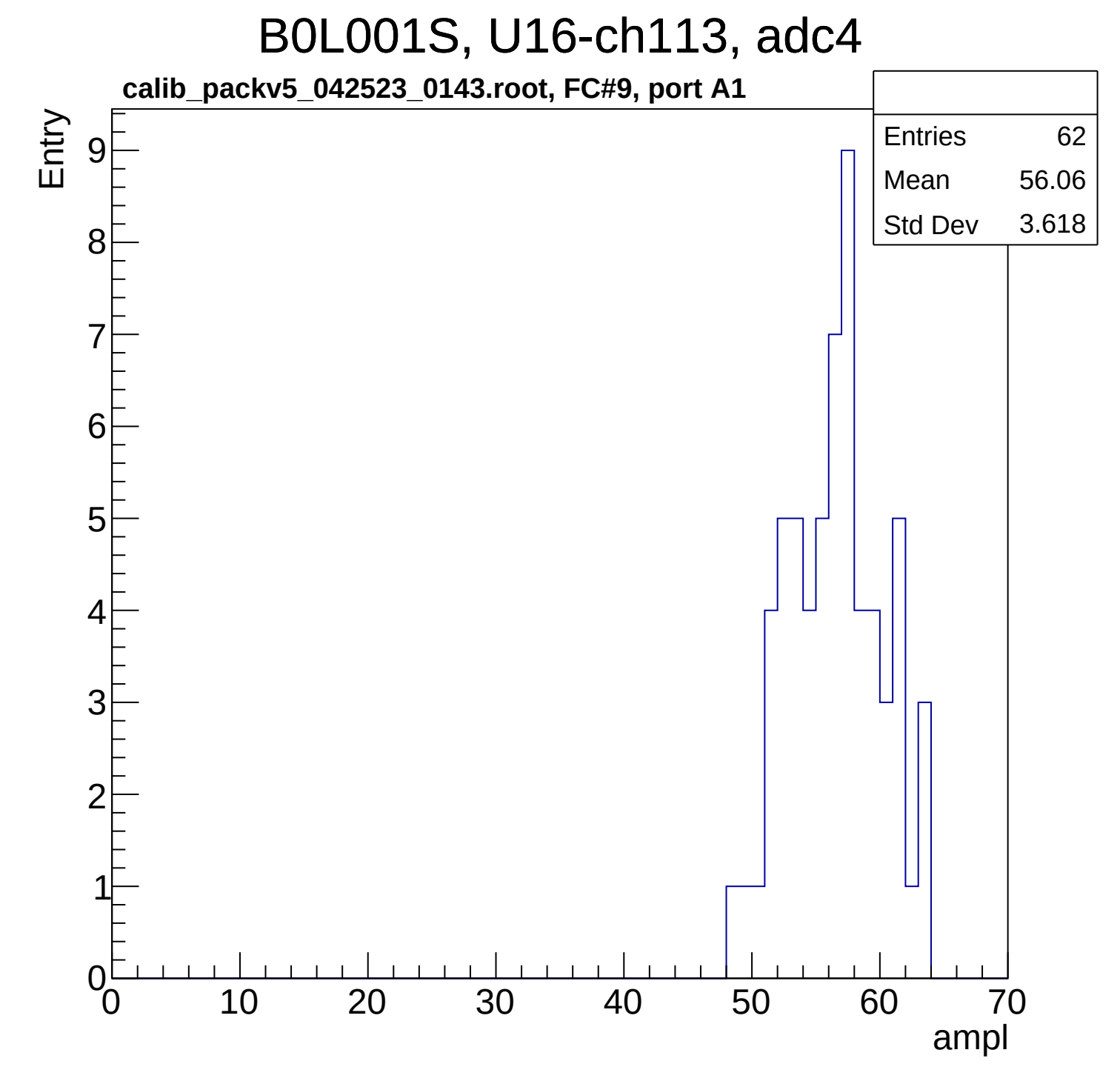

B0L001S, U16-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 43.42
Std Dev 3.51
Gaus mean : 44.1006
Gaus Width: 3.1969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.73
Std Dev 3.51
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.06
Std Dev 3.618
Entry
ampl
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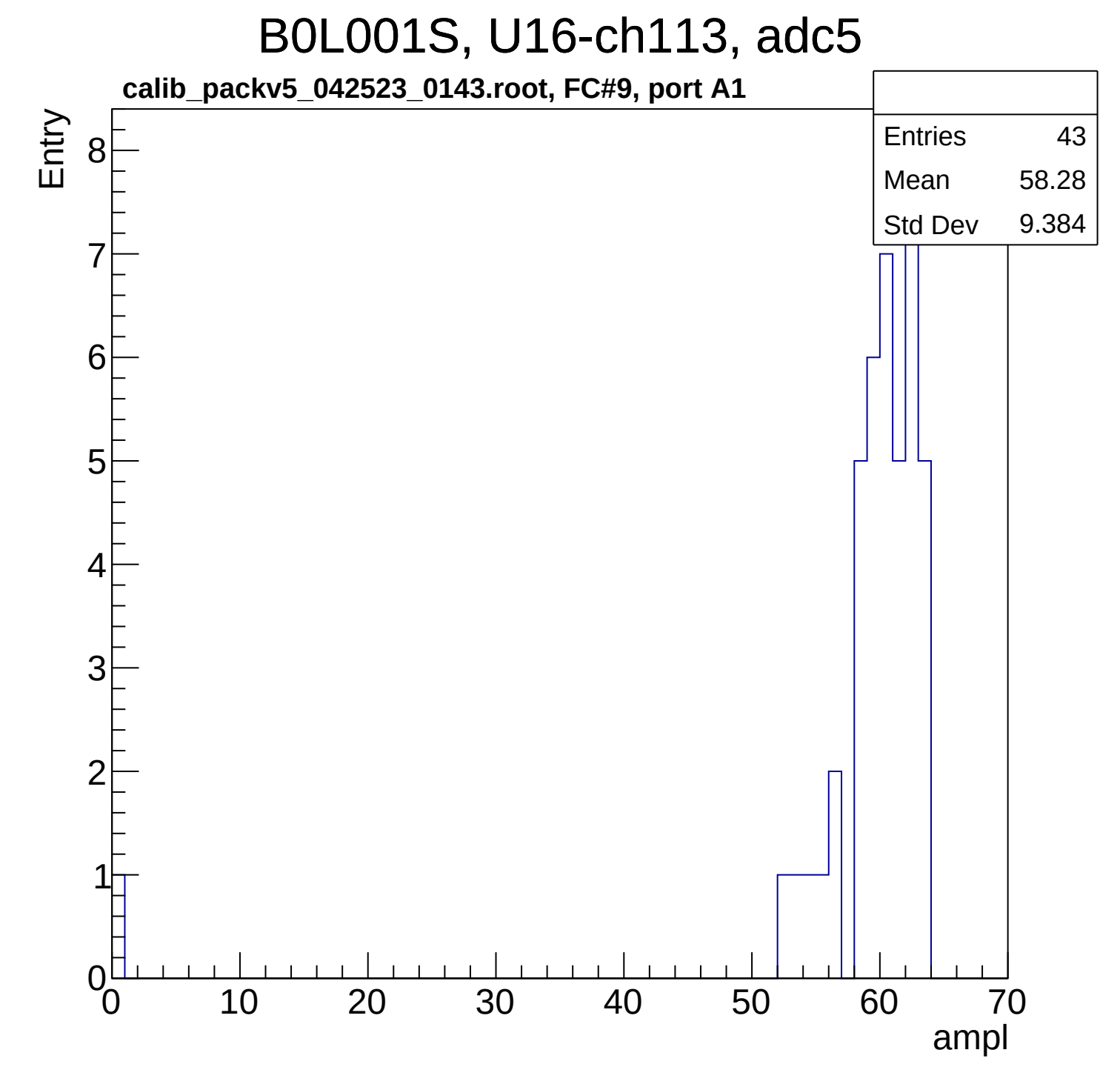
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.28
Std Dev 9.384
Entry
ampl
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1
2
3
4
5
6
7
8
0
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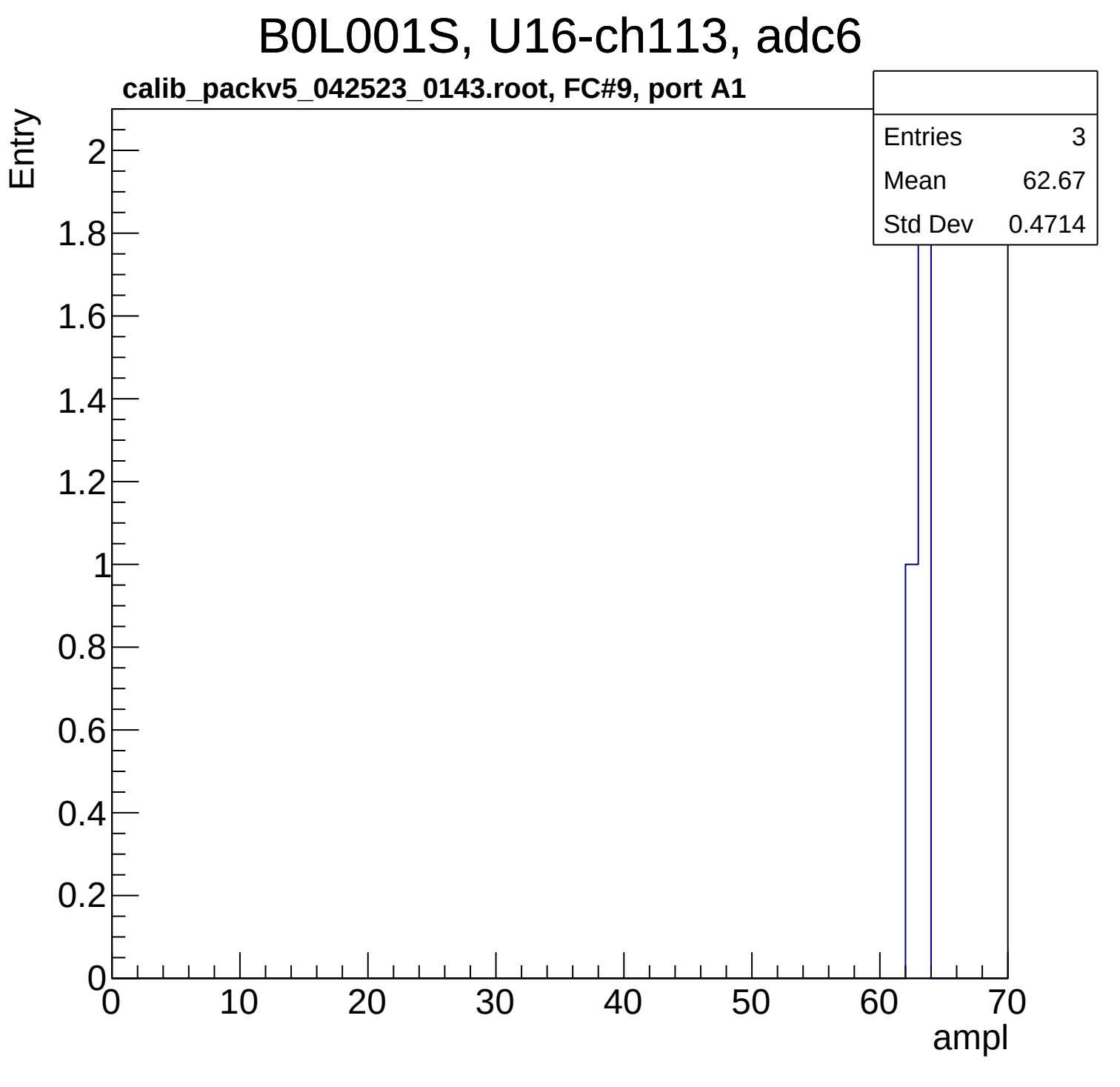
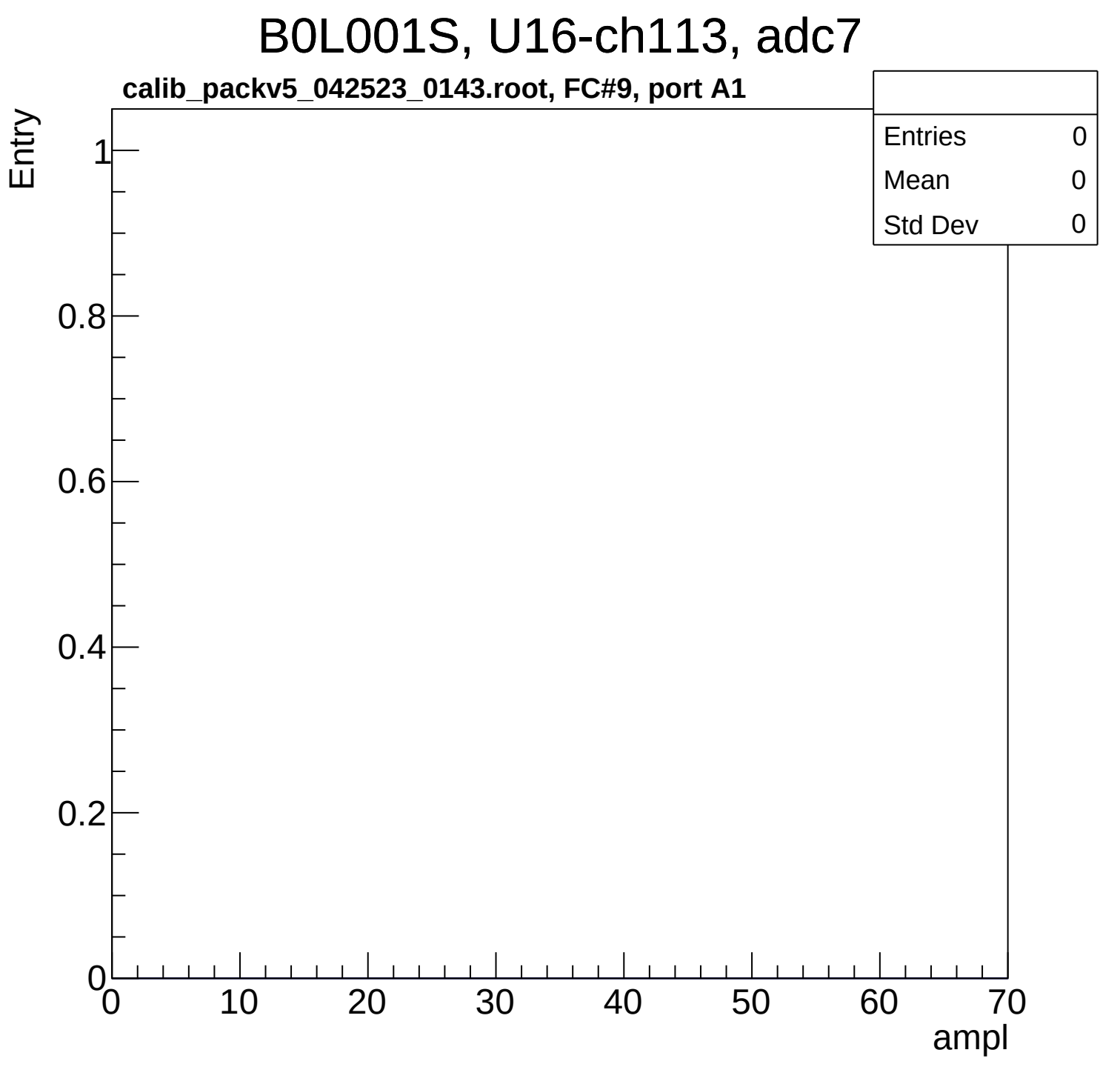
20
30
40
50
60
70


B0L001S, U16-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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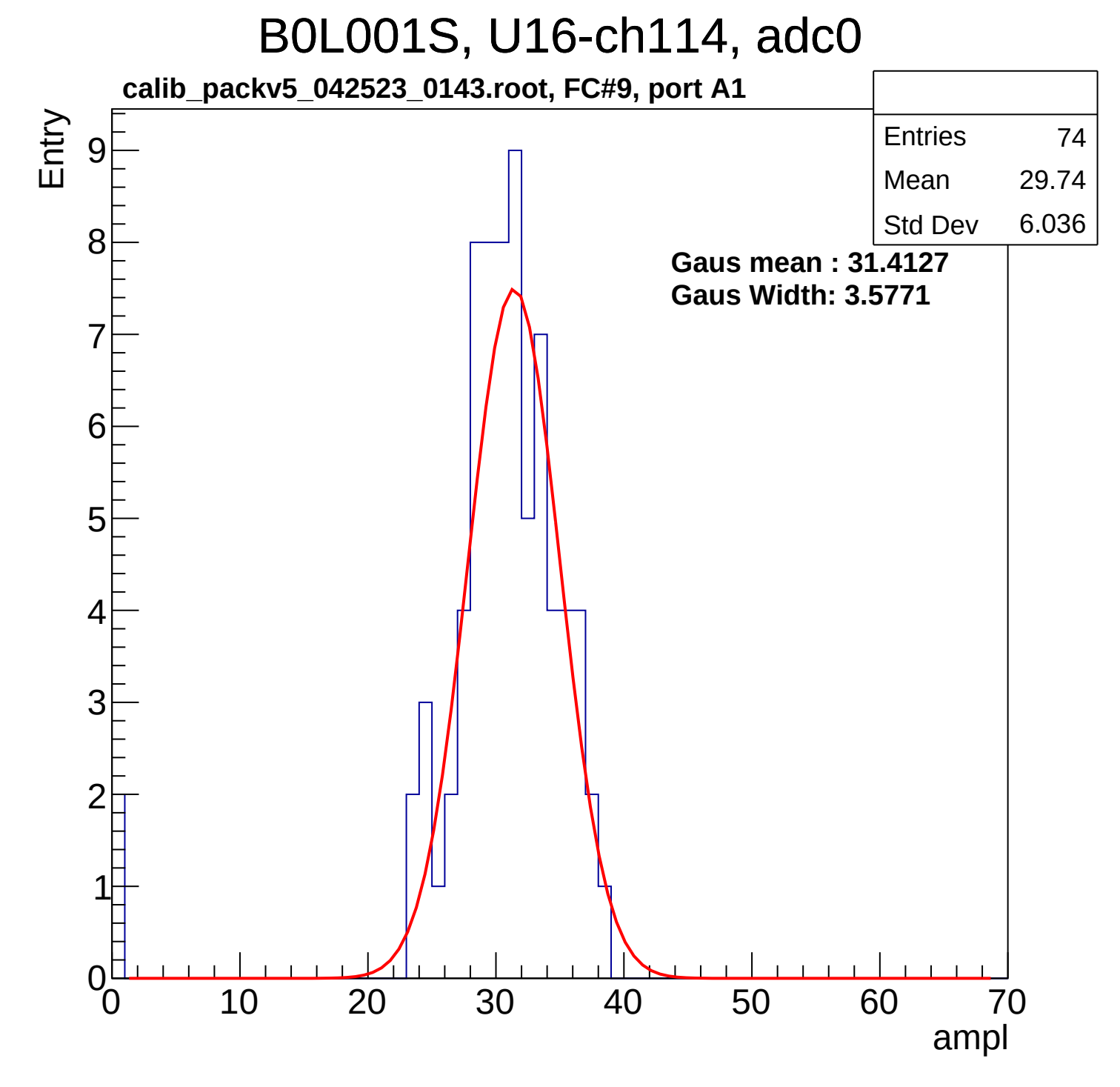
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.74
Std Dev 6.036
Gaus mean : 31.4127
Gaus Width: 3.5771
Entry
ampl
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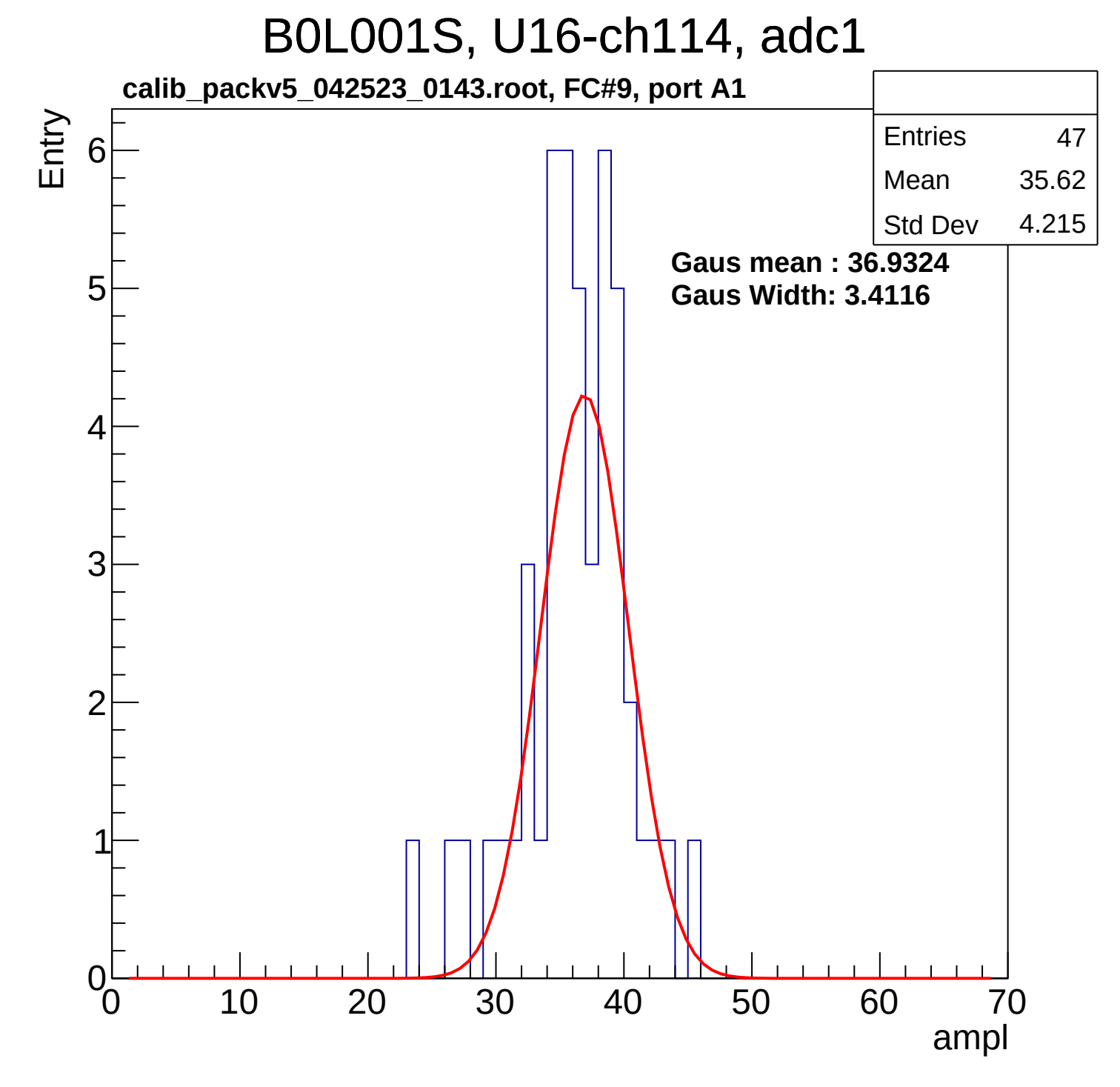
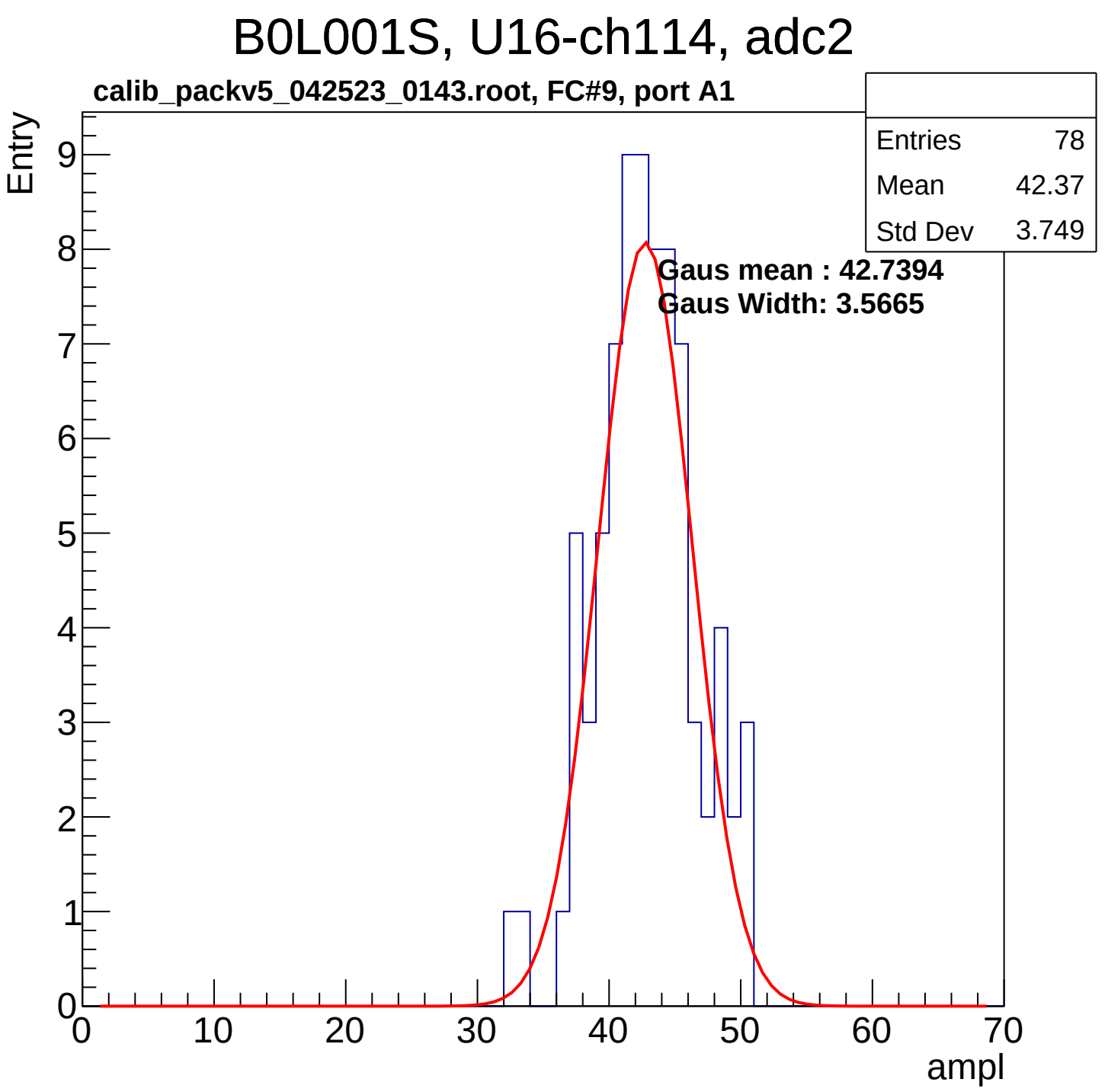
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 35.62
Std Dev 4.215
Gaus mean : 36.9324
Gaus Width: 3.4116
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U16-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 42.37
Std Dev 3.749
Gaus mean : 42.7394
Gaus Width: 3.5665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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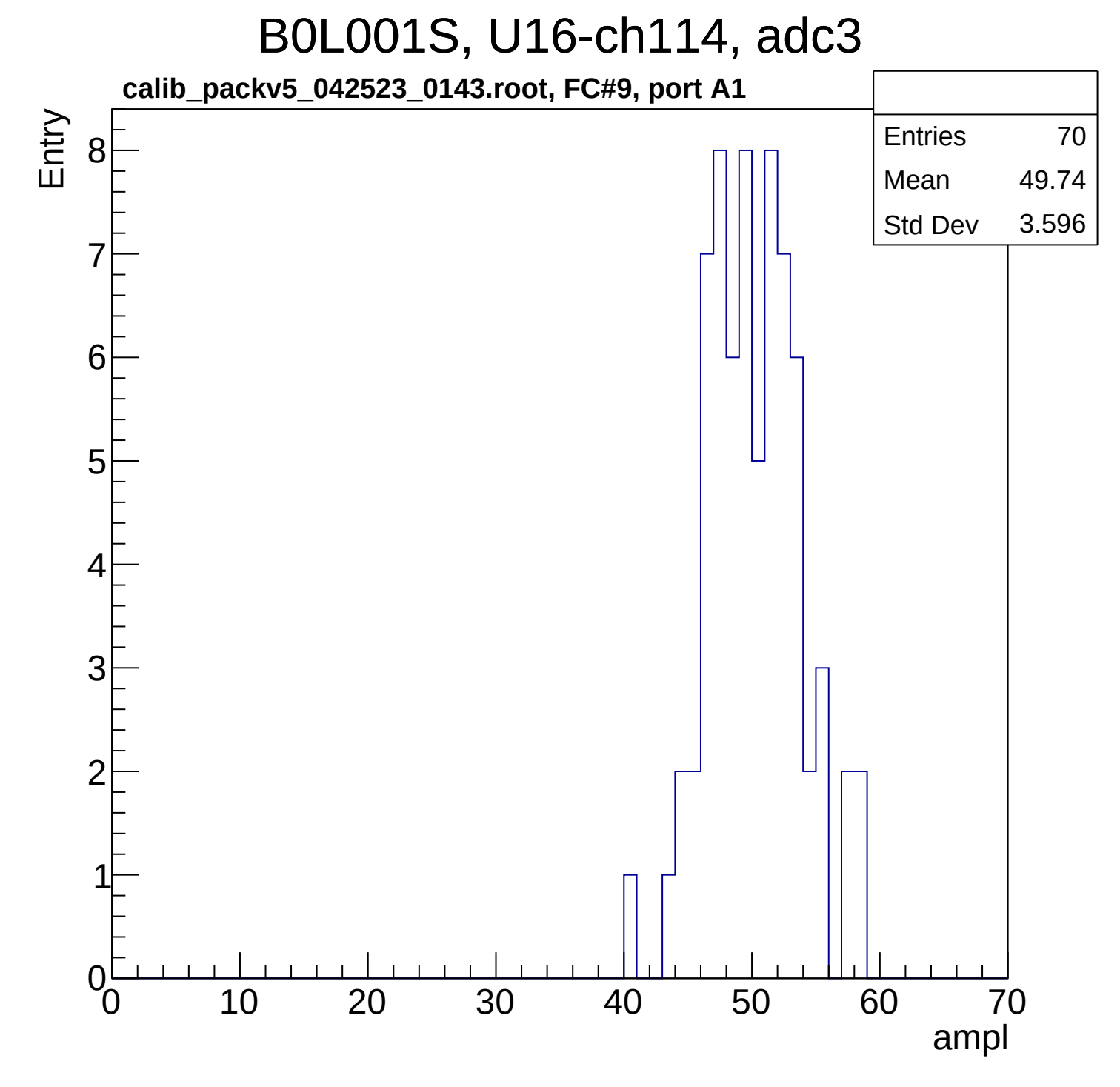
B0L001S, U16-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.74
Std Dev 3.596
Entry
ampl
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1
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3
4
5
6
7
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0
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40
50
60
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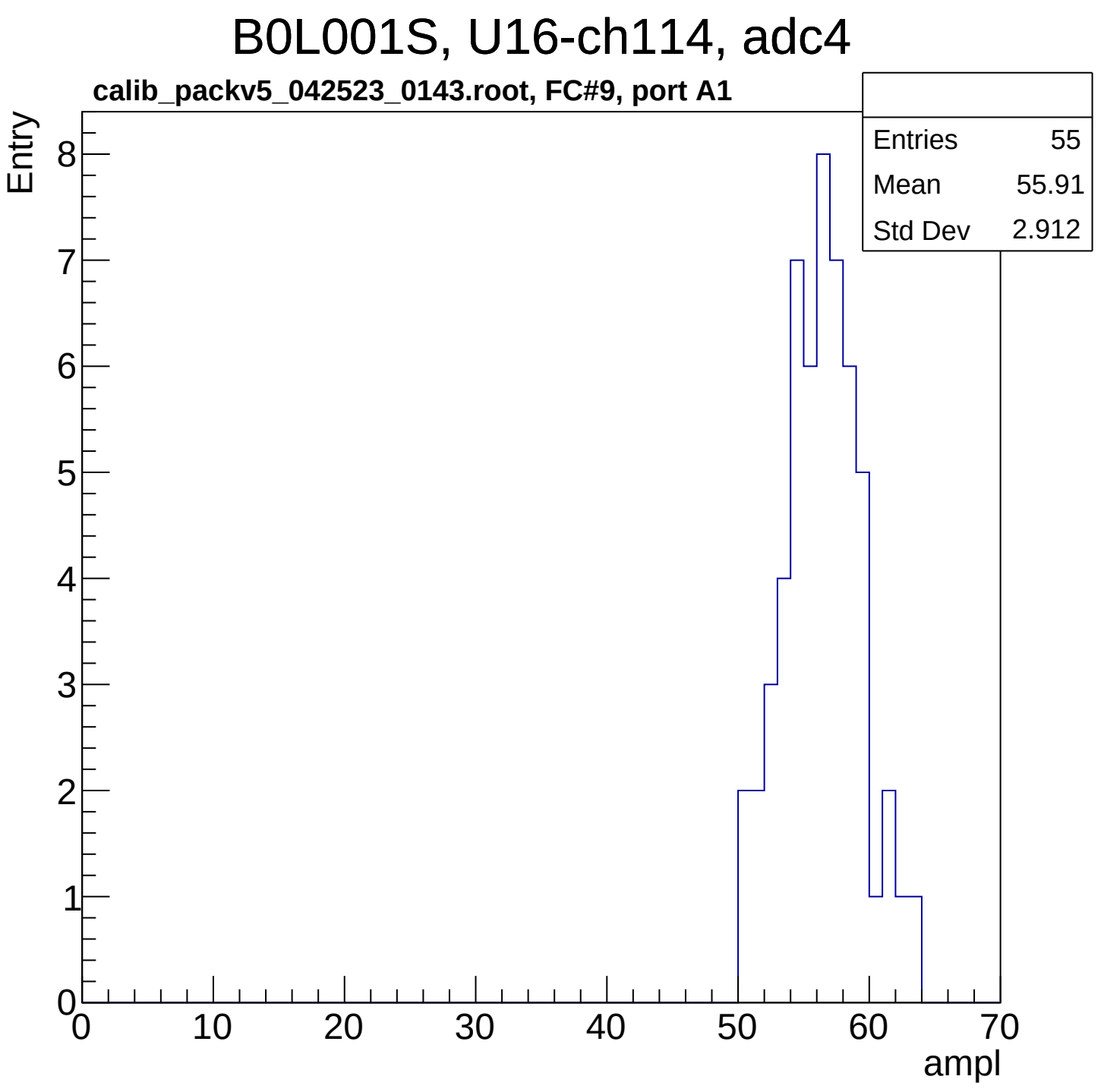
B0L001S, U16-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 55.91
Std Dev 2.912
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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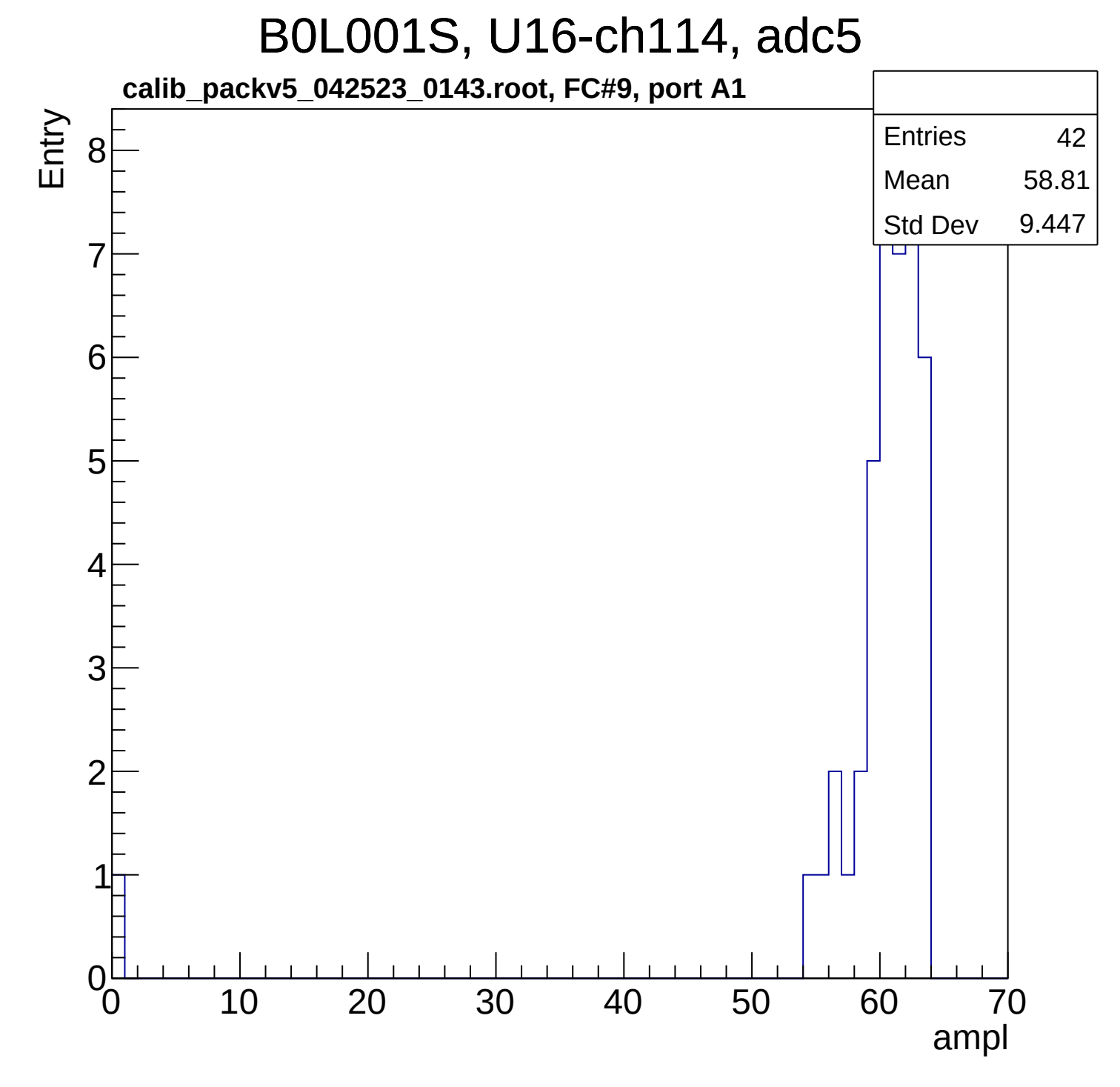
B0L001S, U16-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.81
Std Dev 9.447
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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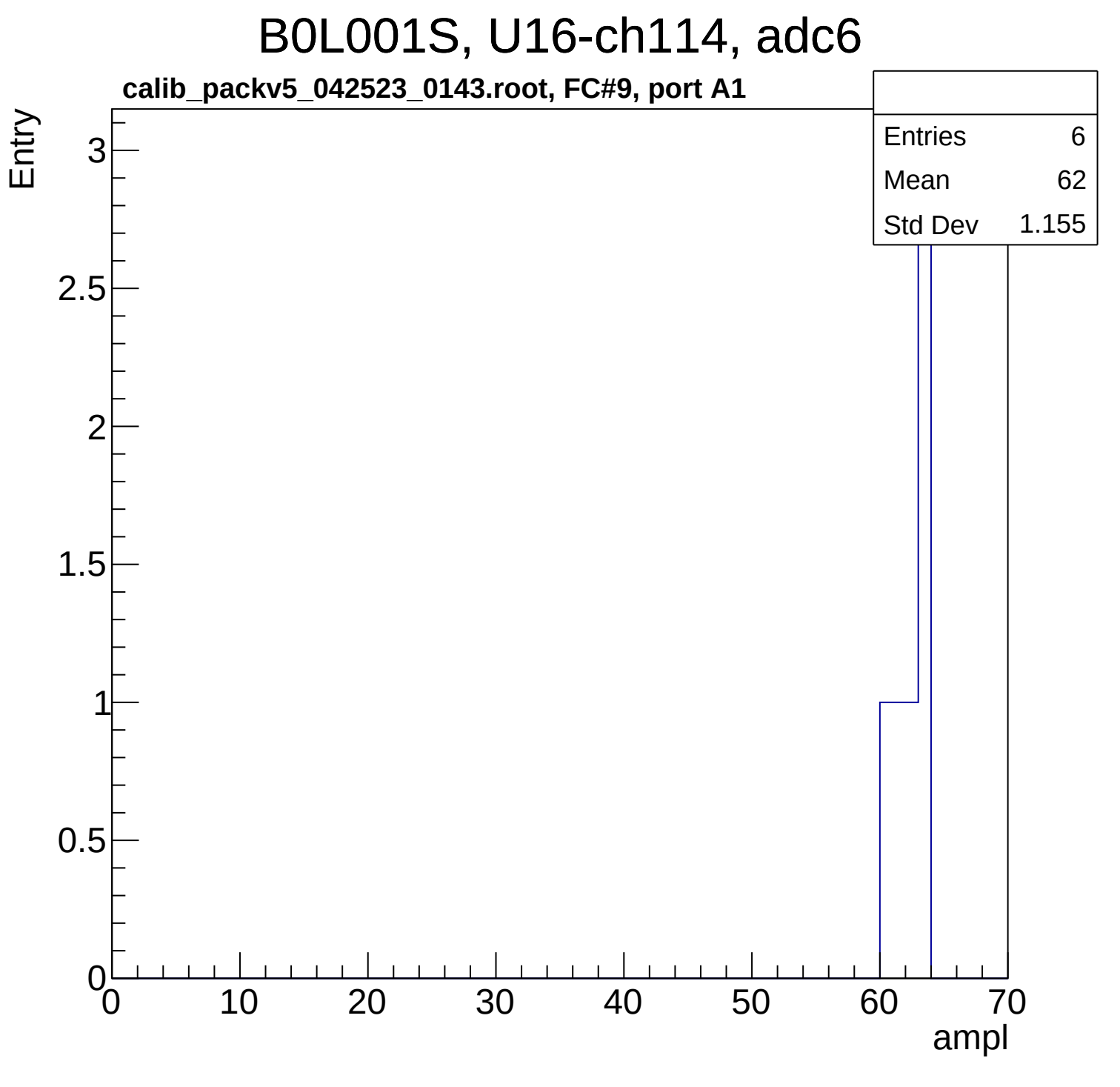
B0L001S, U16-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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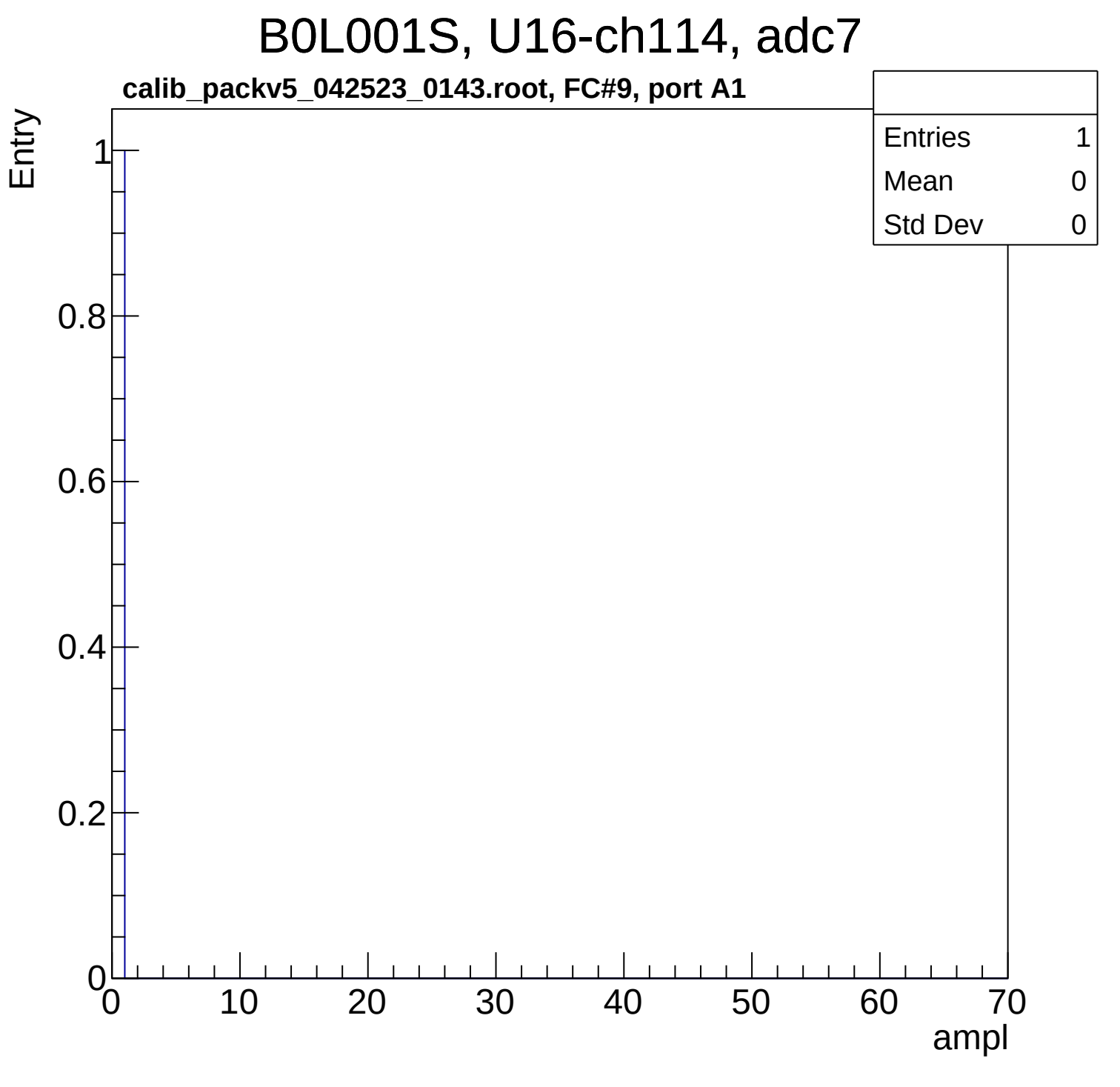
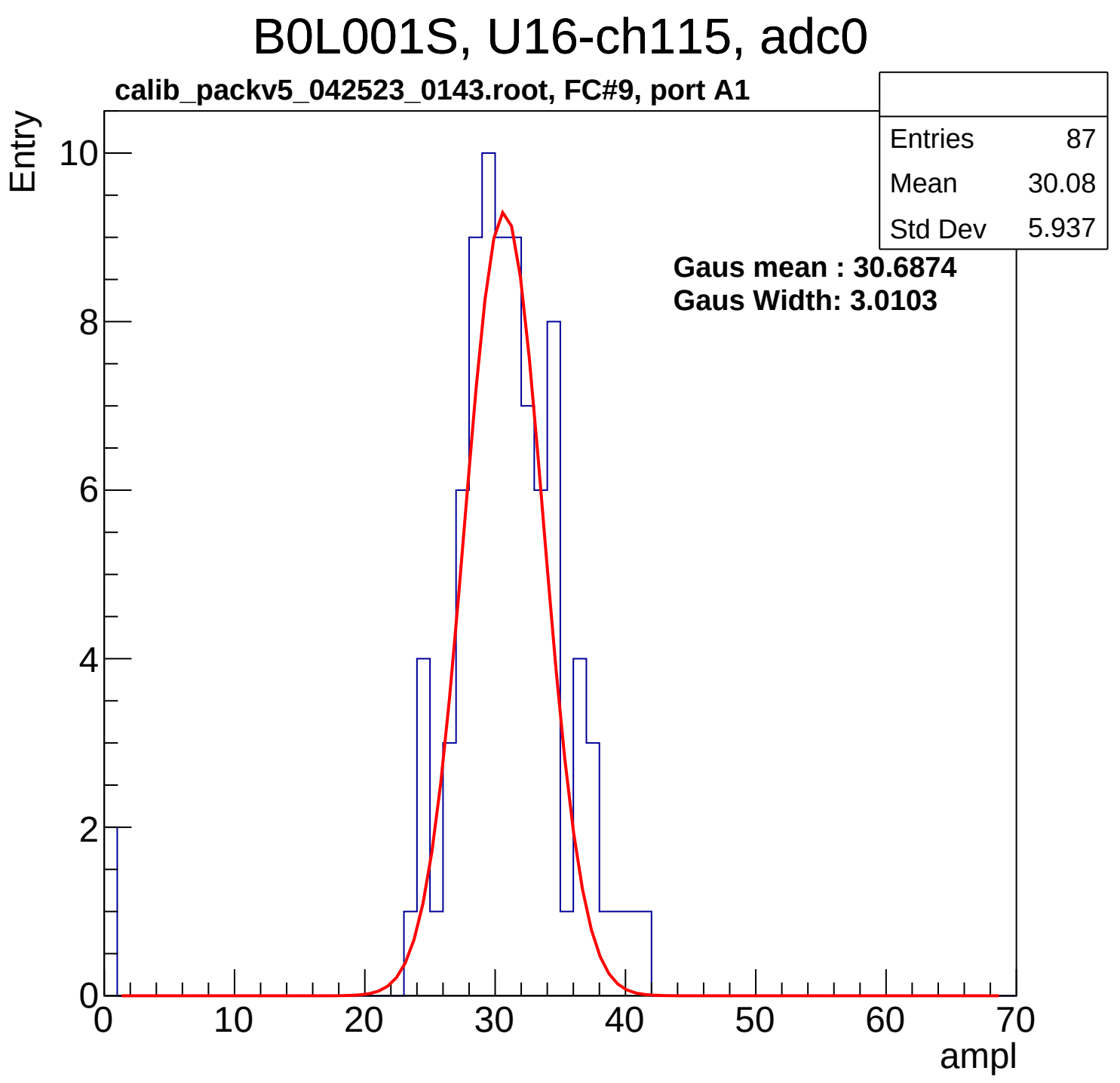
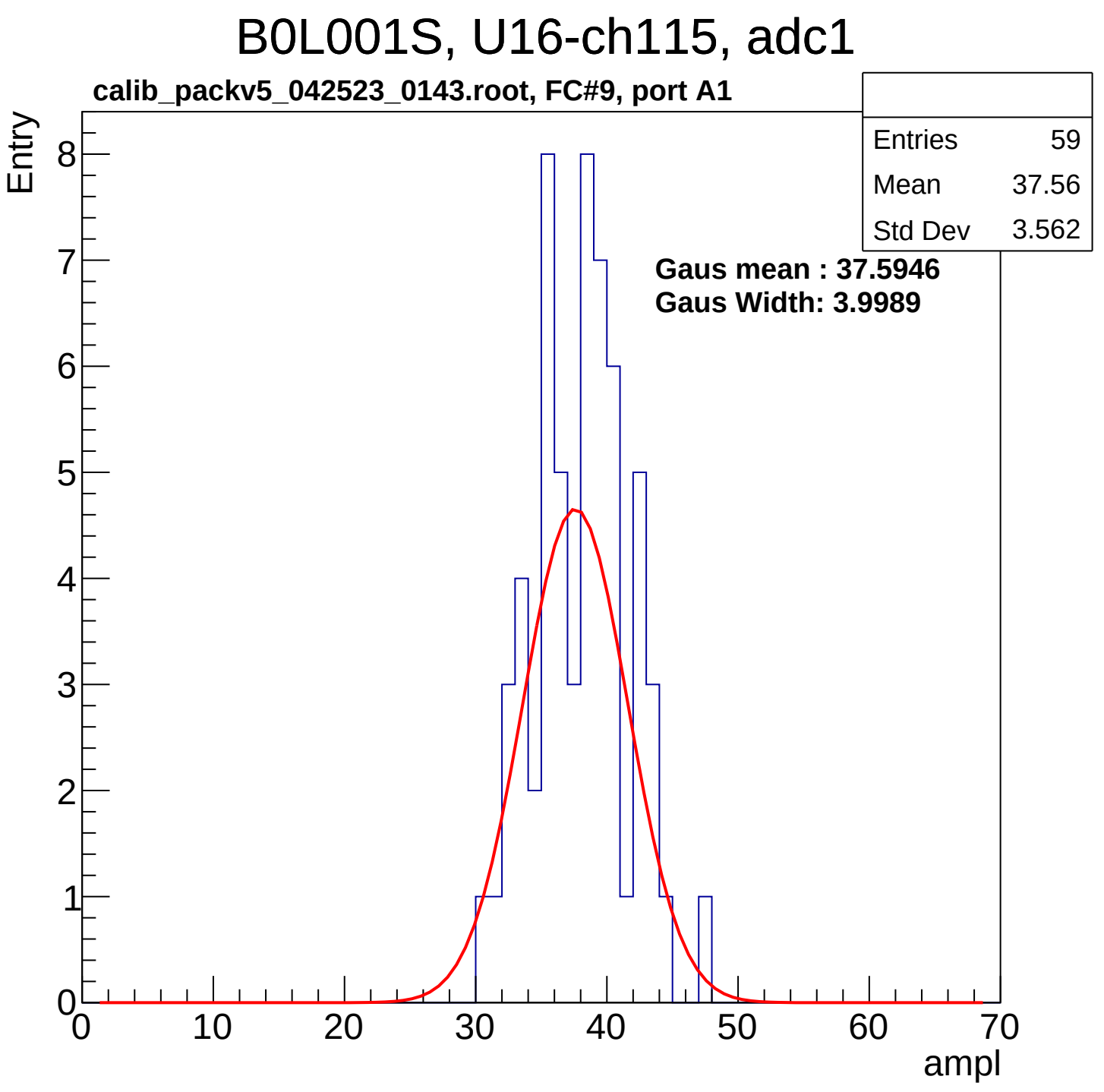
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 30.08
Std Dev 5.937
Gaus mean : 30.6874
Gaus Width: 3.0103
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.56
Std Dev 3.562
Gaus mean : 37.5946
Gaus Width: 3.9989
Entry
ampl
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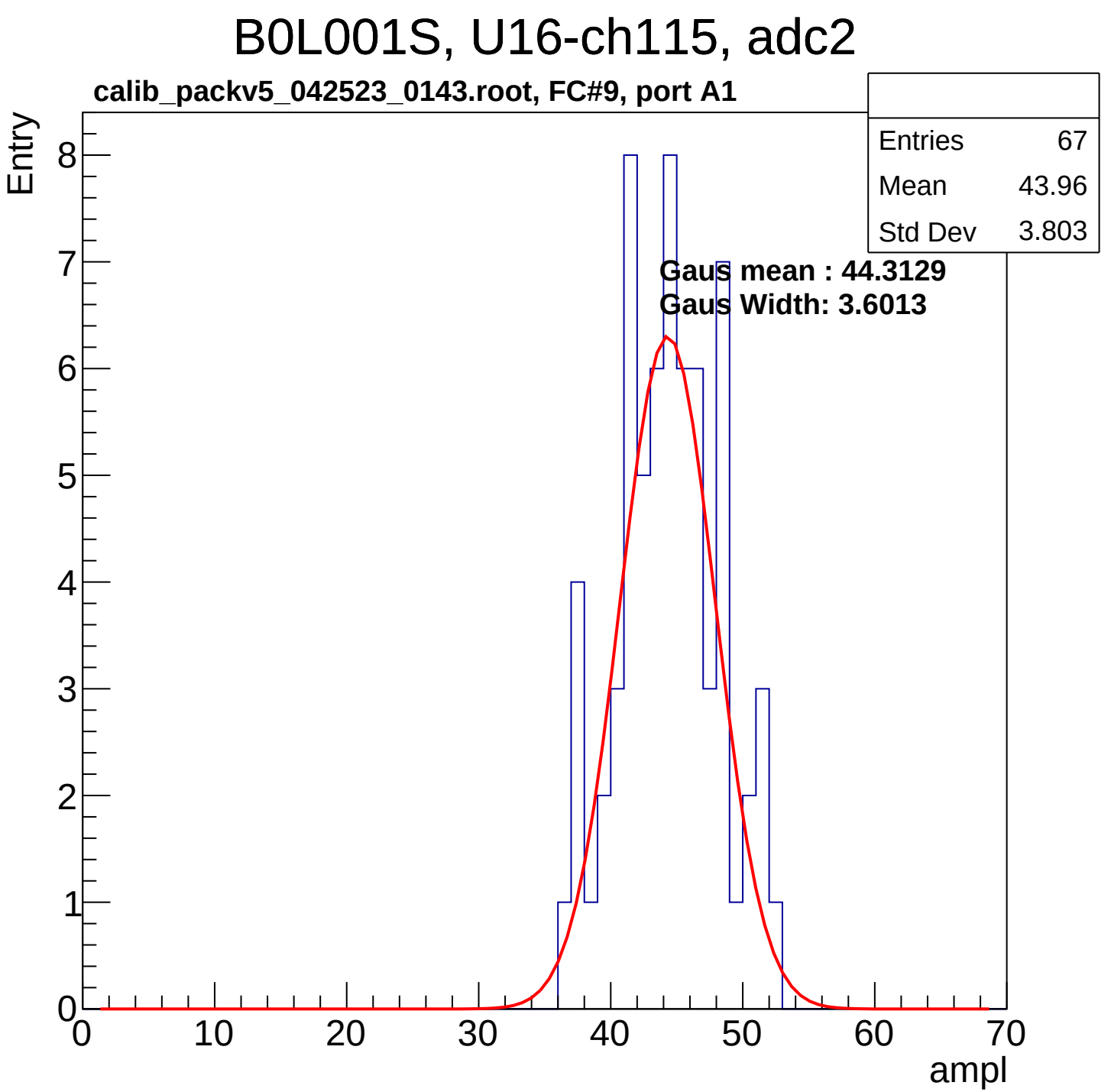
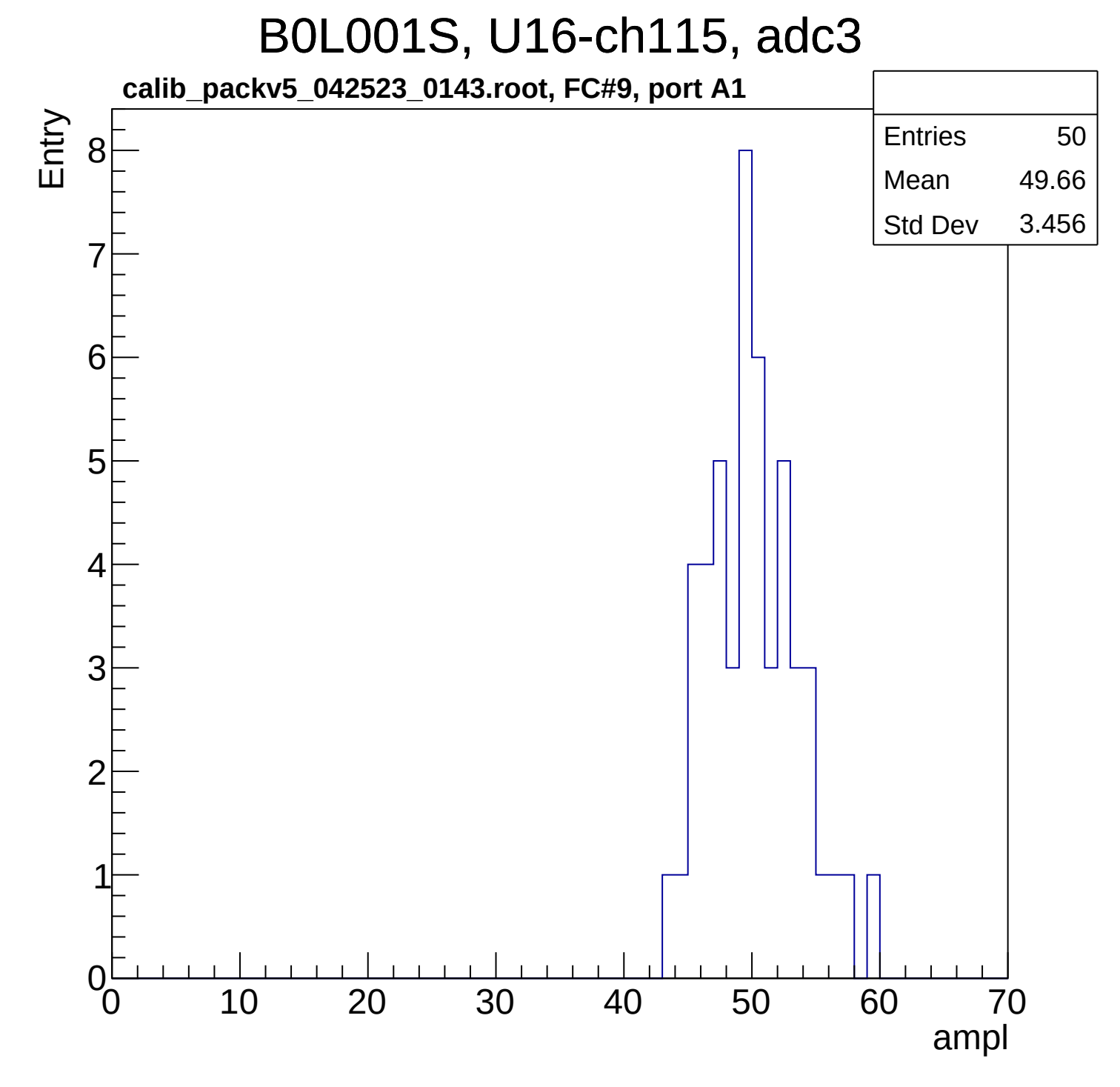
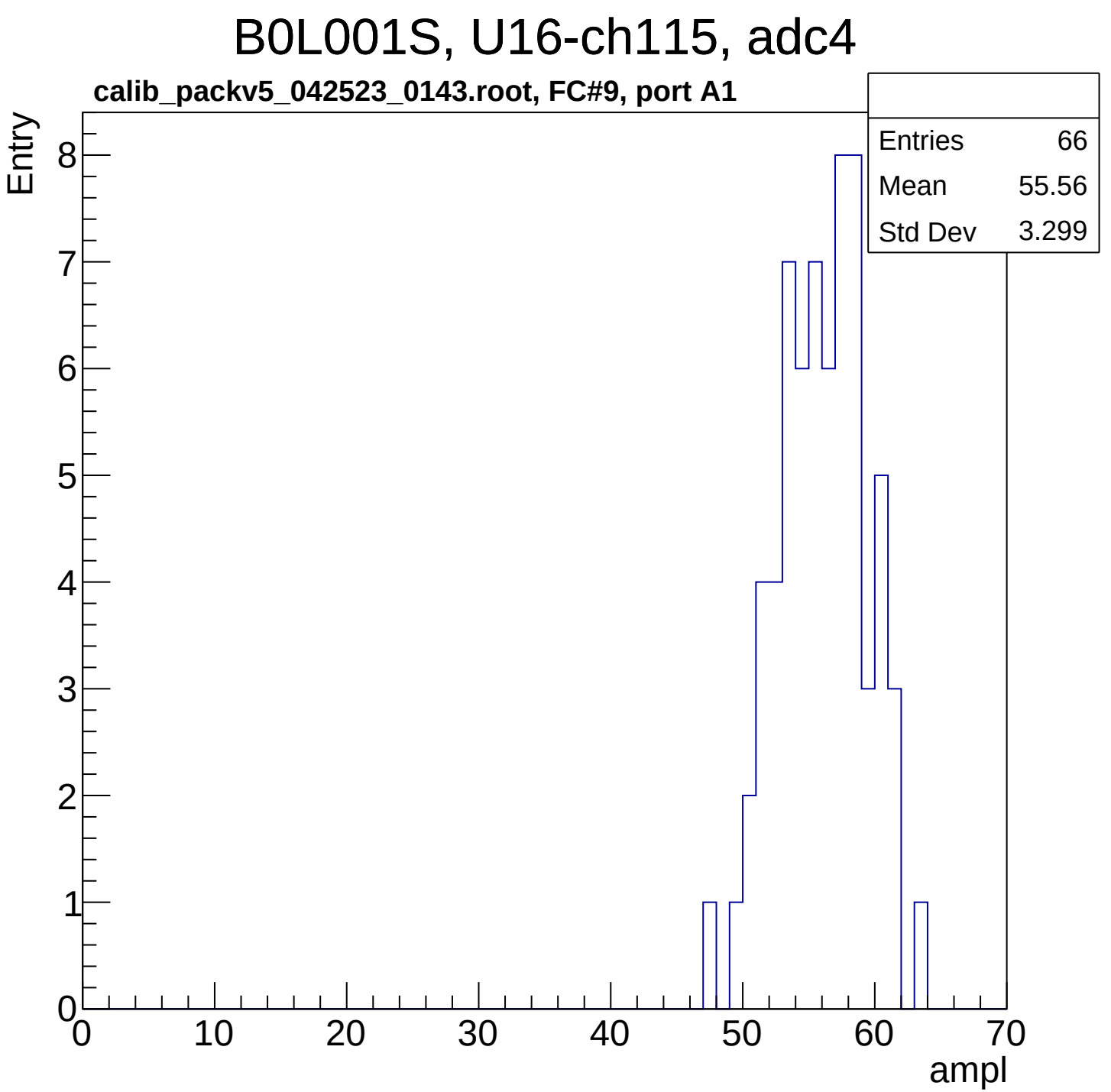
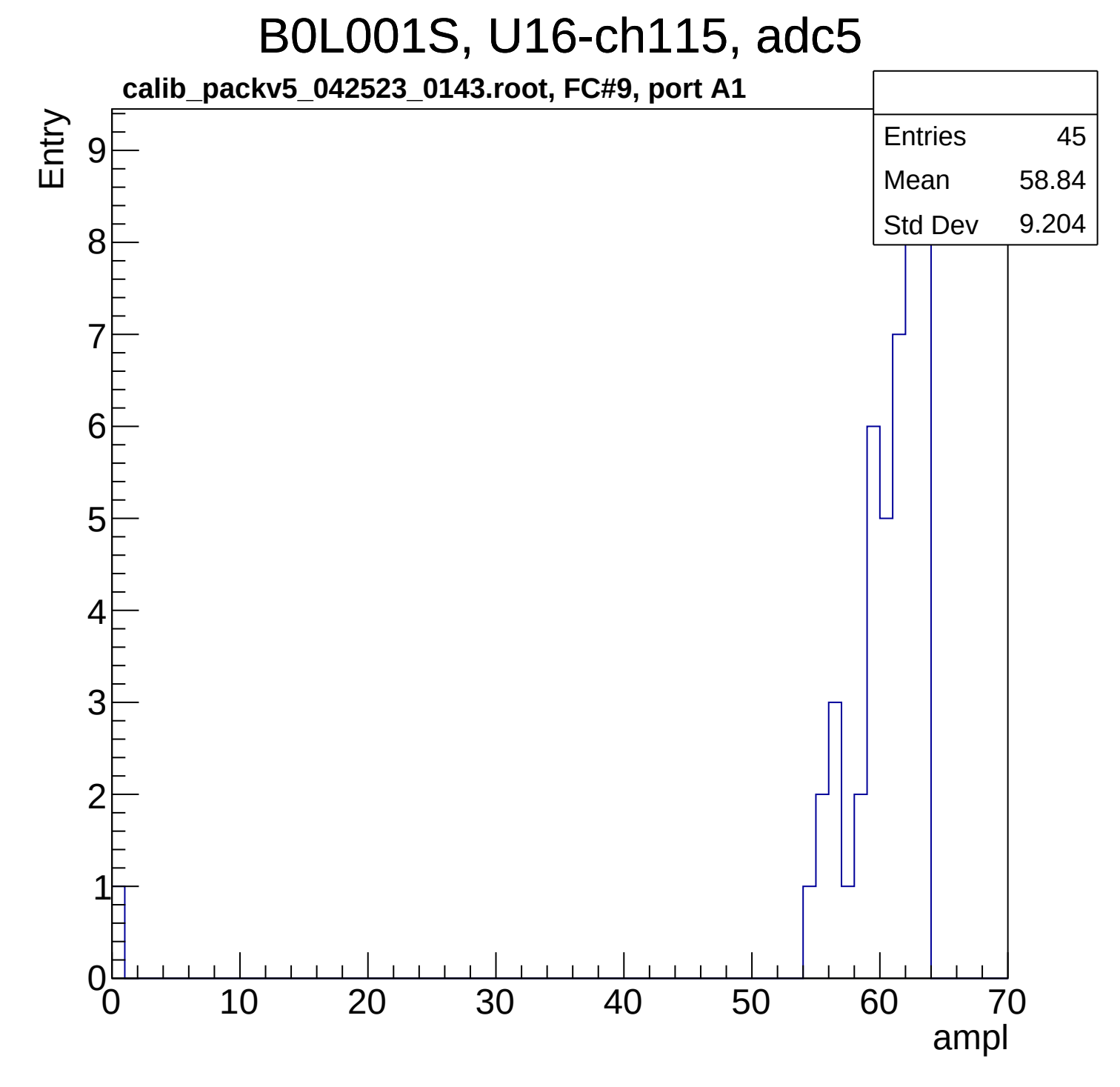
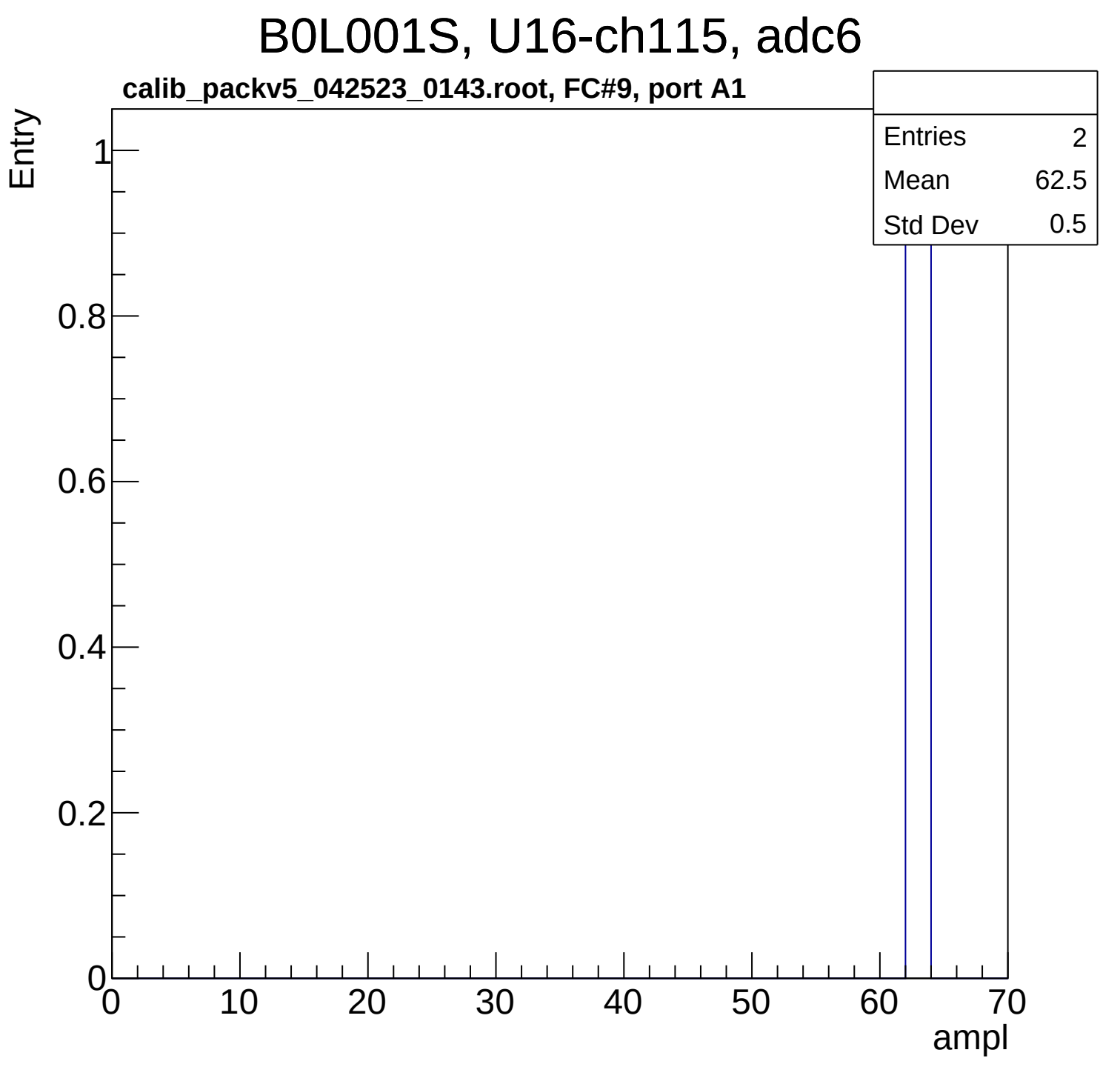
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

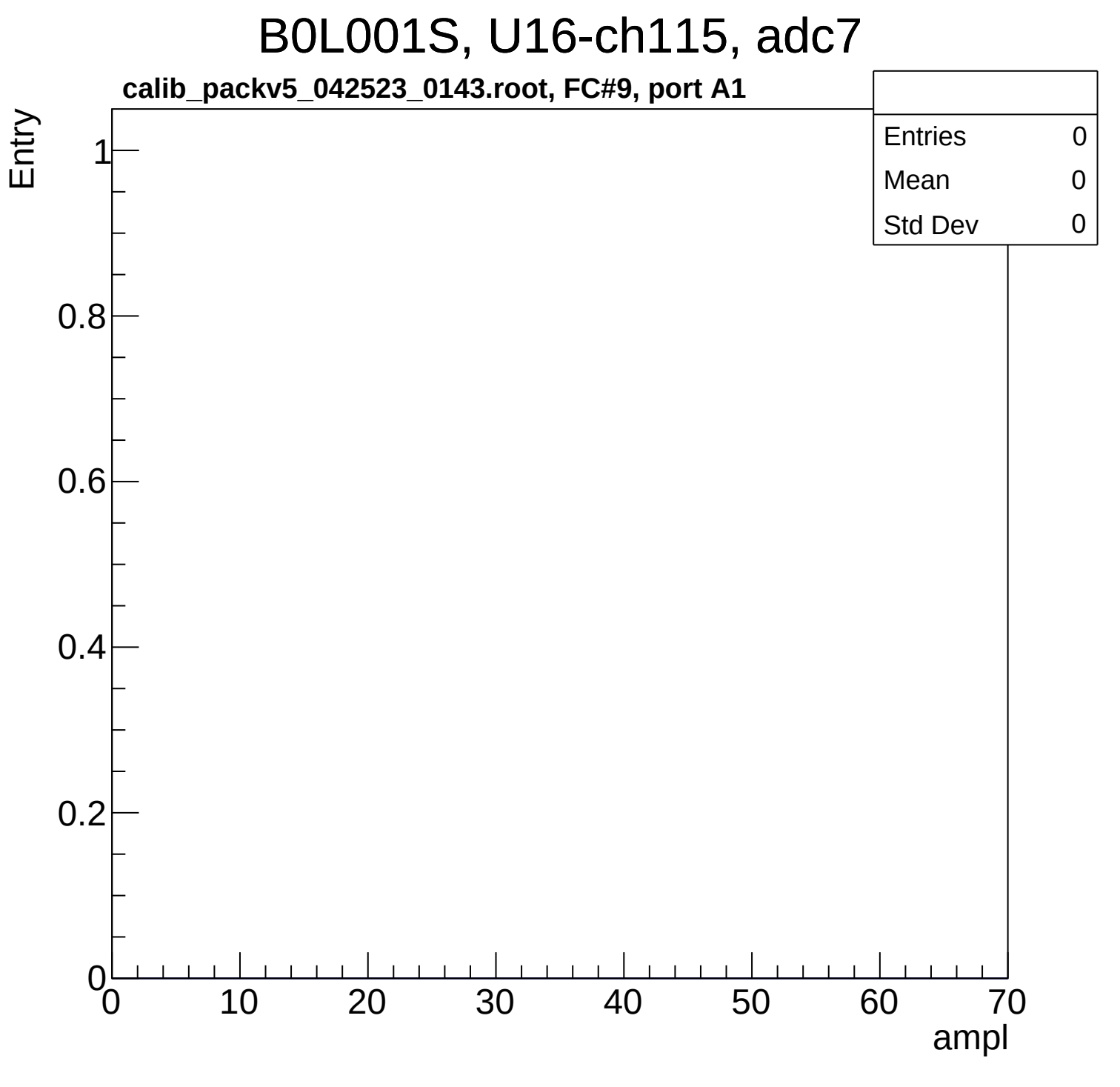

B0L001S, U16-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.96
Std Dev 3.803
Gaus mean : 44.3129
Gaus Width: 3.6013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

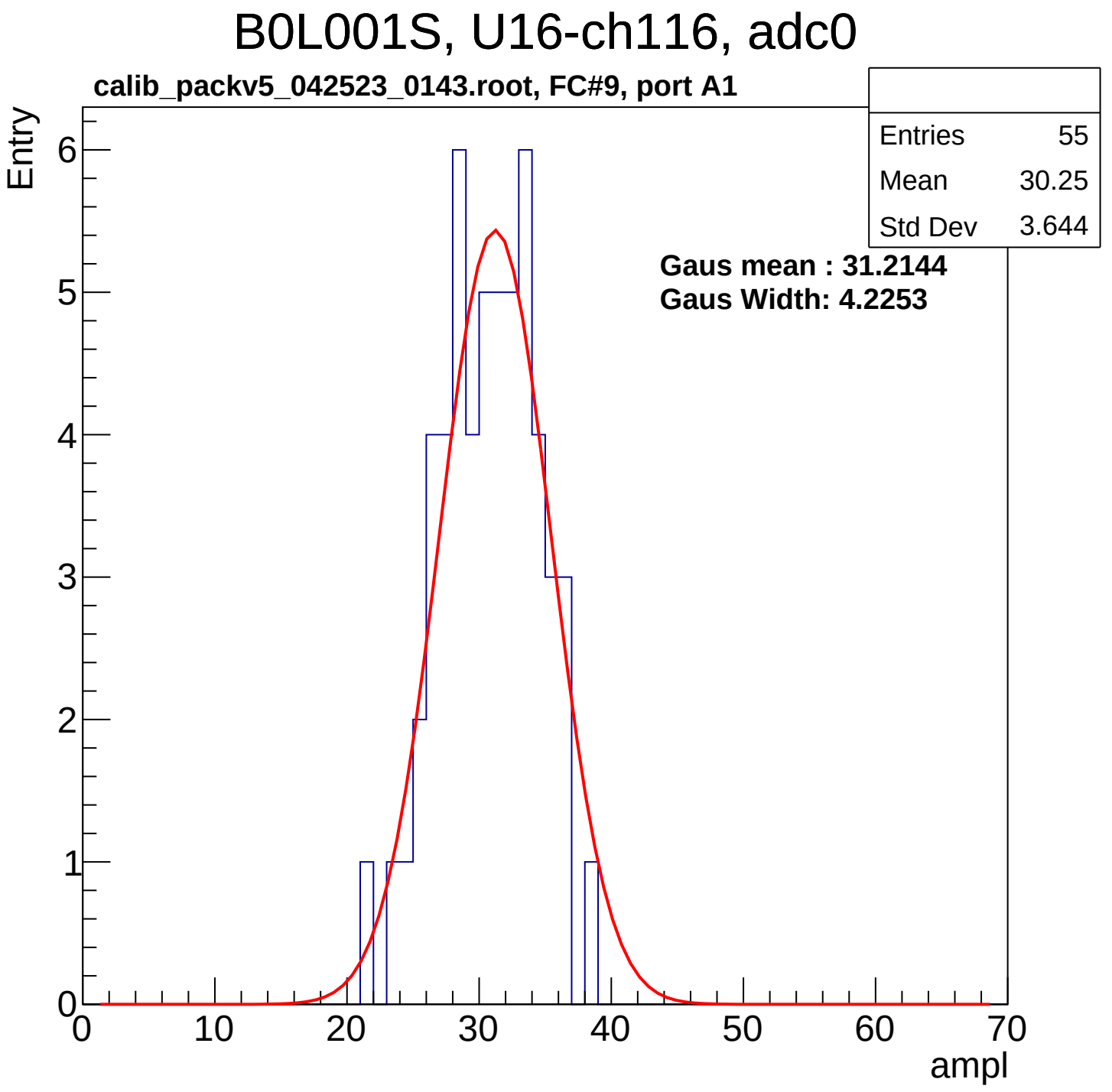

B0L001S, U16-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 49.66
Std Dev 3.456
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

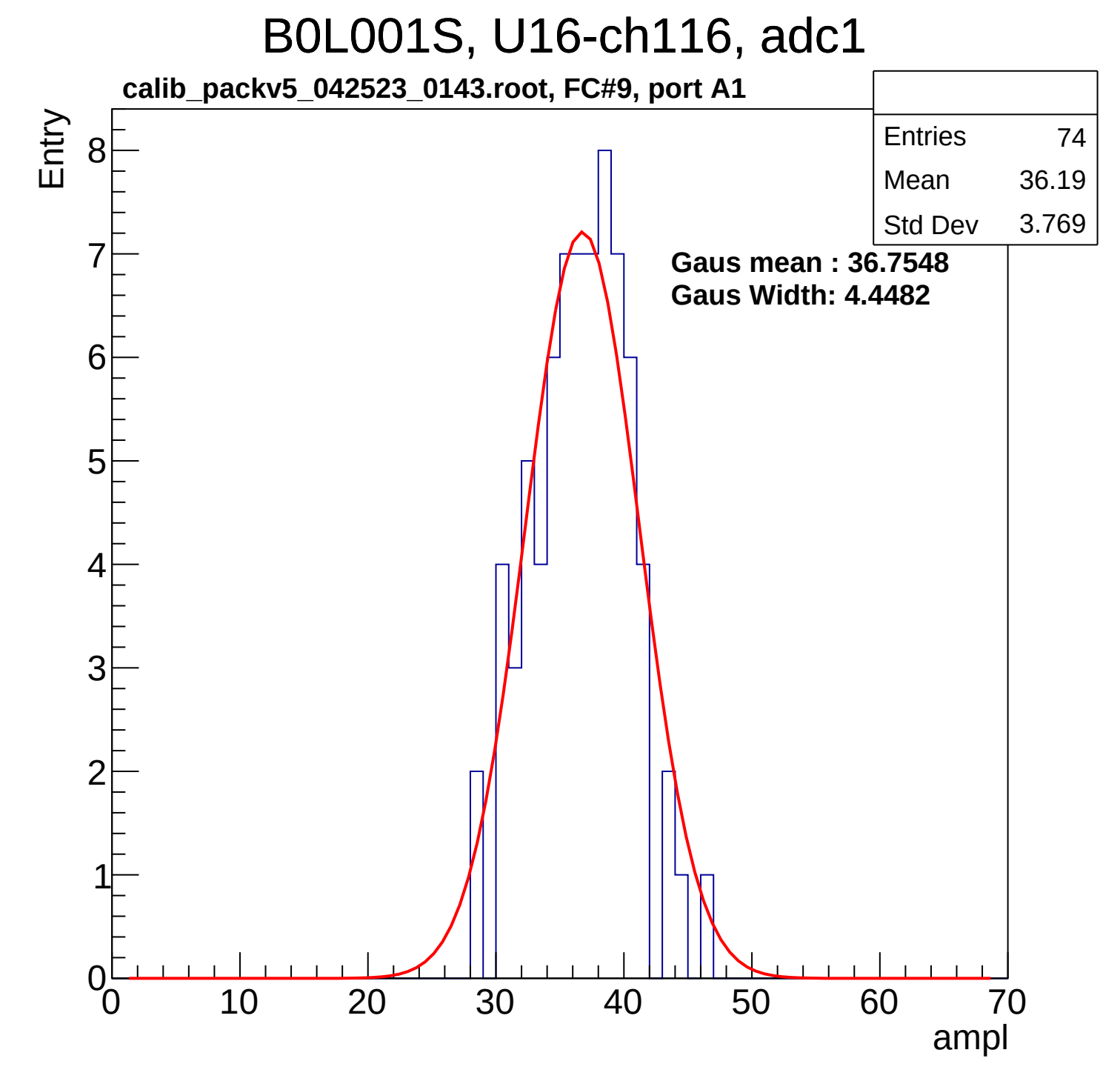

B0L001S, U16-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.56
Std Dev 3.299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

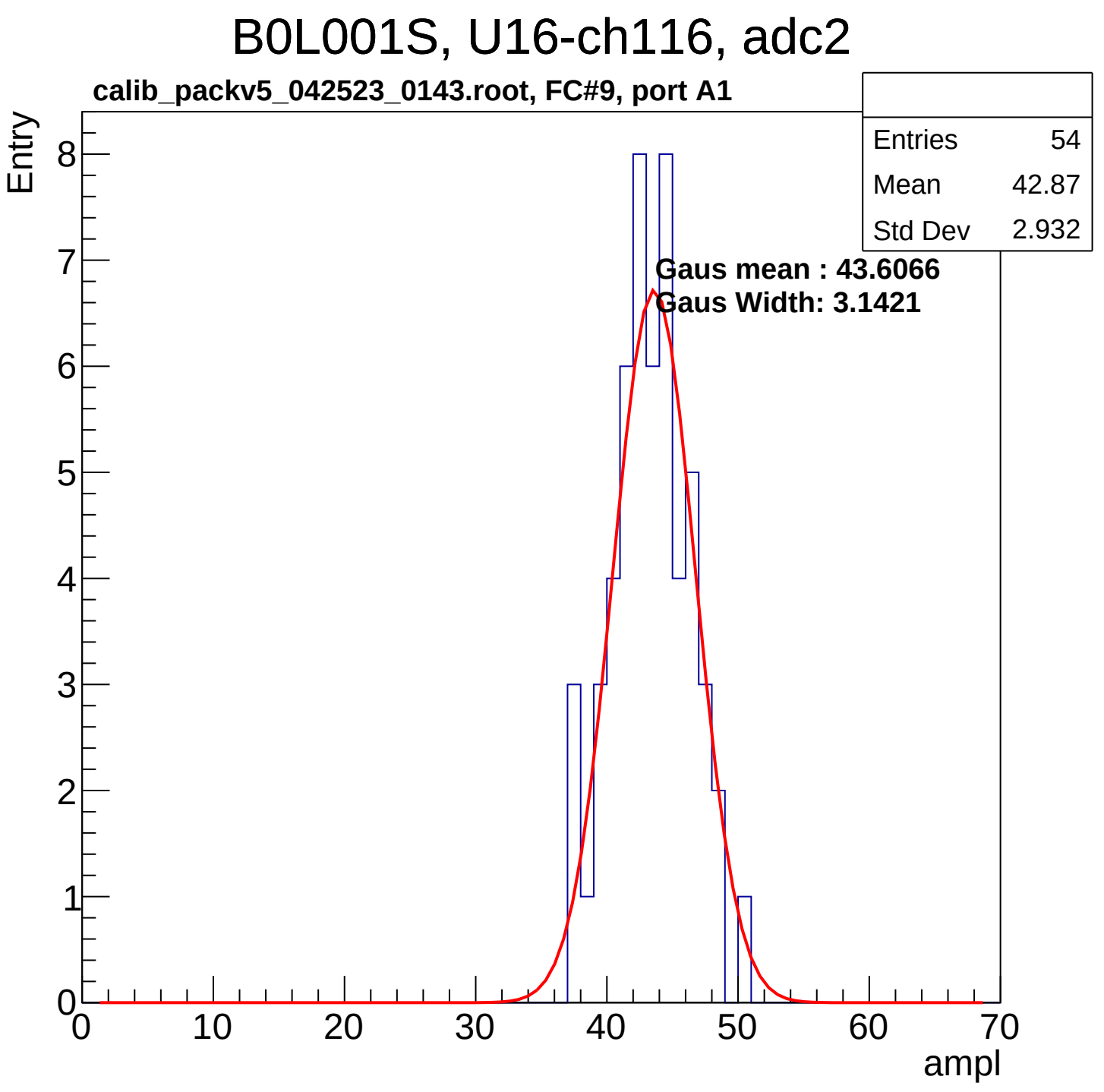

B0L001S, U16-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.84
Std Dev 9.204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

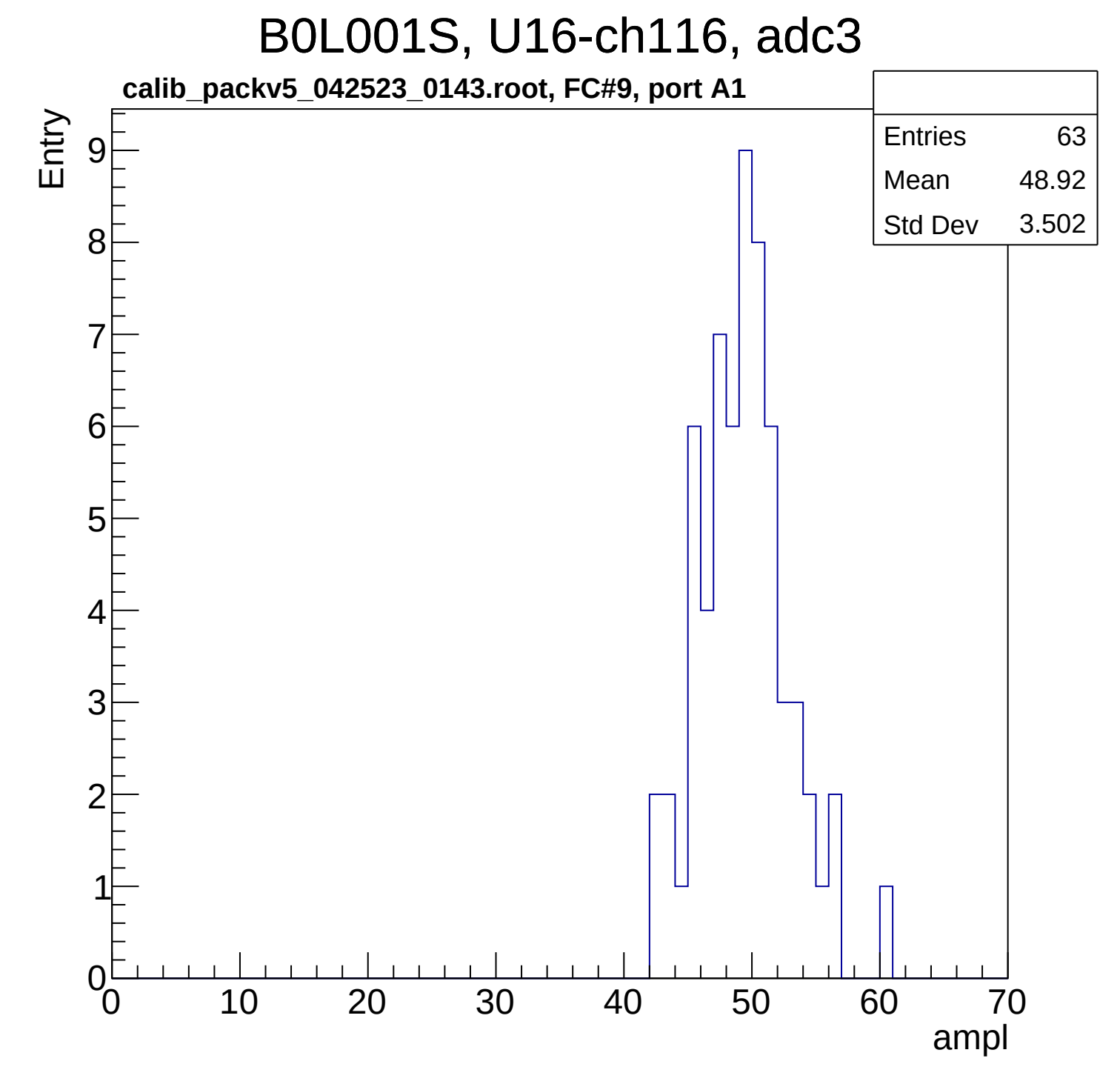

B0L001S, U16-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

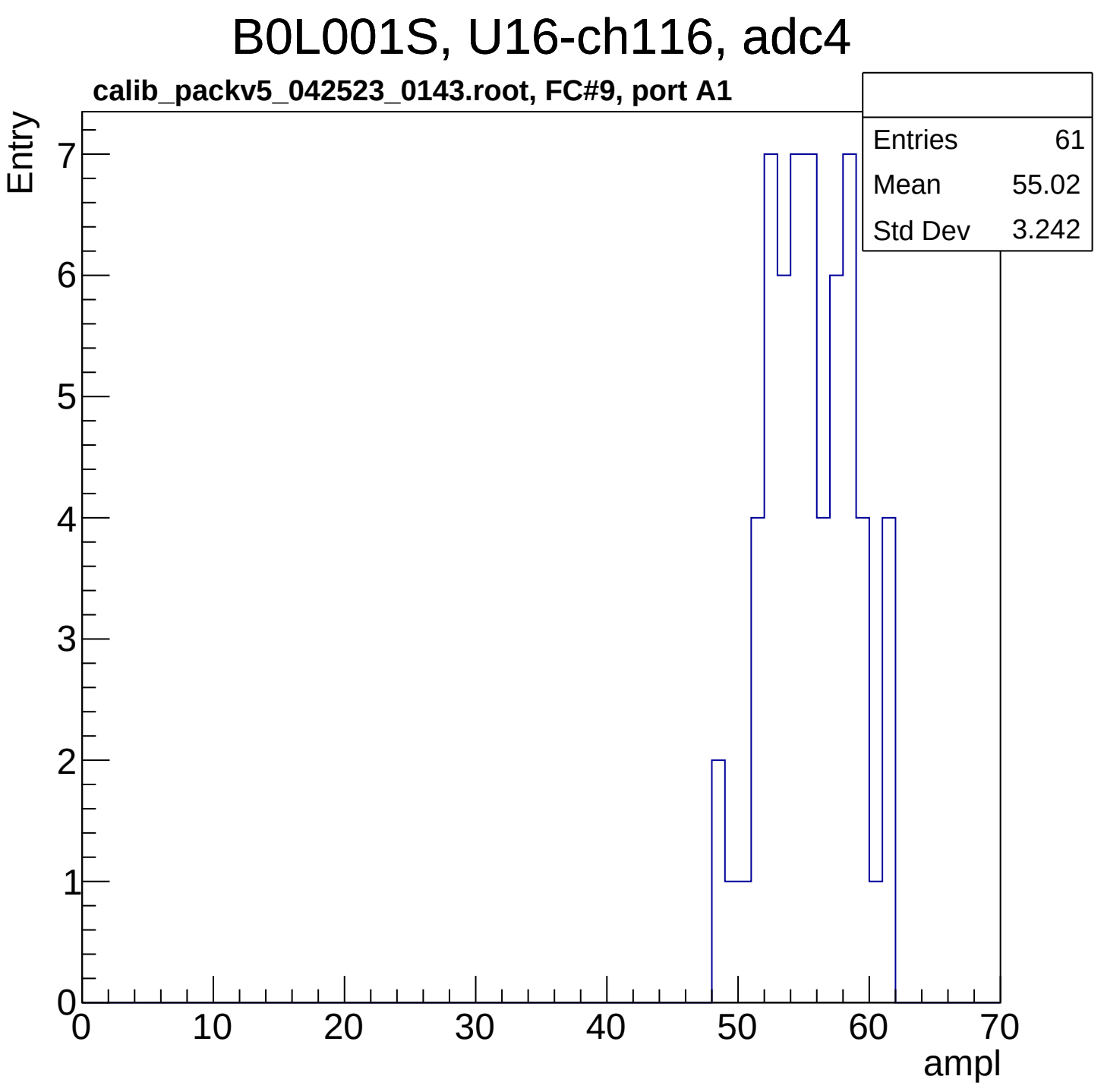

B0L001S, U16-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

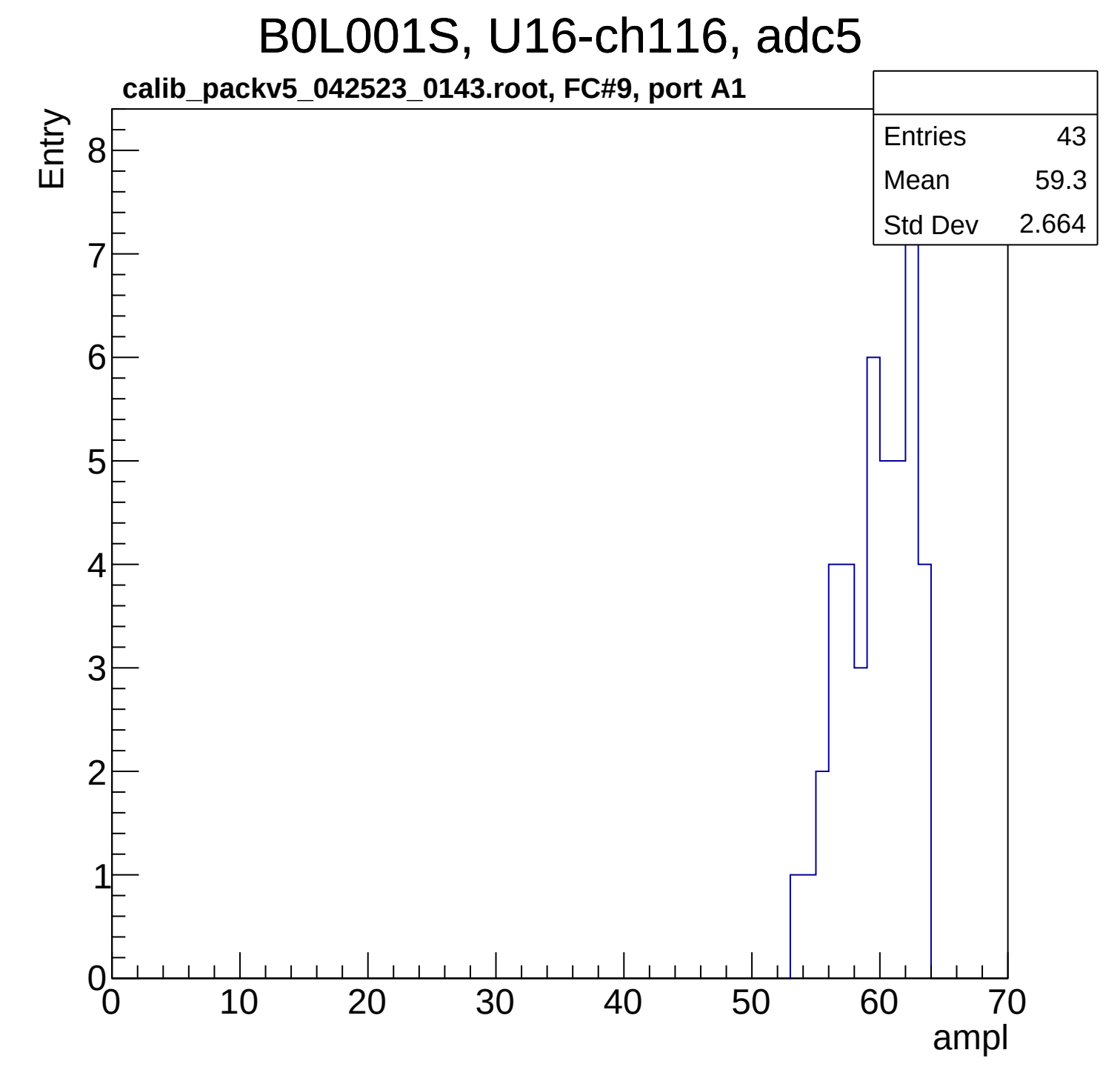

B0L001S, U16-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.25
Std Dev 3.644
Gaus mean : 31.2144
Gaus Width: 4.2253
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

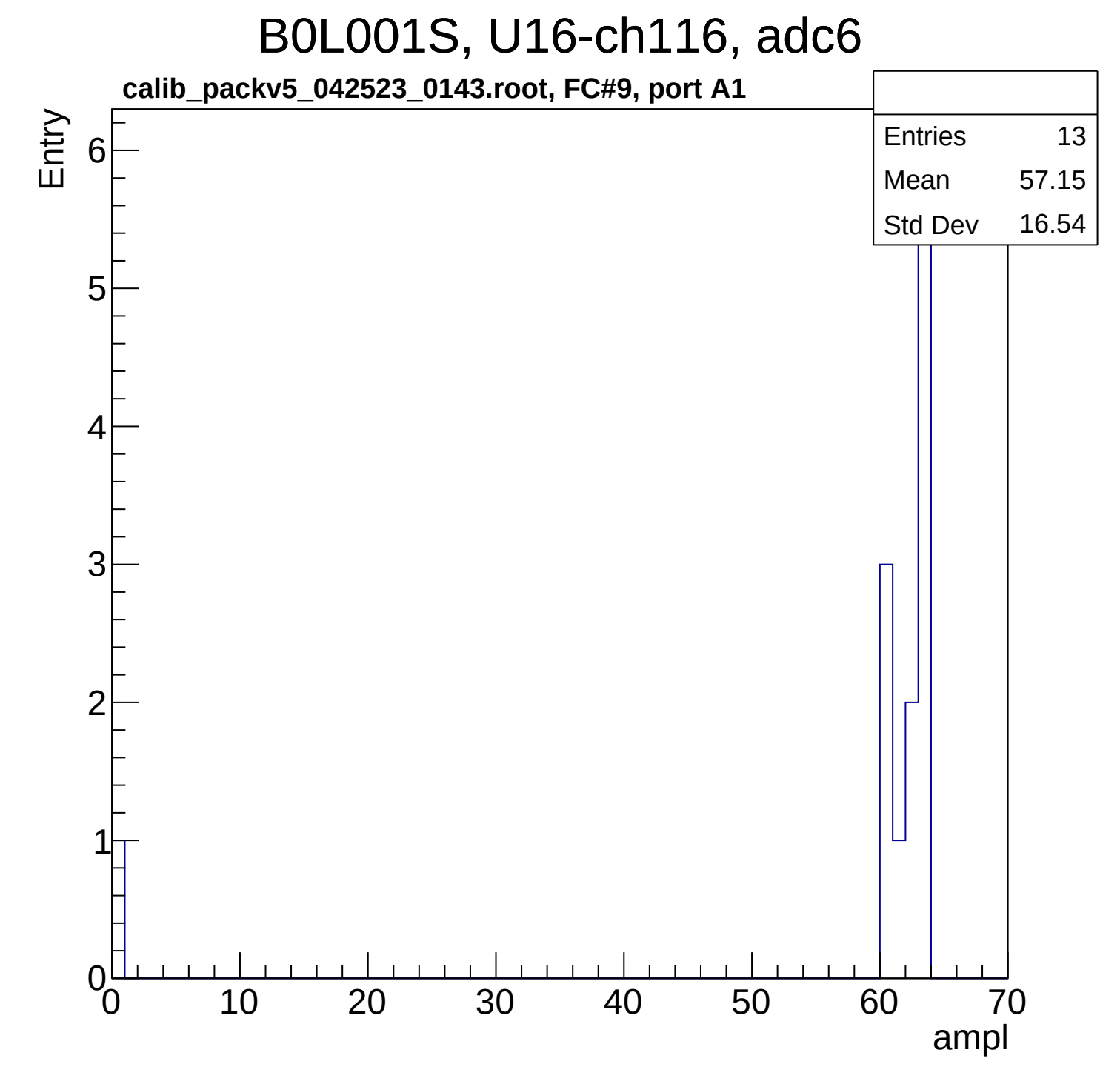

B0L001S, U16-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.19
Std Dev 3.769
Gaus mean : 36.7548
Gaus Width: 4.4482
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

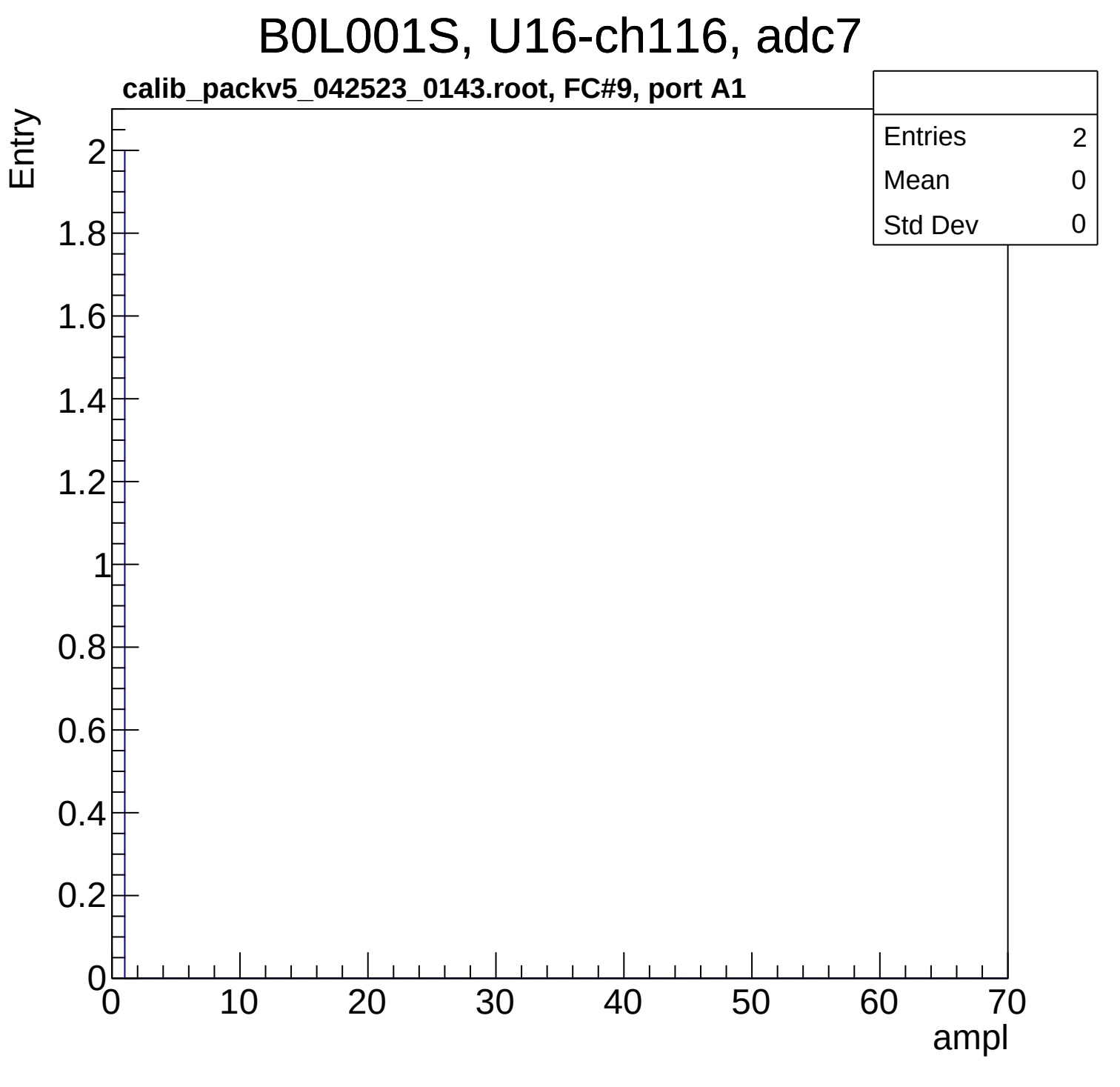

B0L001S, U16-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 42.87
Std Dev 2.932
Gaus mean : 43.6066
Gaus Width: 3.1421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

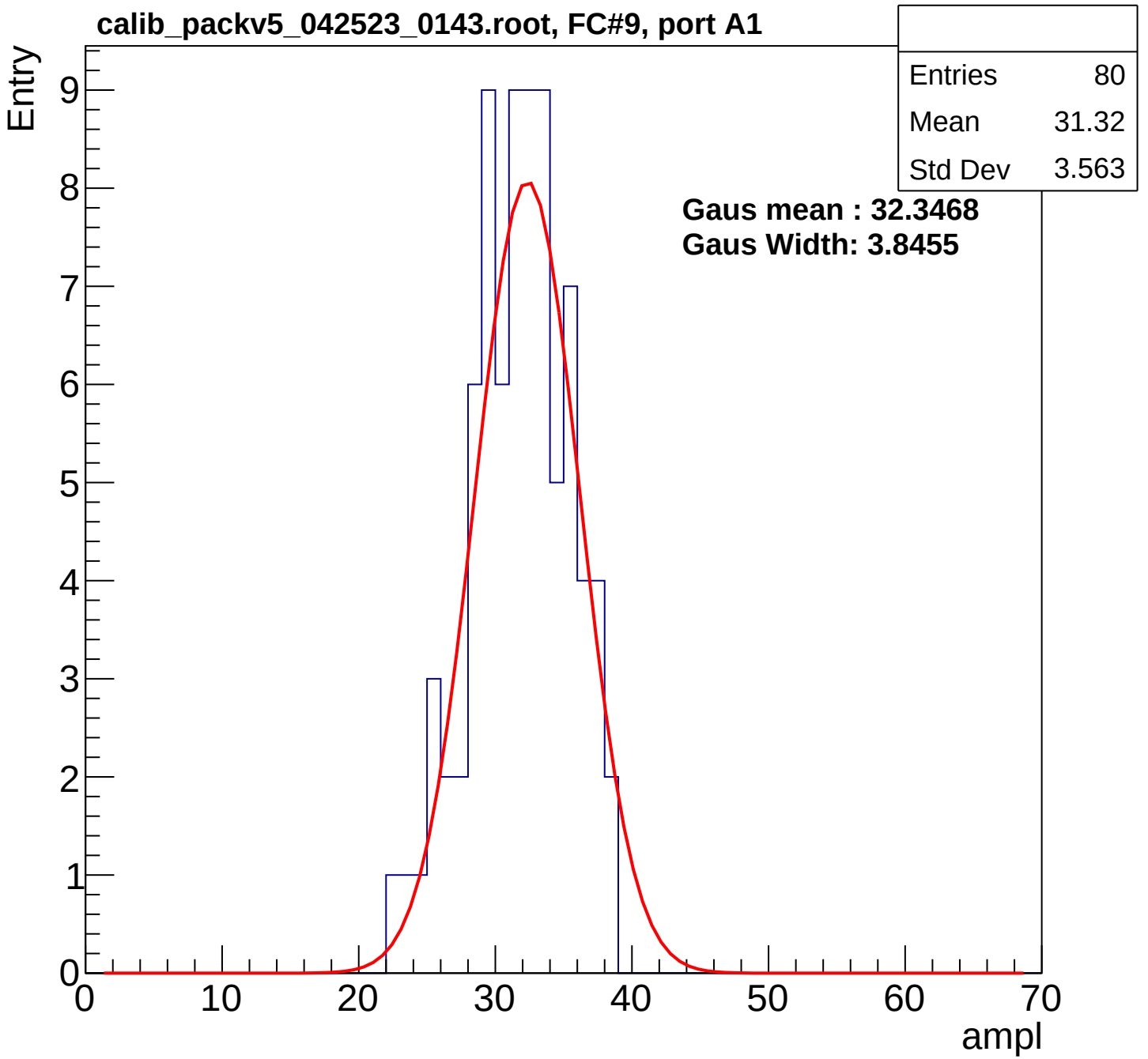

B0L001S, U16-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 48.92
Std Dev 3.502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

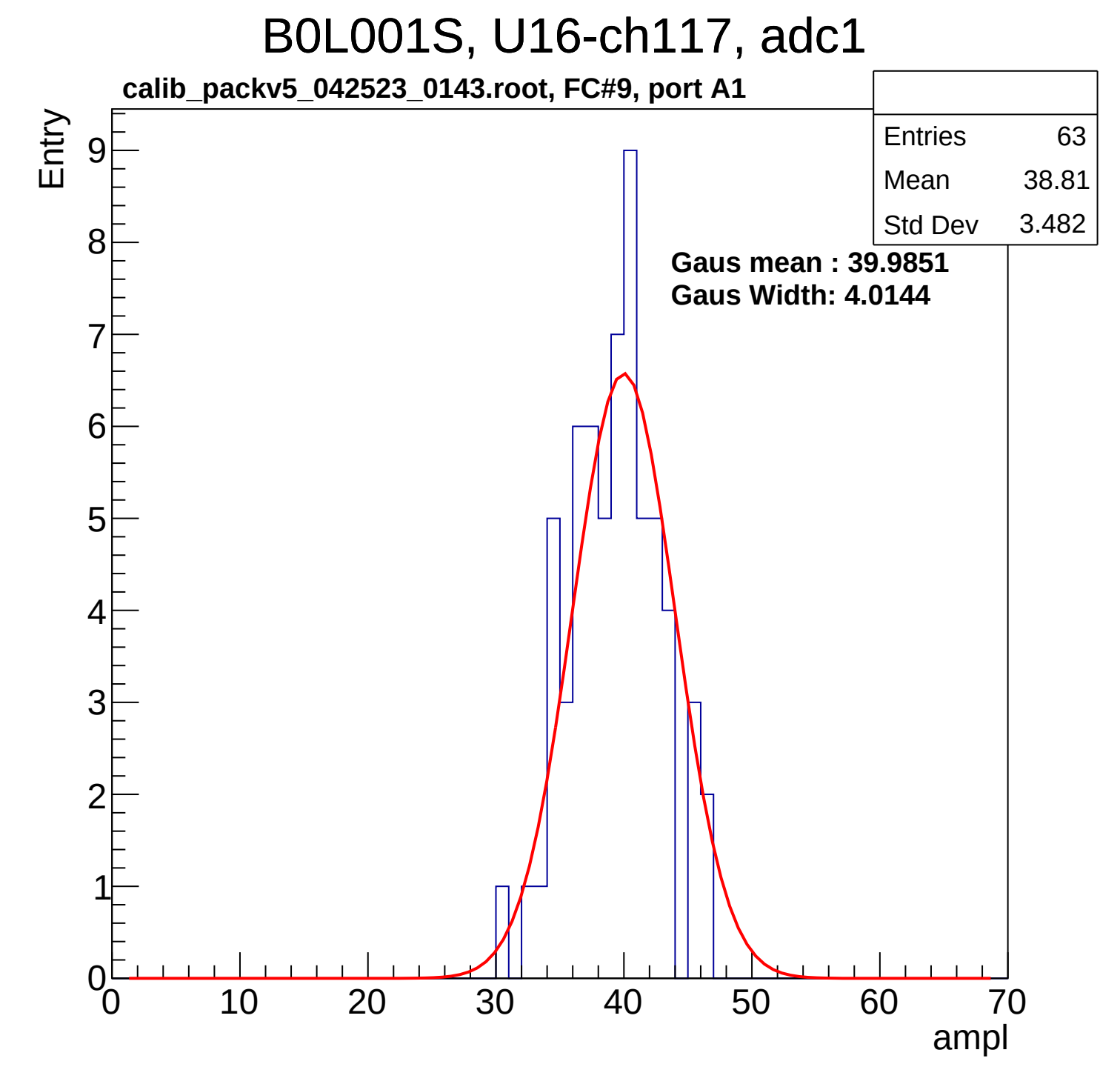

B0L001S, U16-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 55.02
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

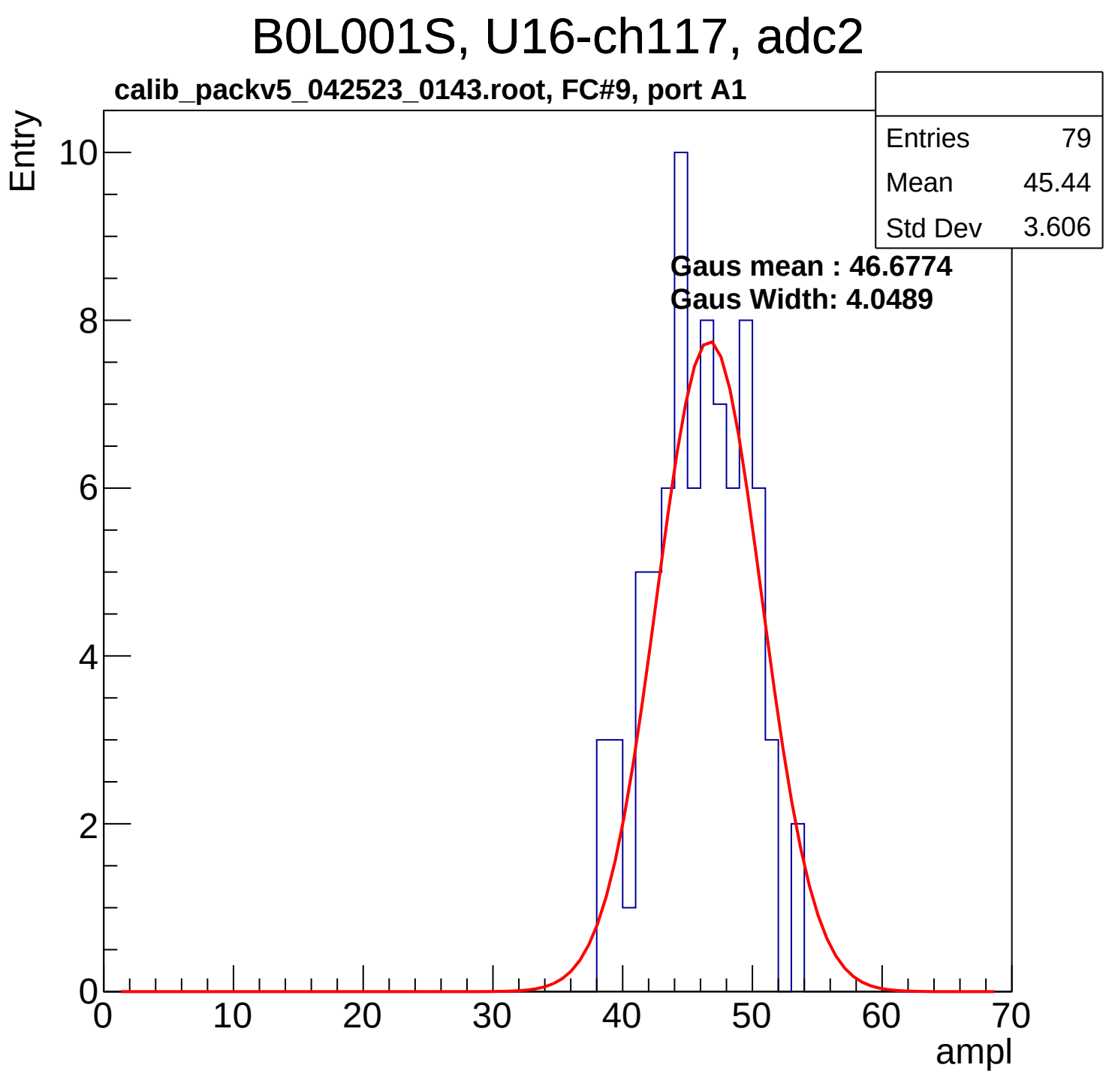

B0L001S, U16-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.3
Std Dev 2.664
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

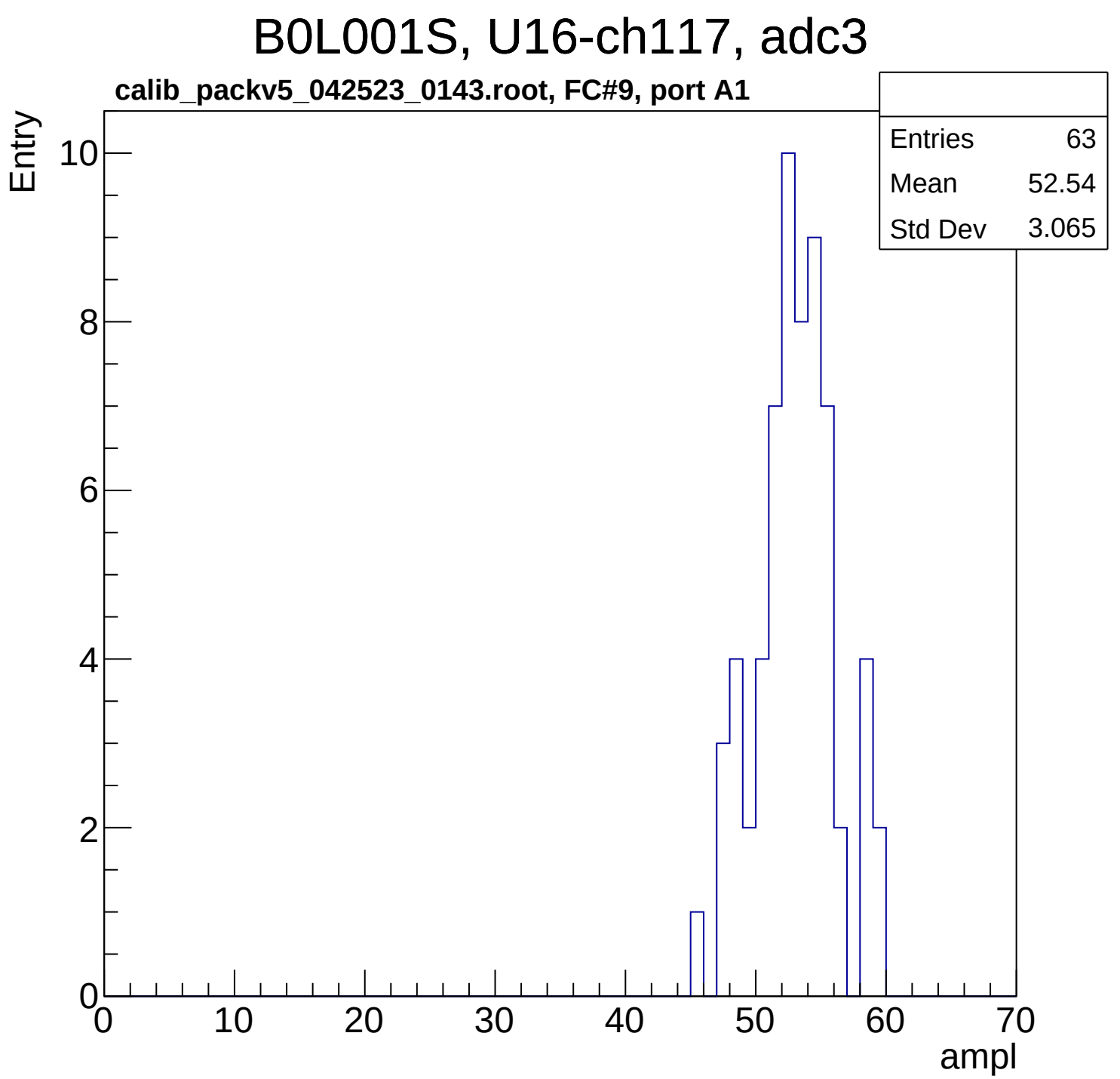

B0L001S, U16-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 57.15
Std Dev 16.54
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

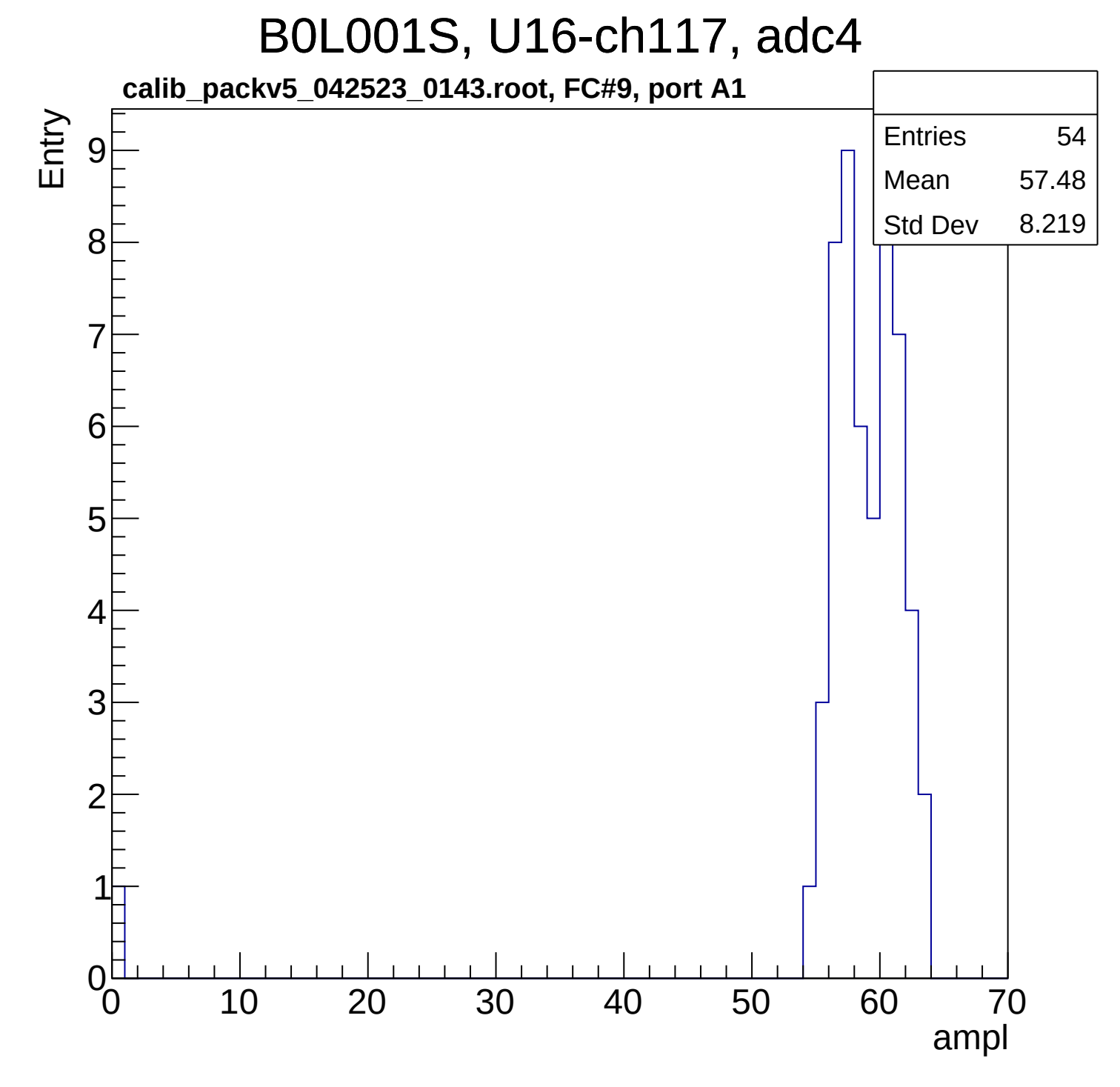

B0L001S, U16-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

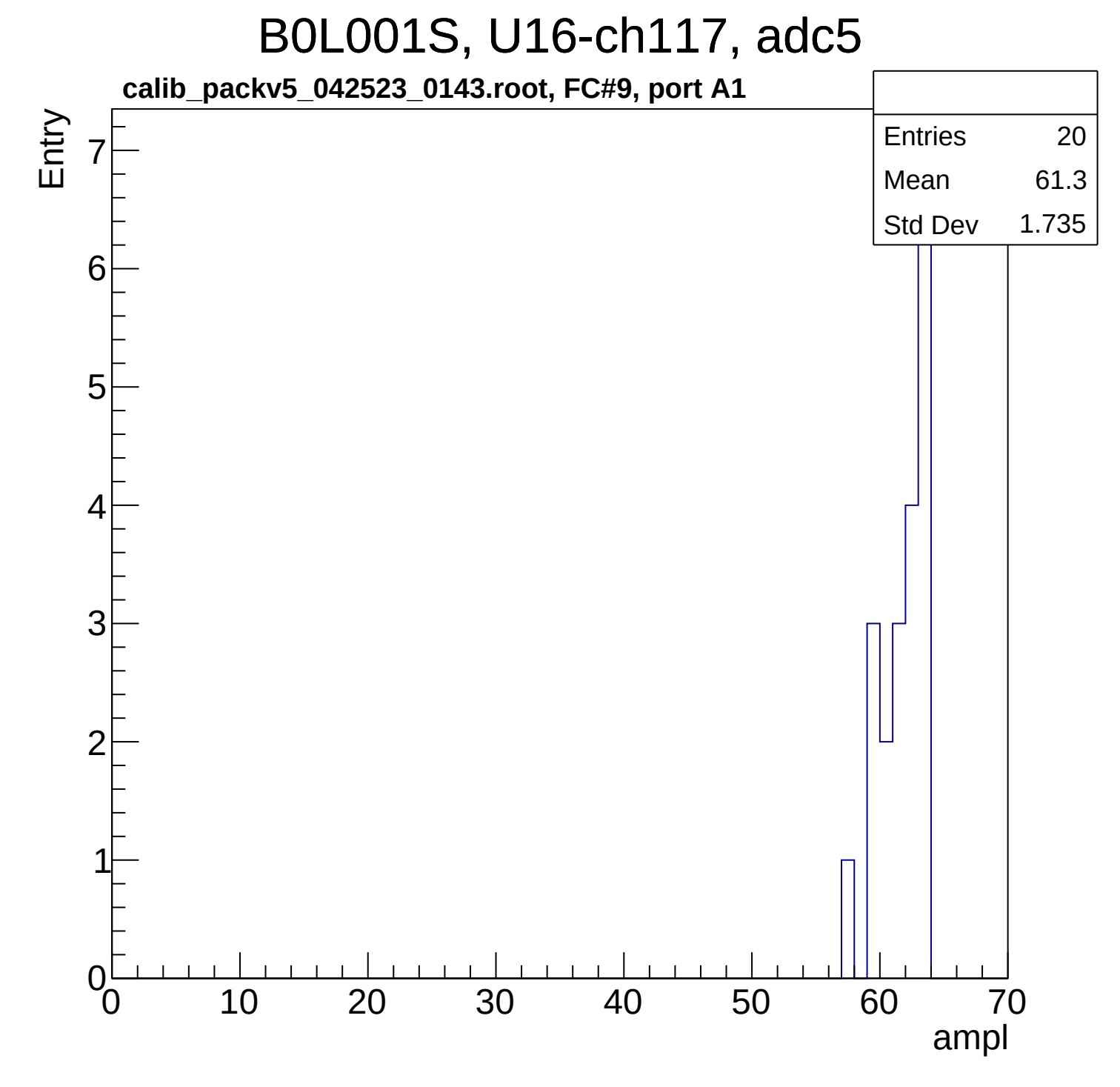

B0L001S, U16-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 31.32
Std Dev 3.563
Gaus mean : 32.3468
Gaus Width: 3.8455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

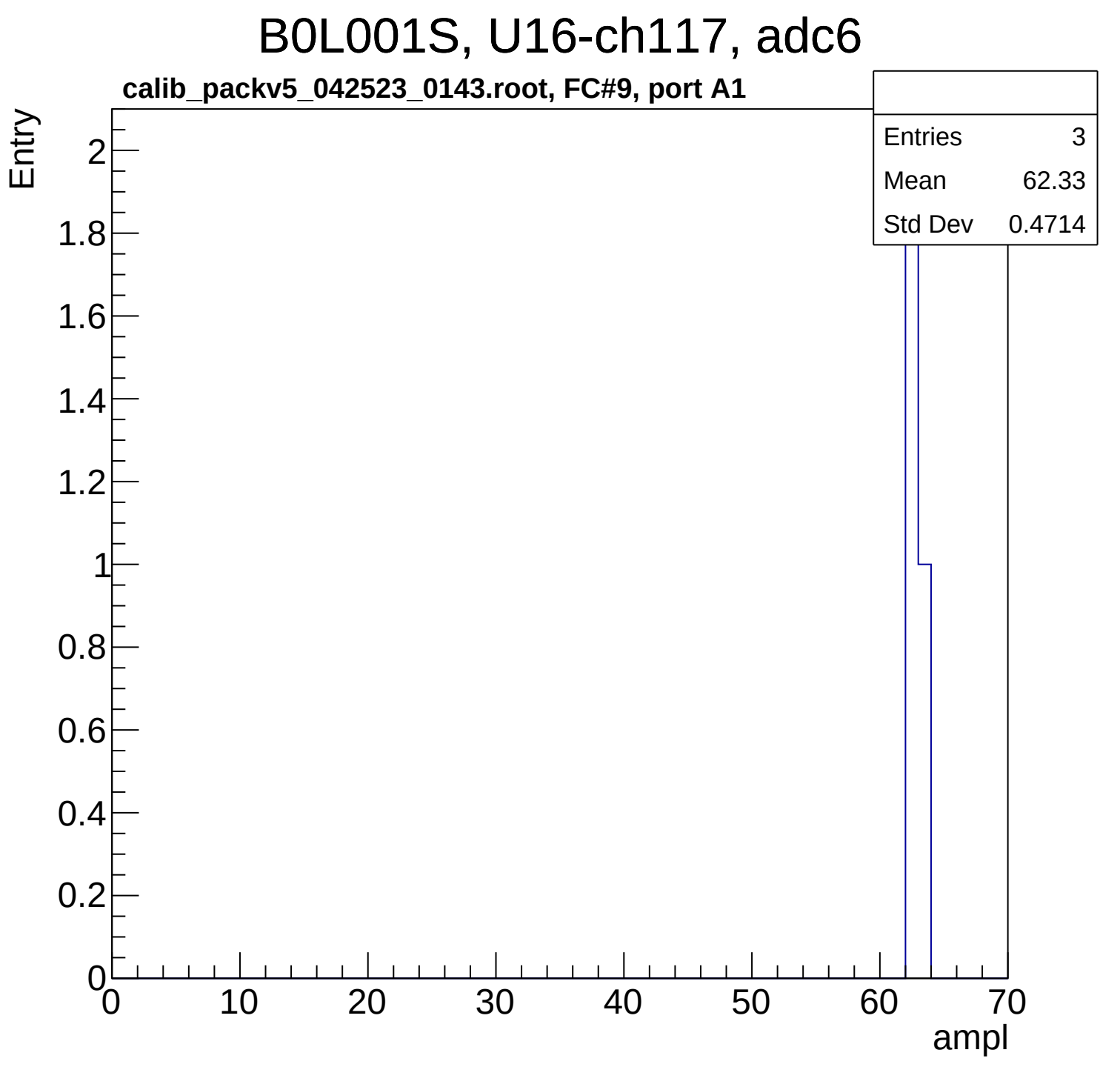

B0L001S, U16-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.81
Std Dev 3.482
Gaus mean : 39.9851
Gaus Width: 4.0144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

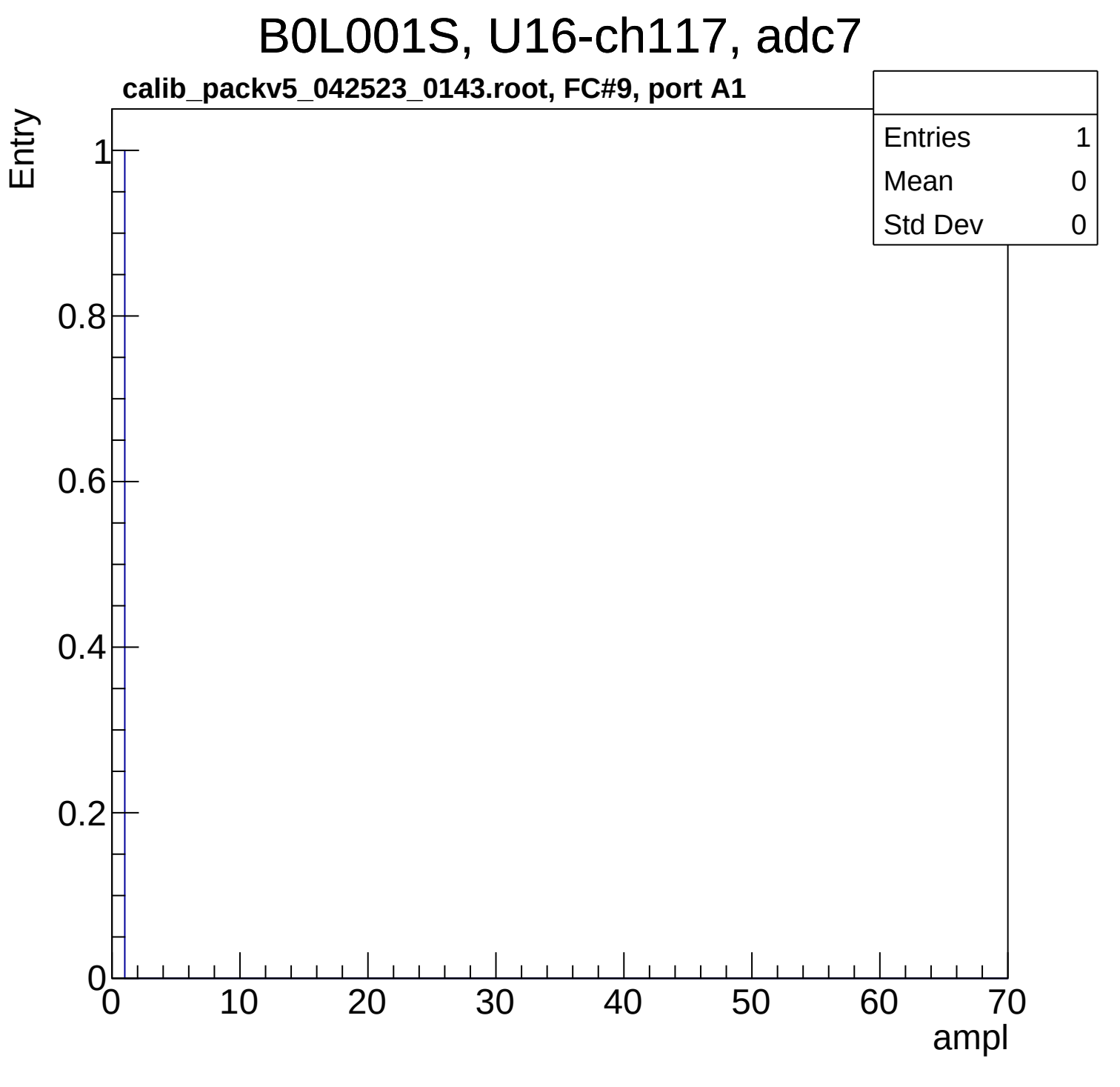

B0L001S, U16-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 45.44
Std Dev 3.606
Gaus mean : 46.6774
Gaus Width: 4.0489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

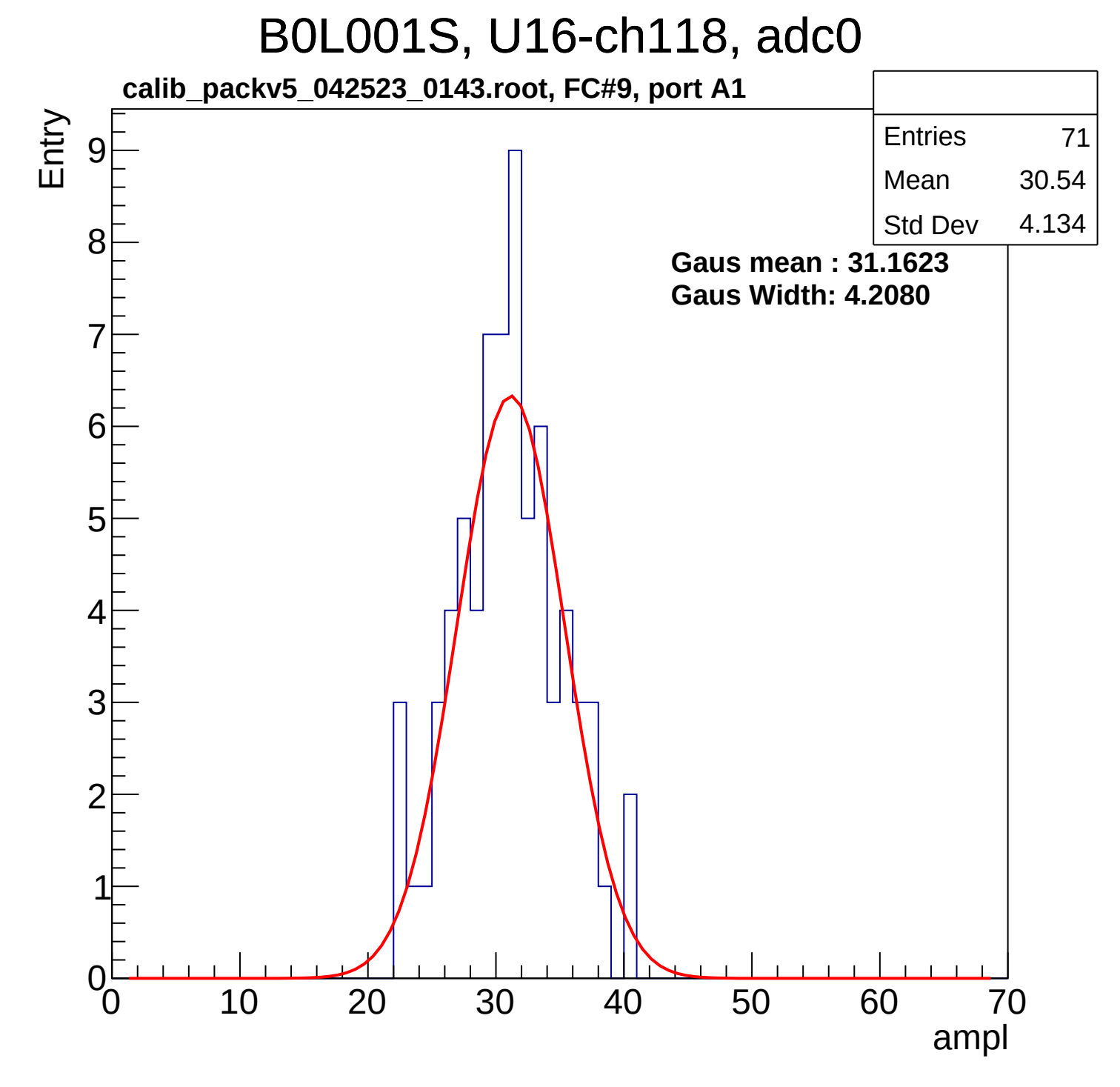

B0L001S, U16-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 52.54
Std Dev 3.065
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

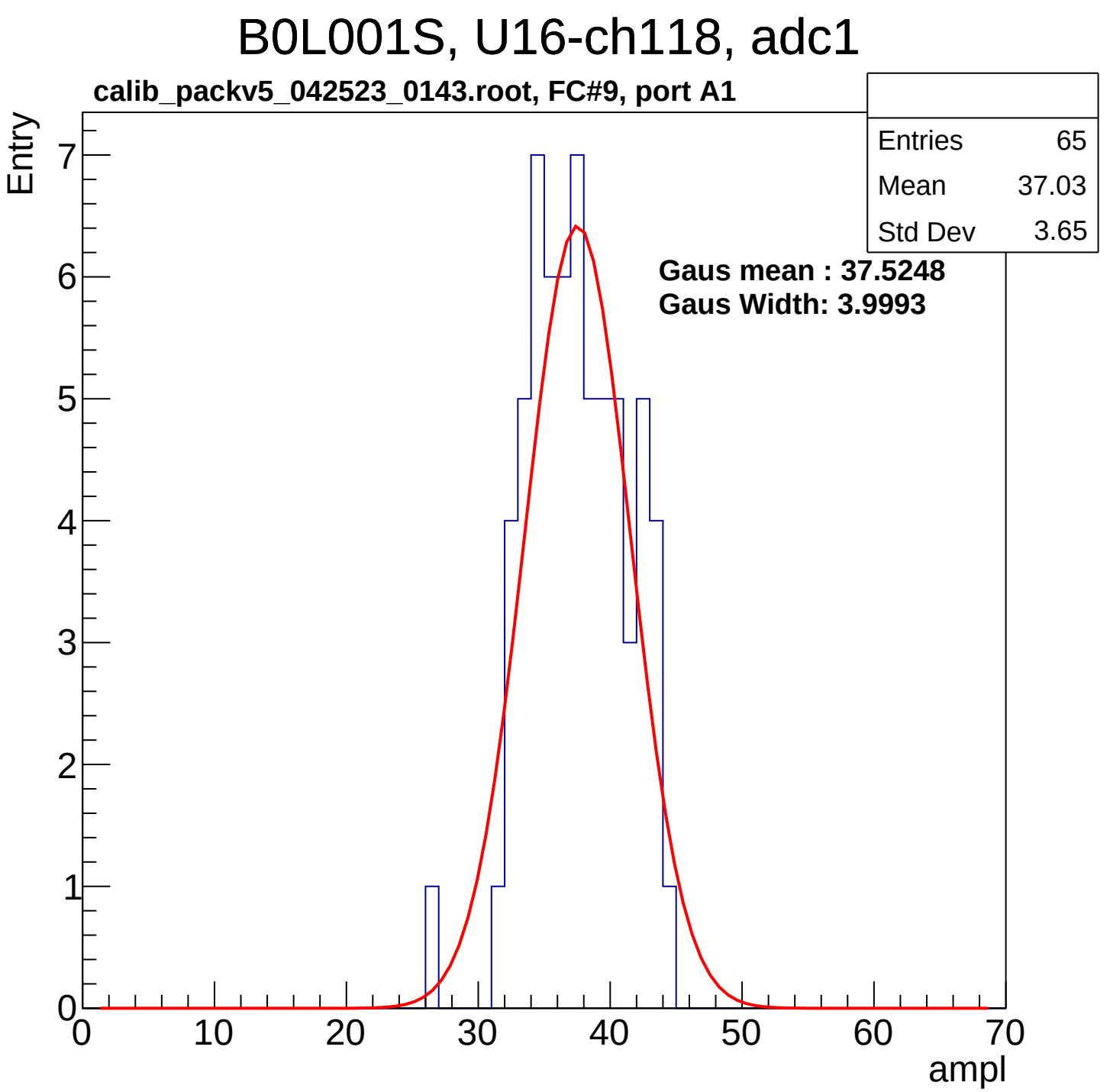

B0L001S, U16-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.48
Std Dev 8.219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

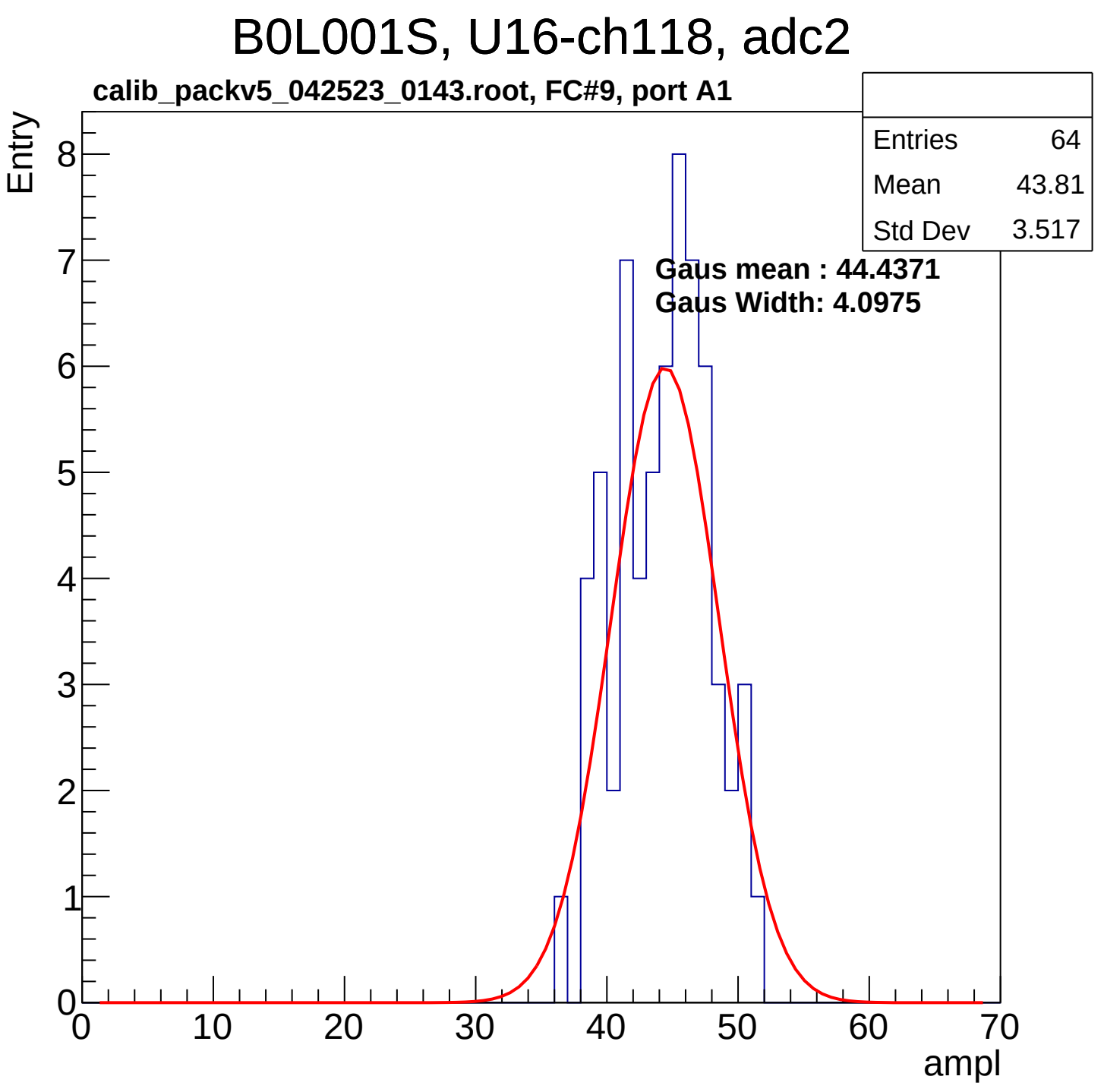

B0L001S, U16-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 61.3
Std Dev 1.735
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

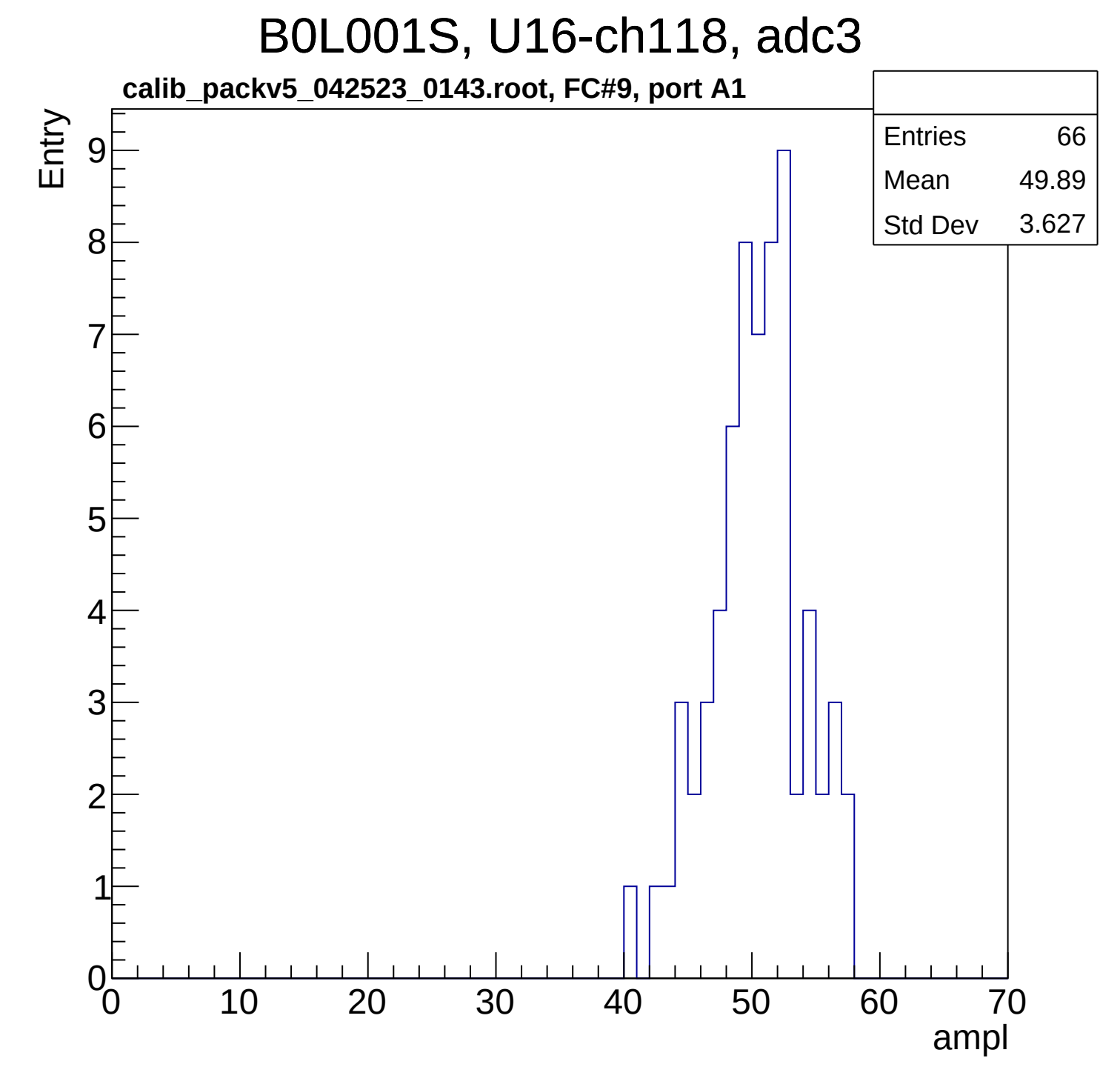

B0L001S, U16-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

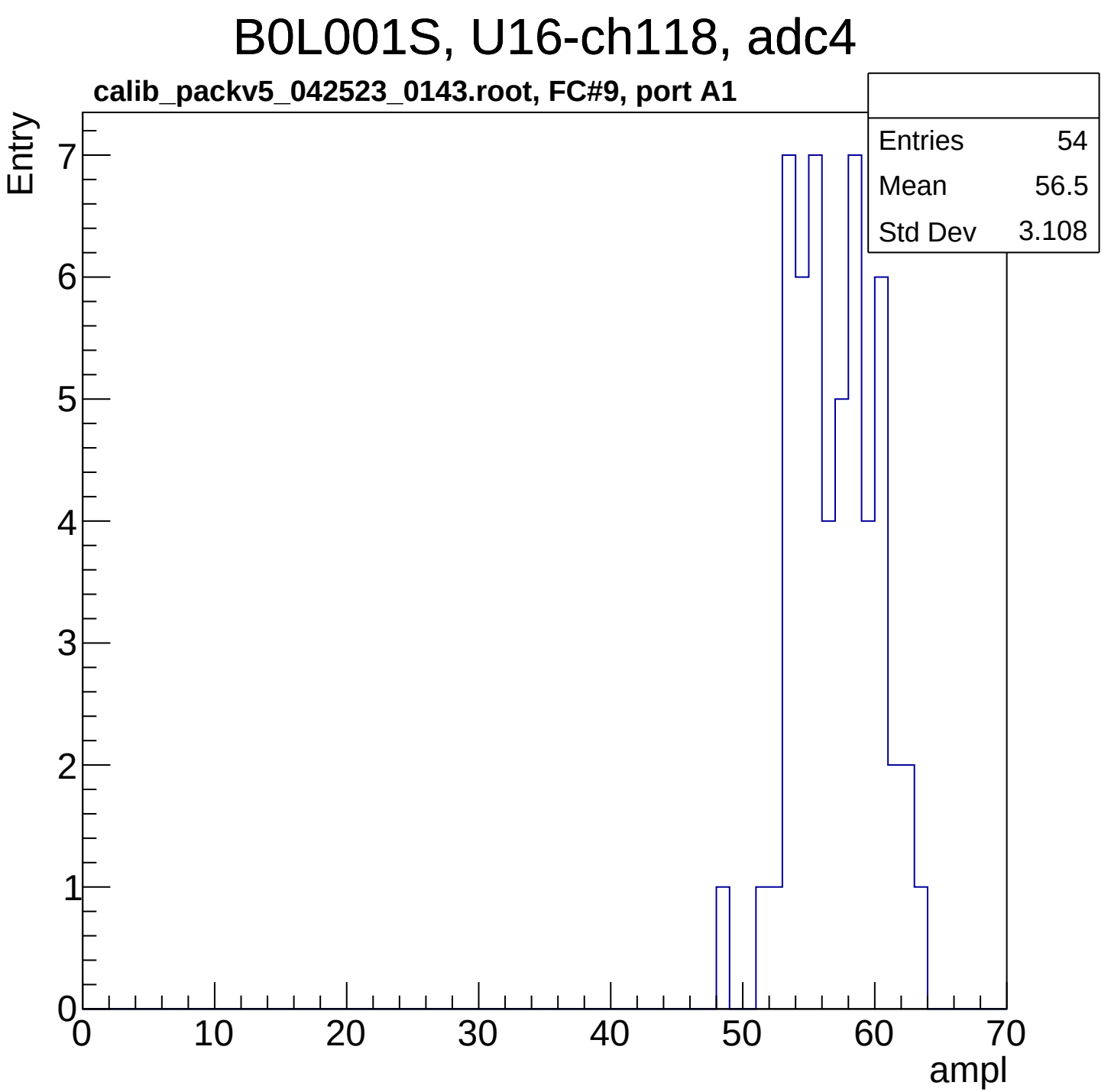

B0L001S, U16-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

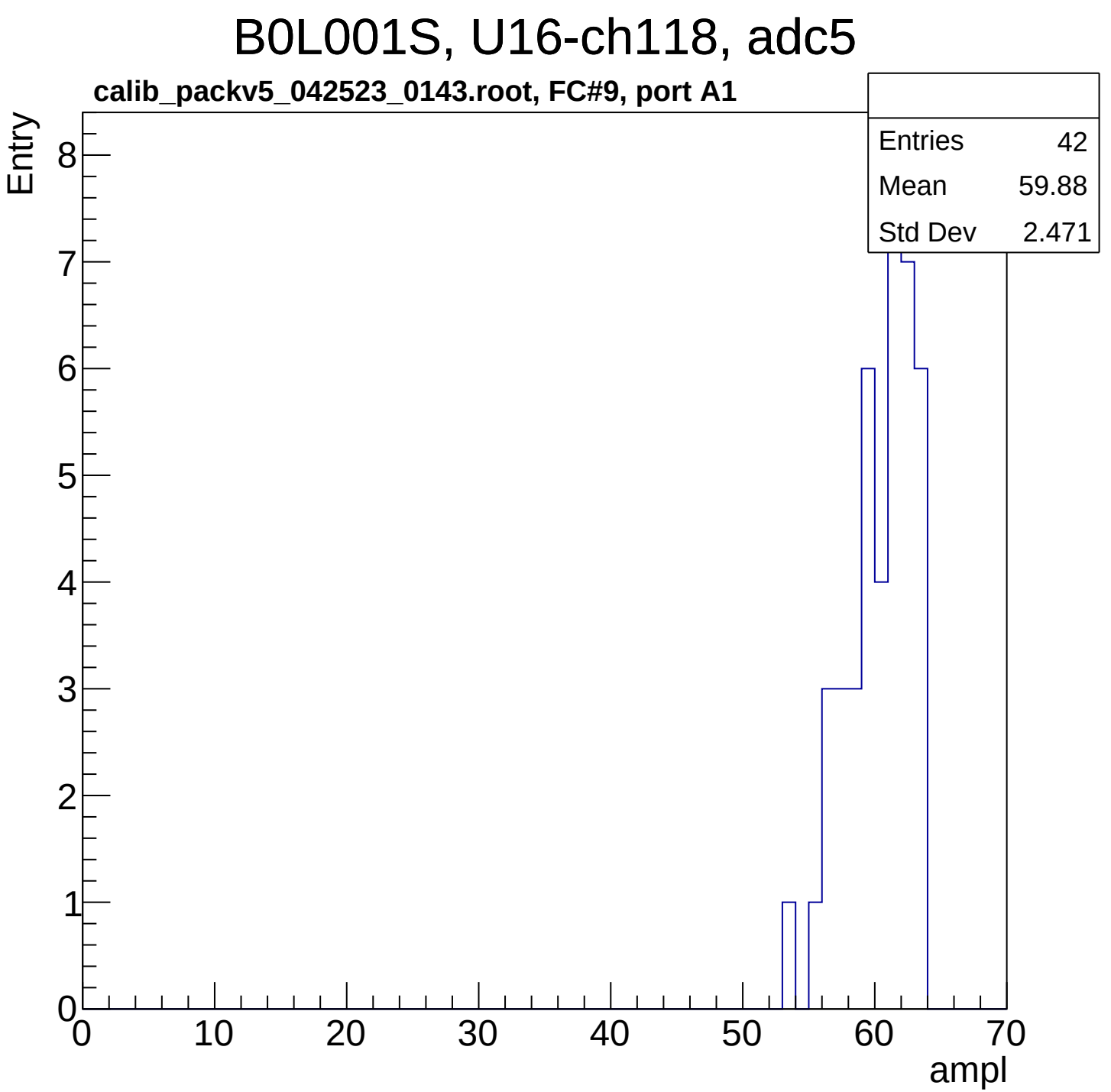

B0L001S, U16-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.54
Std Dev 4.134
Gaus mean : 31.1623
Gaus Width: 4.2080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

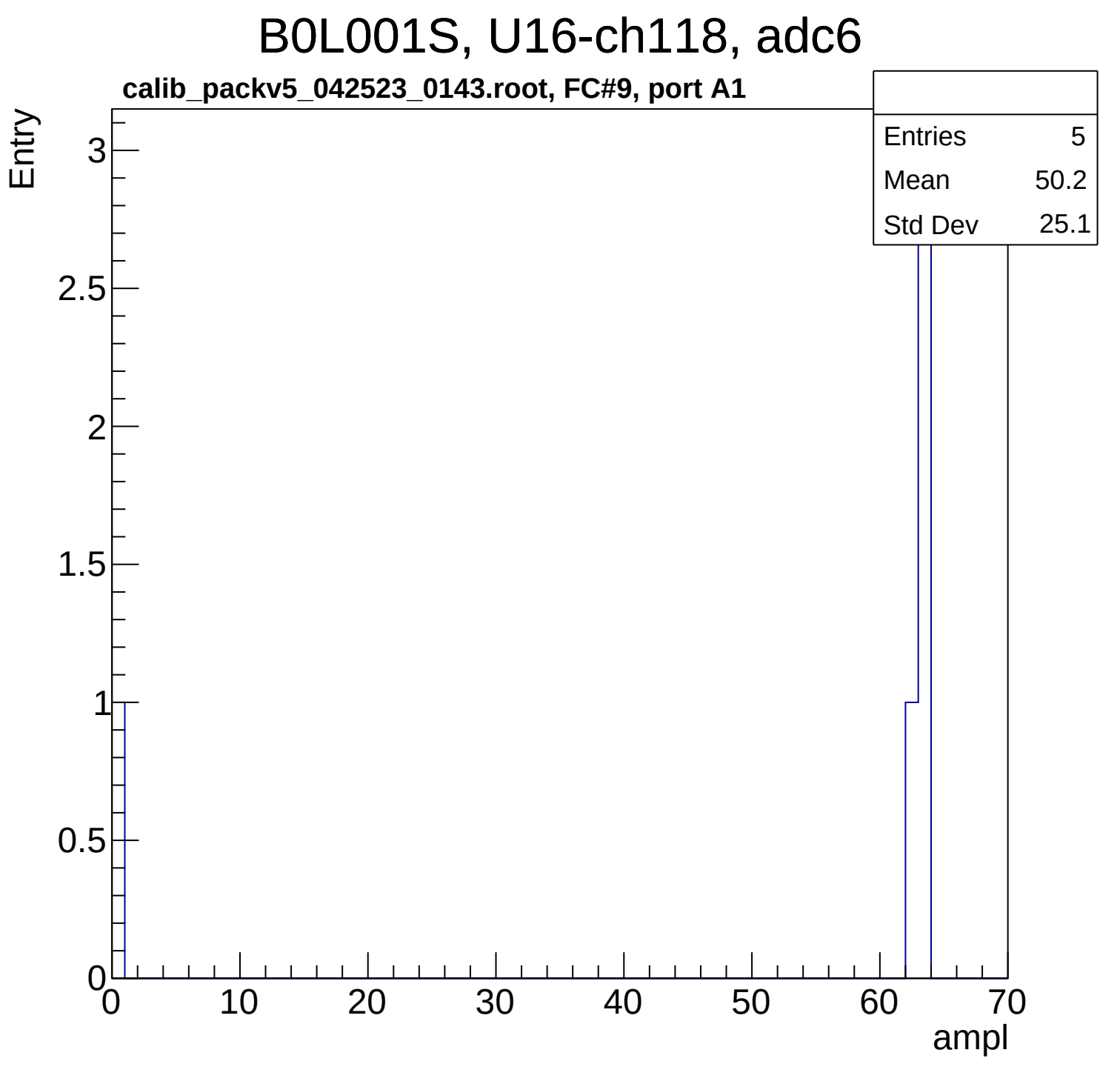

B0L001S, U16-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.03
Std Dev 3.65
Gaus mean : 37.5248
Gaus Width: 3.9993
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

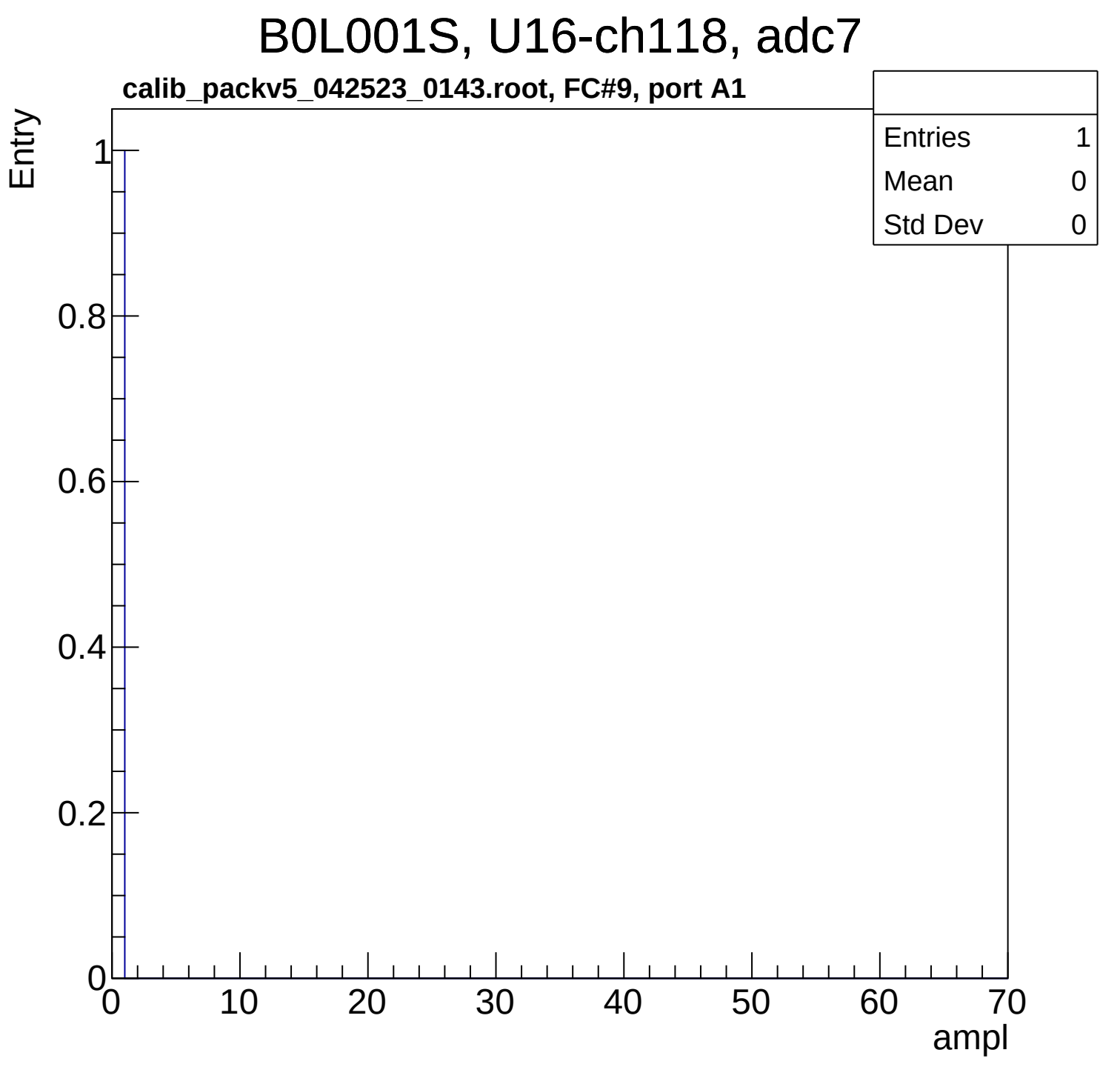

B0L001S, U16-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.81
Std Dev 3.517
Gaus mean : 44.4371
Gaus Width: 4.0975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

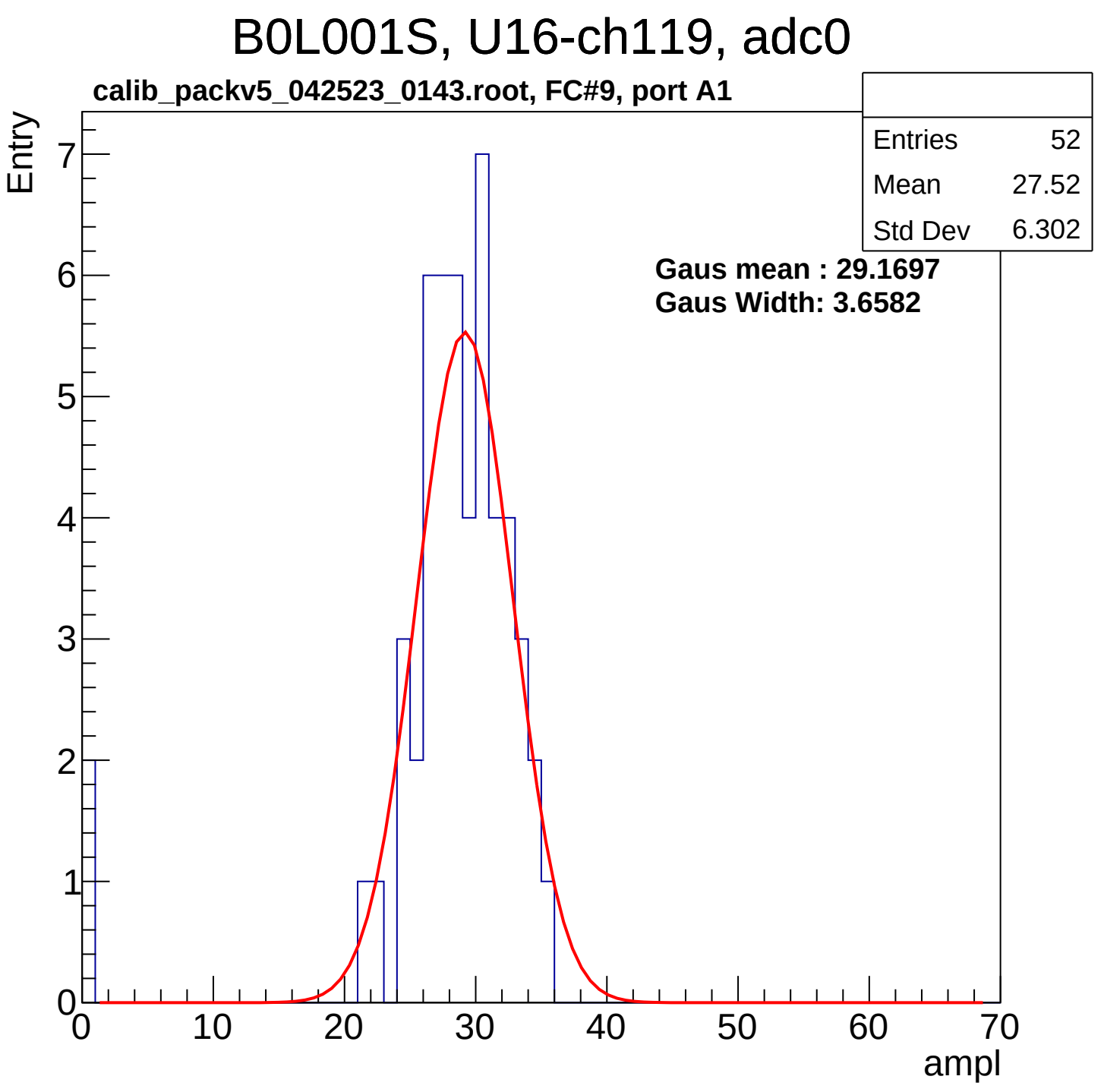

B0L001S, U16-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.89
Std Dev 3.627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

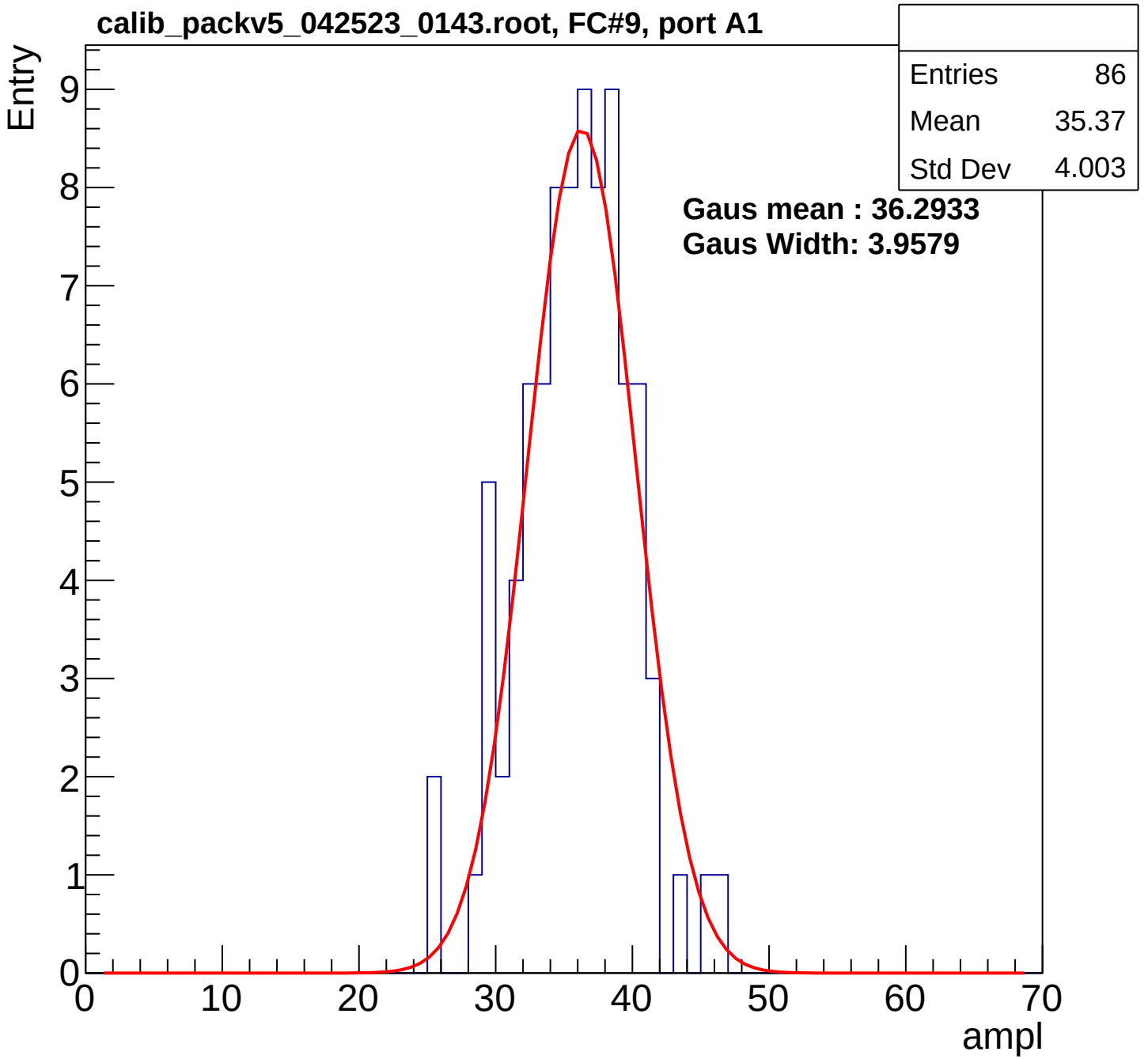

B0L001S, U16-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.5
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

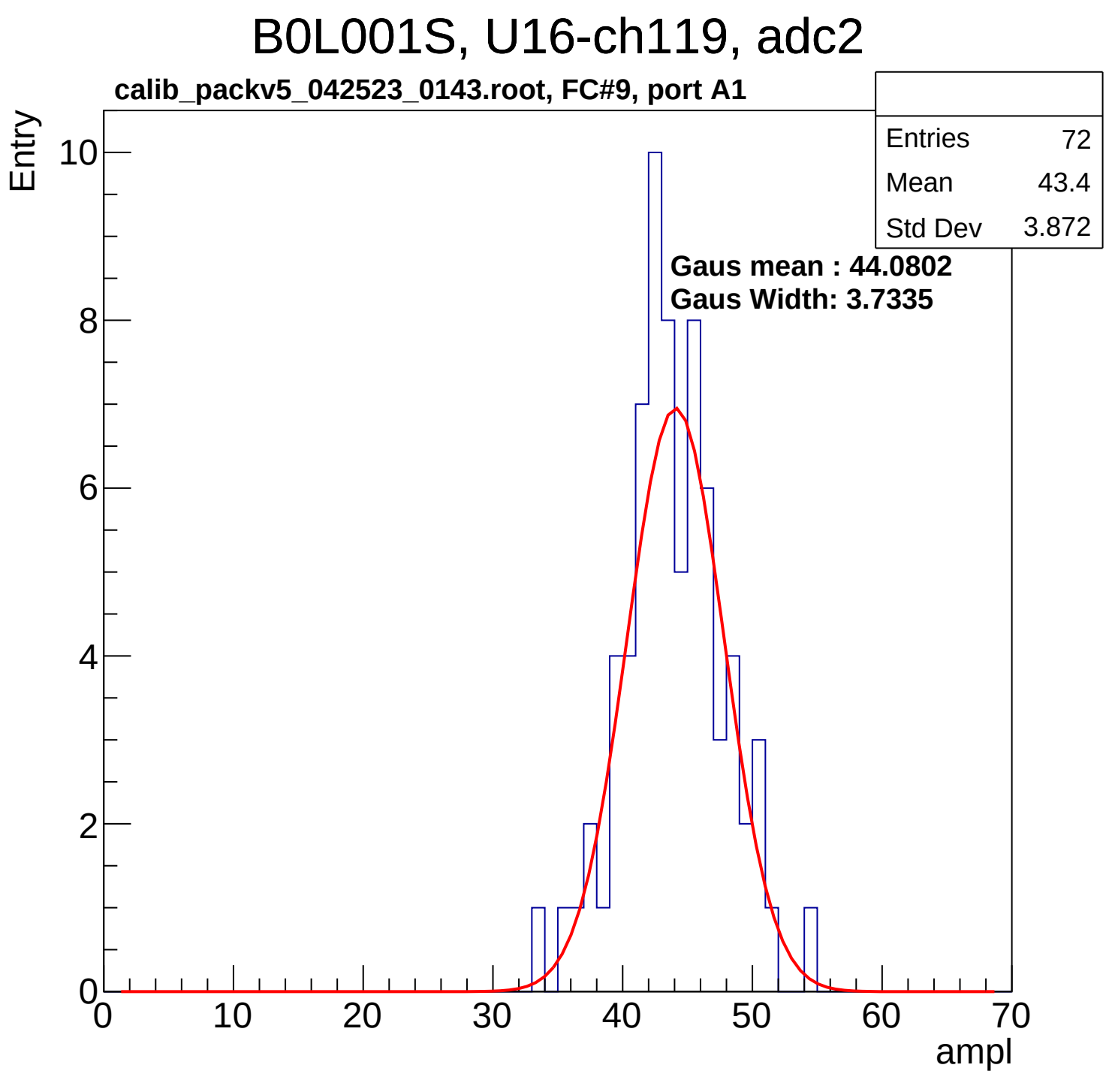

B0L001S, U16-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.88
Std Dev 2.471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 27.52
Std Dev 6.302
Gaus mean : 29.1697
Gaus Width: 3.6582
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 35.37
Std Dev 4.003
Gaus mean : 36.2933
Gaus Width: 3.9579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 43.4
Std Dev 3.872
Gaus mean : 44.0802
Gaus Width: 3.7335
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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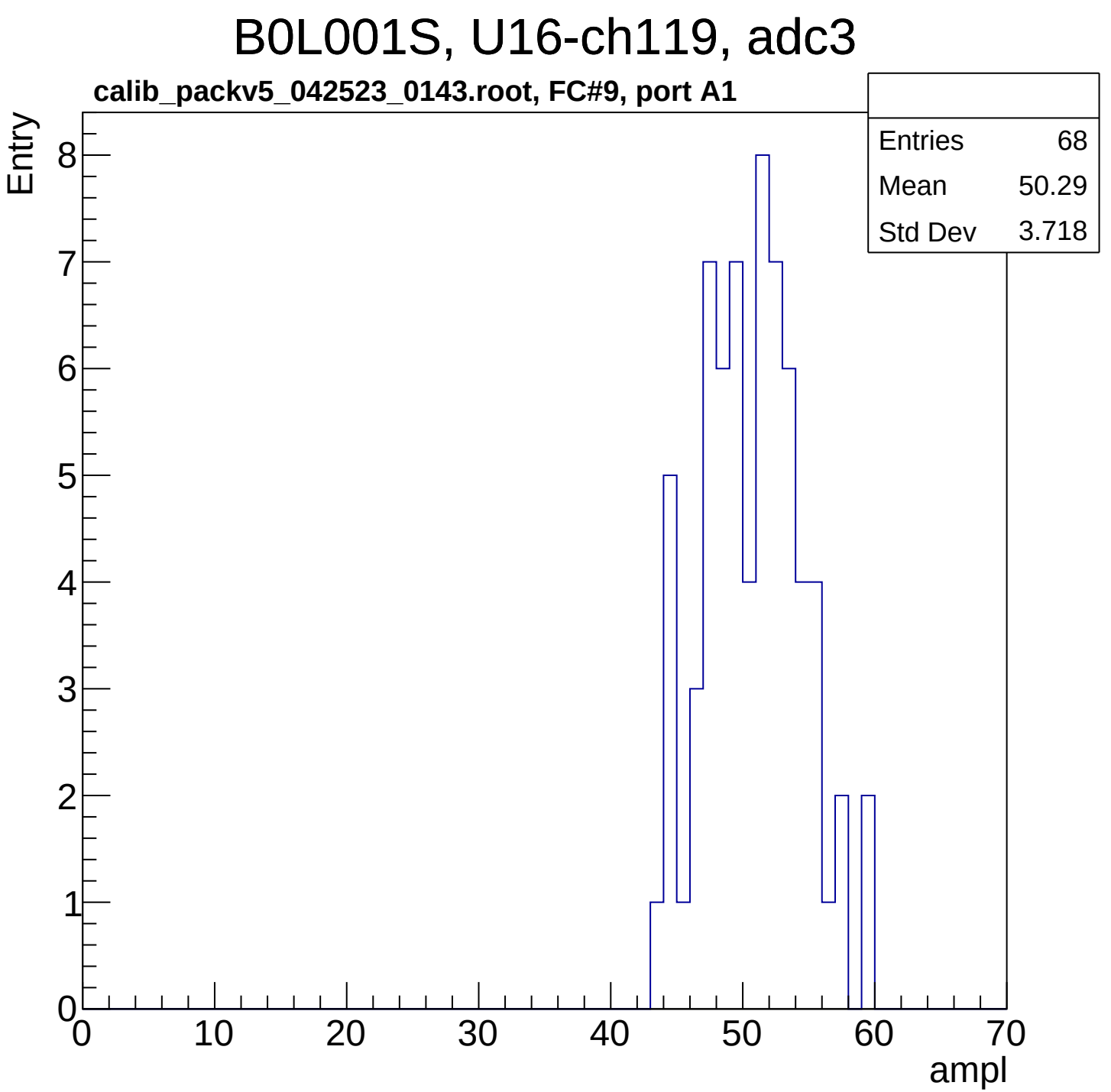
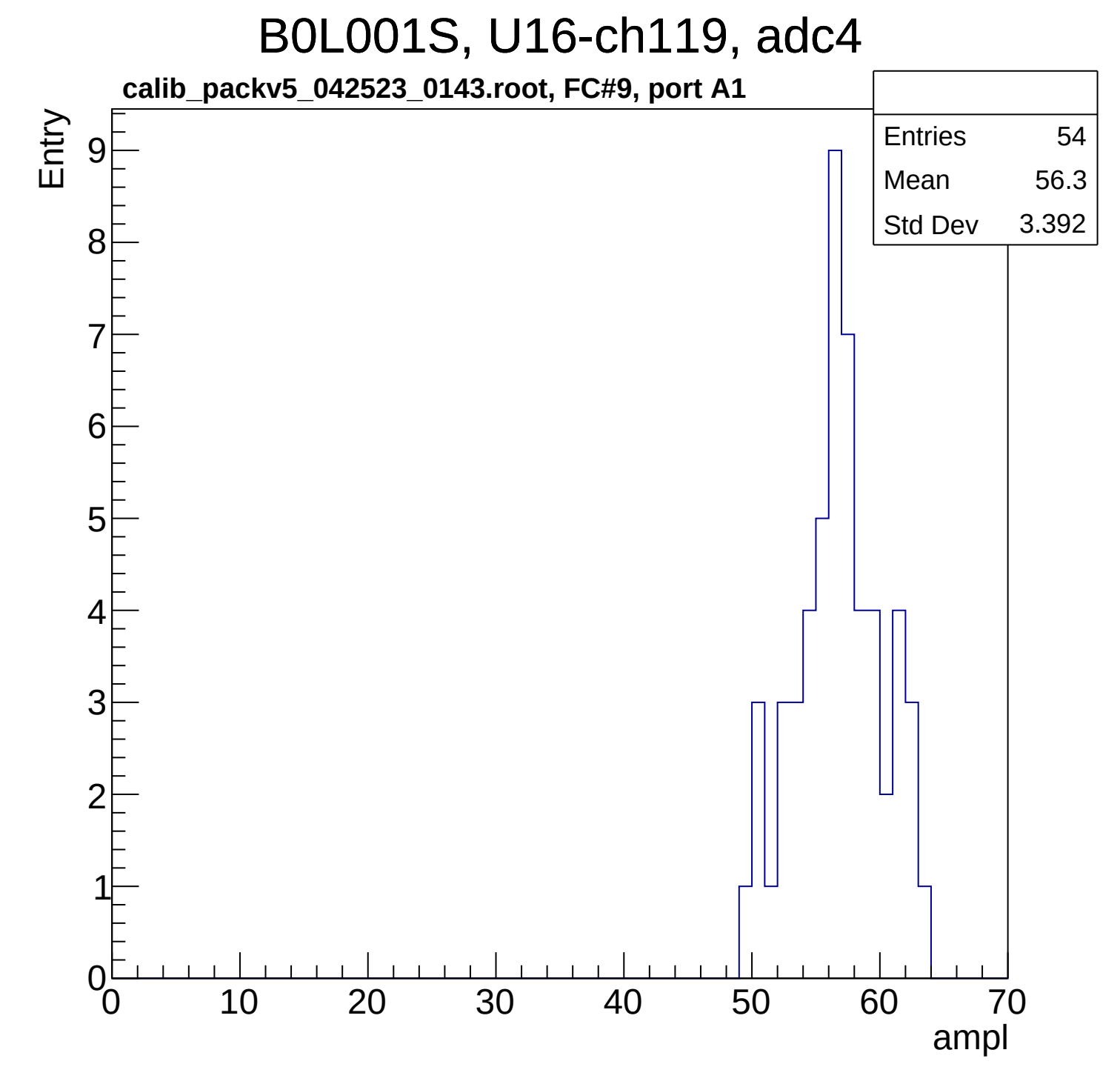
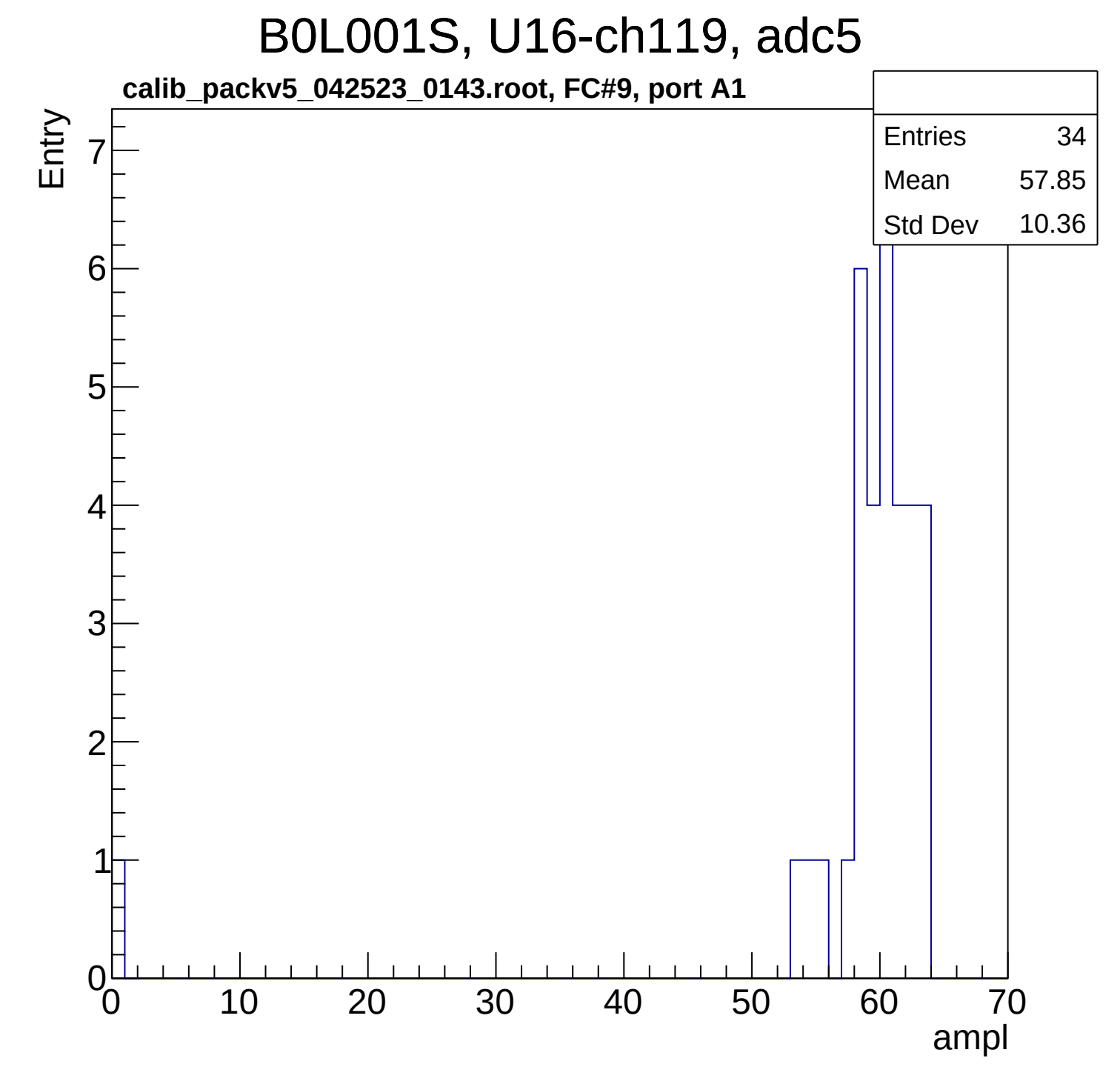
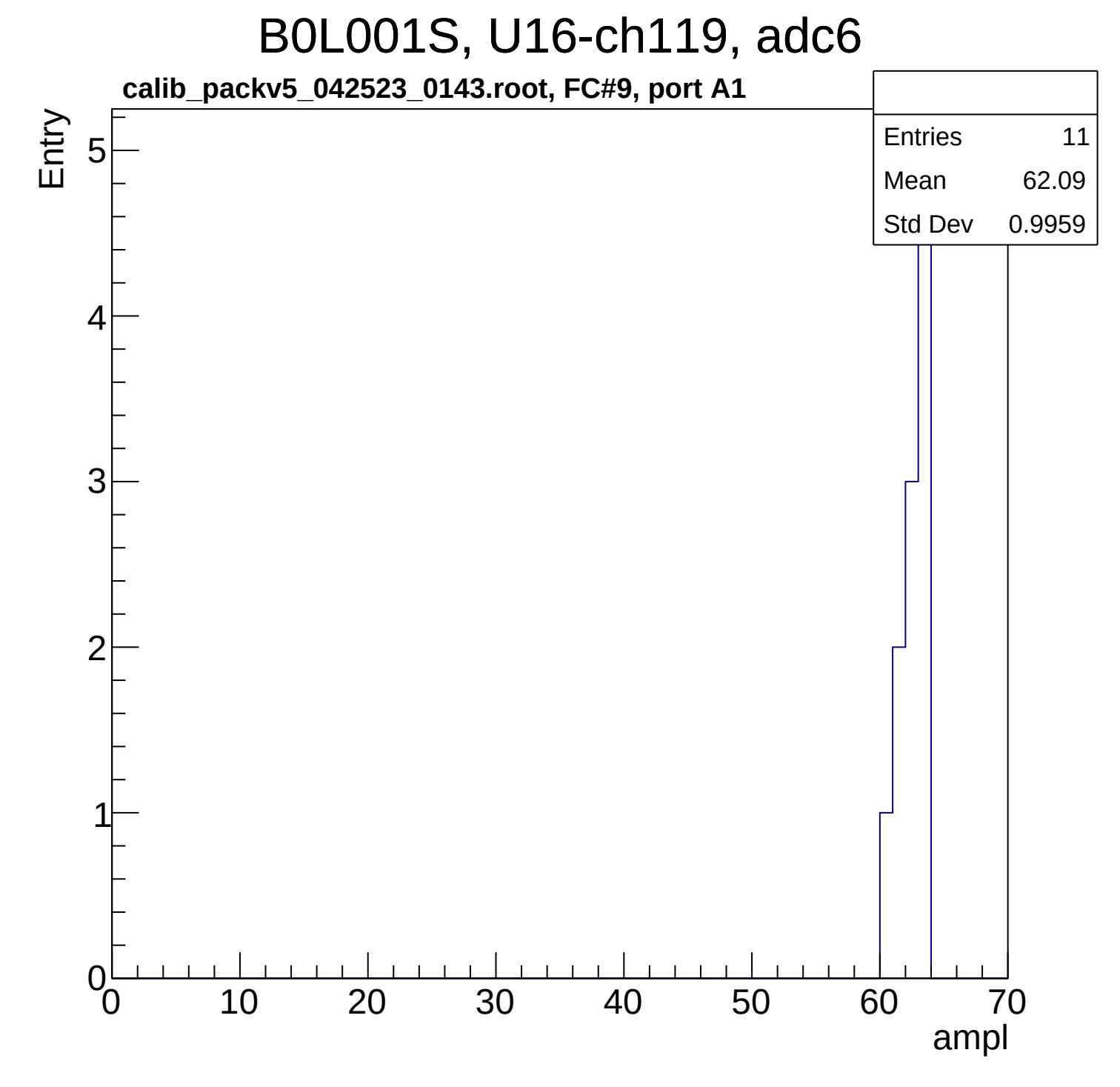
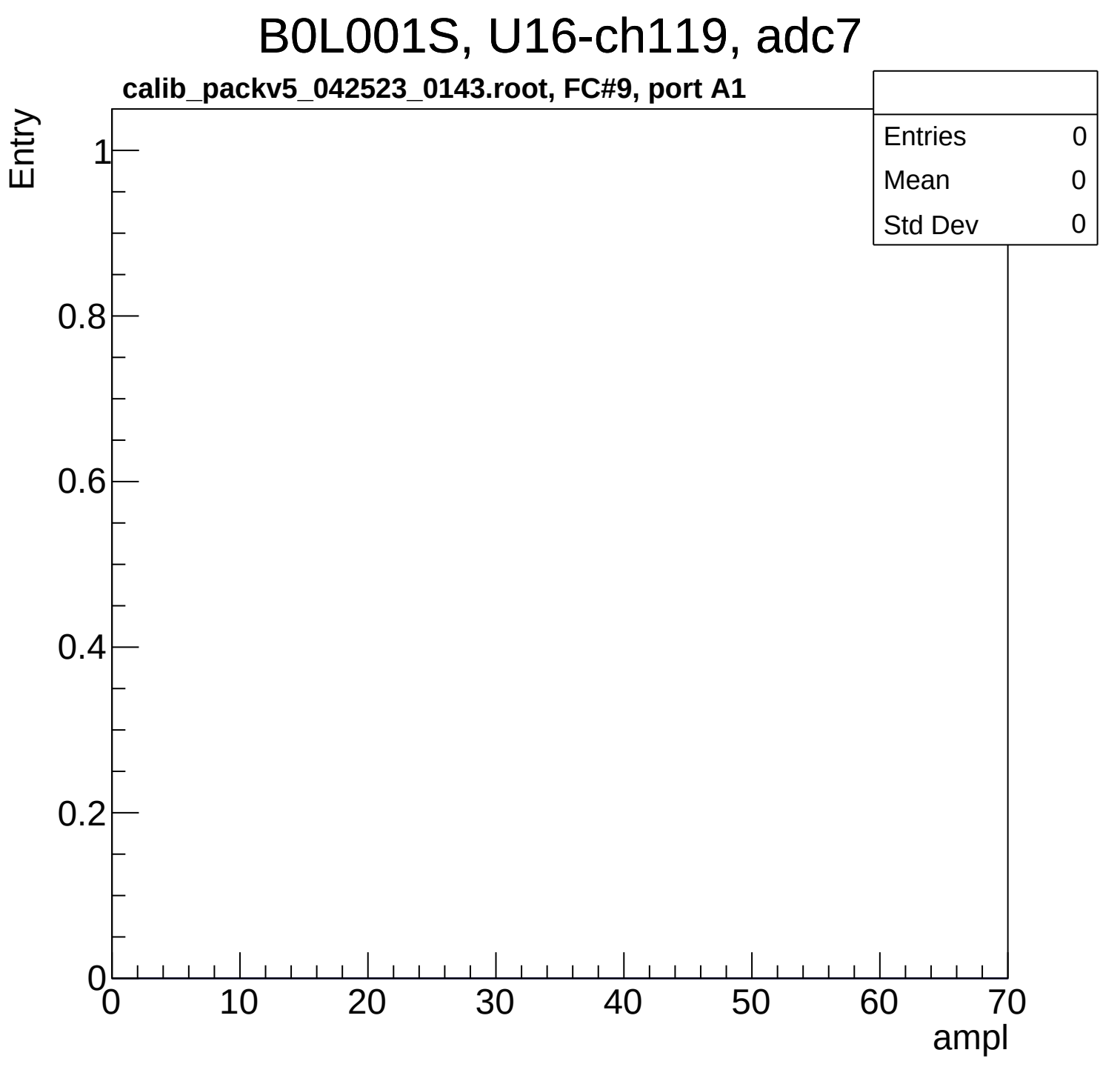
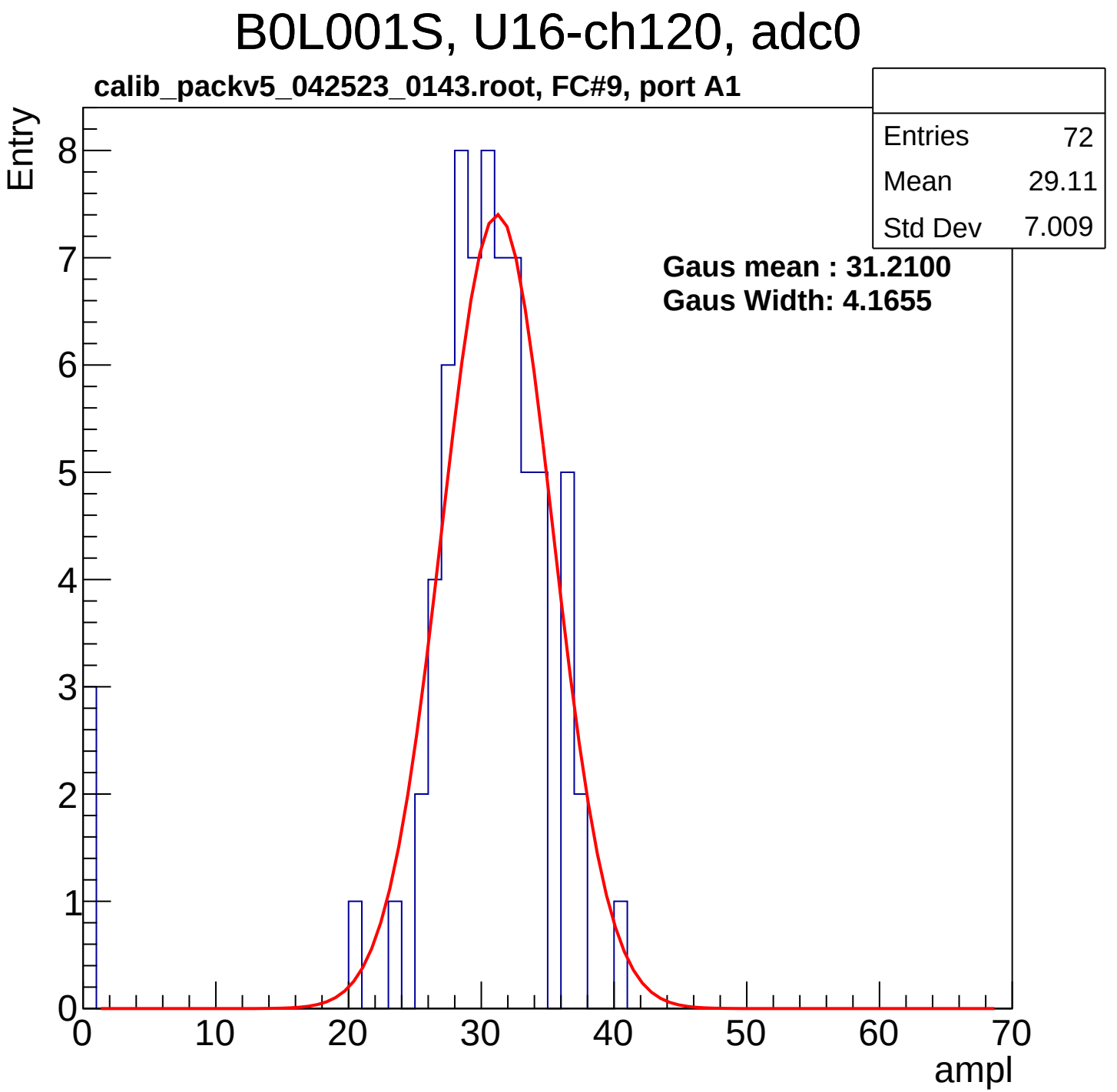

B0L001S, U16-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50.29
Std Dev 3.718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

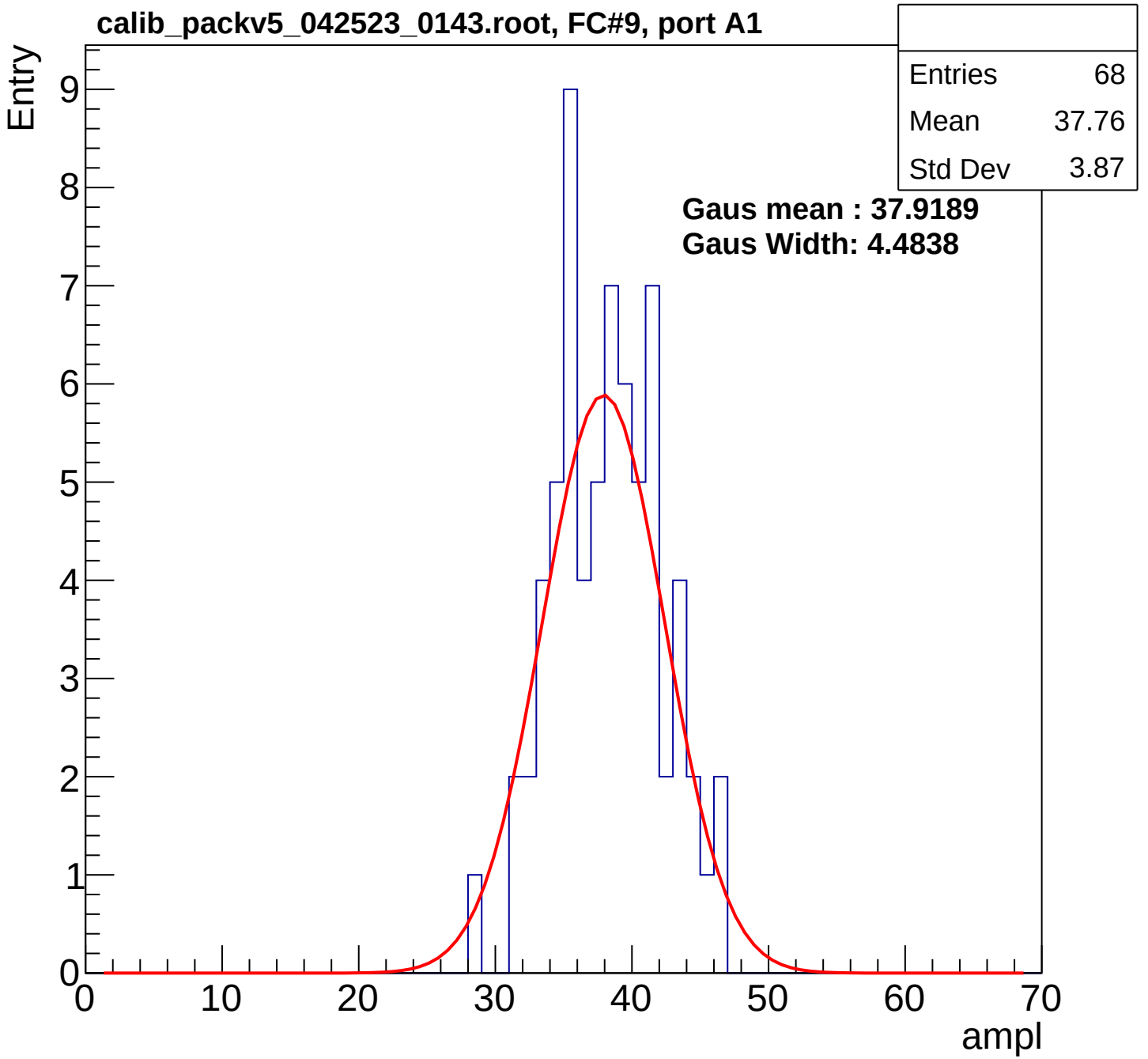

B0L001S, U16-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.3
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

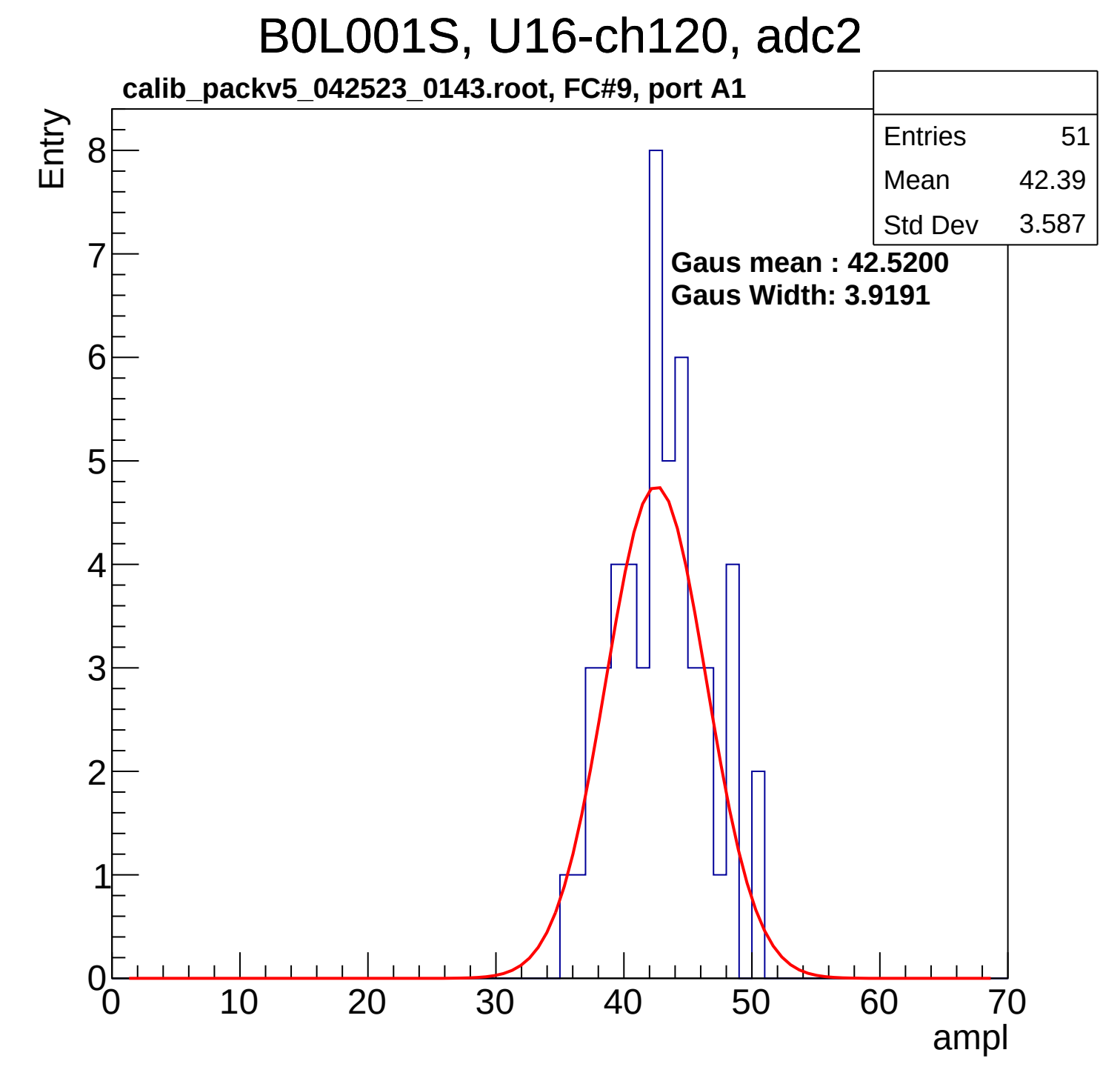

B0L001S, U16-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 57.85
Std Dev 10.36
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

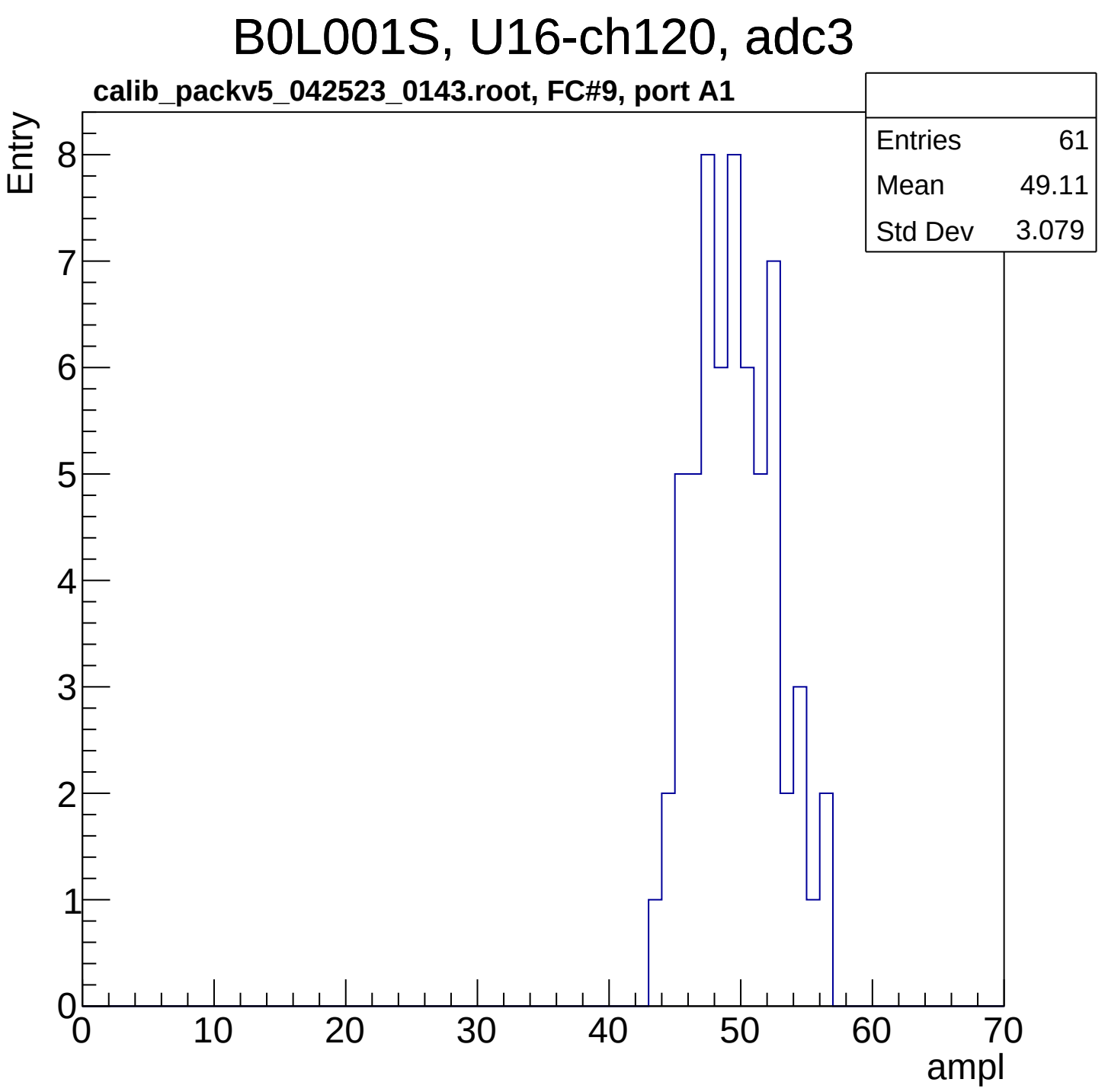

B0L001S, U16-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 62.09
Std Dev 0.9959
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

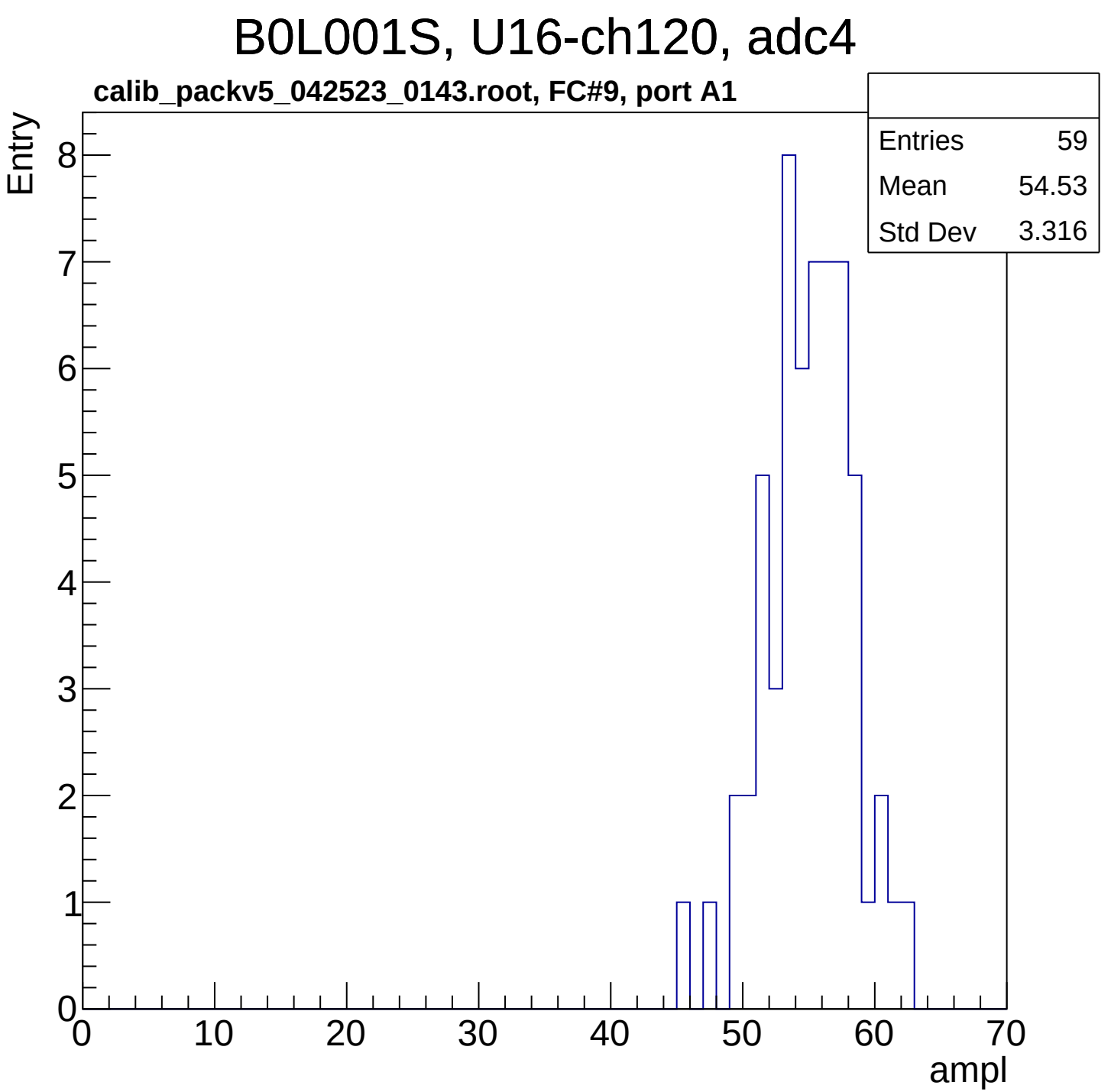

B0L001S, U16-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

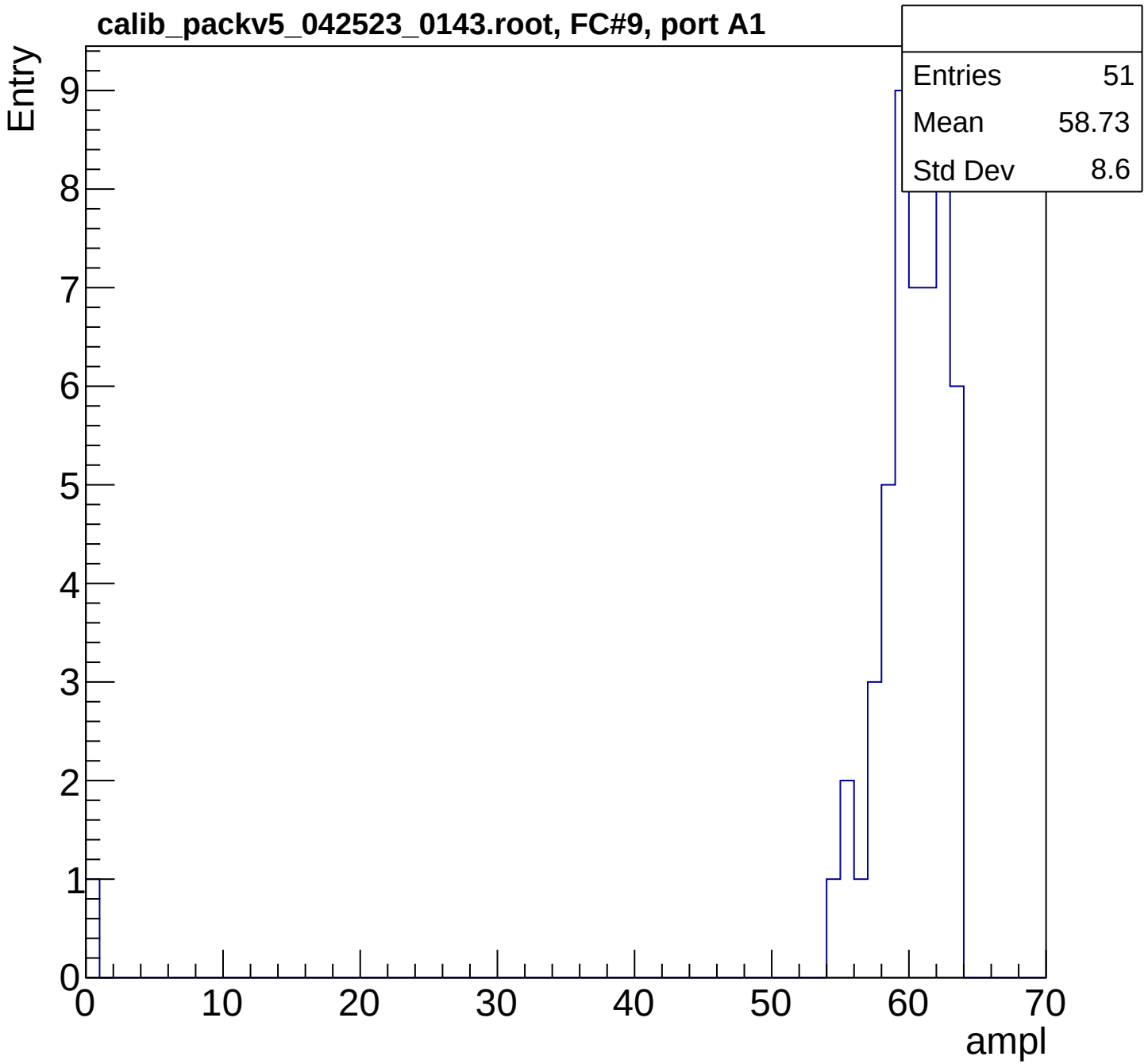

B0L001S, U16-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.11
Std Dev 7.009
Gaus mean : 31.2100
Gaus Width: 4.1655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

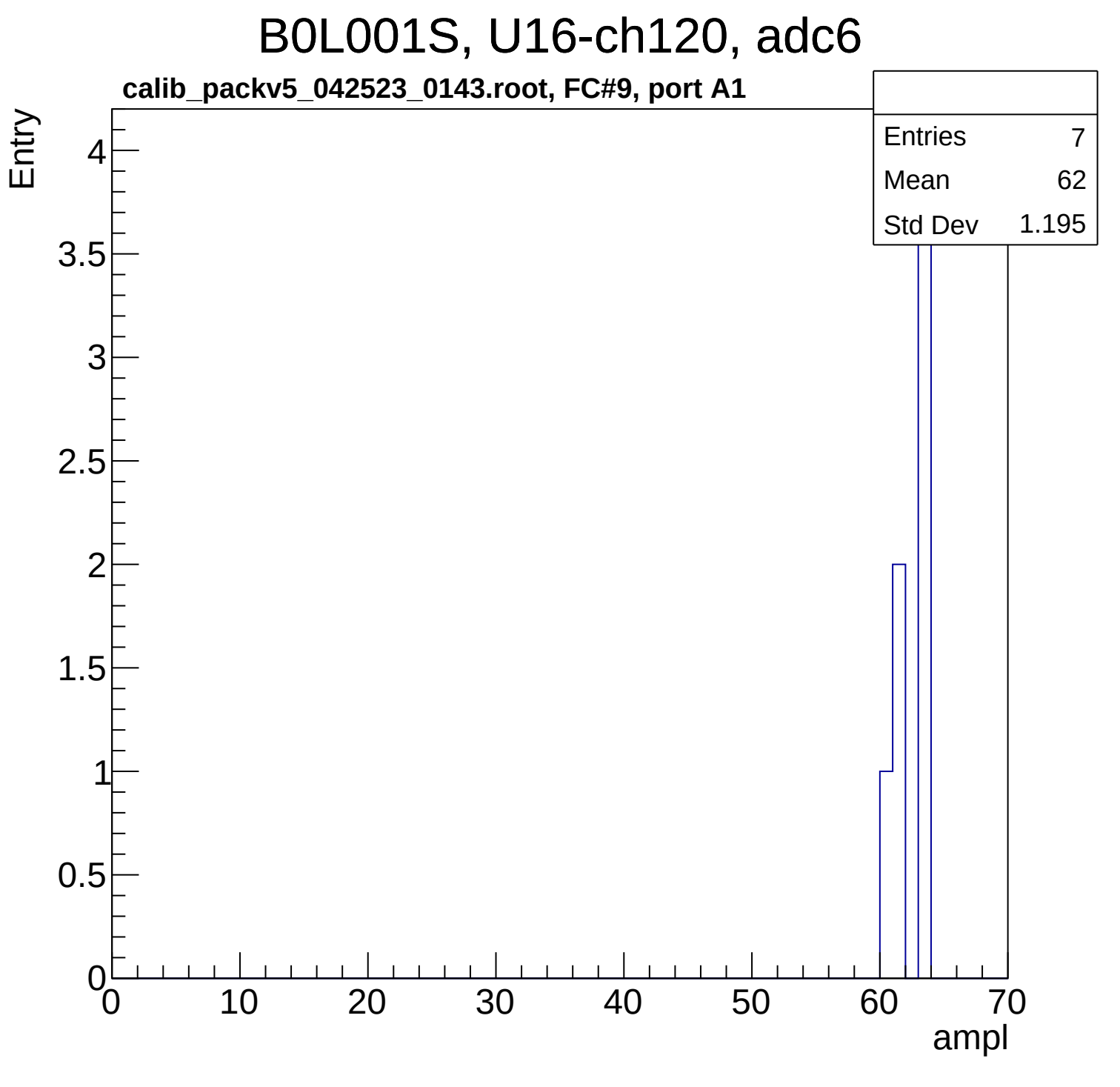

B0L001S, U16-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.76
Std Dev 3.87
Gaus mean : 37.9189
Gaus Width: 4.4838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

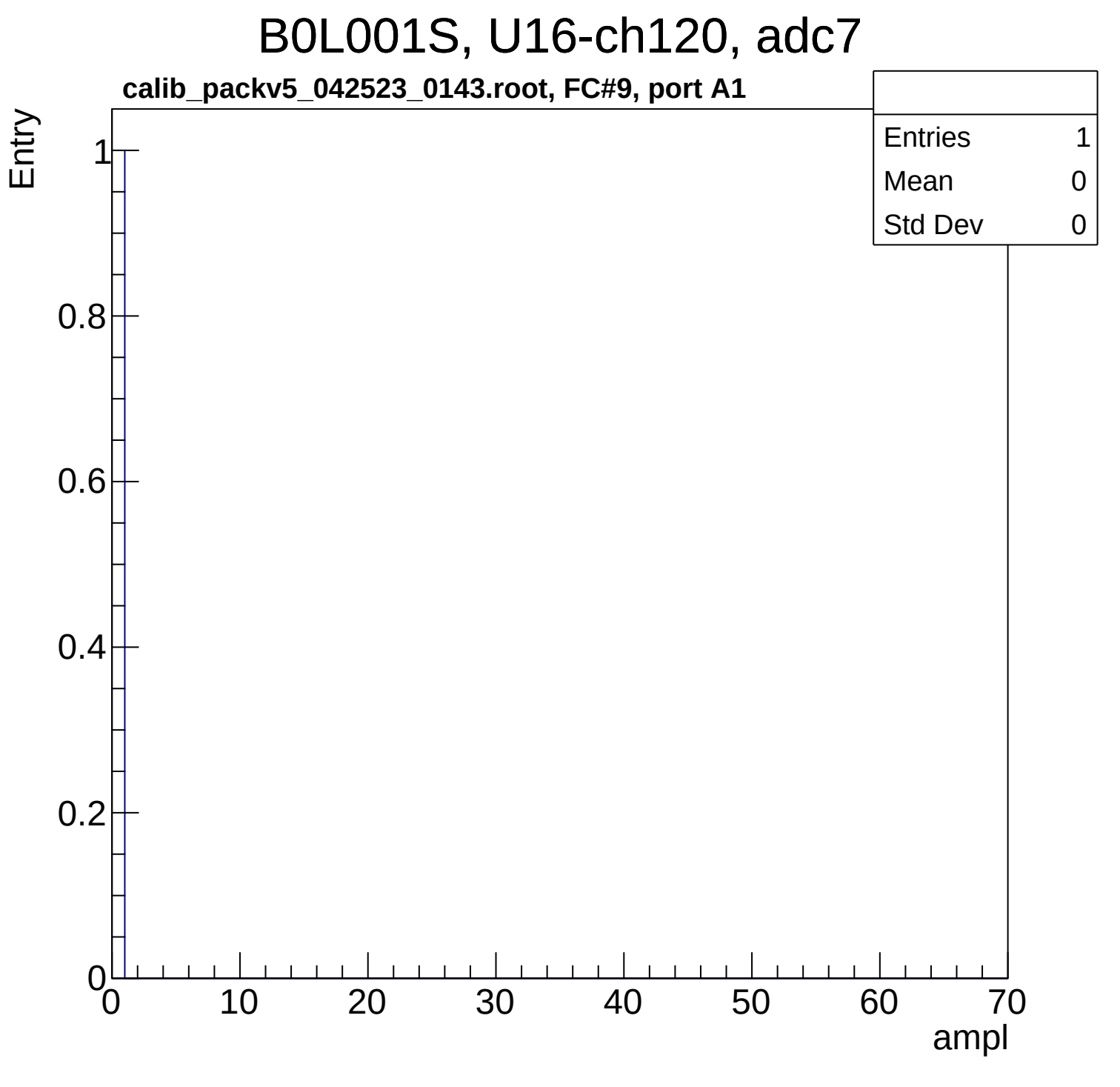

B0L001S, U16-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 42.39
Std Dev 3.587
Gaus mean : 42.5200
Gaus Width: 3.9191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

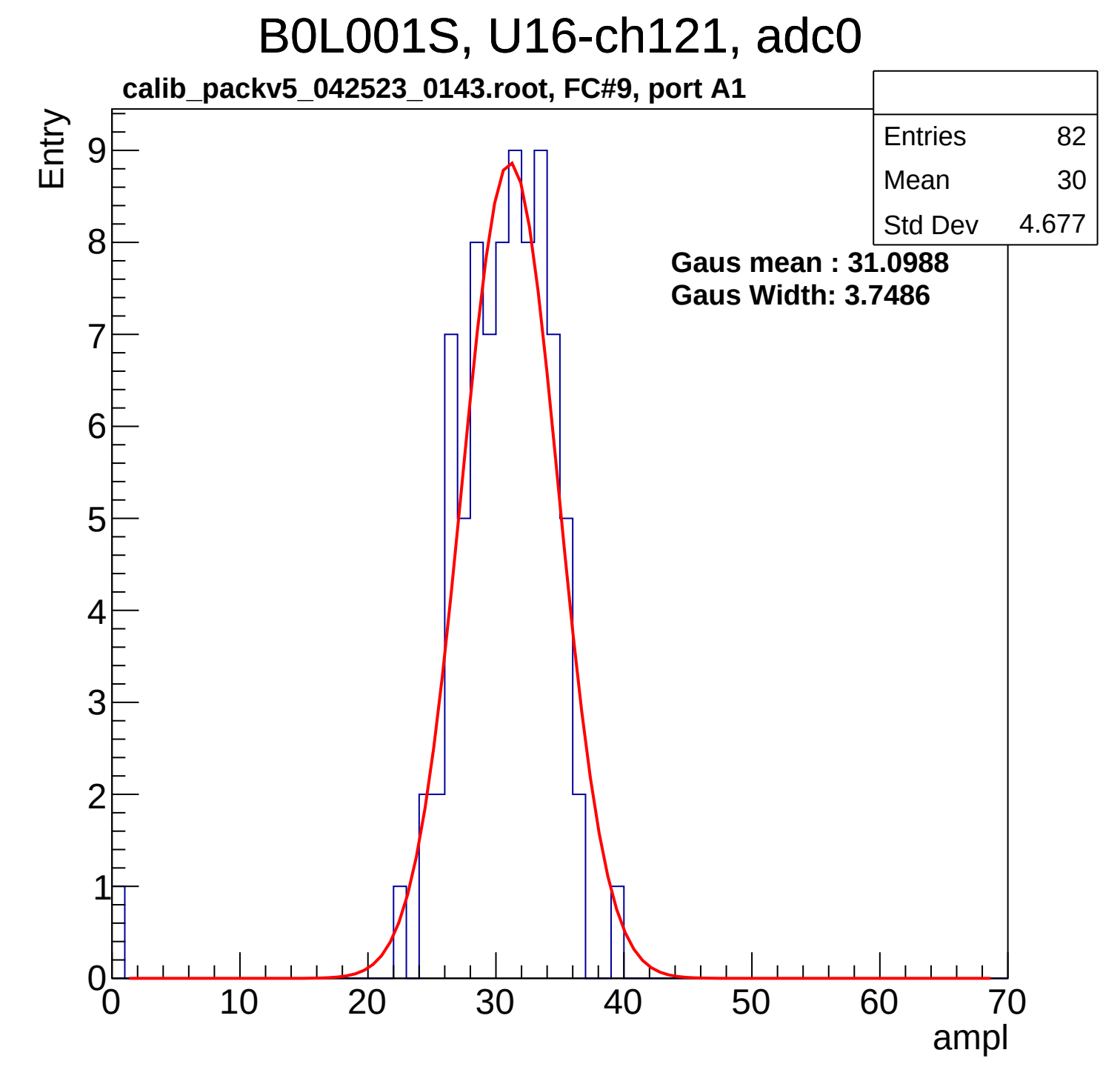

B0L001S, U16-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.11
Std Dev 3.079
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

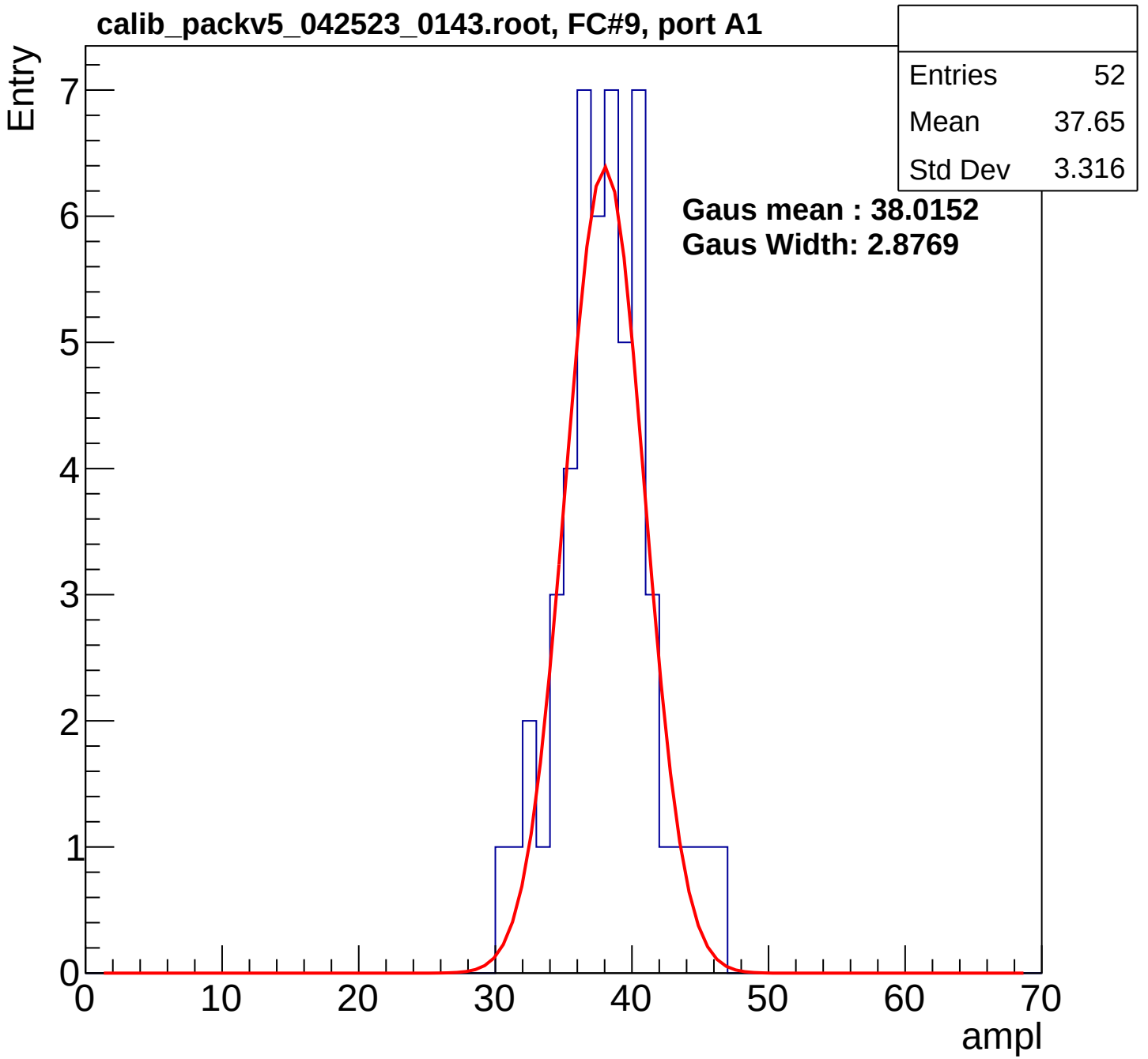

B0L001S, U16-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 54.53
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

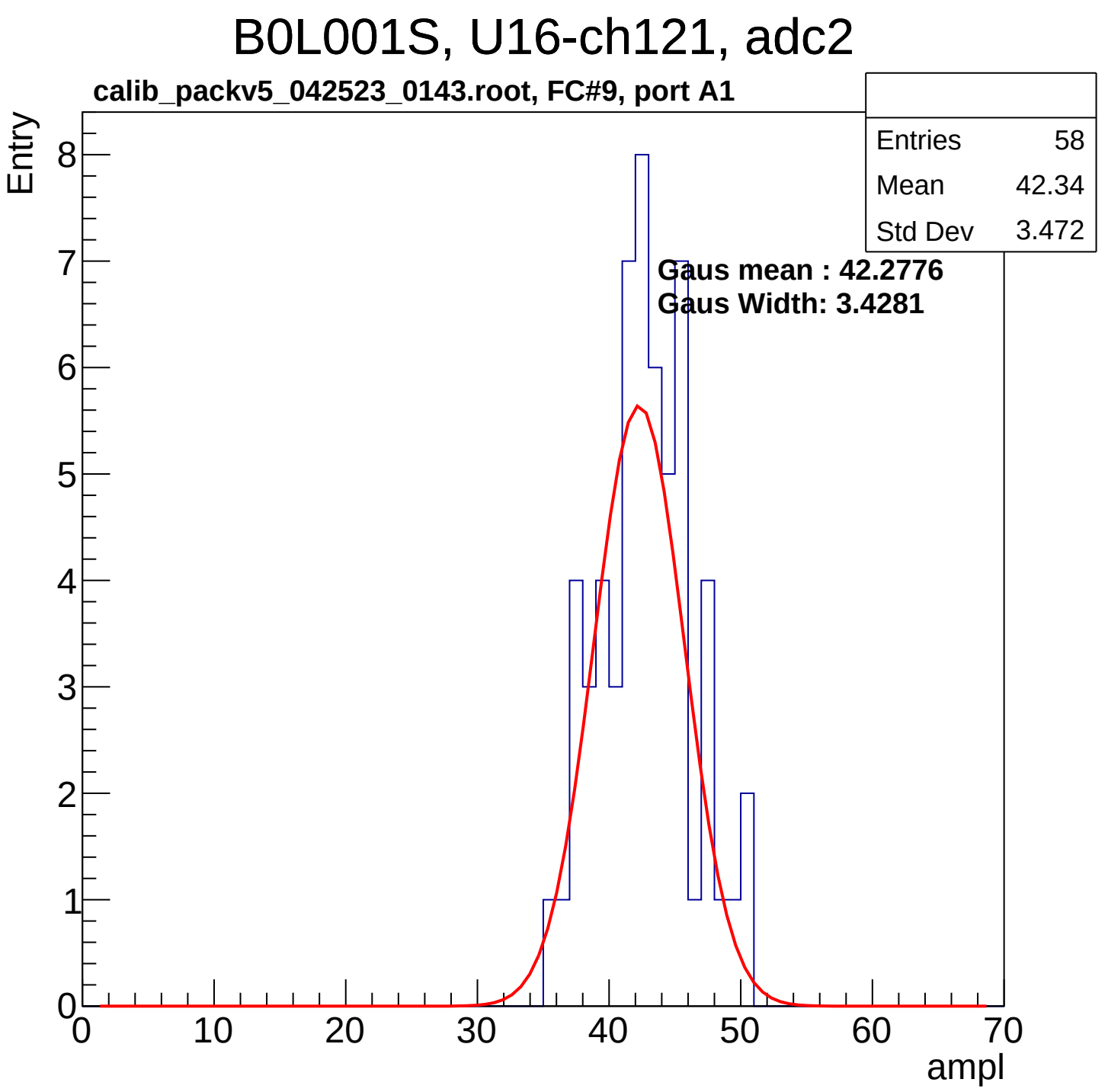

B0L001S, U16-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.73
Std Dev 8.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

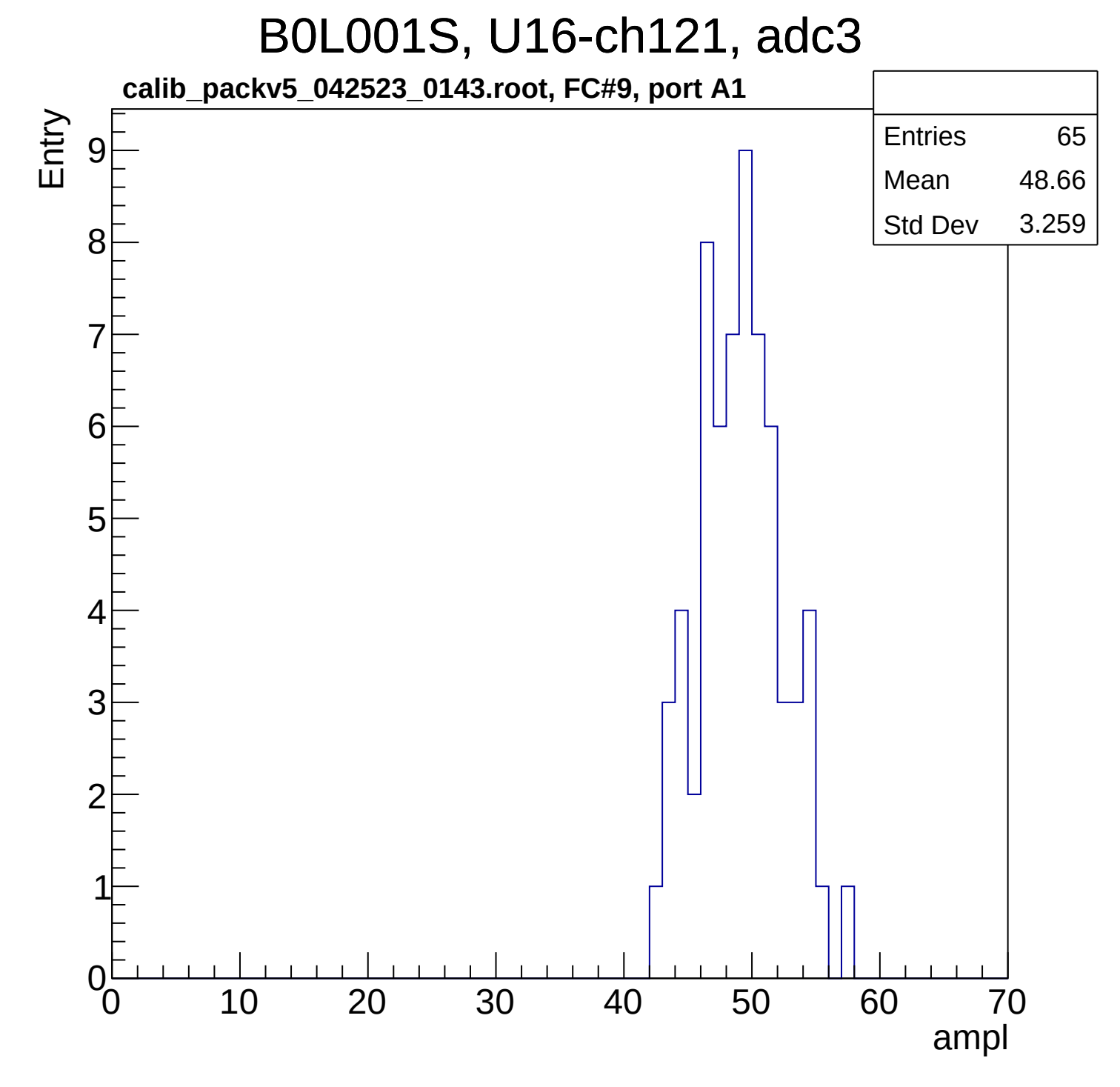

B0L001S, U16-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62
Std Dev 1.195
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U16-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30
Std Dev 4.677
Gaus mean : 31.0988
Gaus Width: 3.7486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 37.65
Std Dev 3.316
Gaus mean : 38.0152
Gaus Width: 2.8769
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 42.34
Std Dev 3.472
Gaus mean : 42.2776
Gaus Width: 3.4281
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 48.66
Std Dev 3.259
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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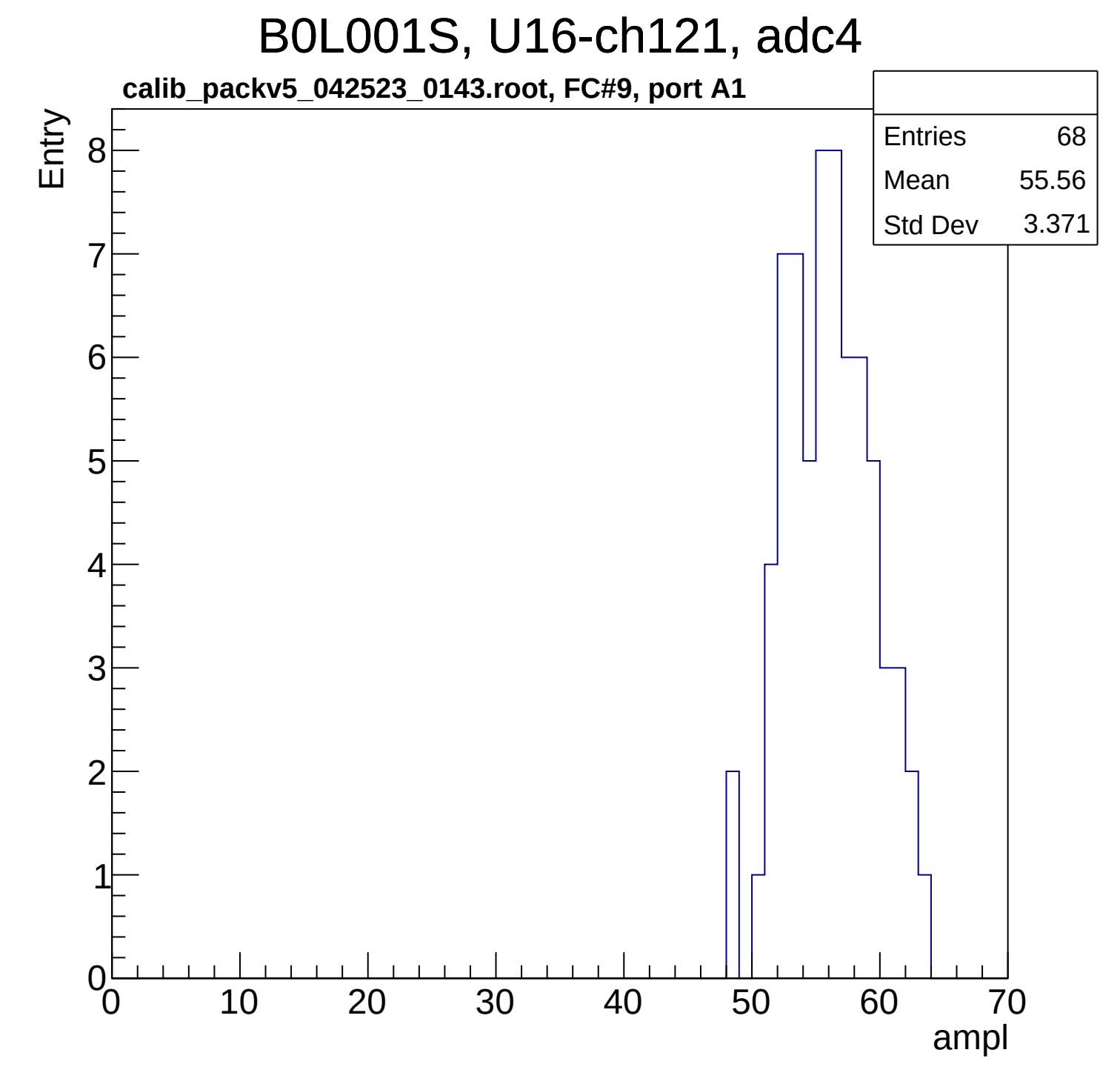
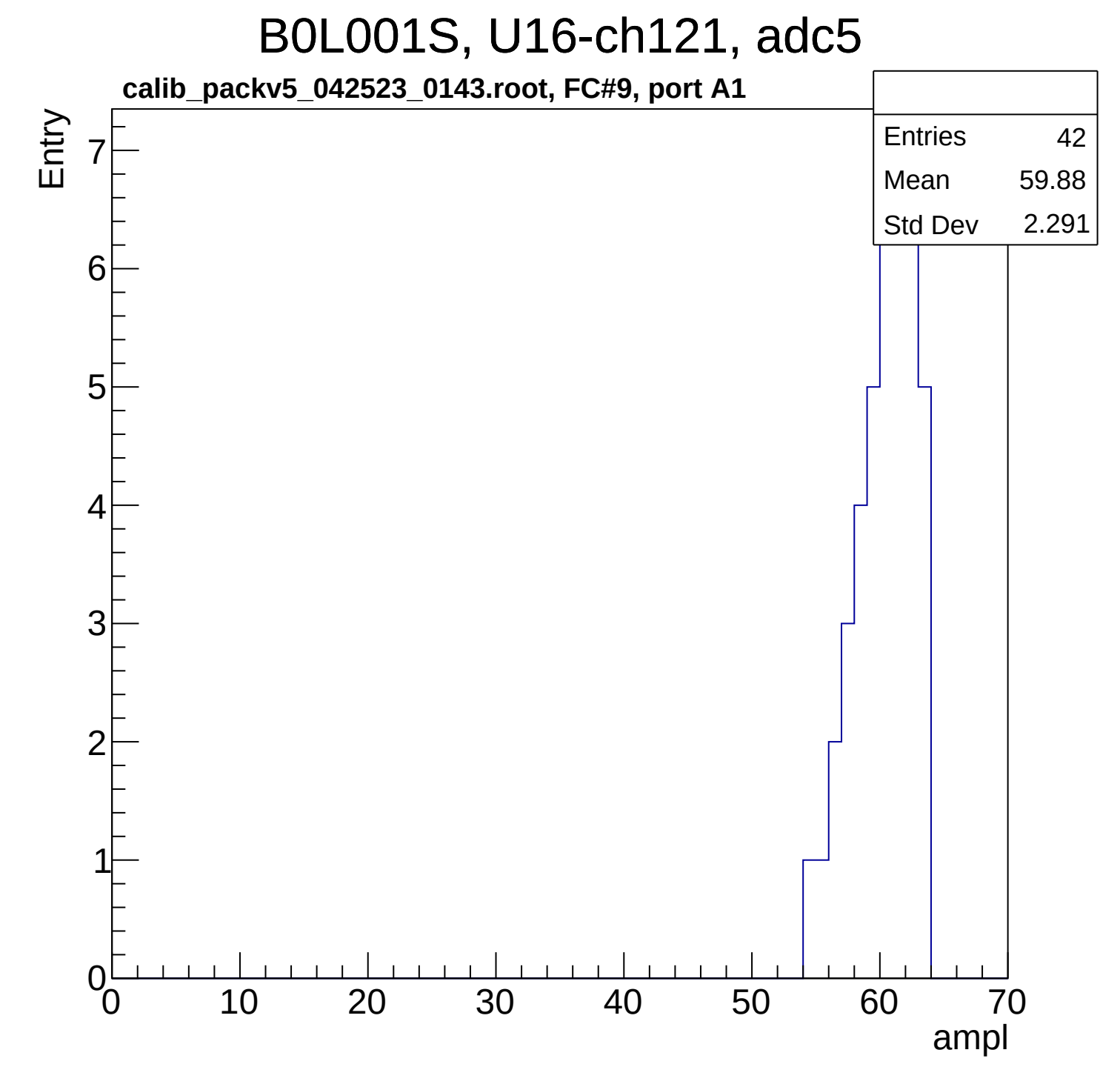
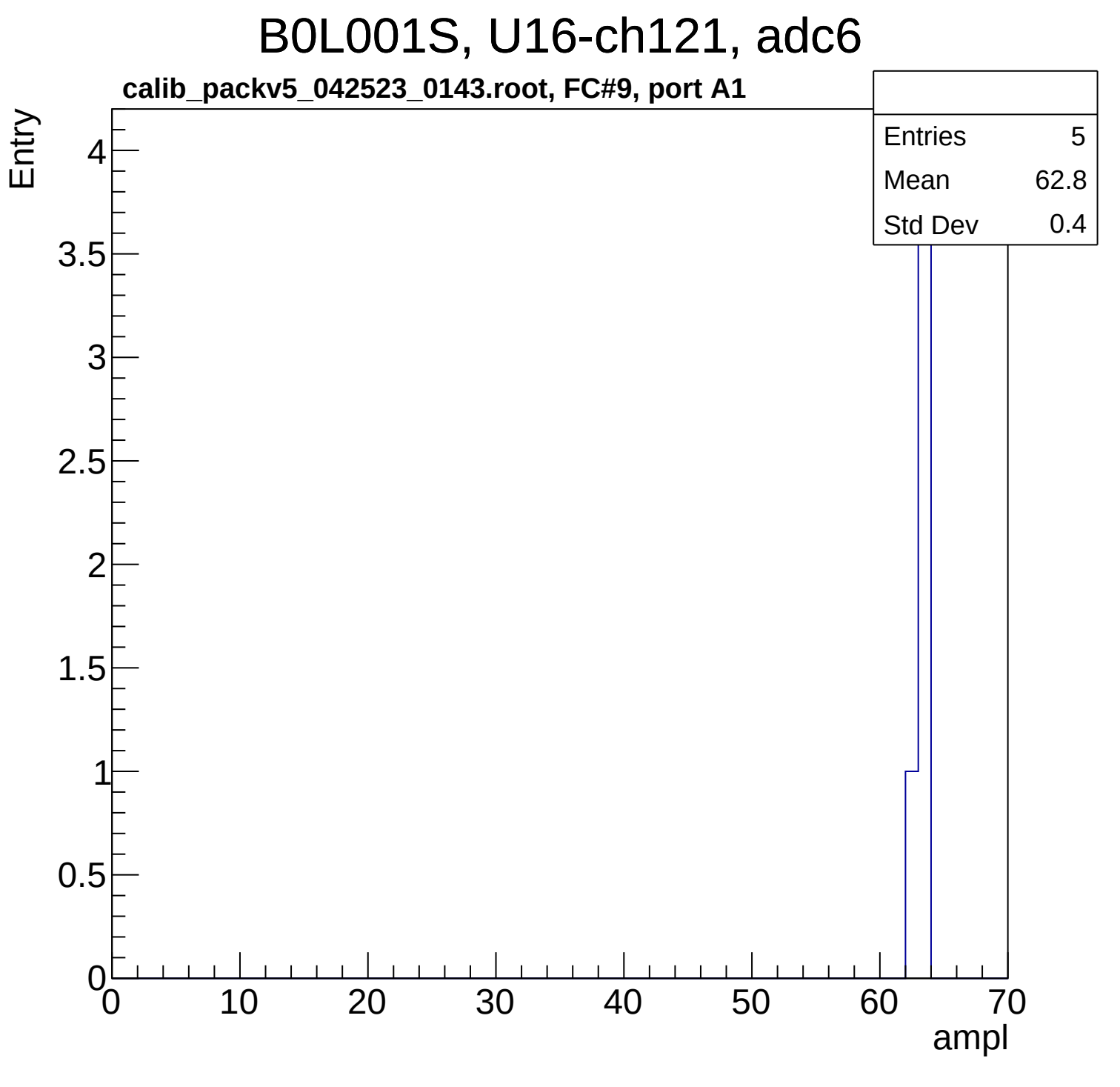
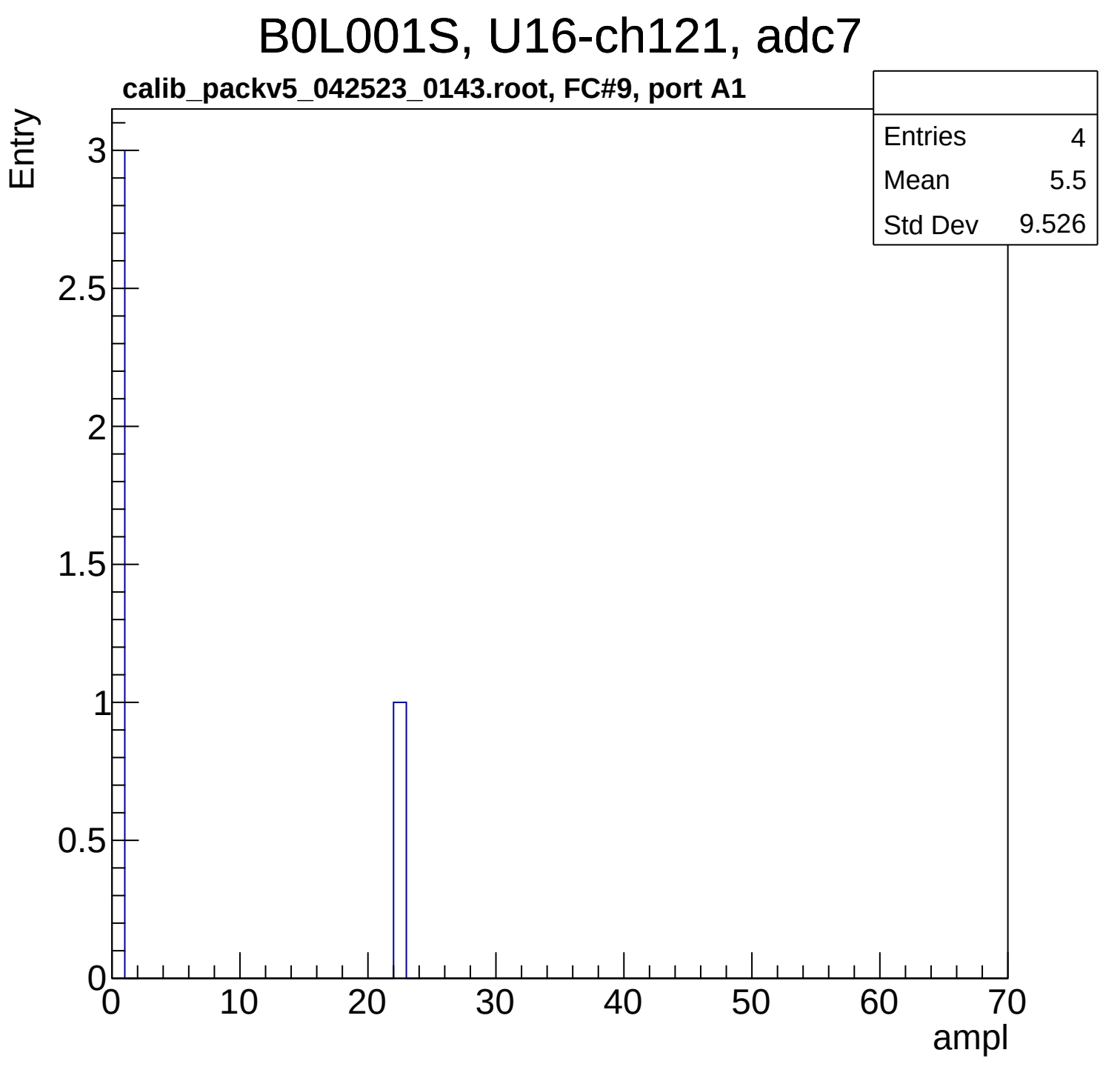
40
50
60
70


B0L001S, U16-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 55.56
Std Dev 3.371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.88
Std Dev 2.291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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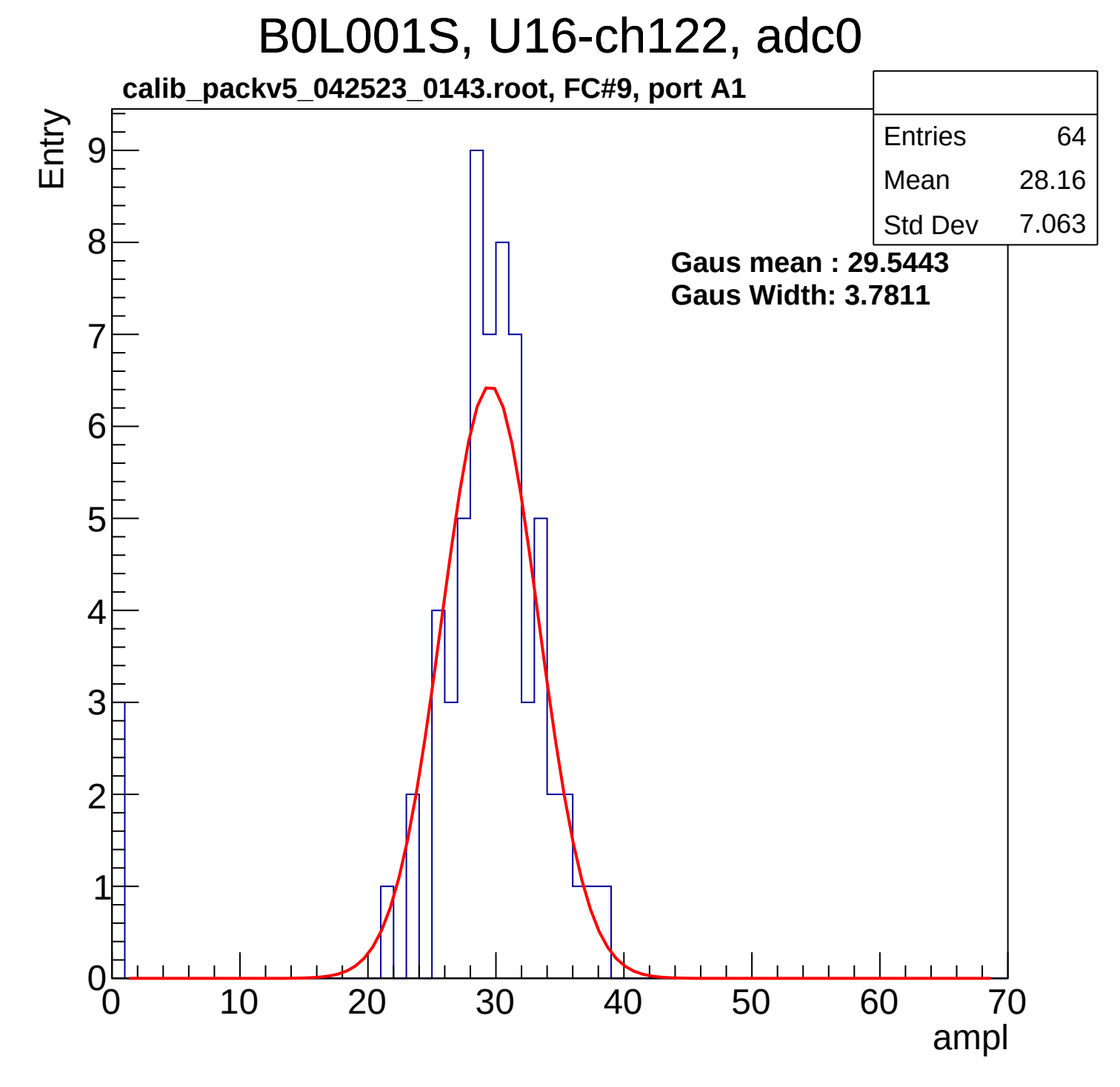
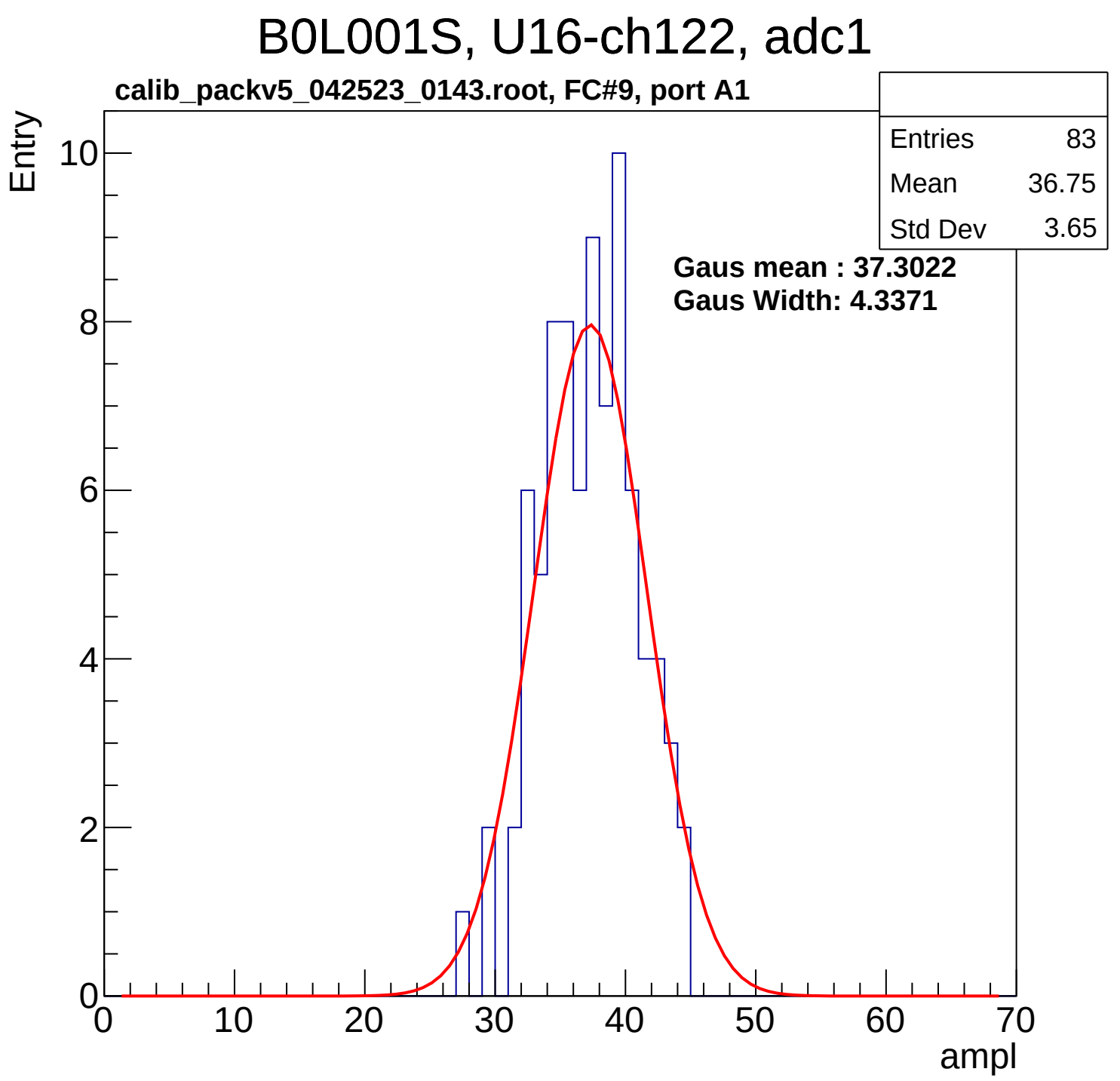
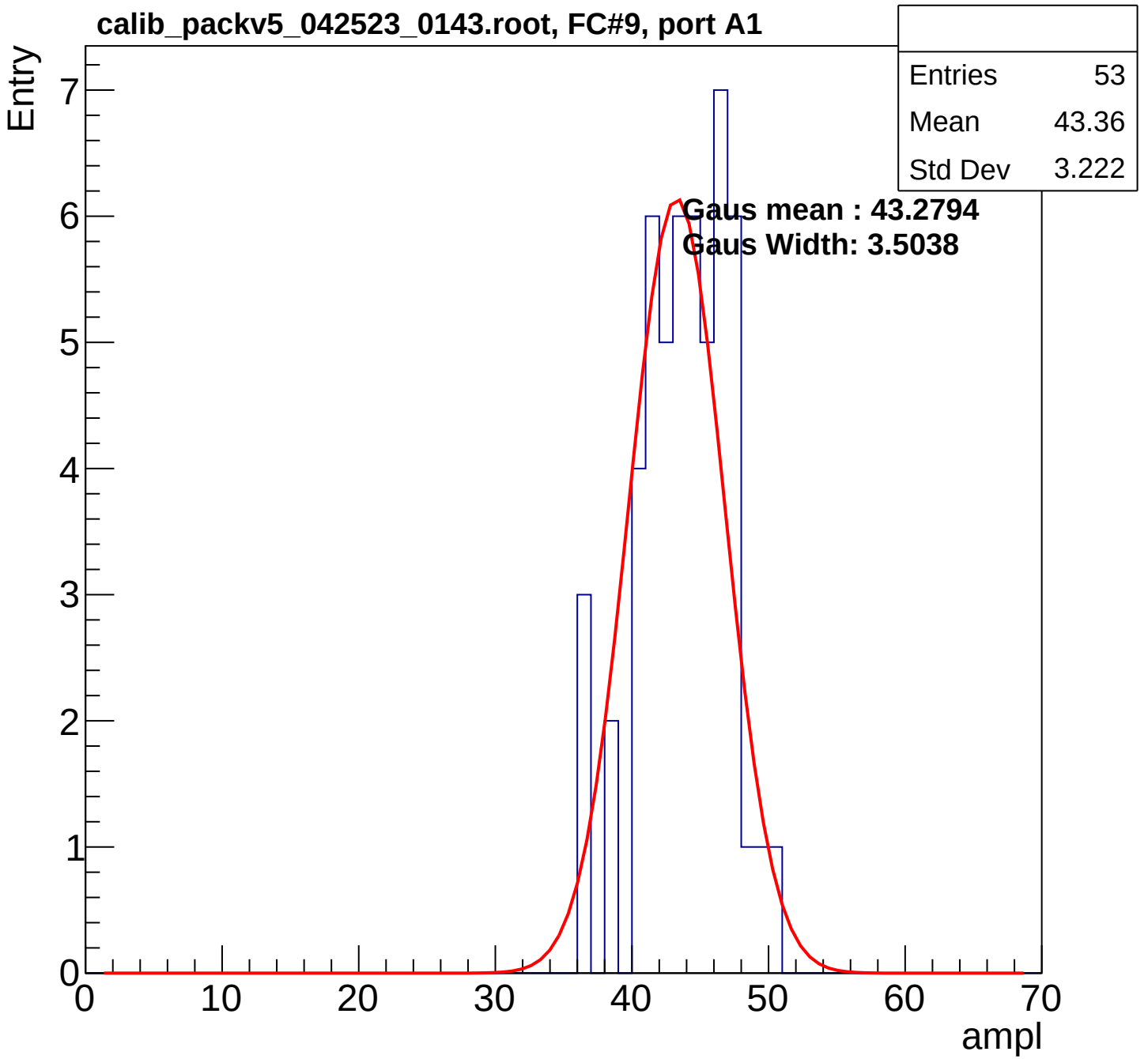
40
50
60
70


B0L001S, U16-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 28.16
Std Dev 7.063
Gaus mean : 29.5443
Gaus Width: 3.7811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 36.75
Std Dev 3.65
Gaus mean : 37.3022
Gaus Width: 4.3371
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 43.36
Std Dev 3.222
Gaus mean : 43.2794
Gaus Width: 3.5038
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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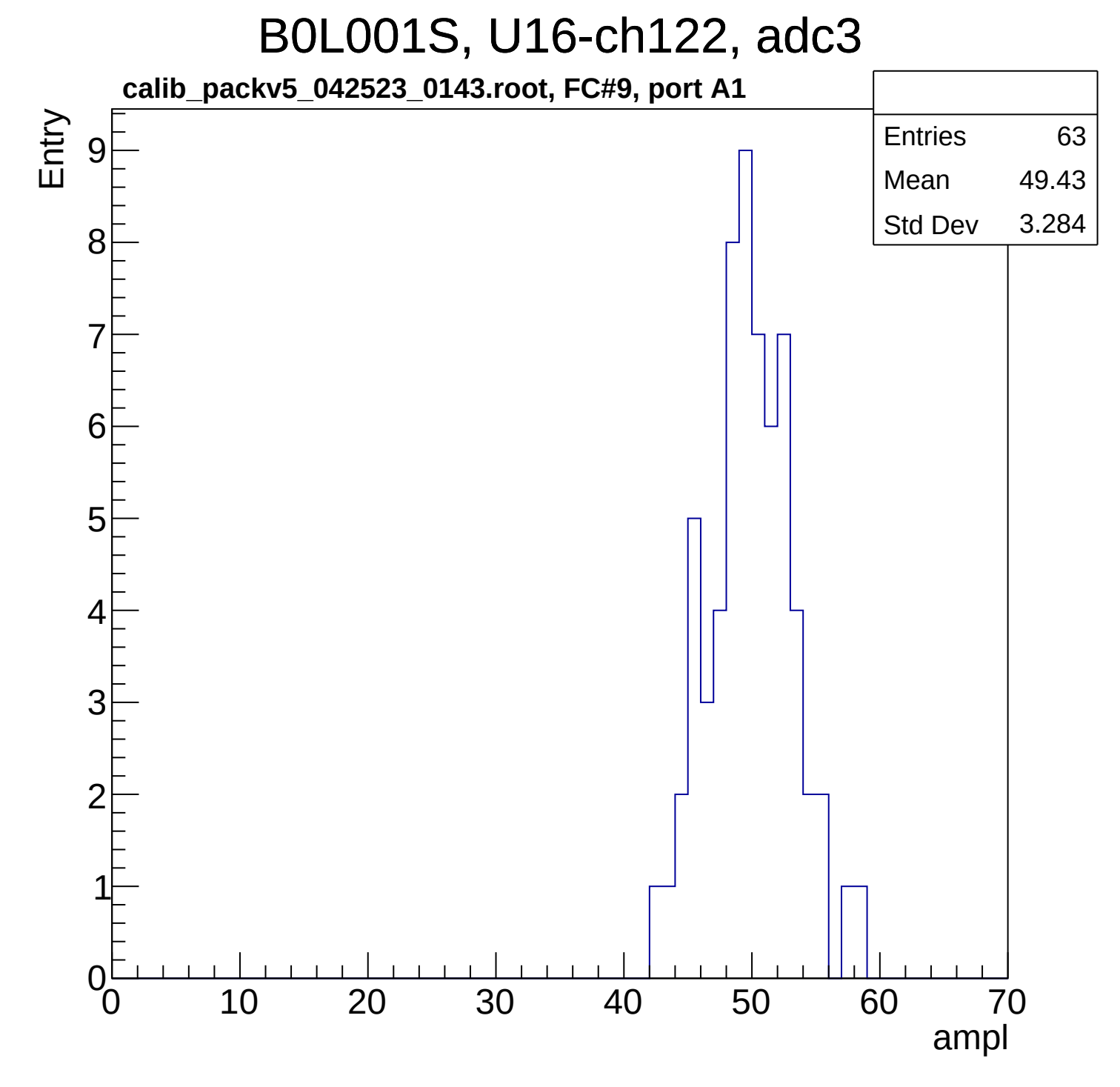
B0L001S, U16-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 49.43
Std Dev 3.284
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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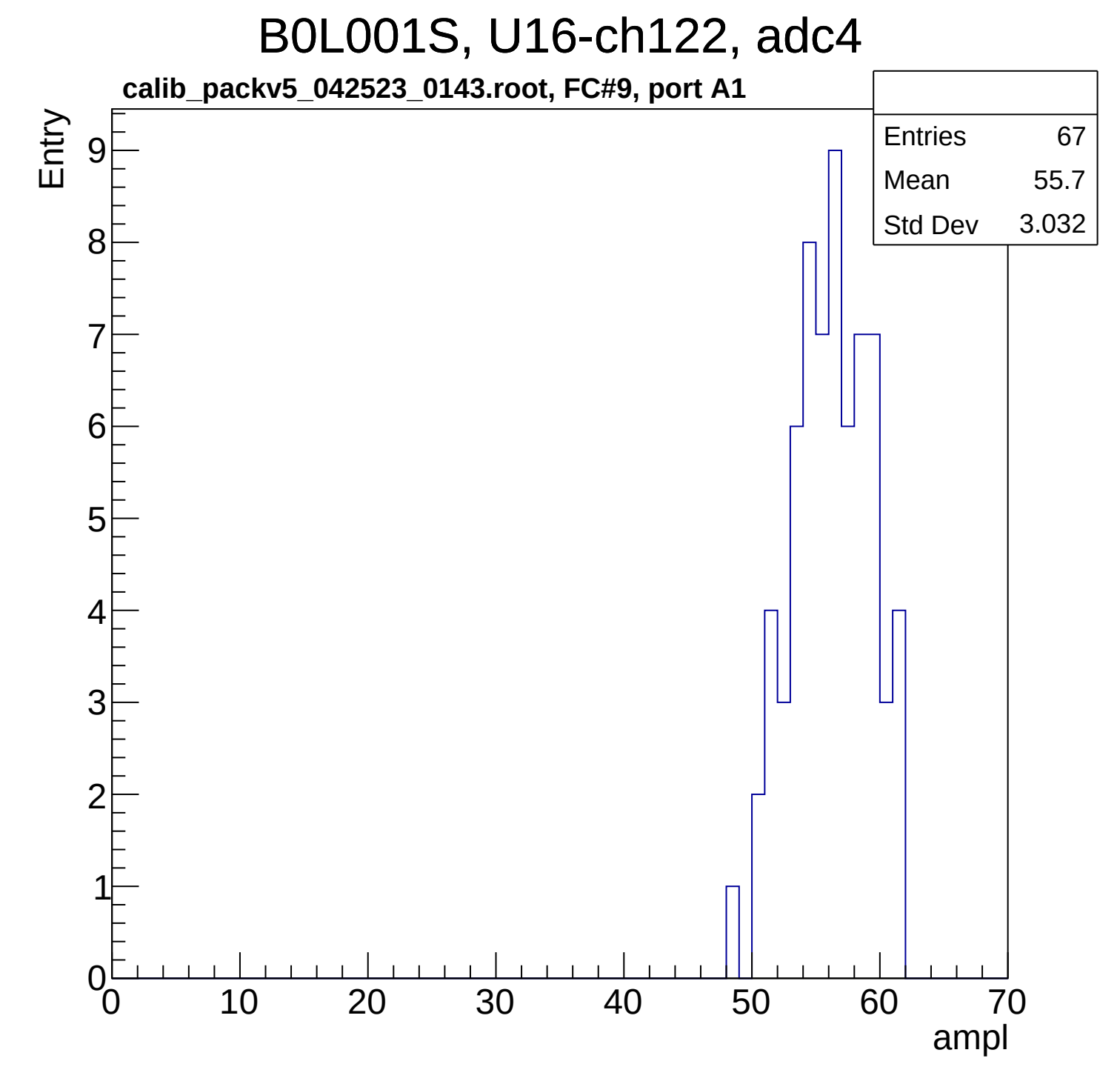
B0L001S, U16-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 55.7
Std Dev 3.032
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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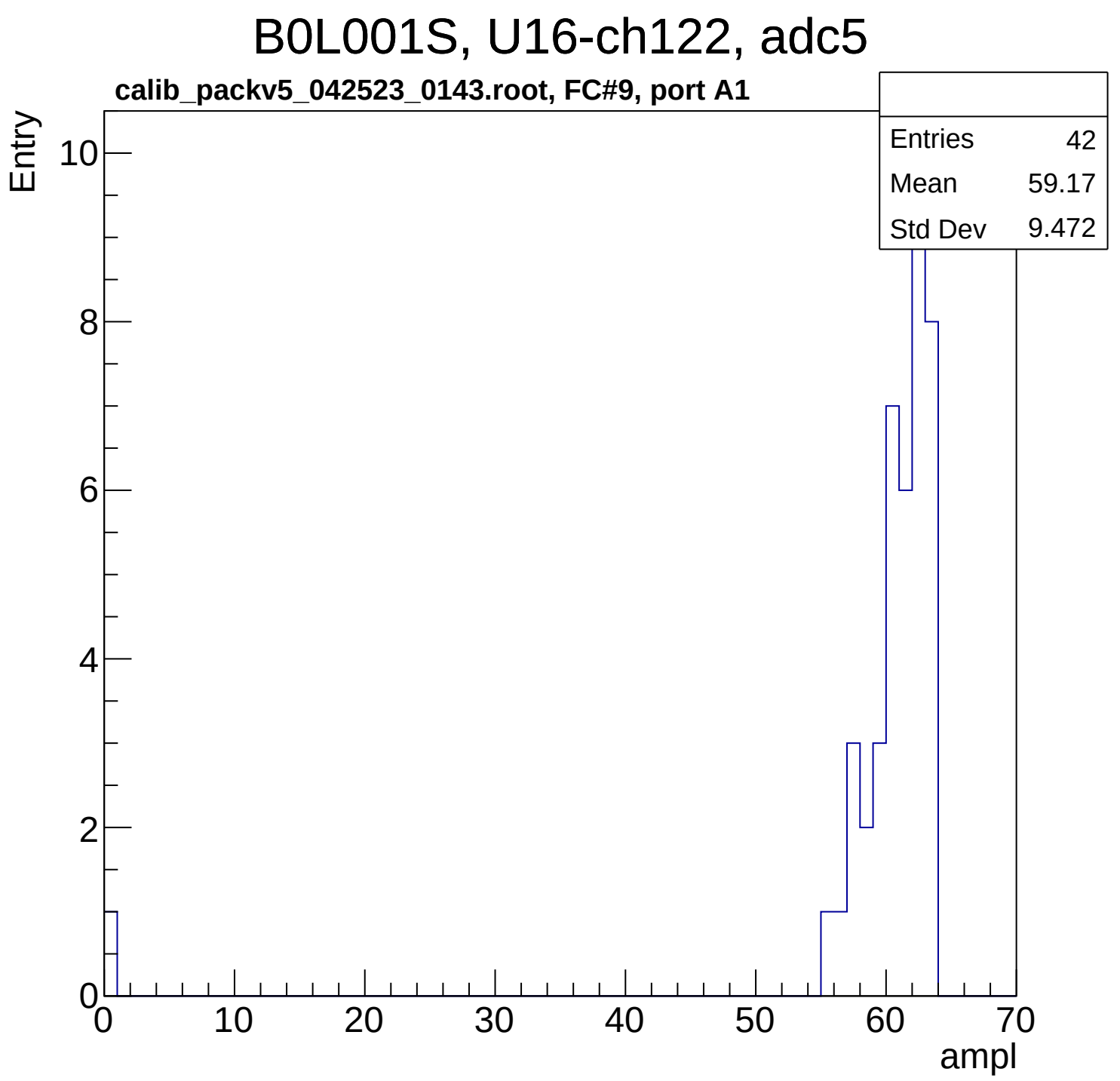
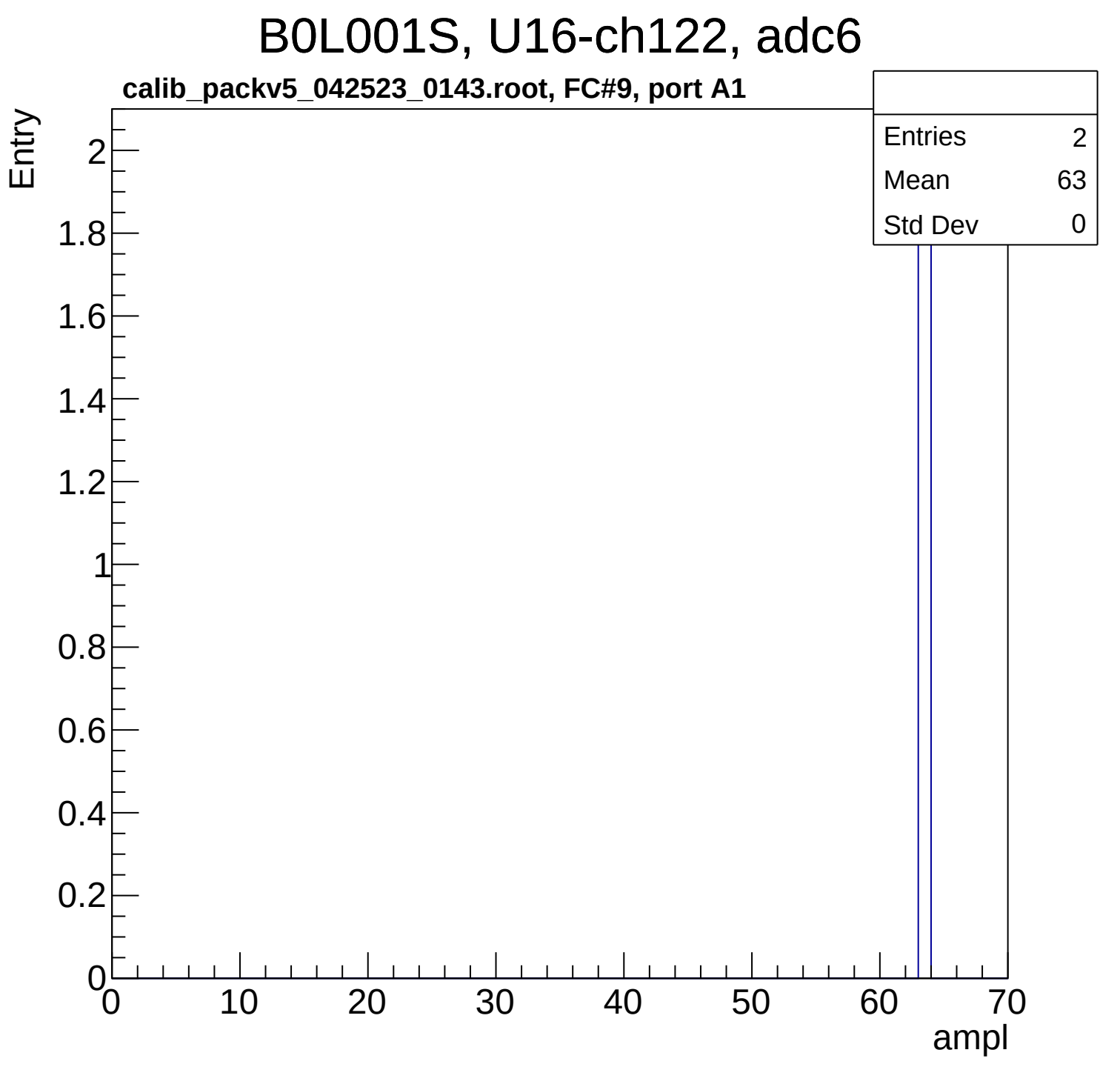
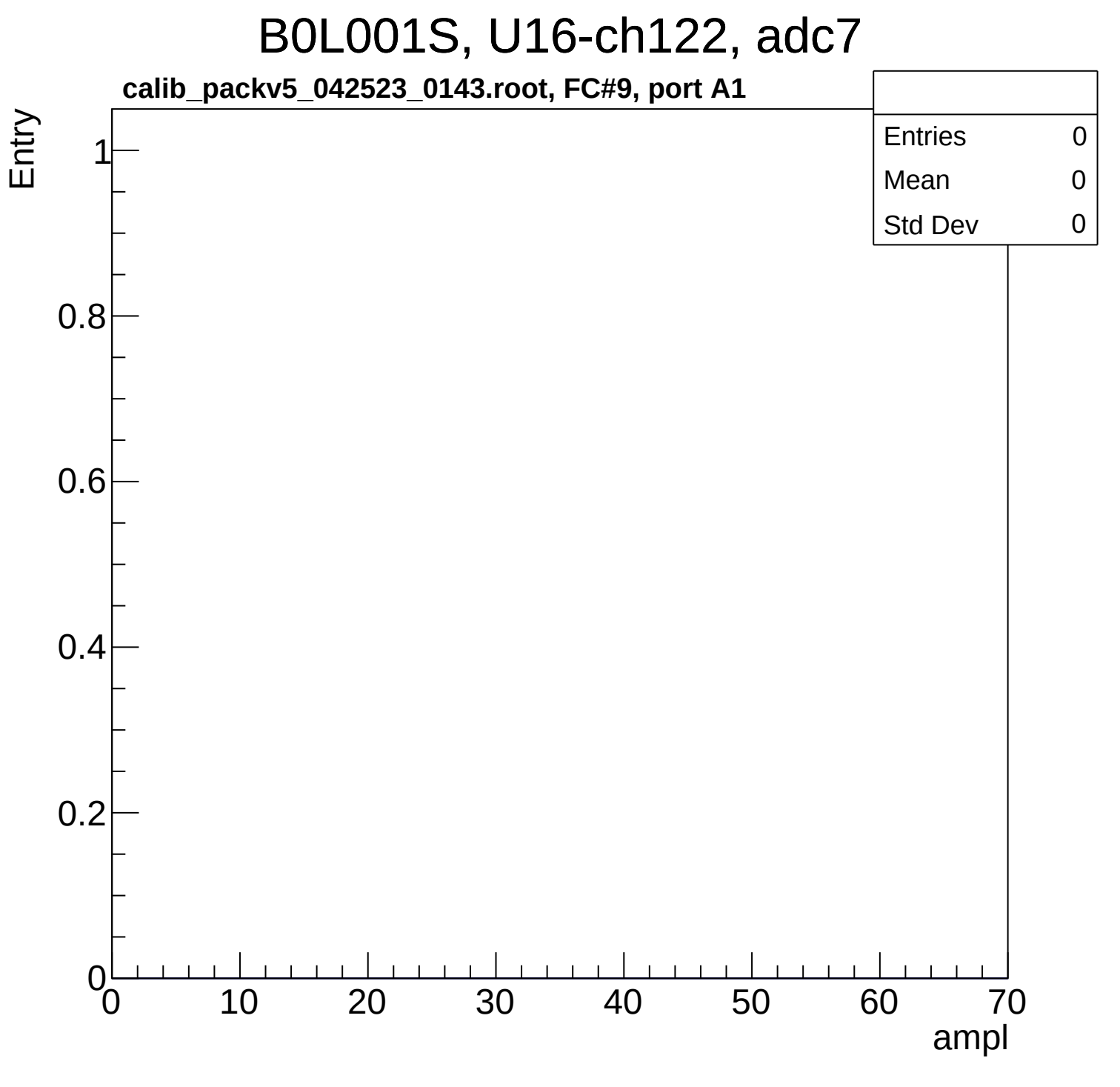
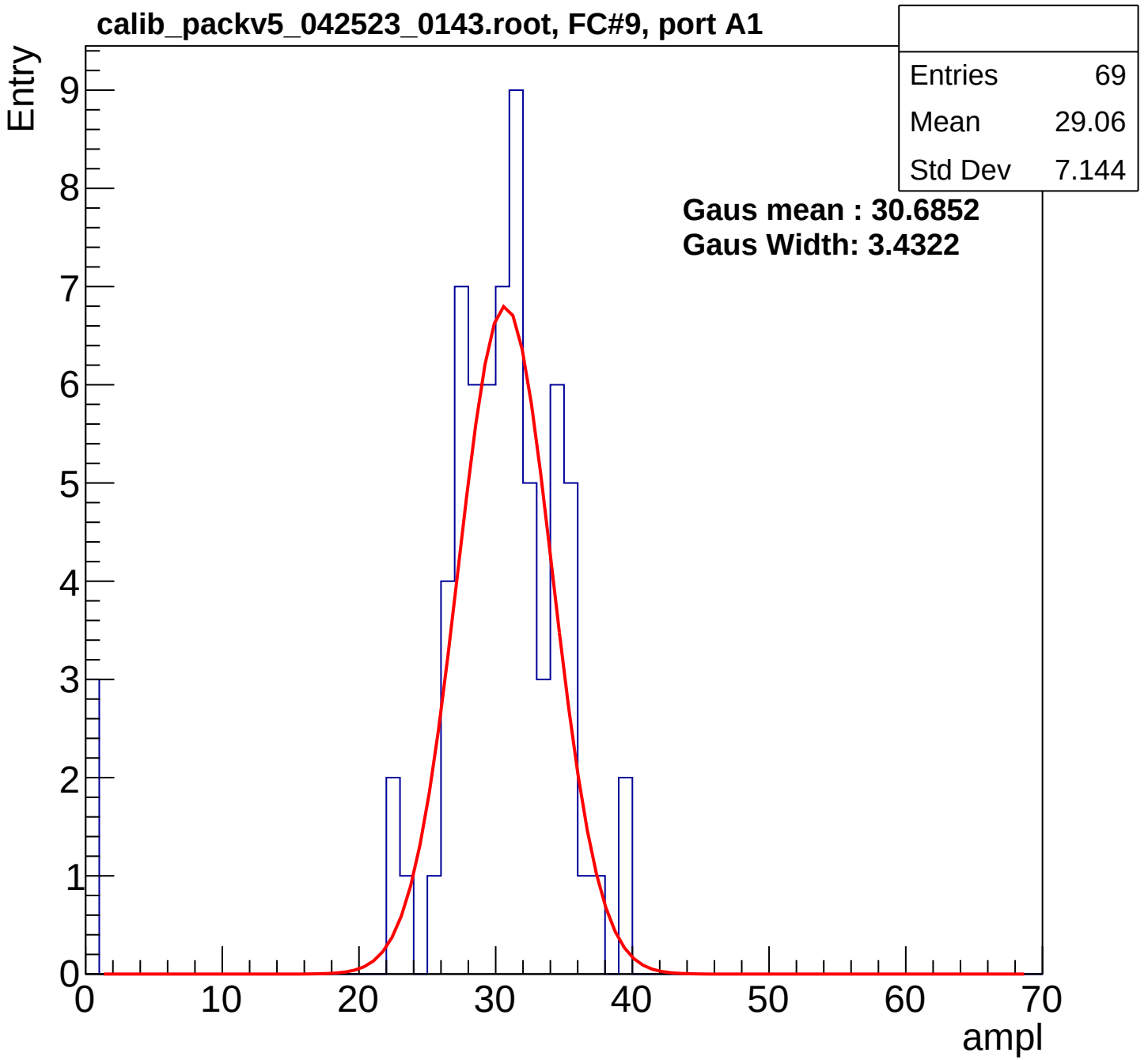
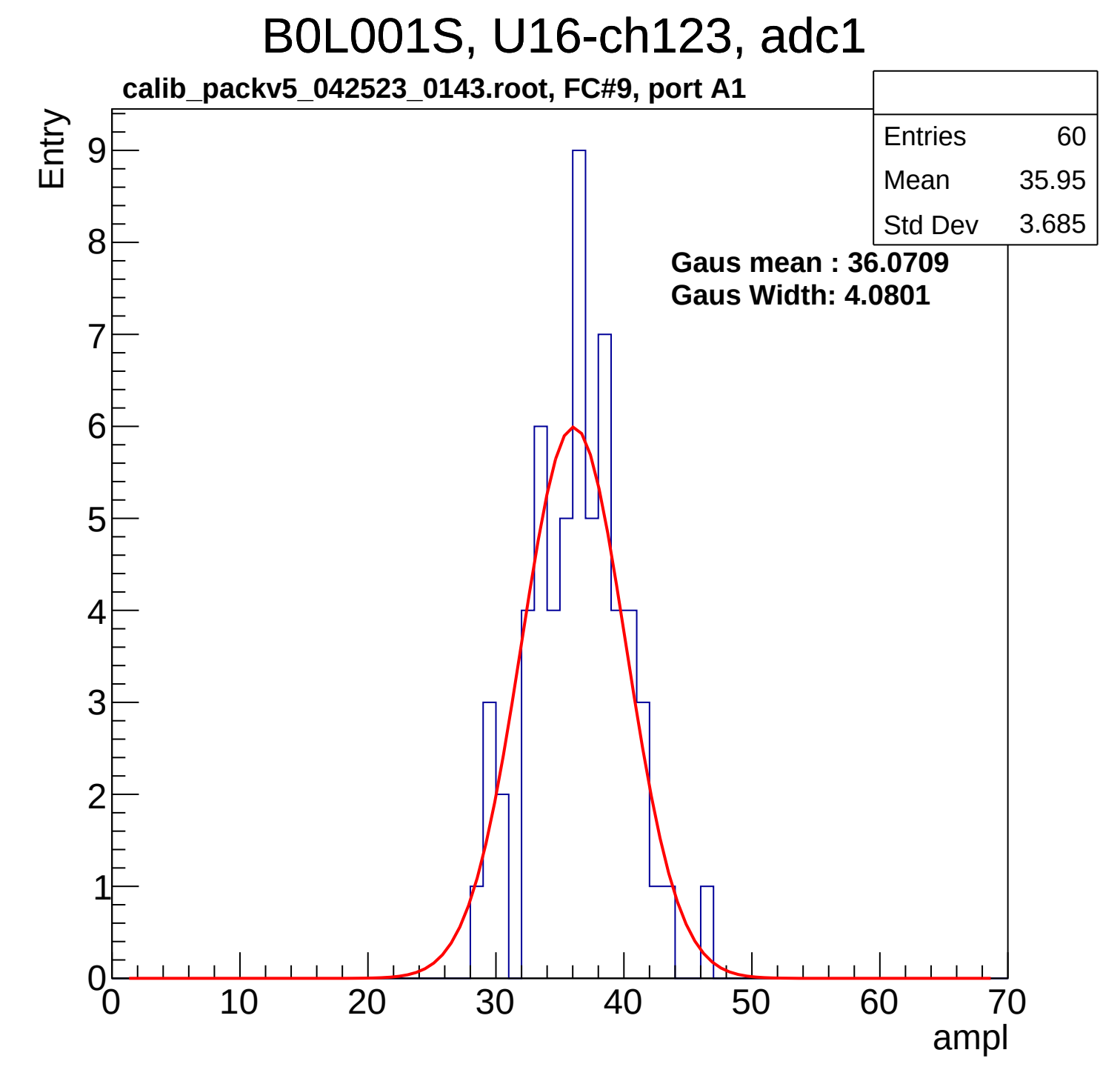

B0L001S, U16-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.17
Std Dev 9.472
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.06
Std Dev 7.144
Gaus mean : 30.6852
Gaus Width: 3.4322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 35.95
Std Dev 3.685
Gaus mean : 36.0709
Gaus Width: 4.0801
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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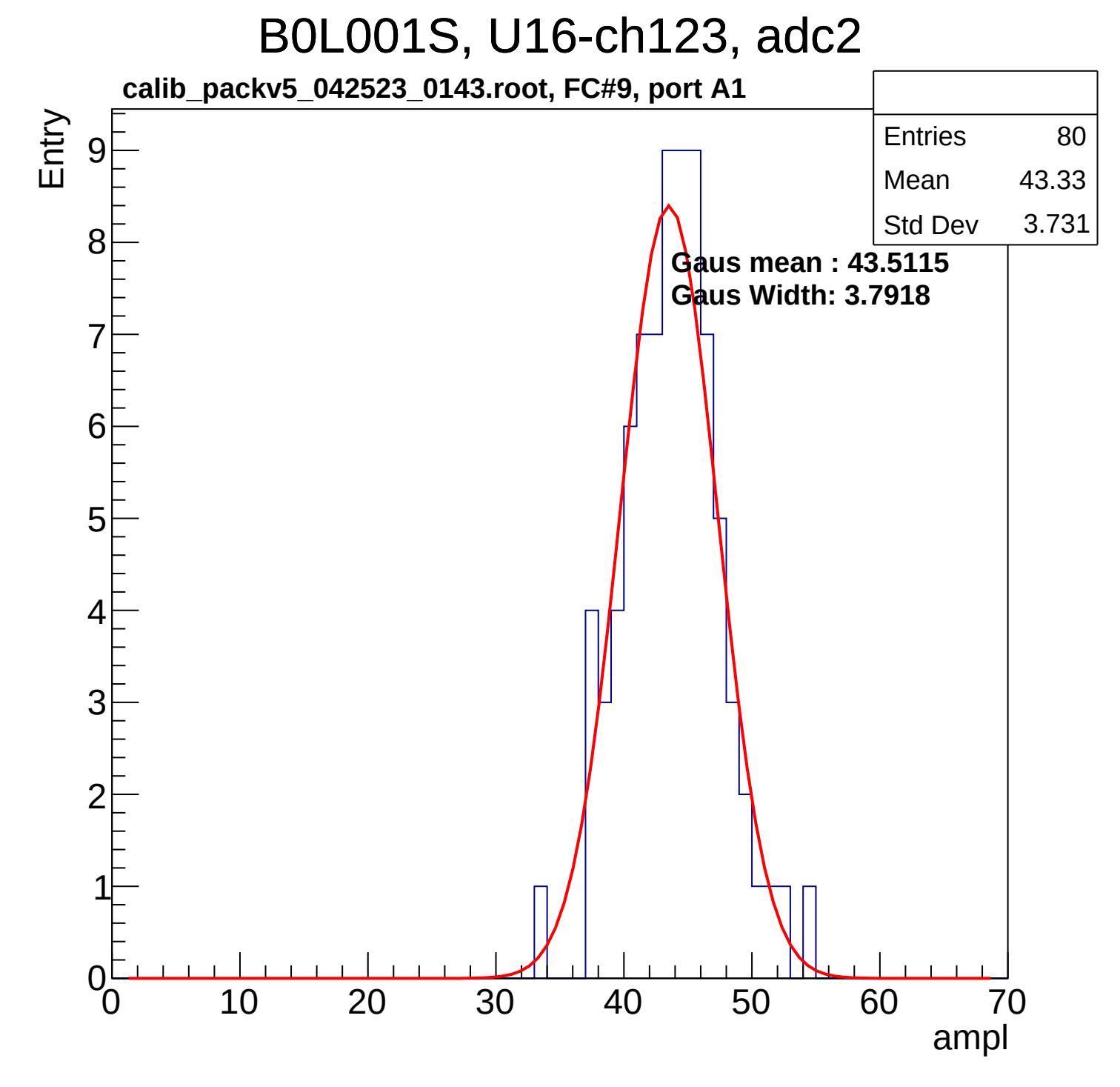
B0L001S, U16-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.33
Std Dev 3.731
Gaus mean : 43.5115
Gaus Width: 3.7918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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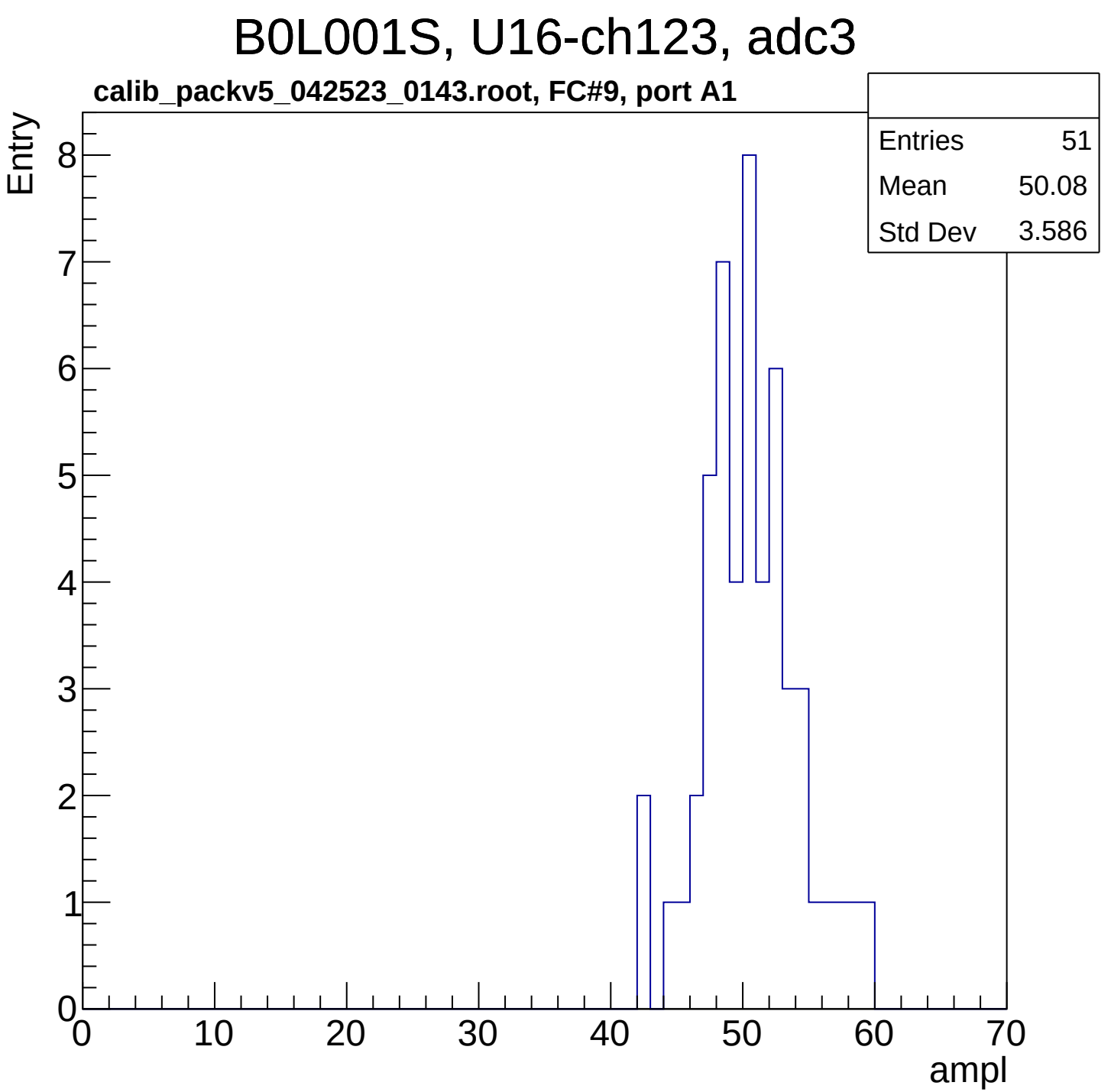
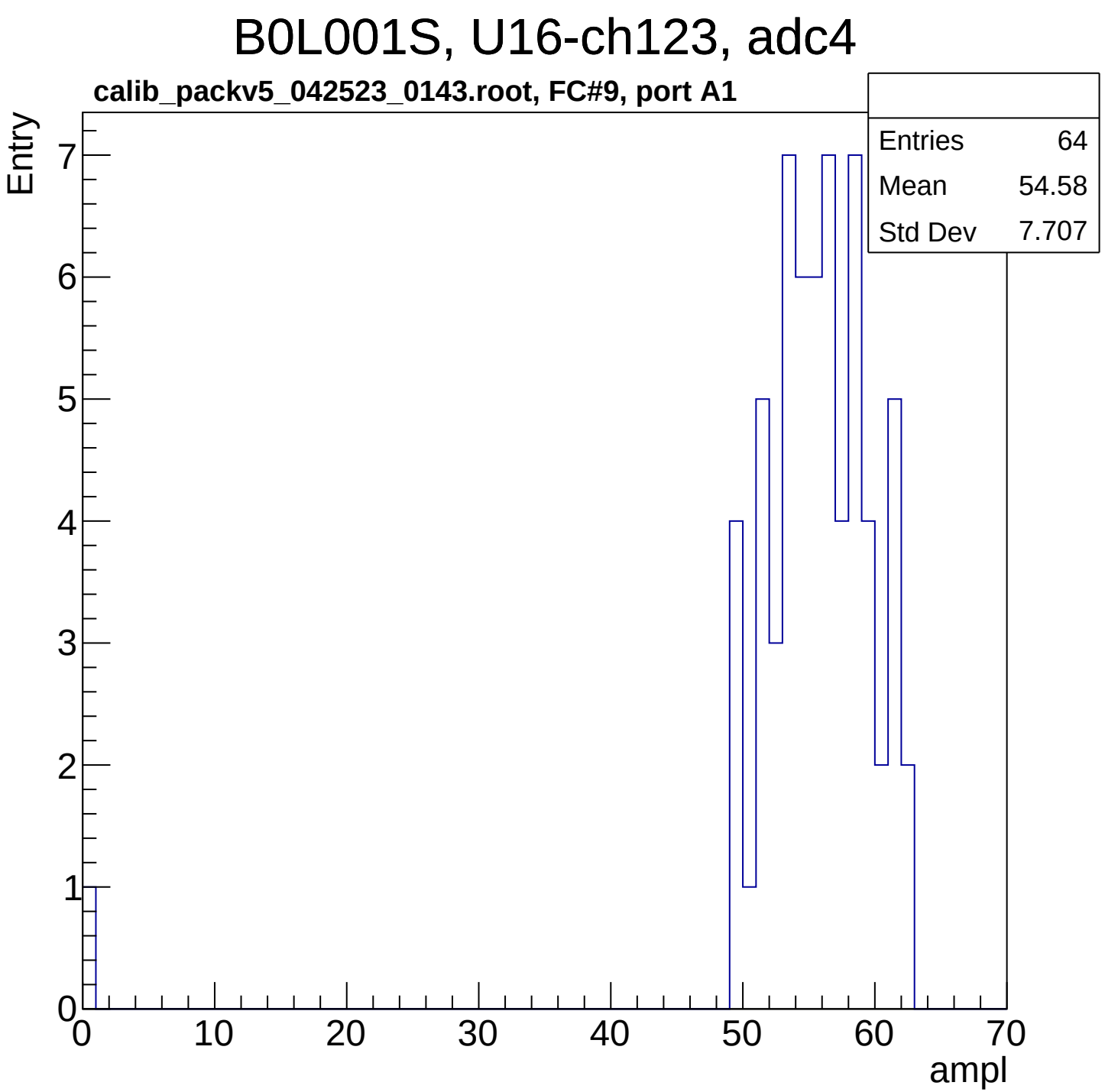

B0L001S, U16-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 50.08
Std Dev 3.586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 54.58
Std Dev 7.707
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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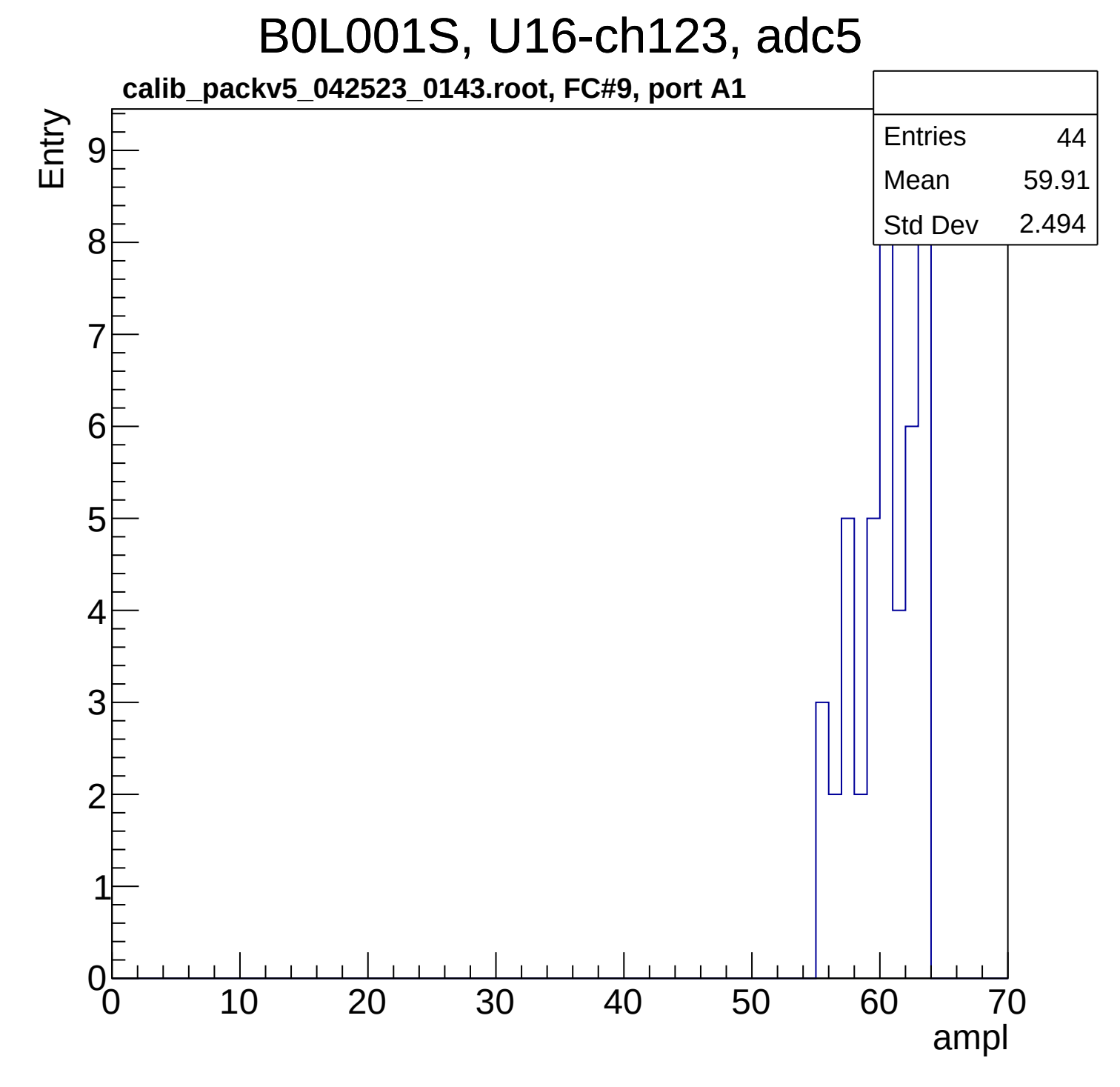
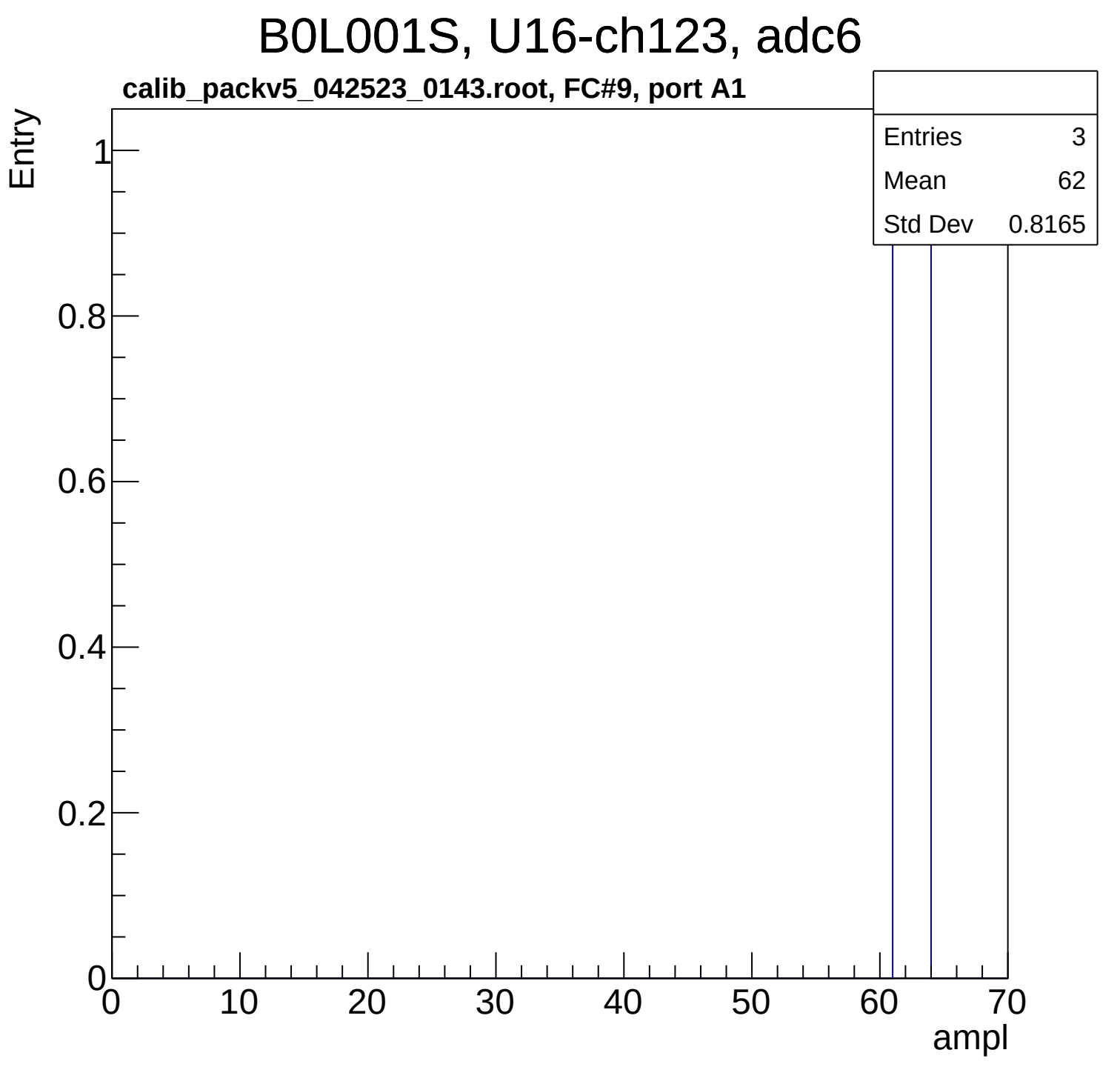

B0L001S, U16-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.91
Std Dev 2.494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
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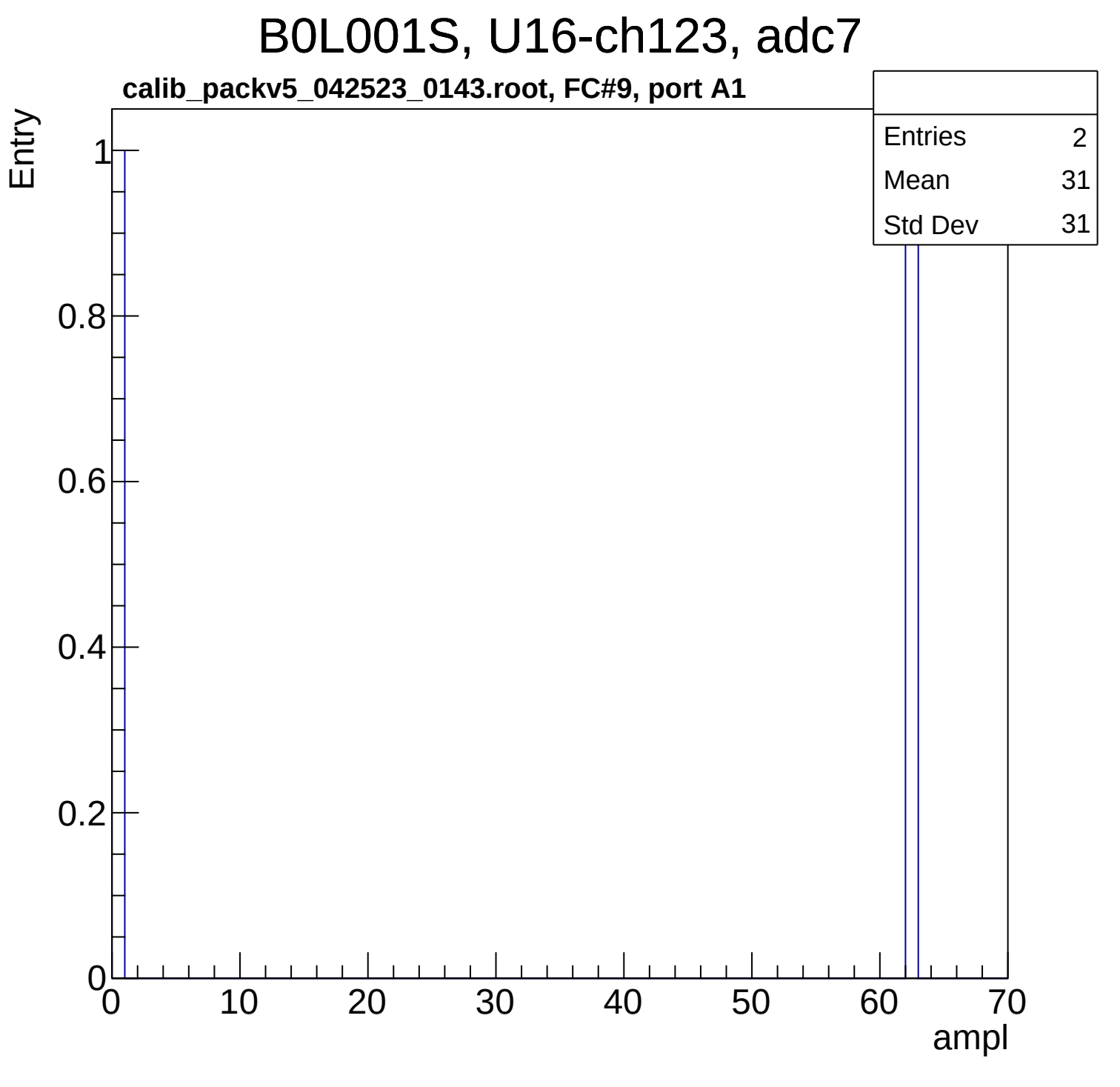
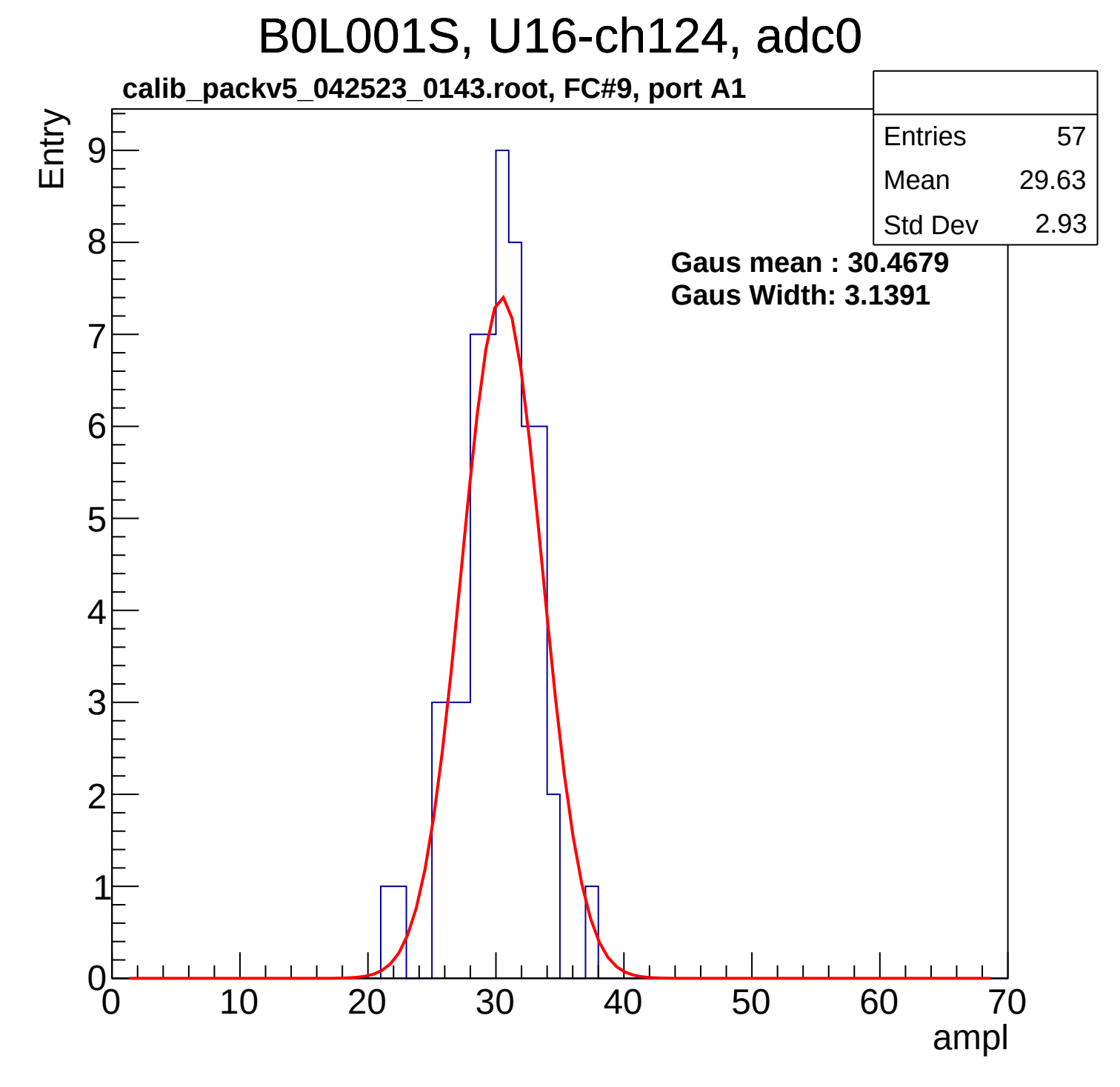
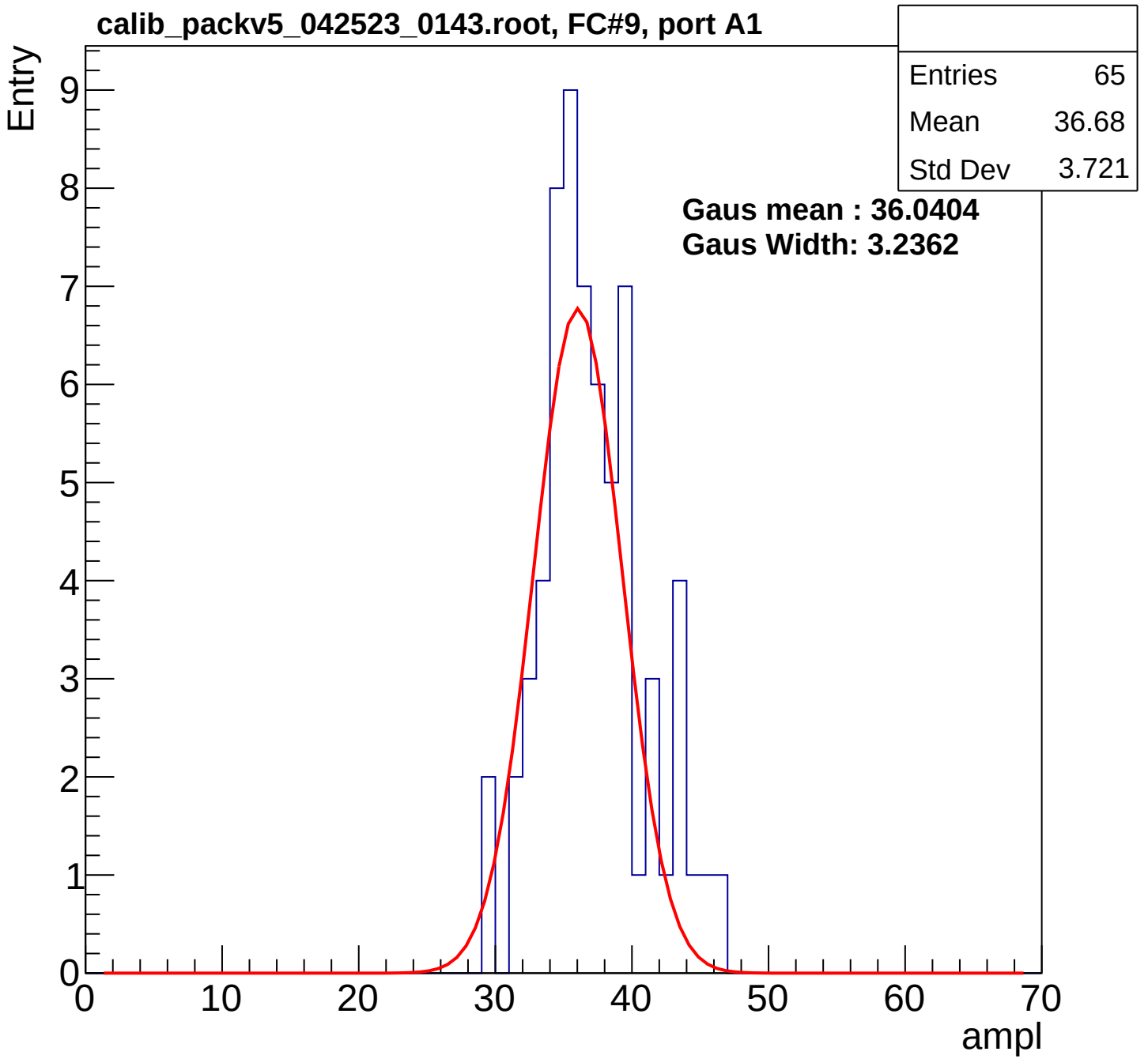
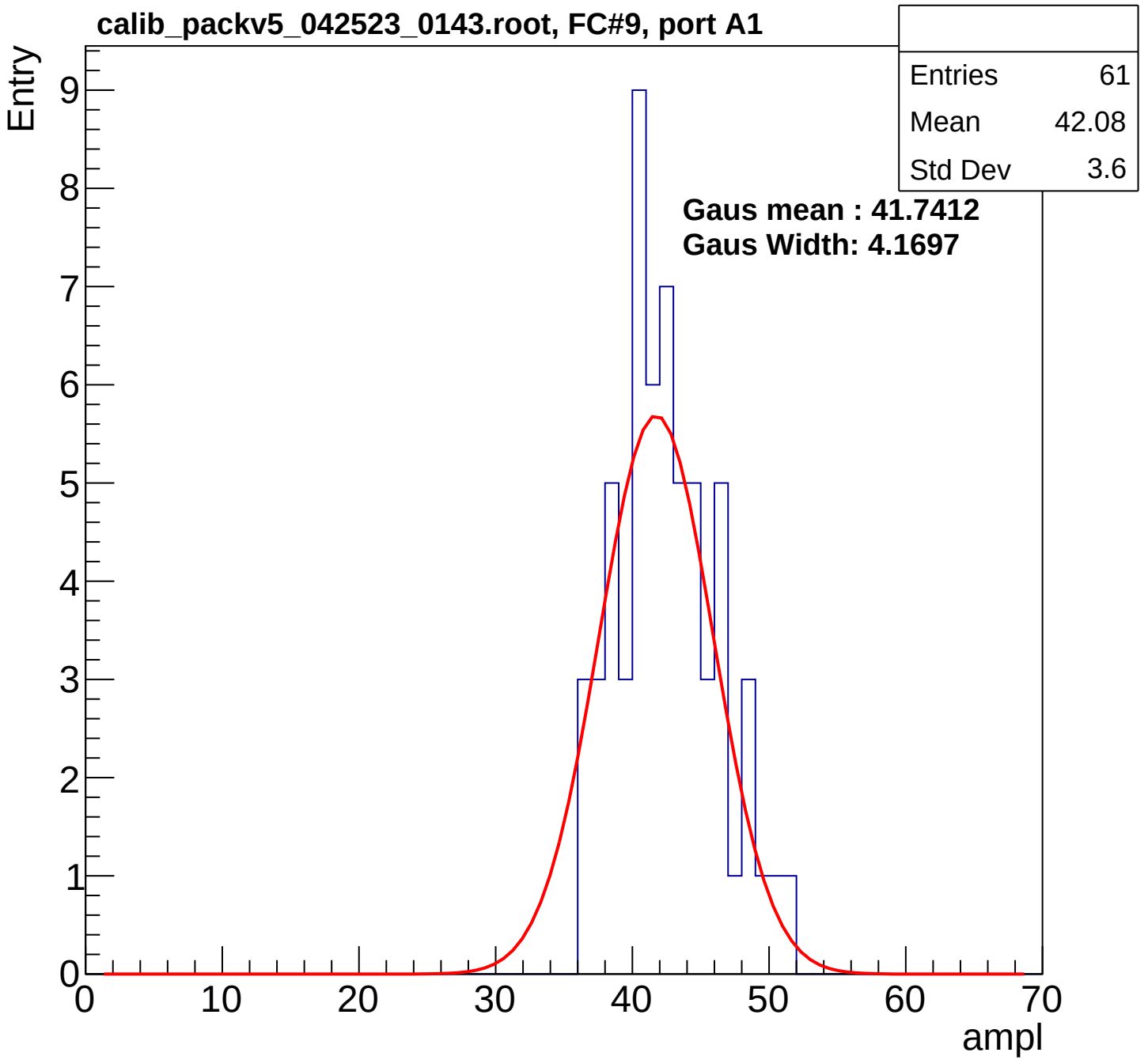
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 29.63
Std Dev 2.93
Gaus mean : 30.4679
Gaus Width: 3.1391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.68
Std Dev 3.721
Gaus mean : 36.0404
Gaus Width: 3.2362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 42.08
Std Dev 3.6
Gaus mean : 41.7412
Gaus Width: 4.1697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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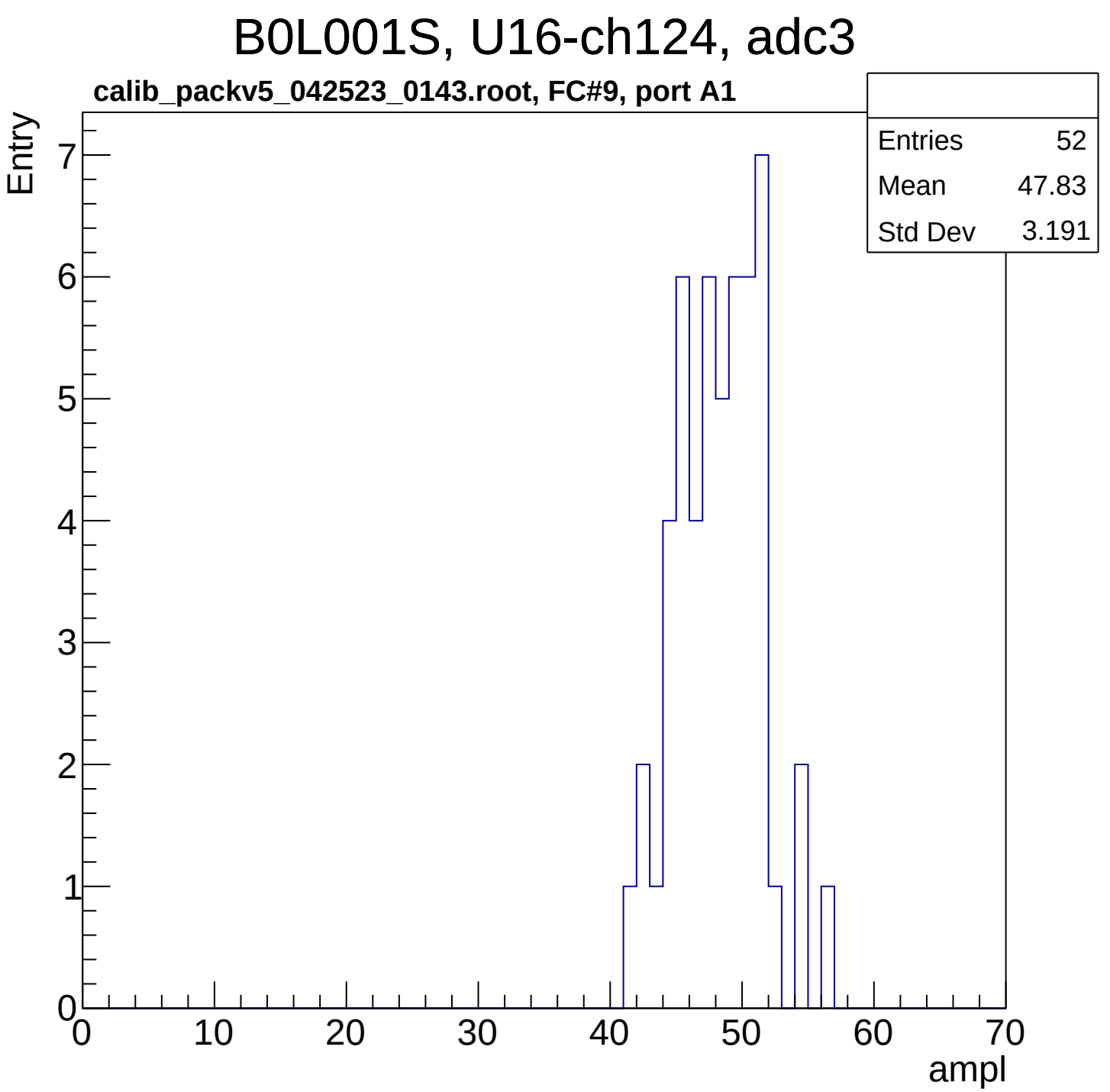
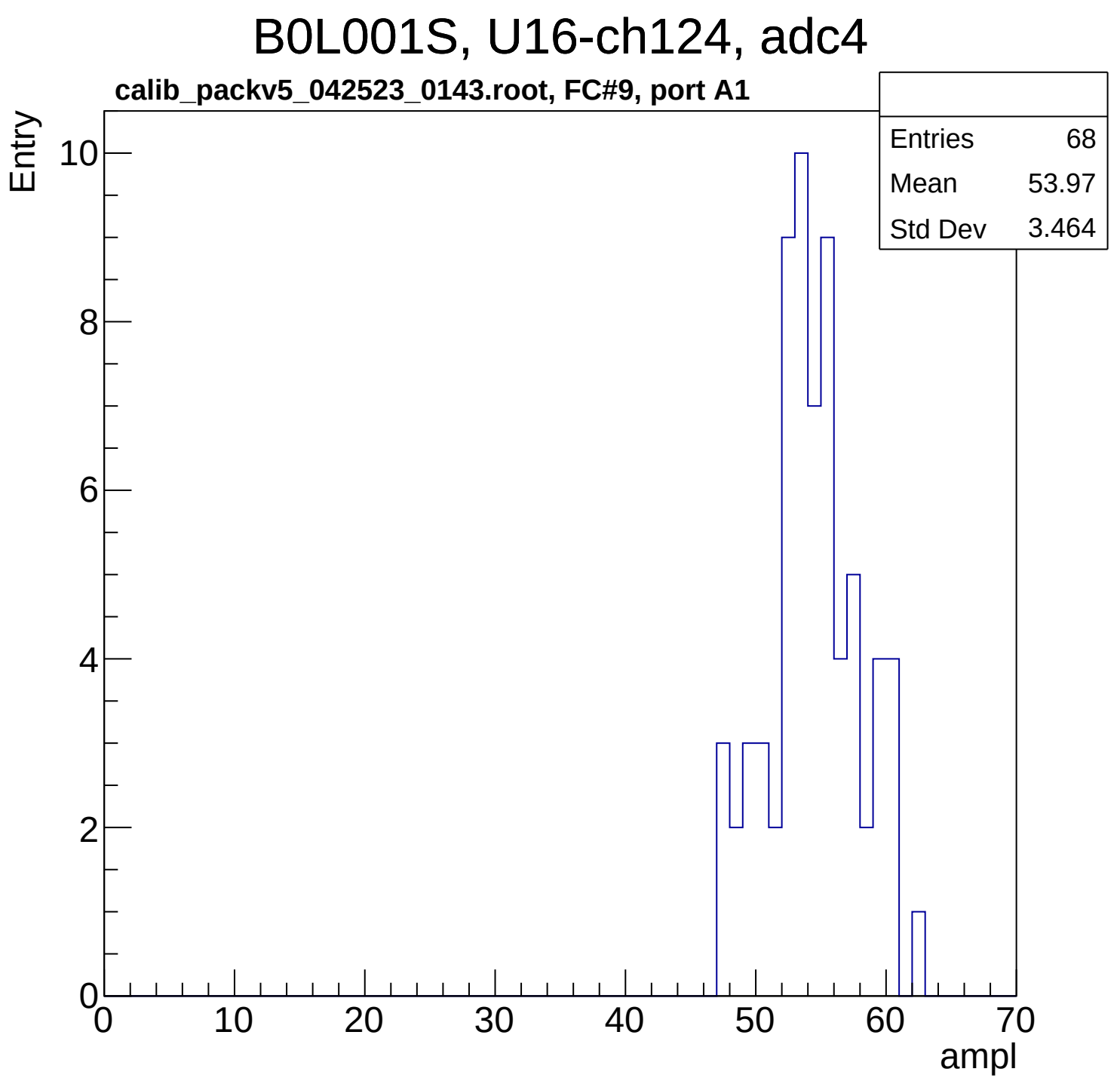
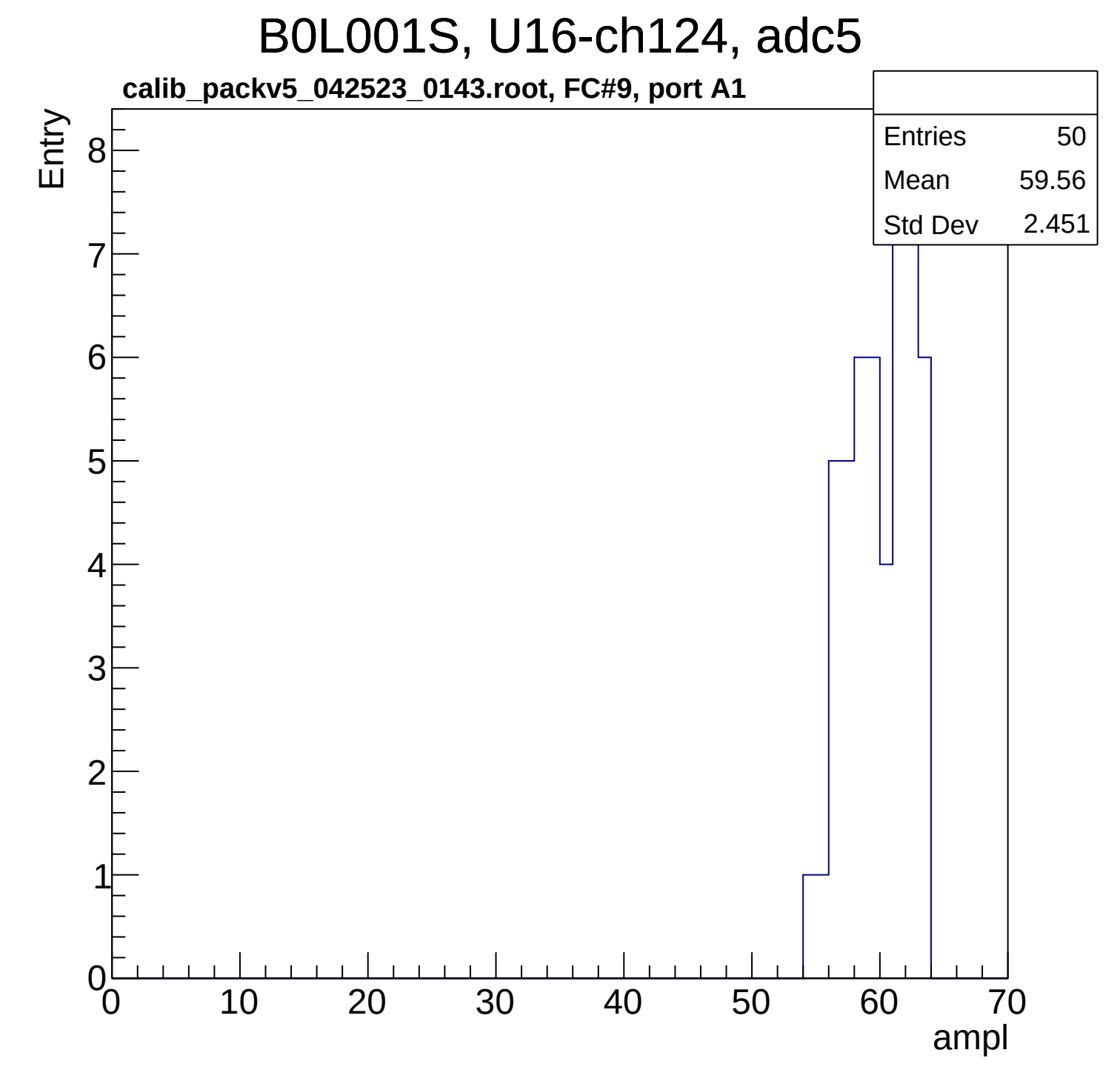

B0L001S, U16-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 47.83
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 53.97
Std Dev 3.464
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.56
Std Dev 2.451
Entry
ampl
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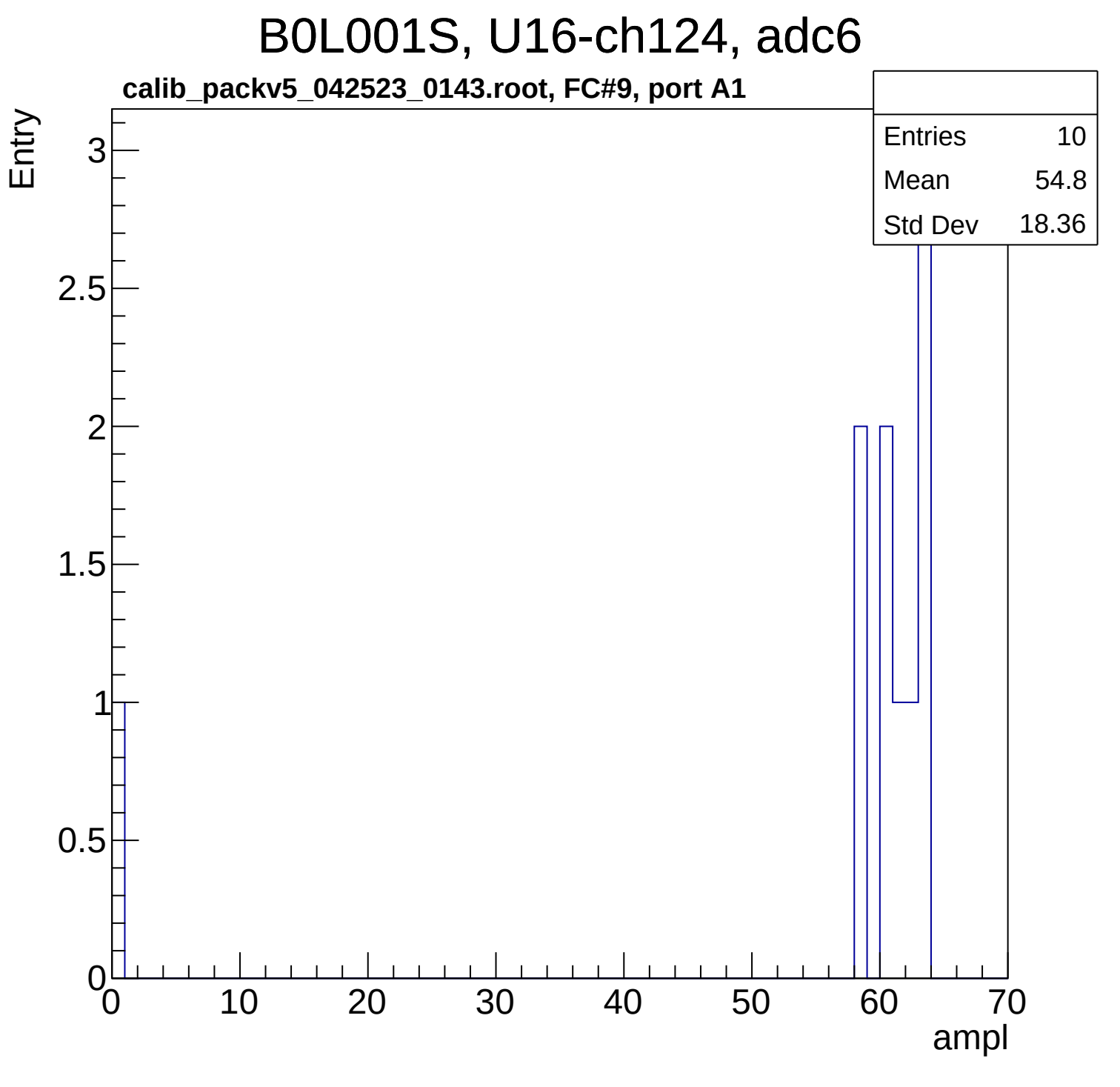
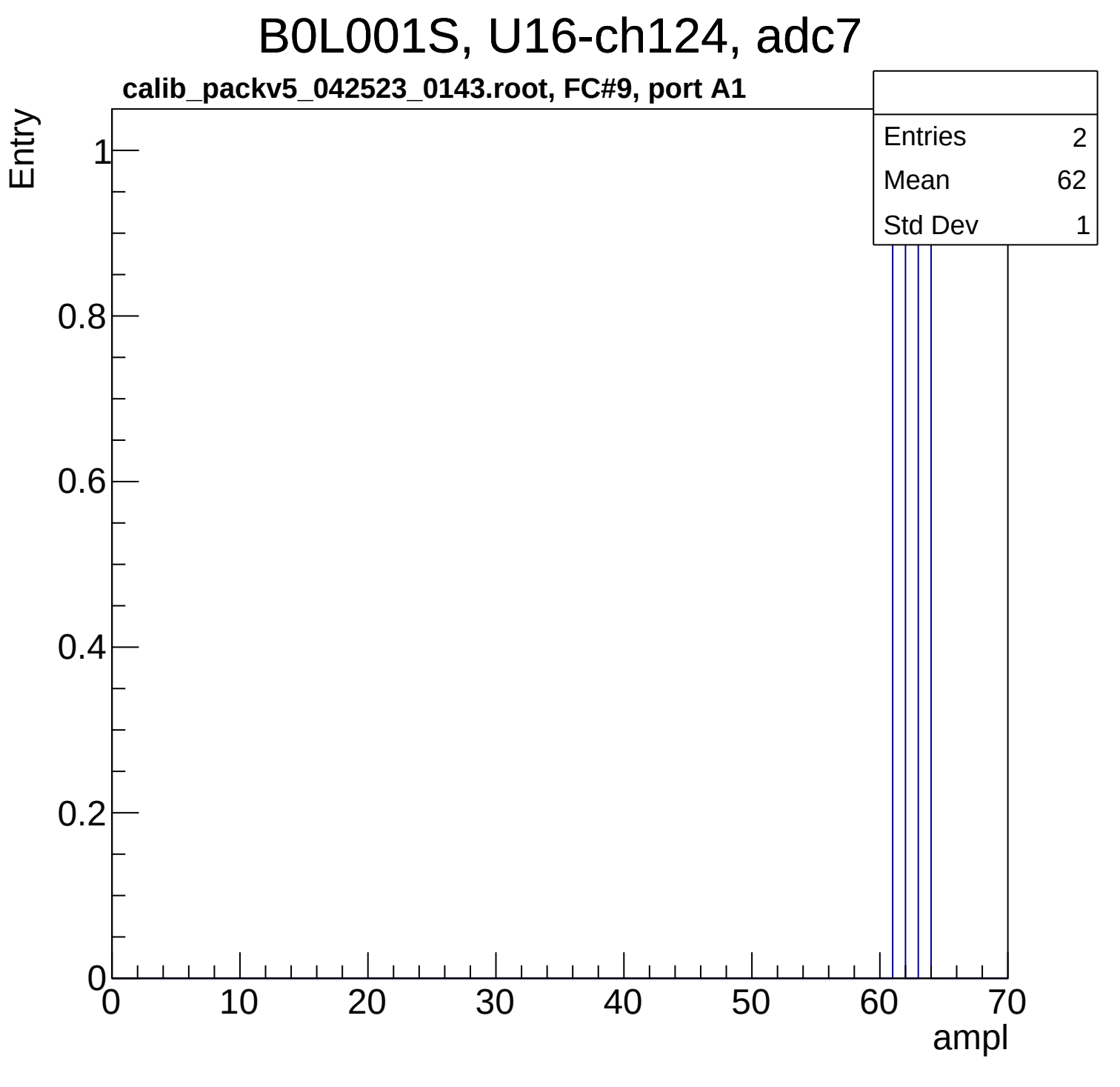
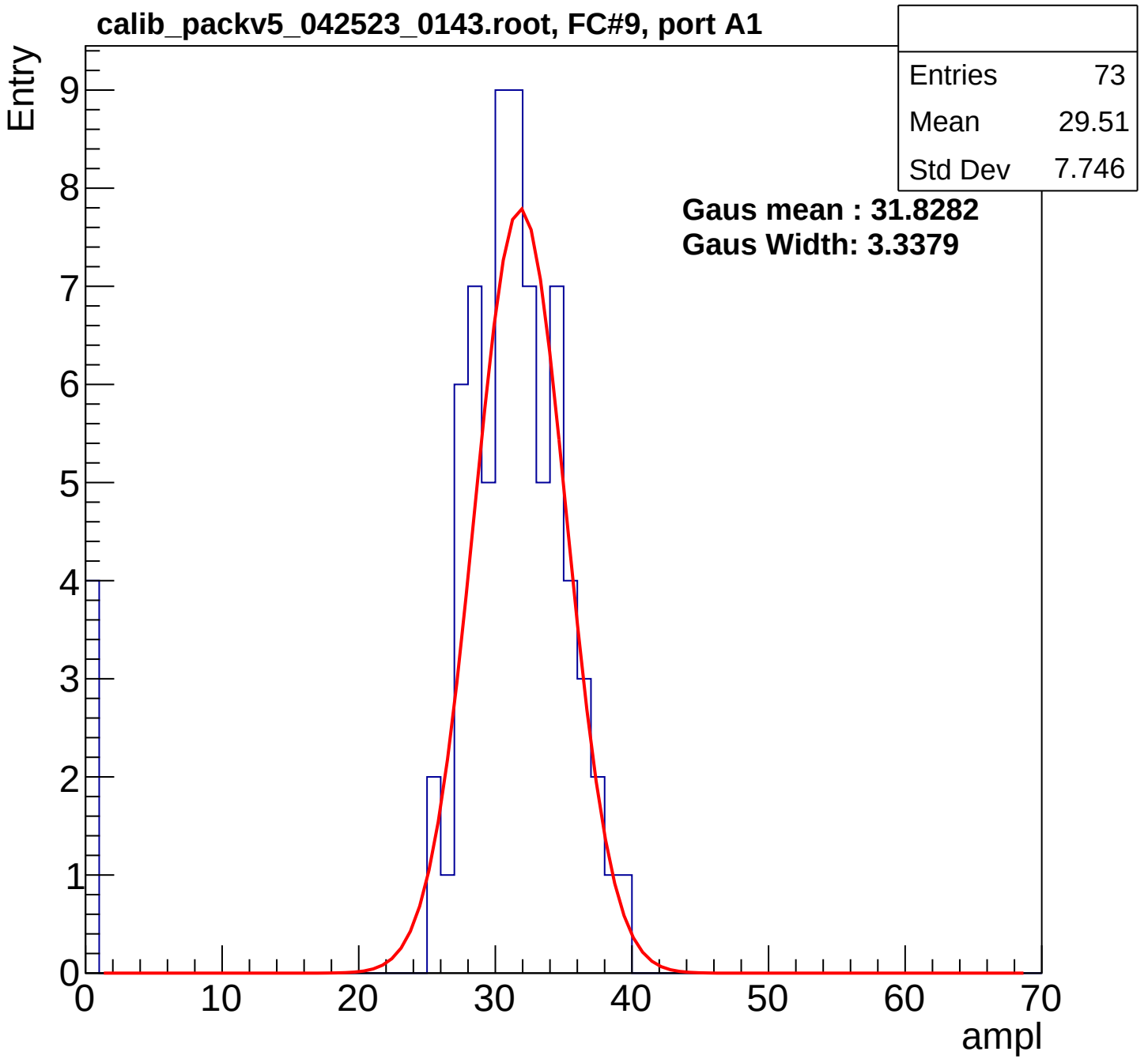
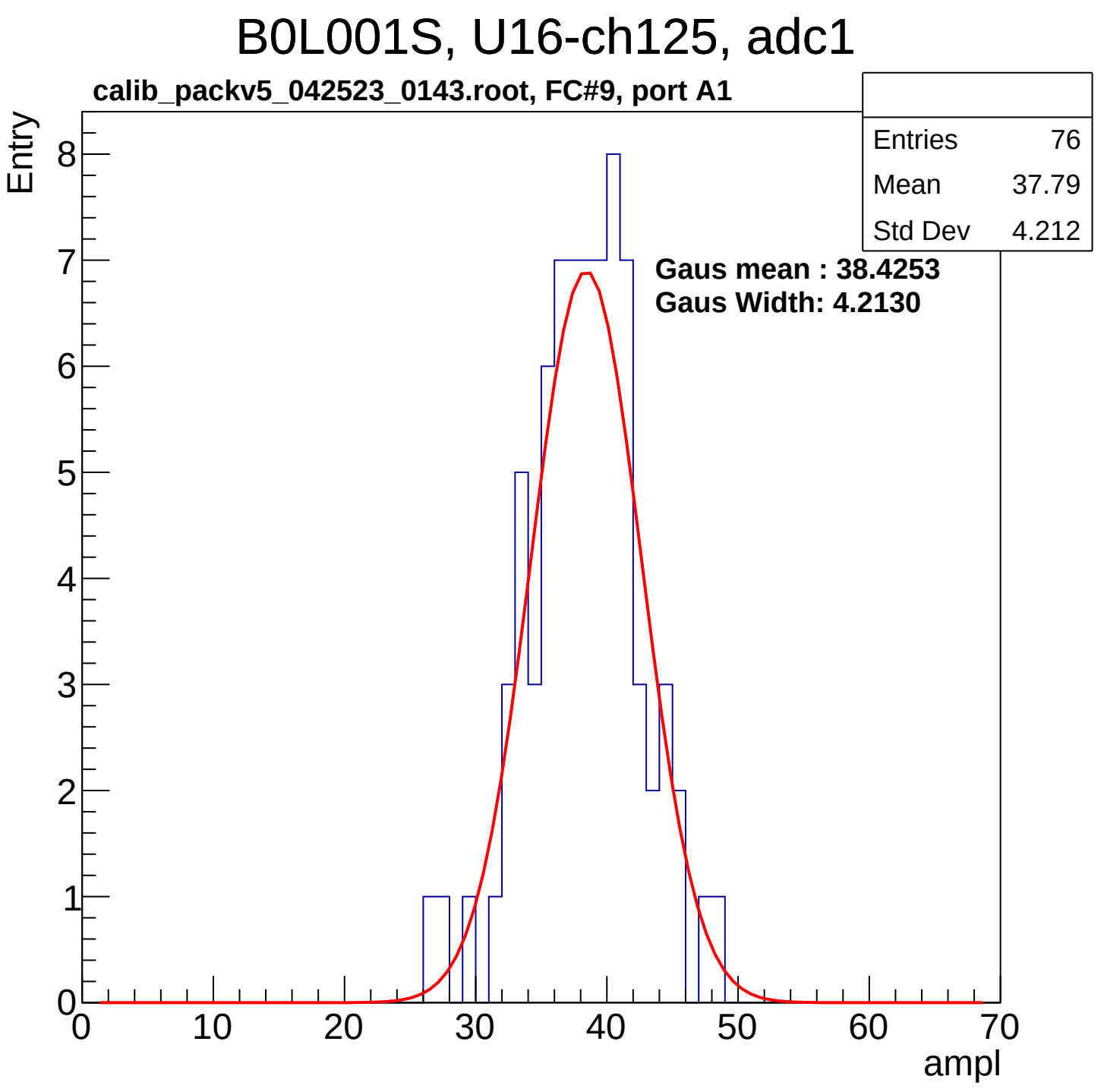
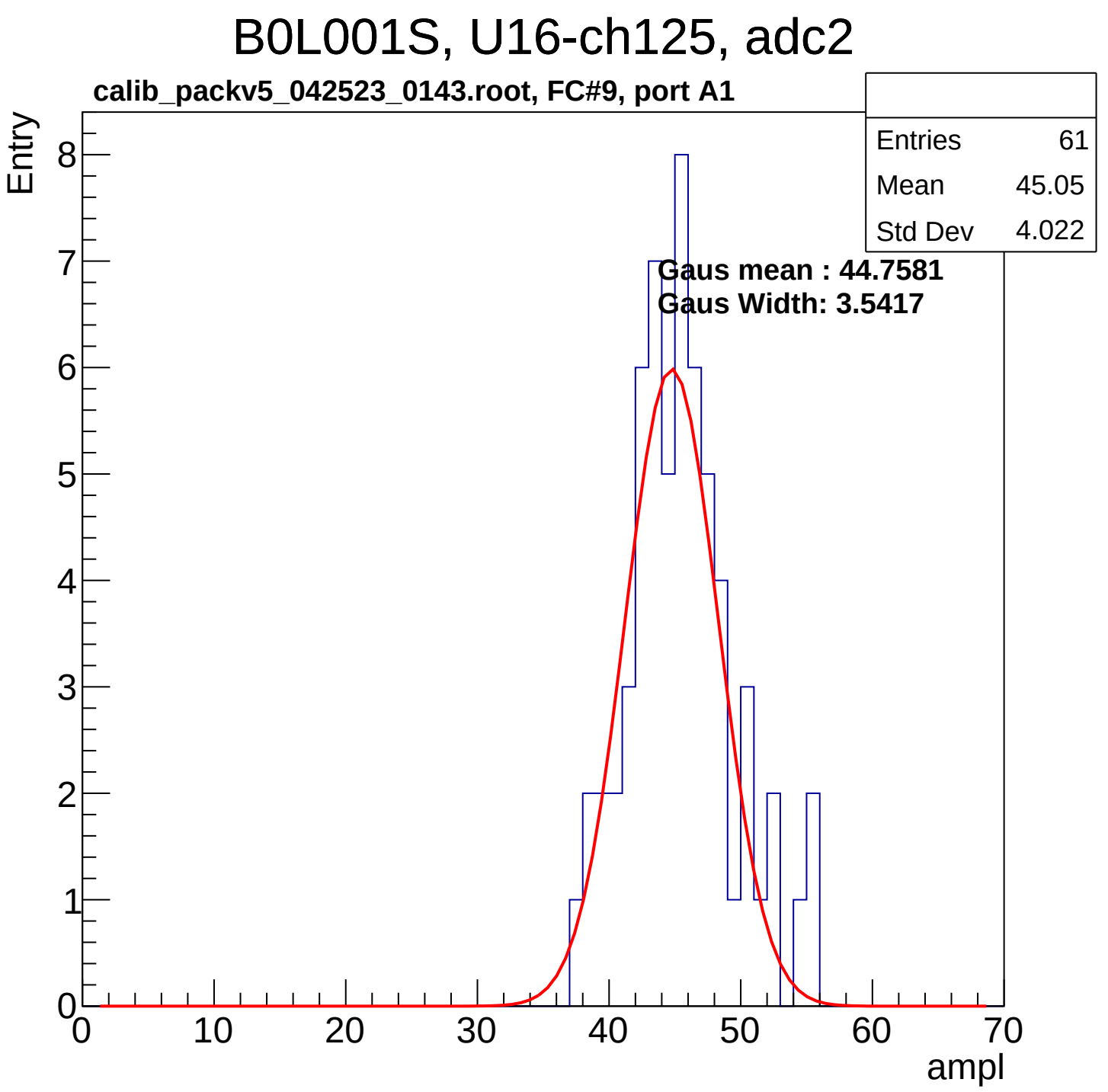
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 54.8
Std Dev 18.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U16-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.51
Std Dev 7.746
Gaus mean : 31.8282
Gaus Width: 3.3379
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.79
Std Dev 4.212
Gaus mean : 38.4253
Gaus Width: 4.2130
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U16-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.05
Std Dev 4.022
Gaus mean : 44.7581
Gaus Width: 3.5417
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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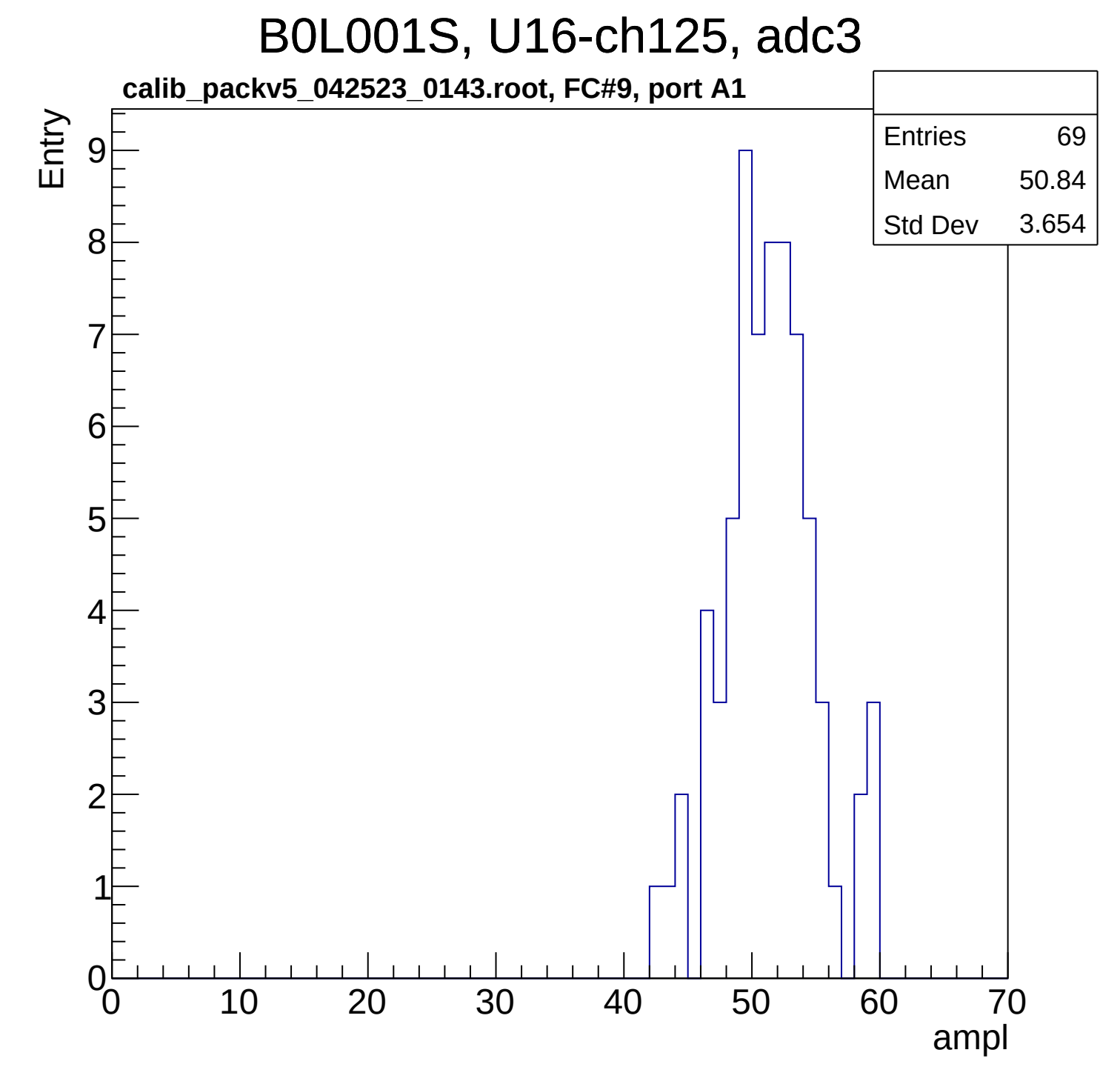
B0L001S, U16-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.84
Std Dev 3.654
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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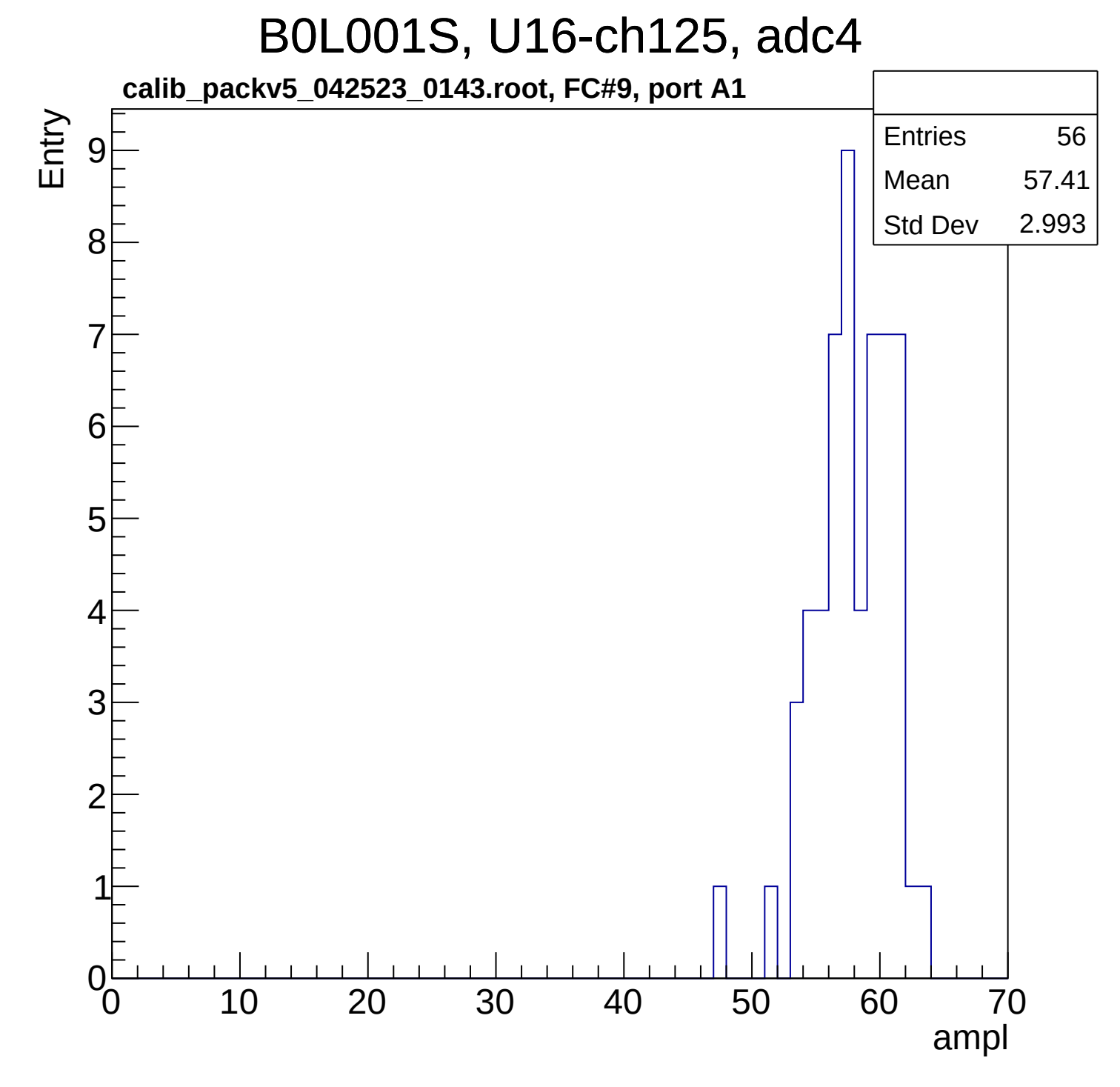
B0L001S, U16-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.41
Std Dev 2.993
Entry
ampl
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2
3
4
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6
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8
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0
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20
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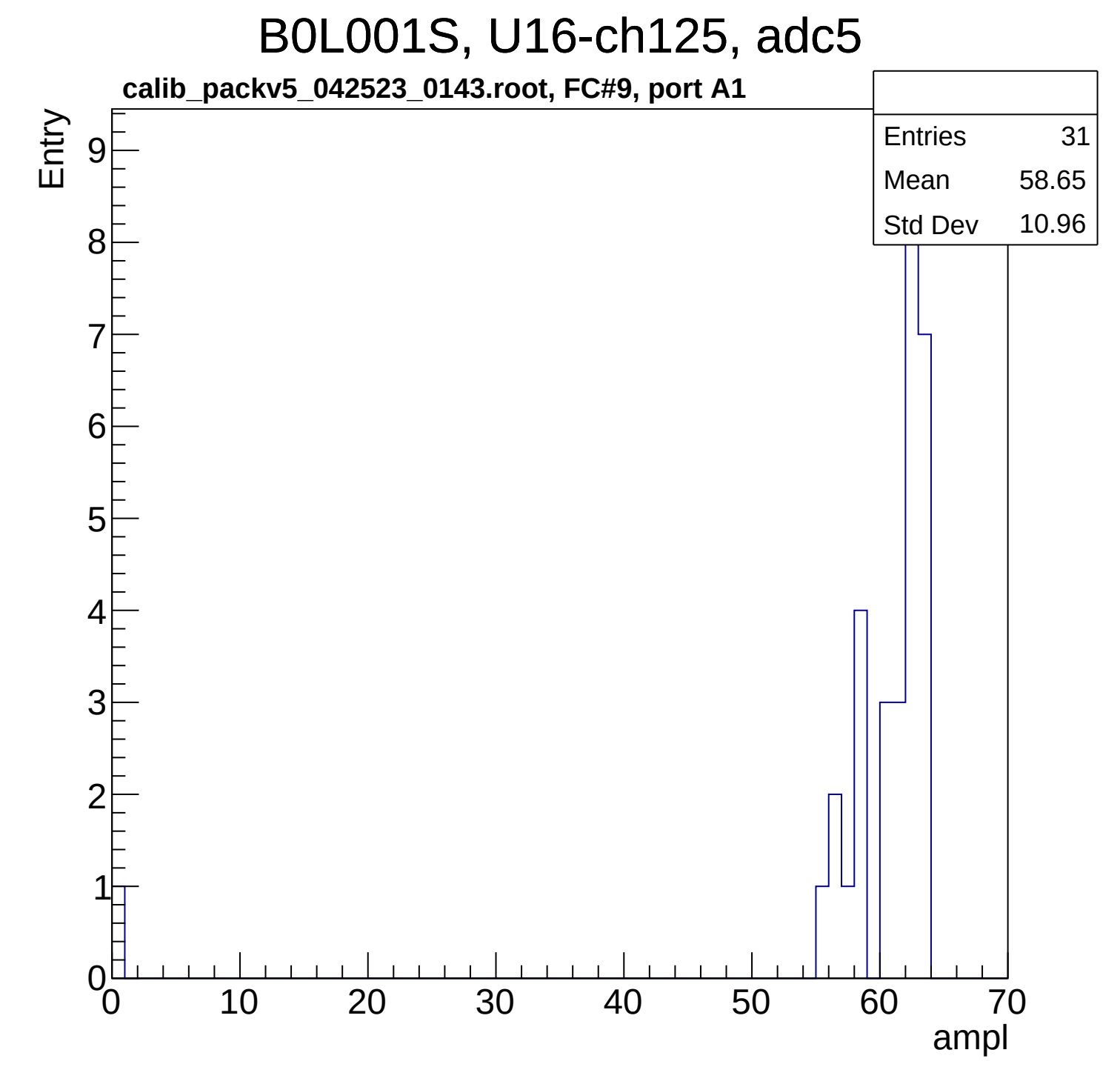
B0L001S, U16-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.65
Std Dev 10.96
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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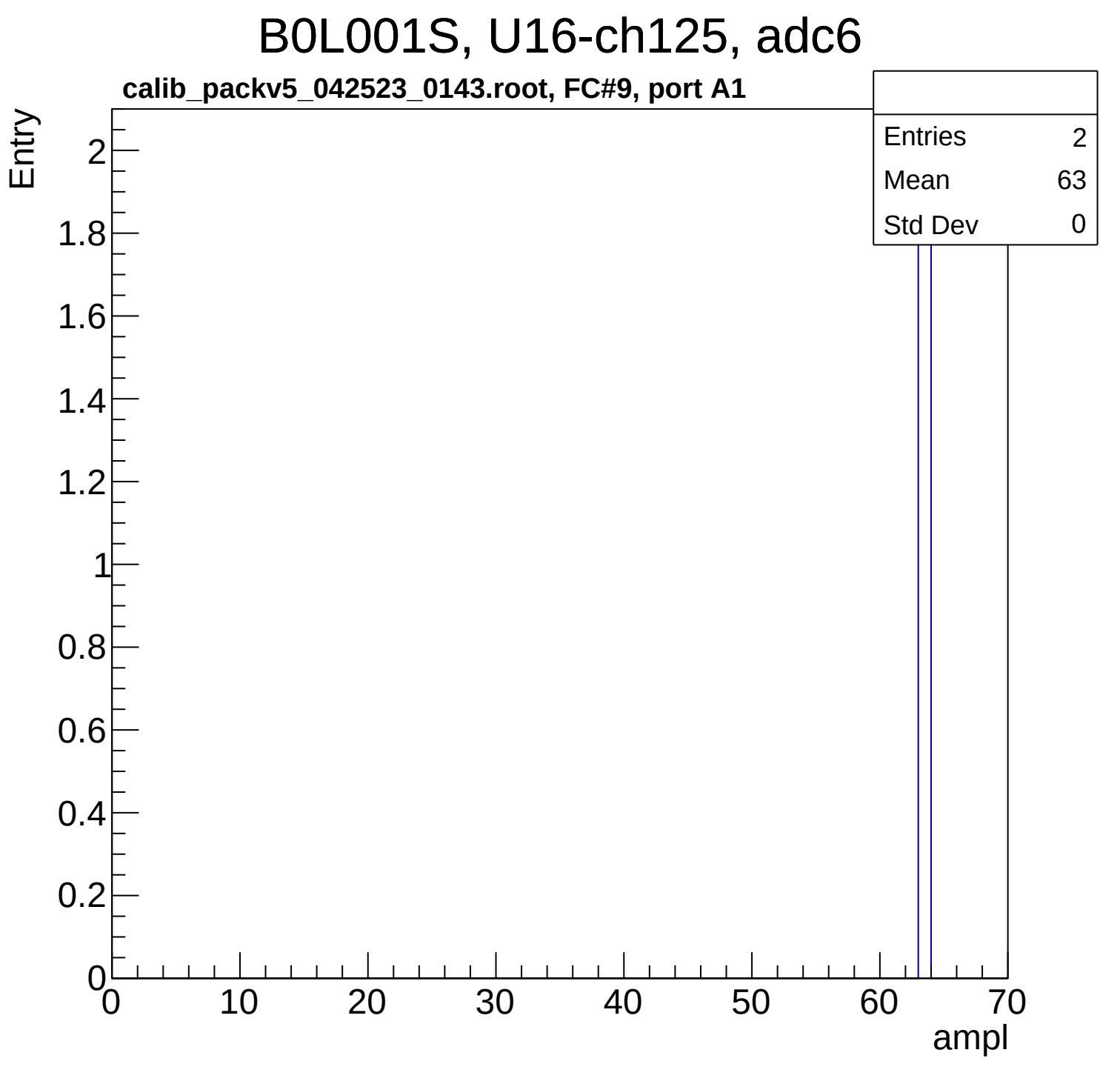
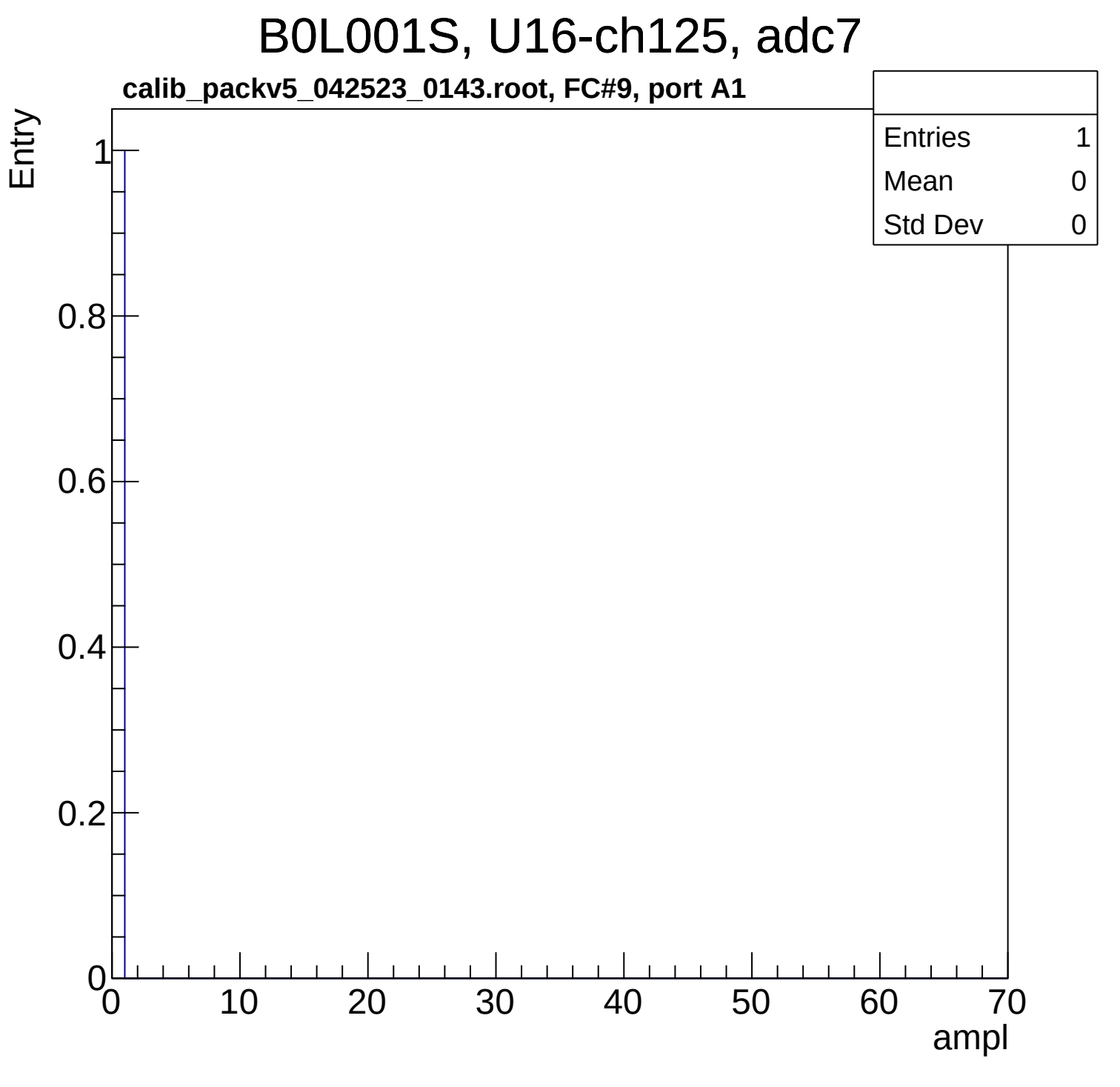

B0L001S, U16-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U16-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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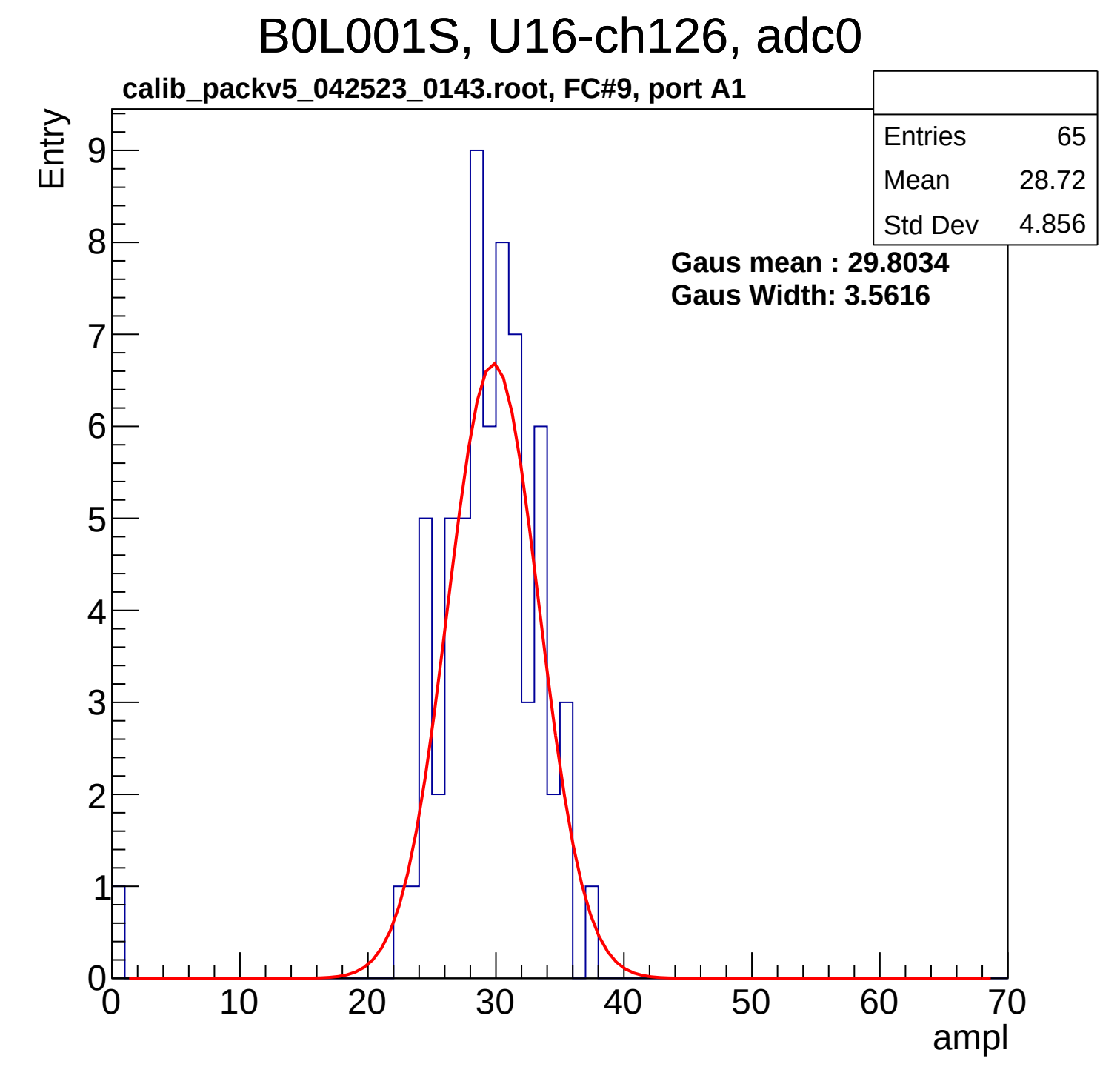
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 28.72
Std Dev 4.856
Gaus mean : 29.8034
Gaus Width: 3.5616
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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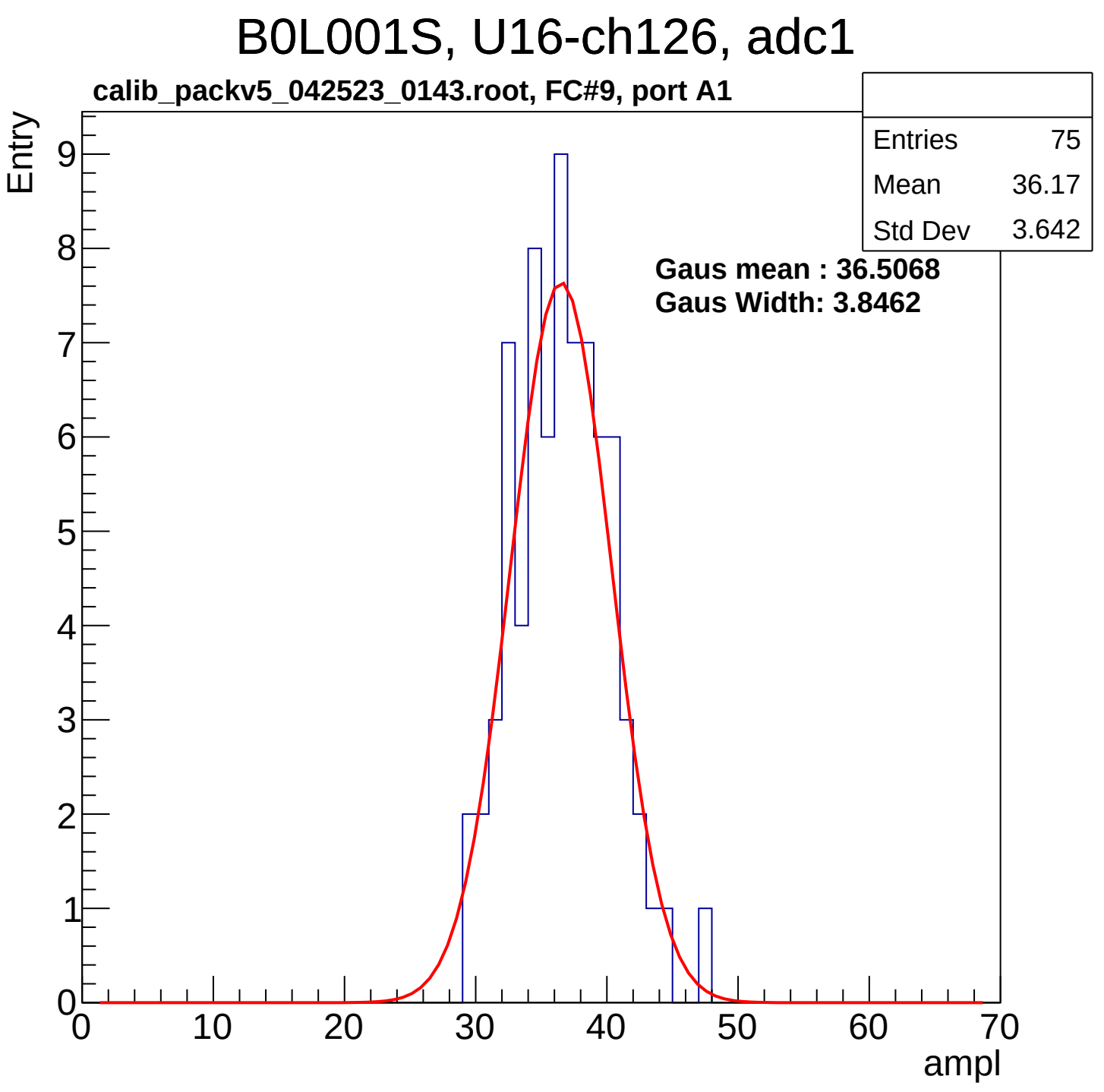
20
30
40
50
60
70


B0L001S, U16-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 36.17
Std Dev 3.642
Gaus mean : 36.5068
Gaus Width: 3.8462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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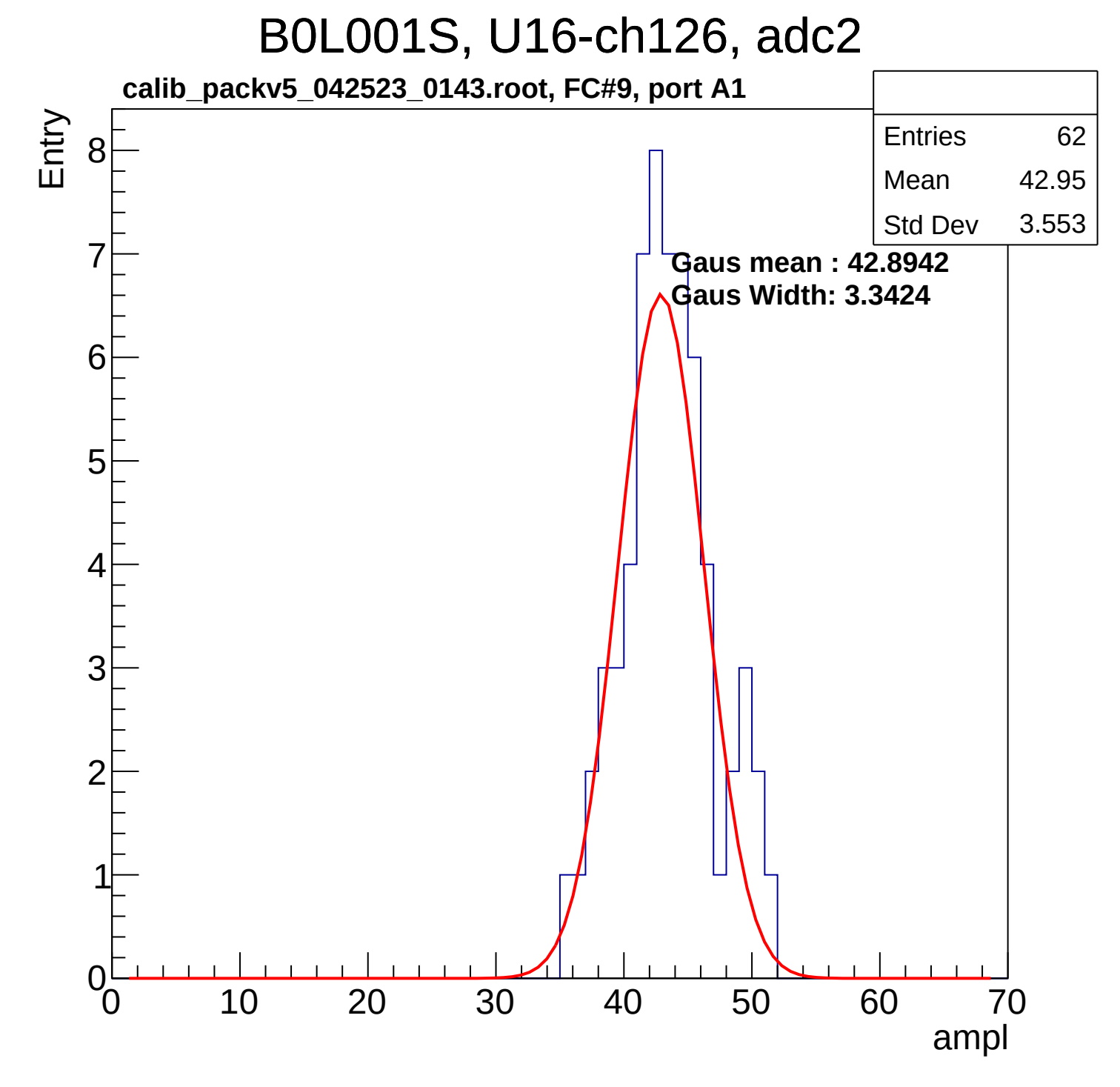
B0L001S, U16-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 42.95
Std Dev 3.553
Gaus mean : 42.8942
Gaus Width: 3.3424
Entry
ampl
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1
2
3
4
5
6
7
8
0
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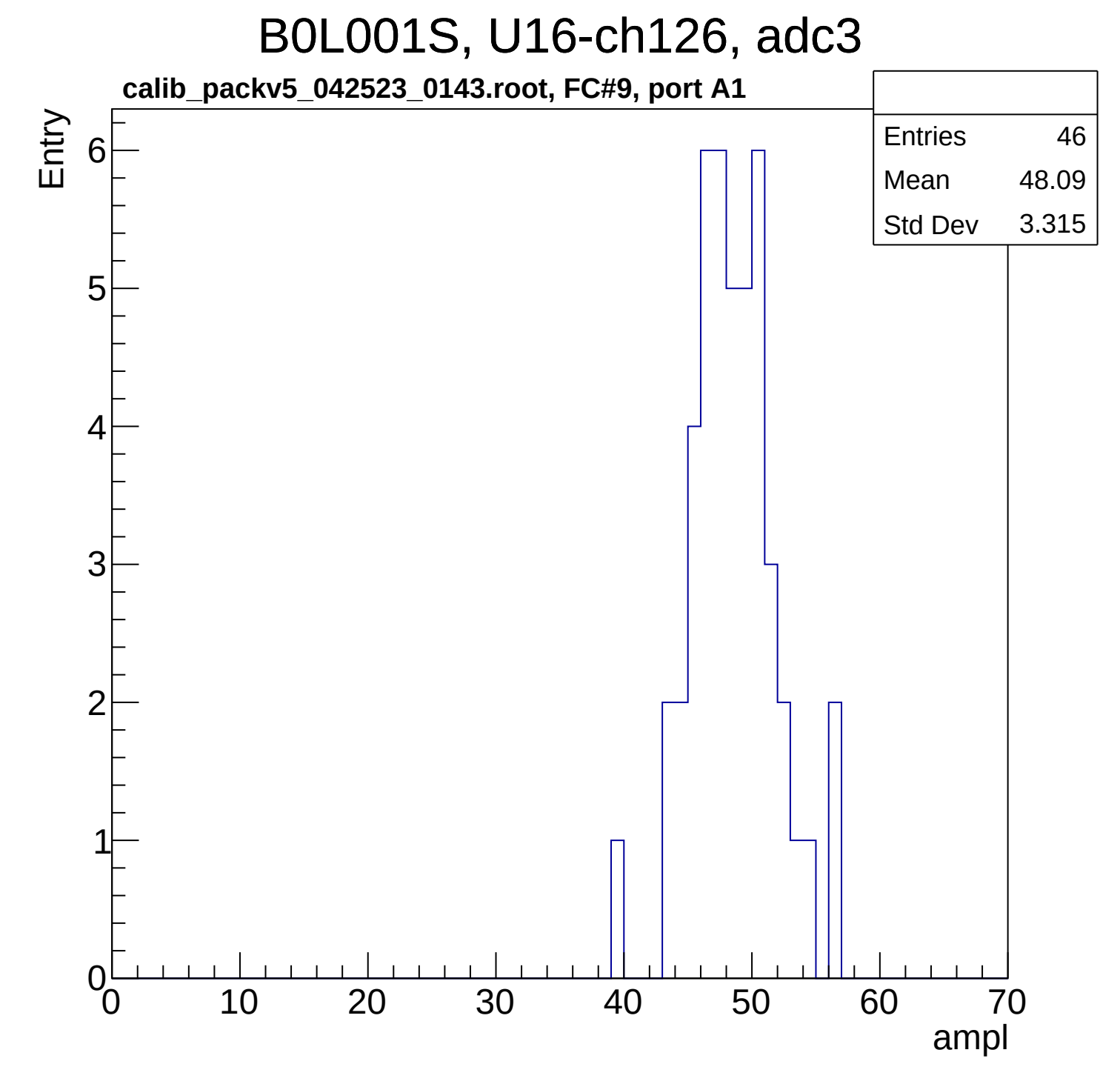
B0L001S, U16-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 48.09
Std Dev 3.315
Entry
ampl
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4
5
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0
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40
50
60
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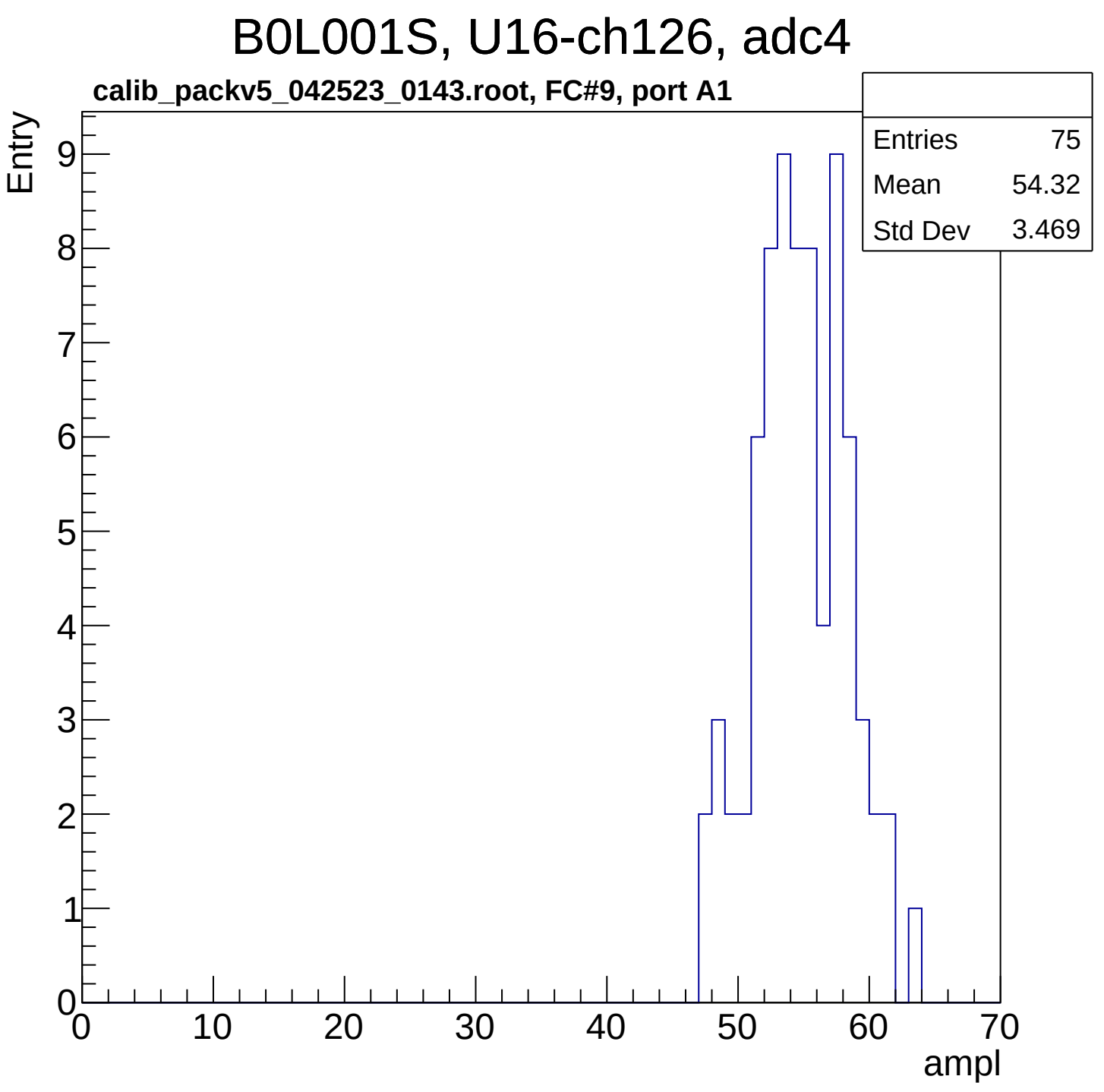
B0L001S, U16-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 54.32
Std Dev 3.469
Entry
ampl
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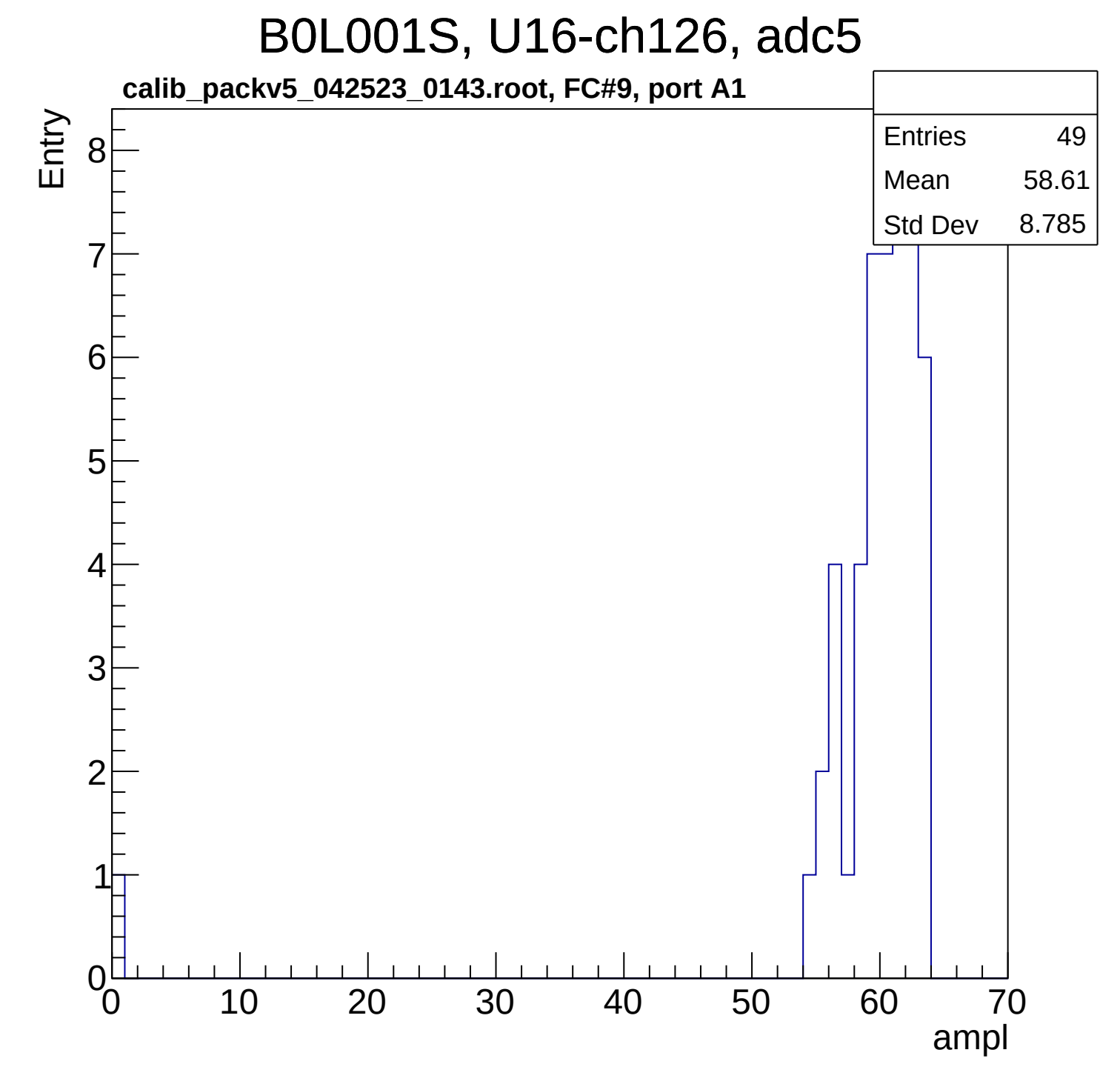
B0L001S, U16-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.61
Std Dev 8.785
Entry
ampl
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1
2
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4
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40
50
60
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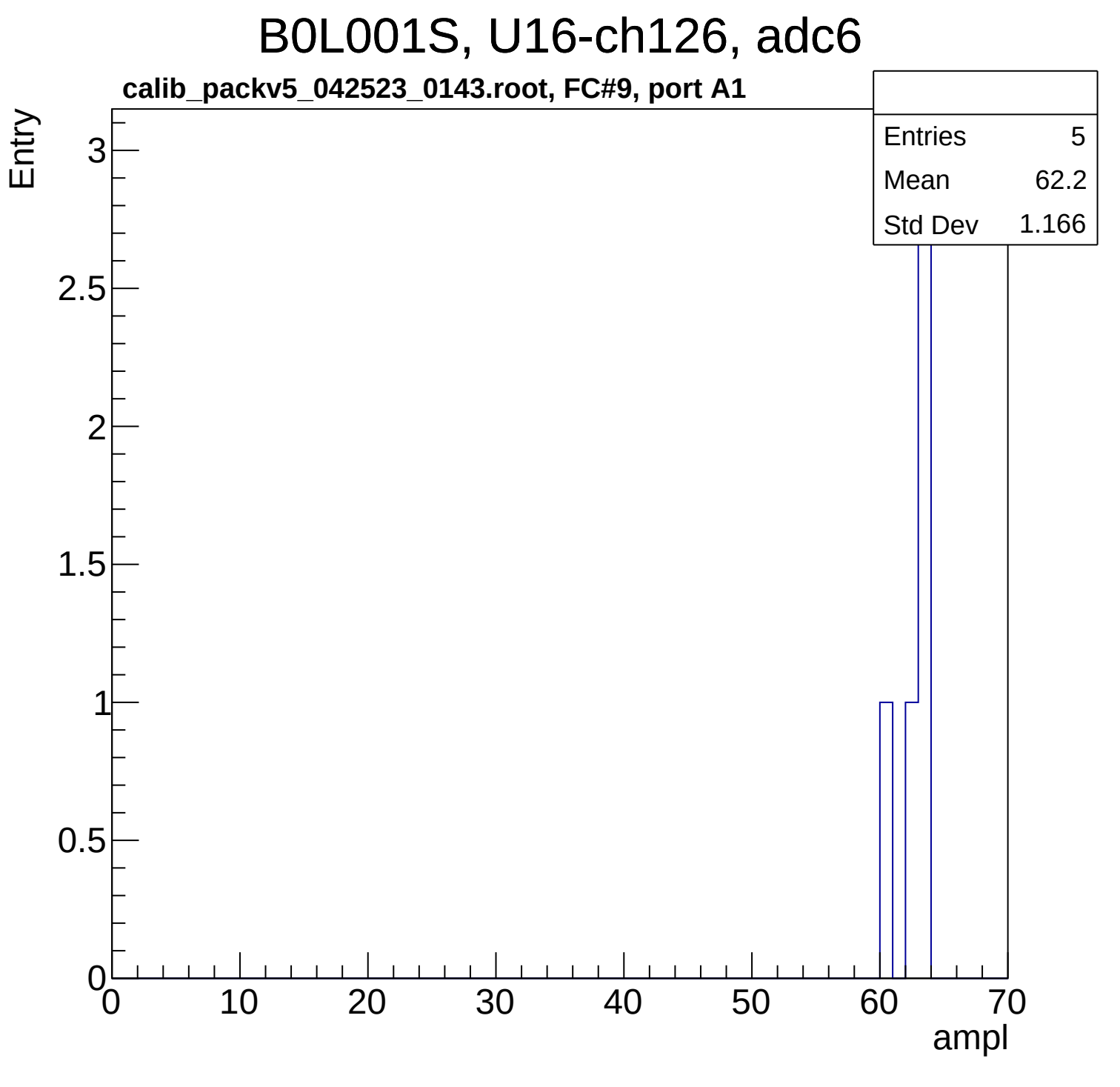
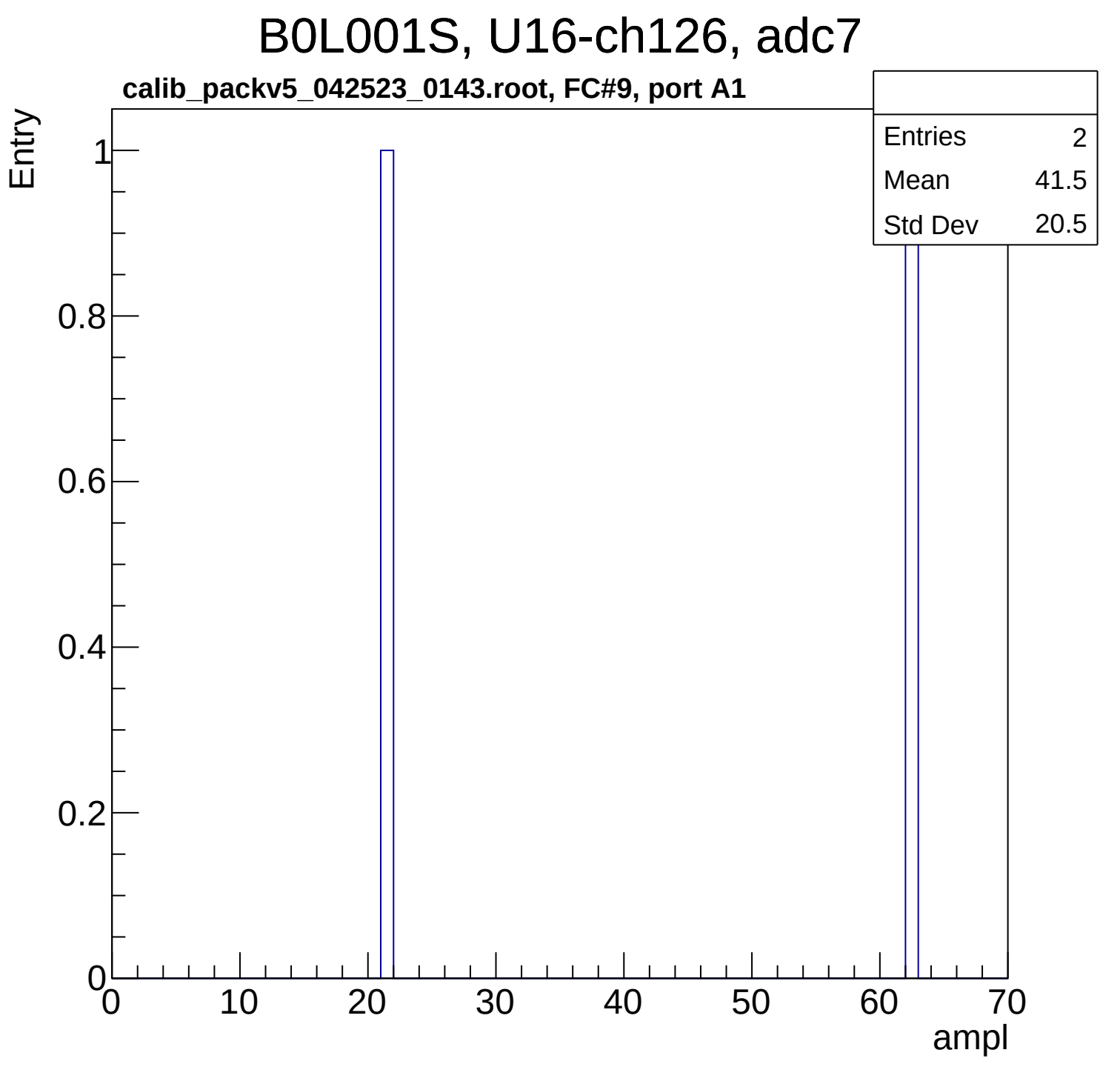
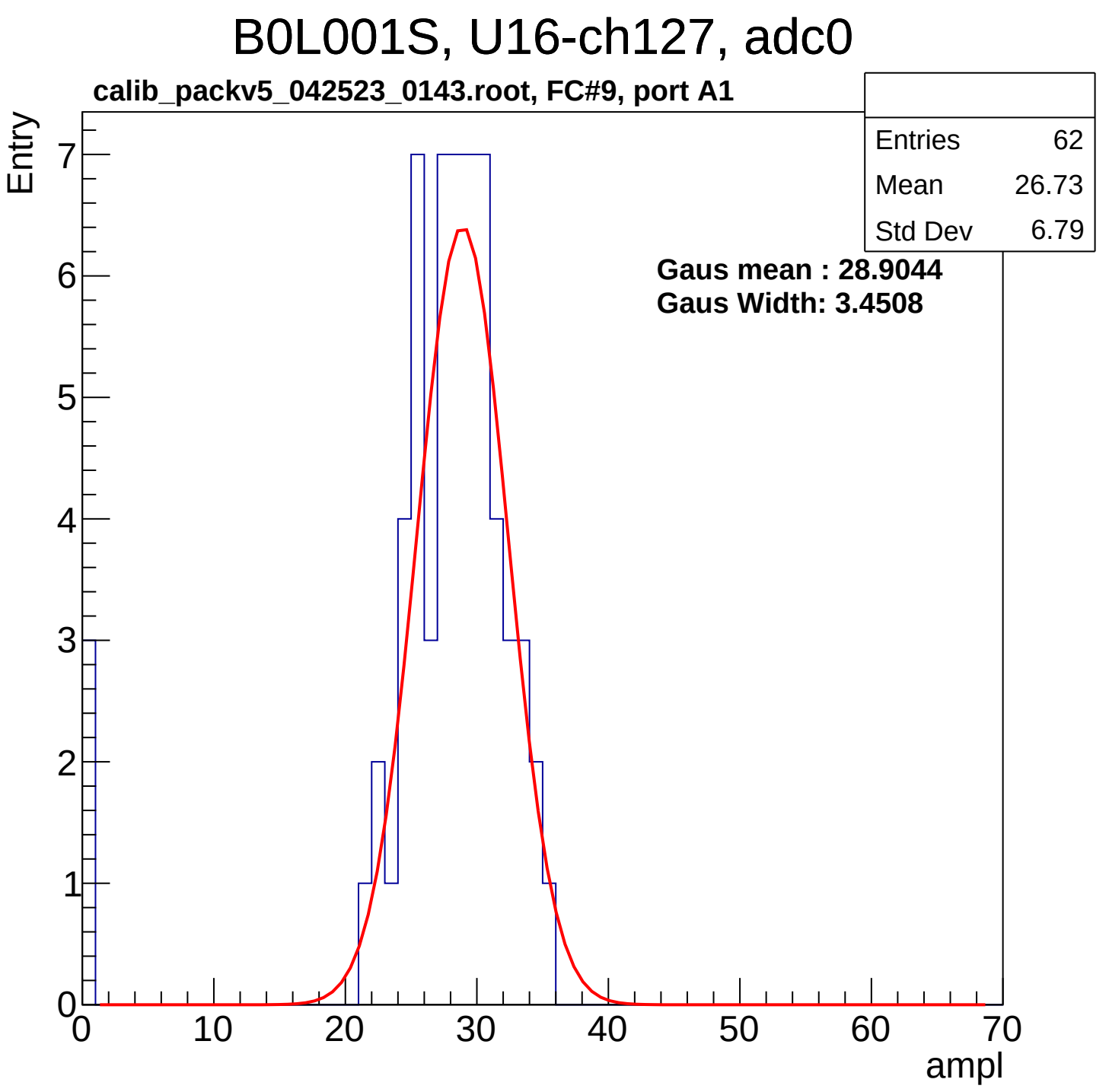
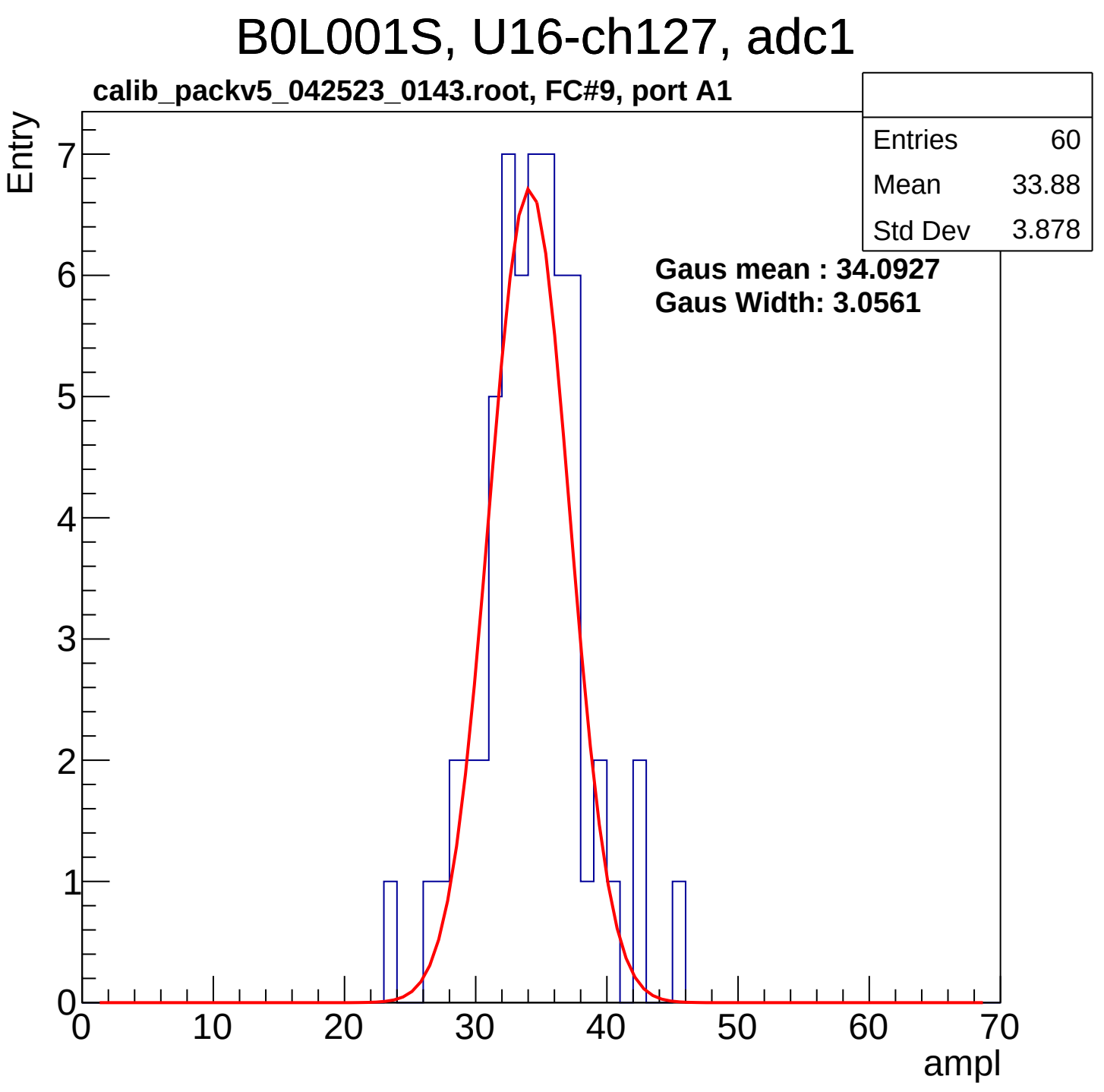
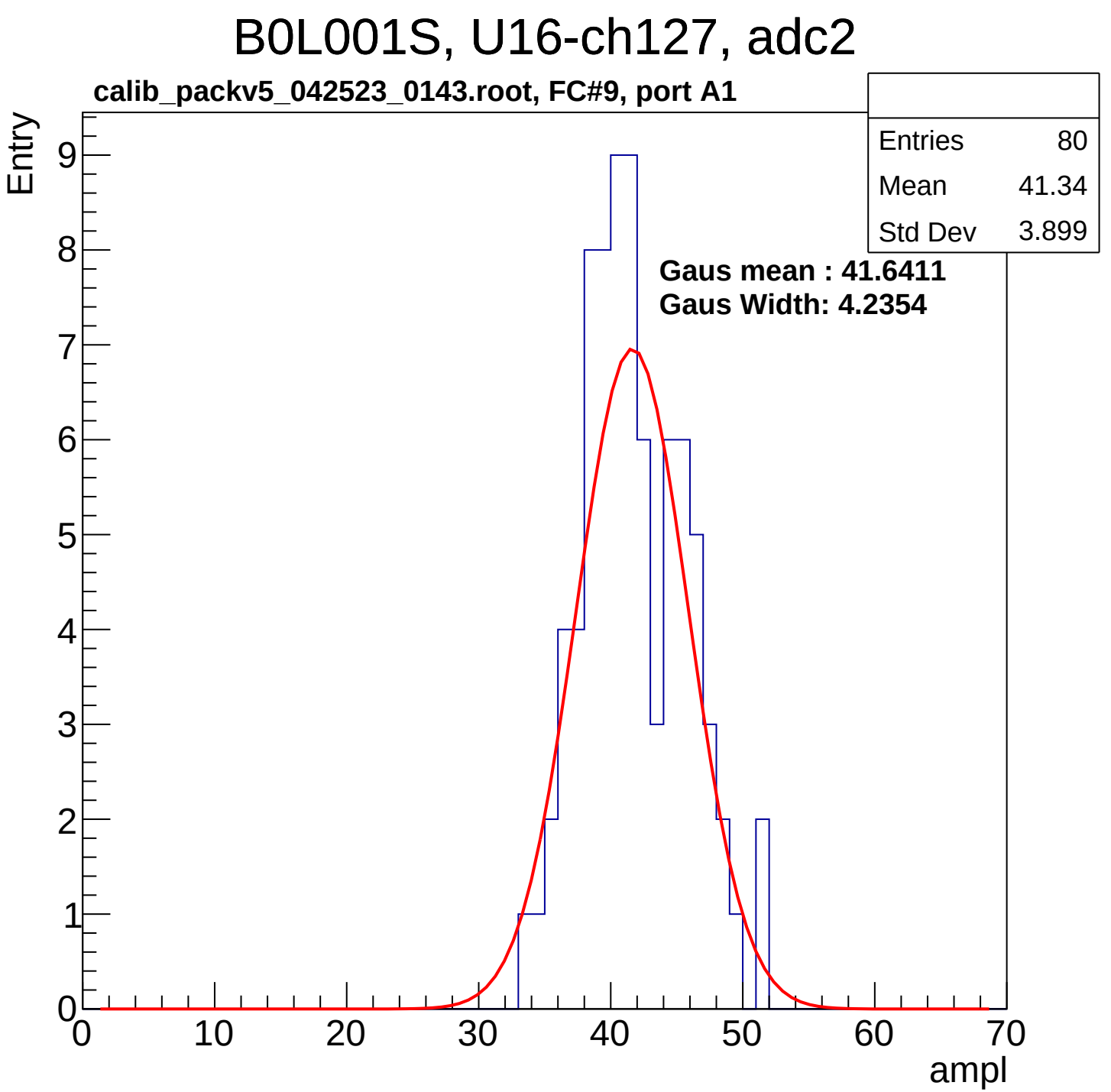
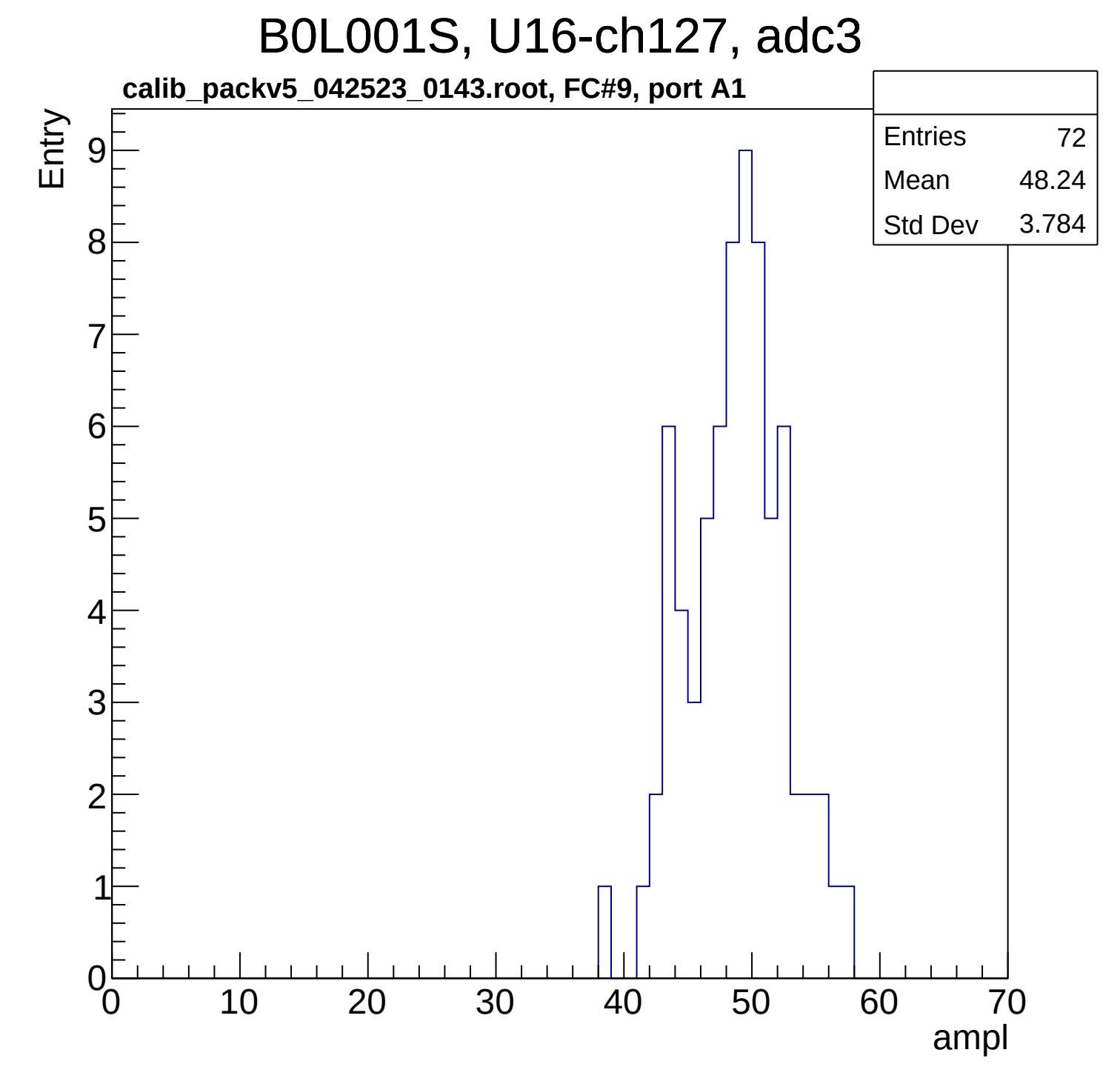

B0L001S, U16-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

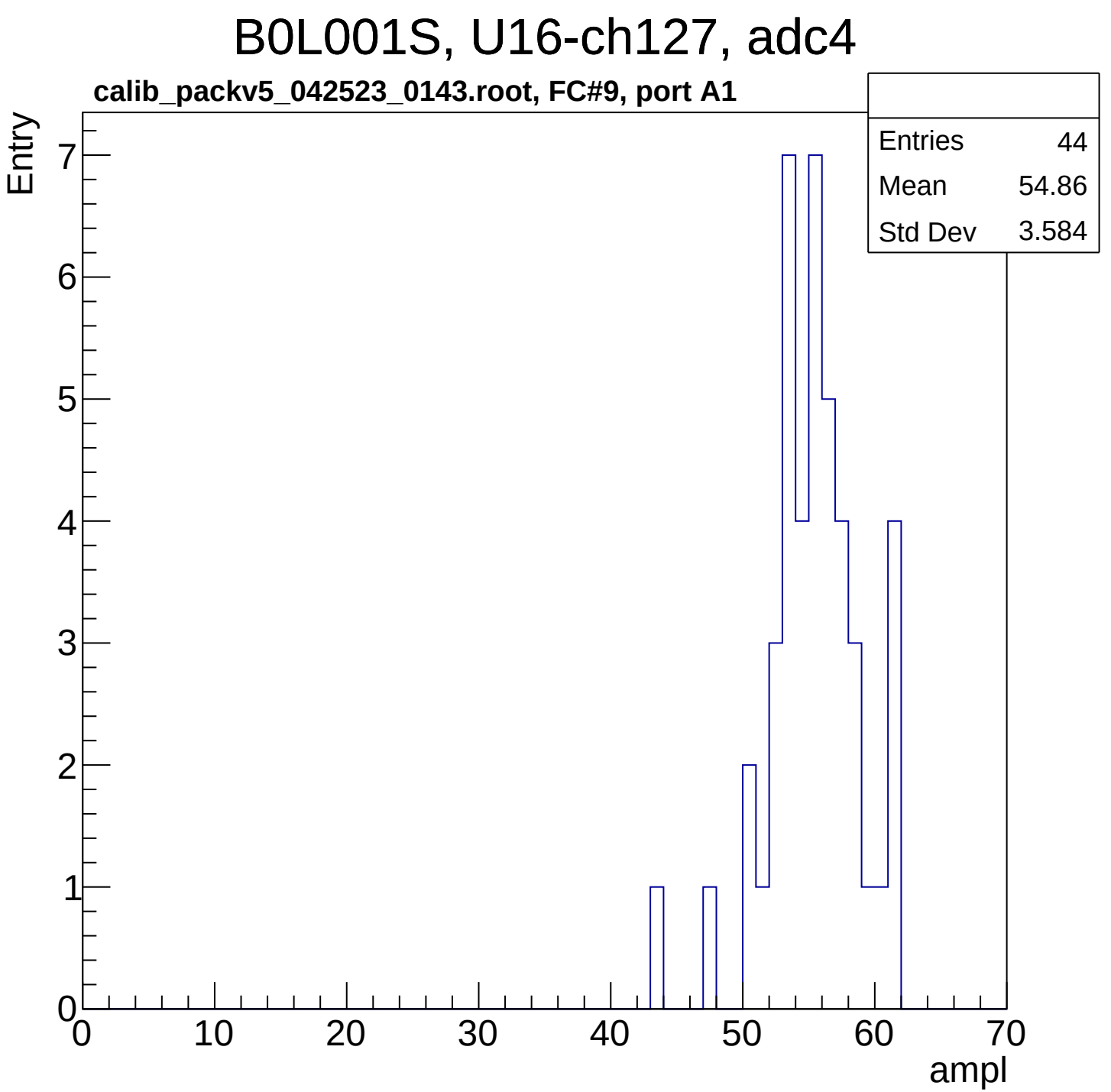

B0L001S, U16-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 41.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

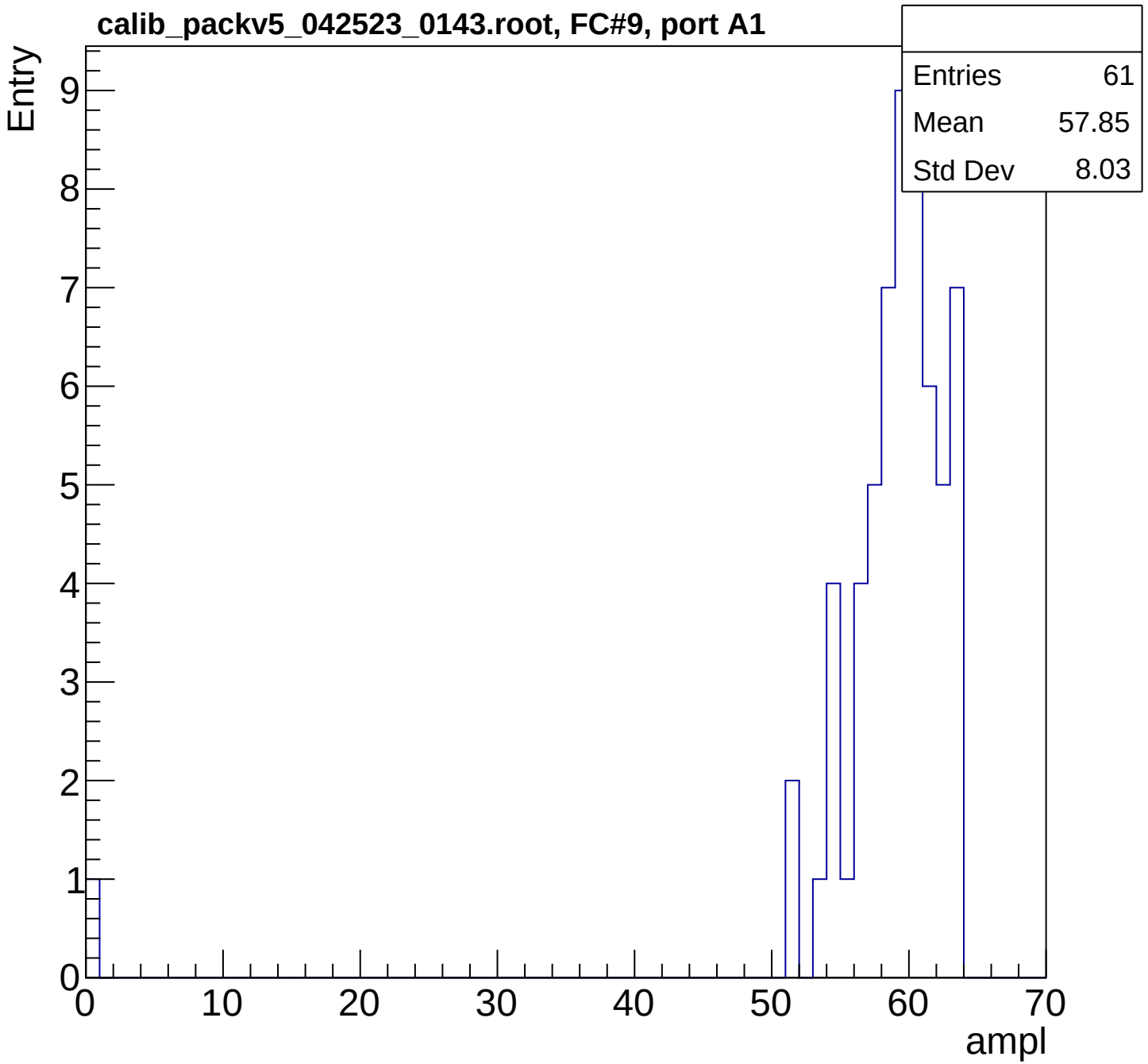

B0L001S, U16-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 26.73
Std Dev 6.79
Gaus mean : 28.9044
Gaus Width: 3.4508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

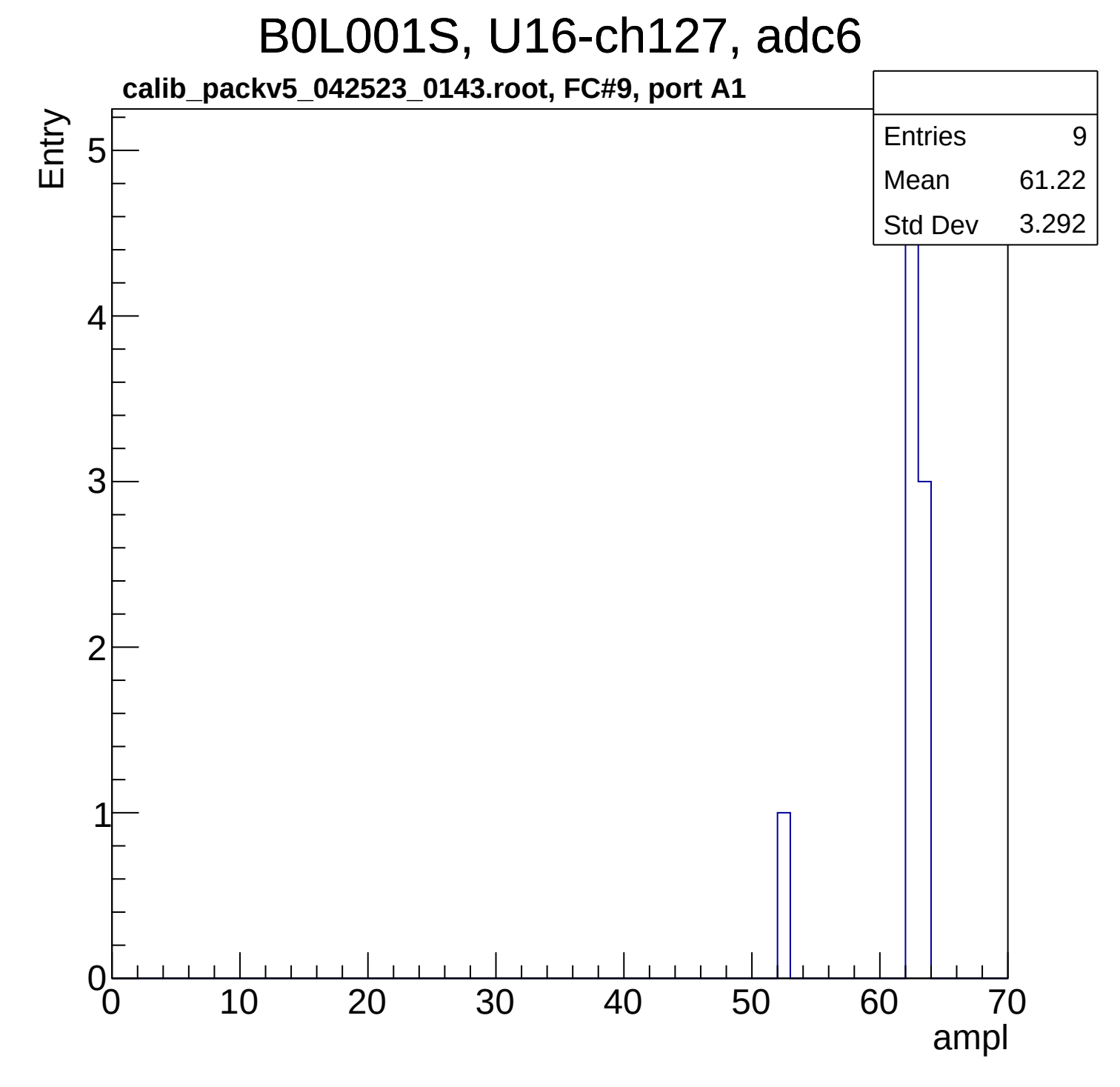

B0L001S, U16-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 33.88
Std Dev 3.878
Gaus mean : 34.0927
Gaus Width: 3.0561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

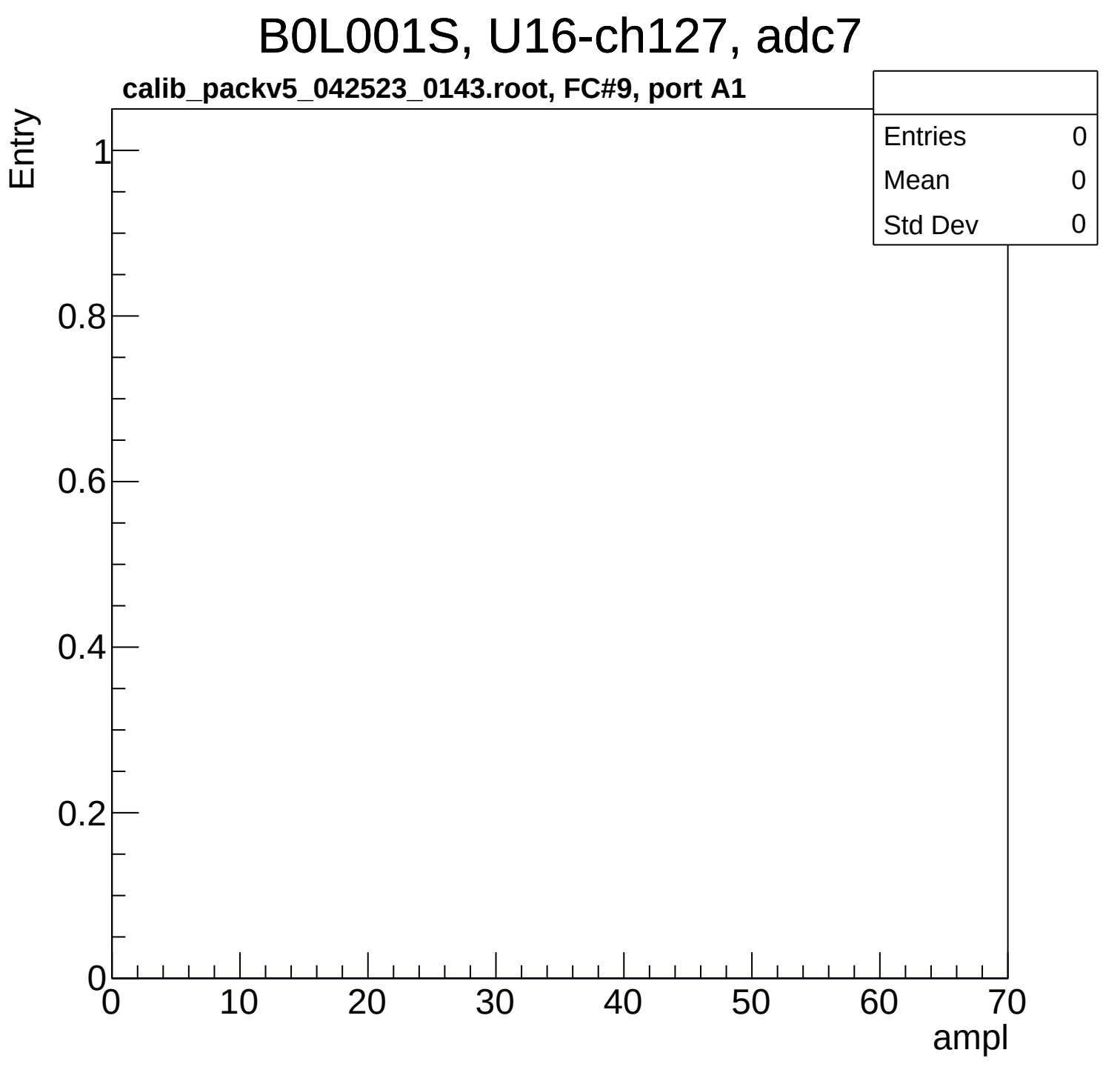

B0L001S, U16-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 41.34
Std Dev 3.899
Gaus mean : 41.6411
Gaus Width: 4.2354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

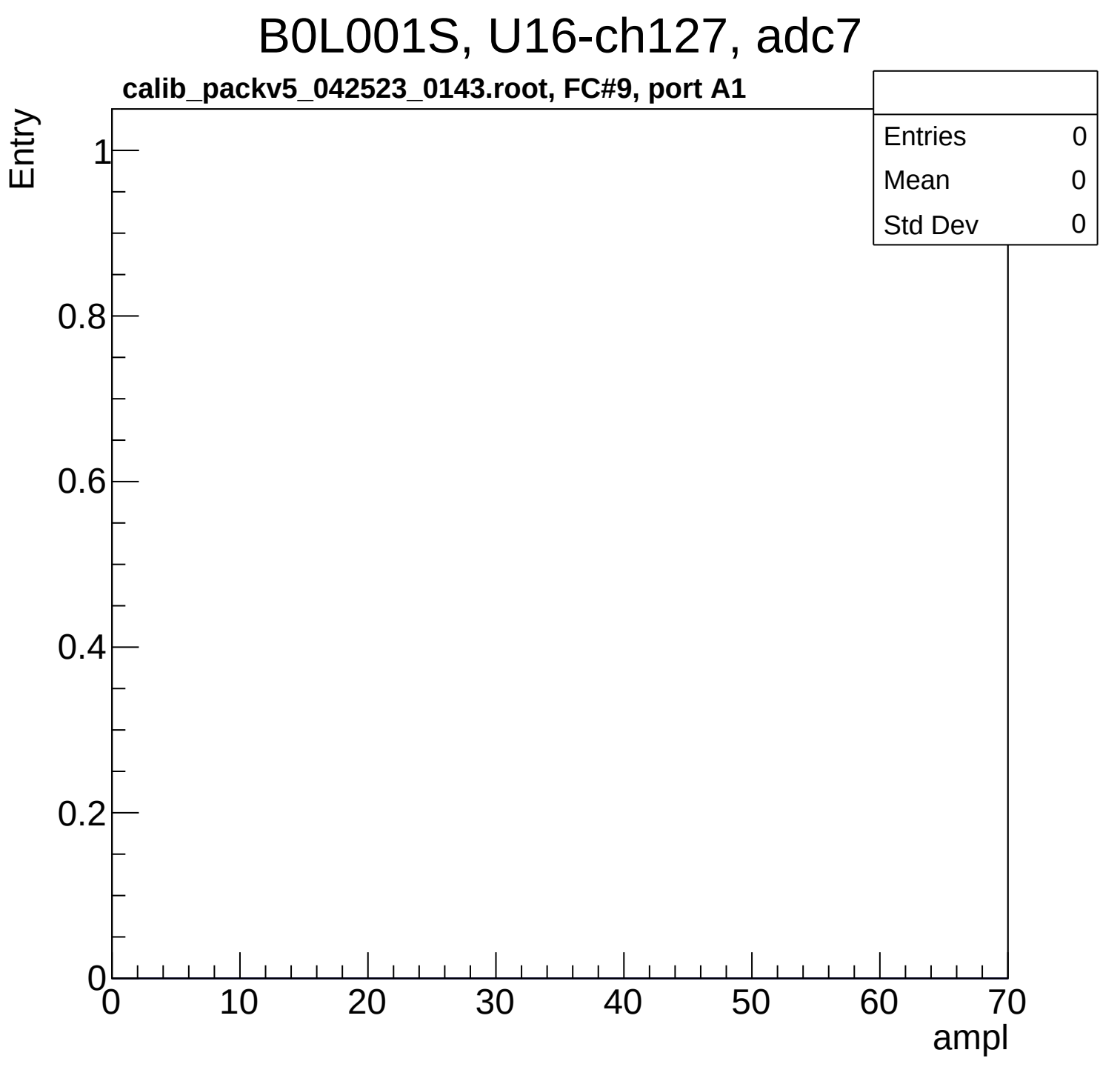

B0L001S, U16-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 48.24
Std Dev 3.784
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U16-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 54.86
Std Dev 3.584
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U16-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.85
Std Dev 8.03
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.22
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U16-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U16-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl