

B1L100S, U12-ch0, adc0

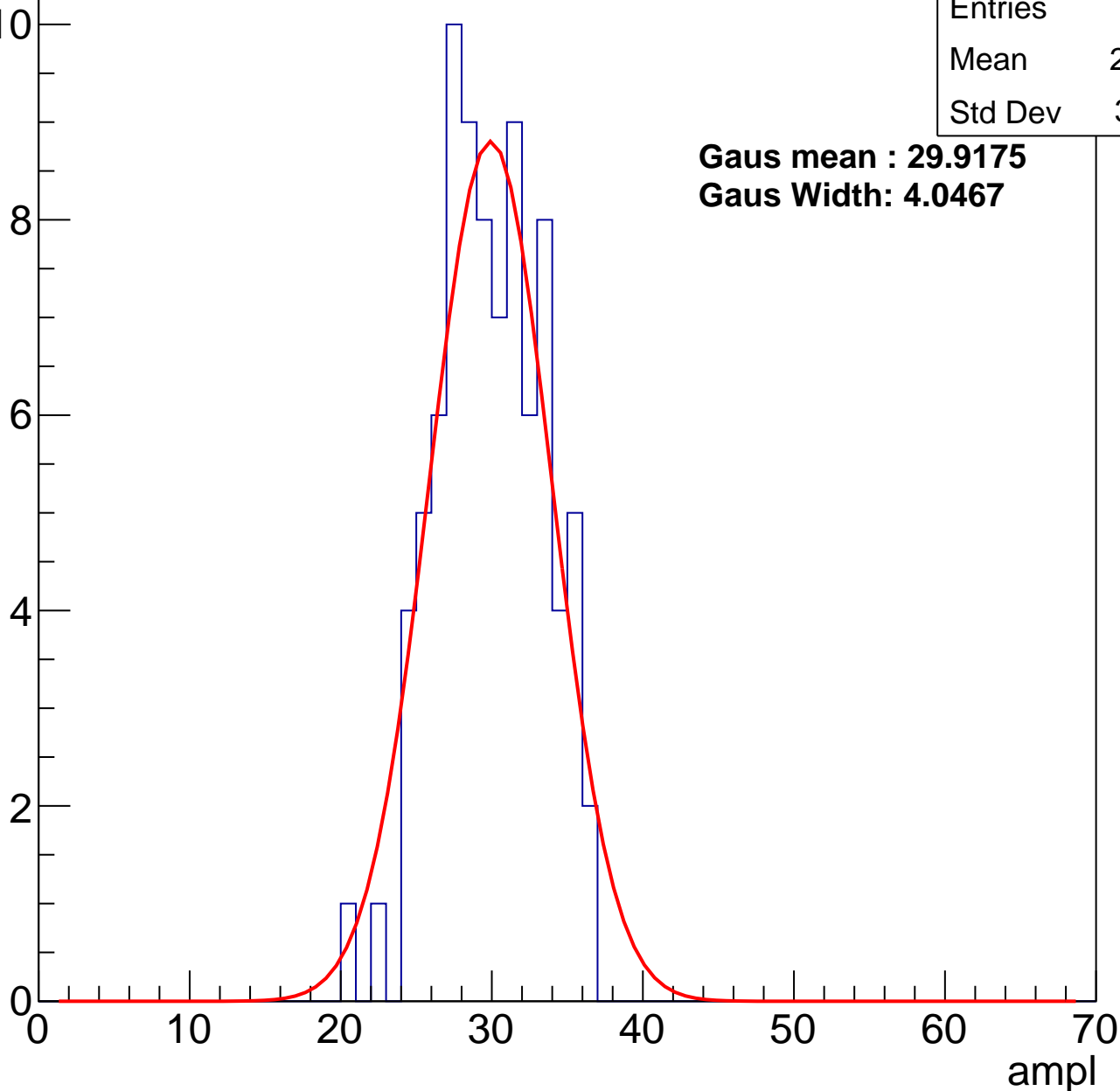
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	29.42
Std Dev	3.431

Gaus mean : 29.9175

Gaus Width: 4.0467



B1L100S, U12-ch0, adc1

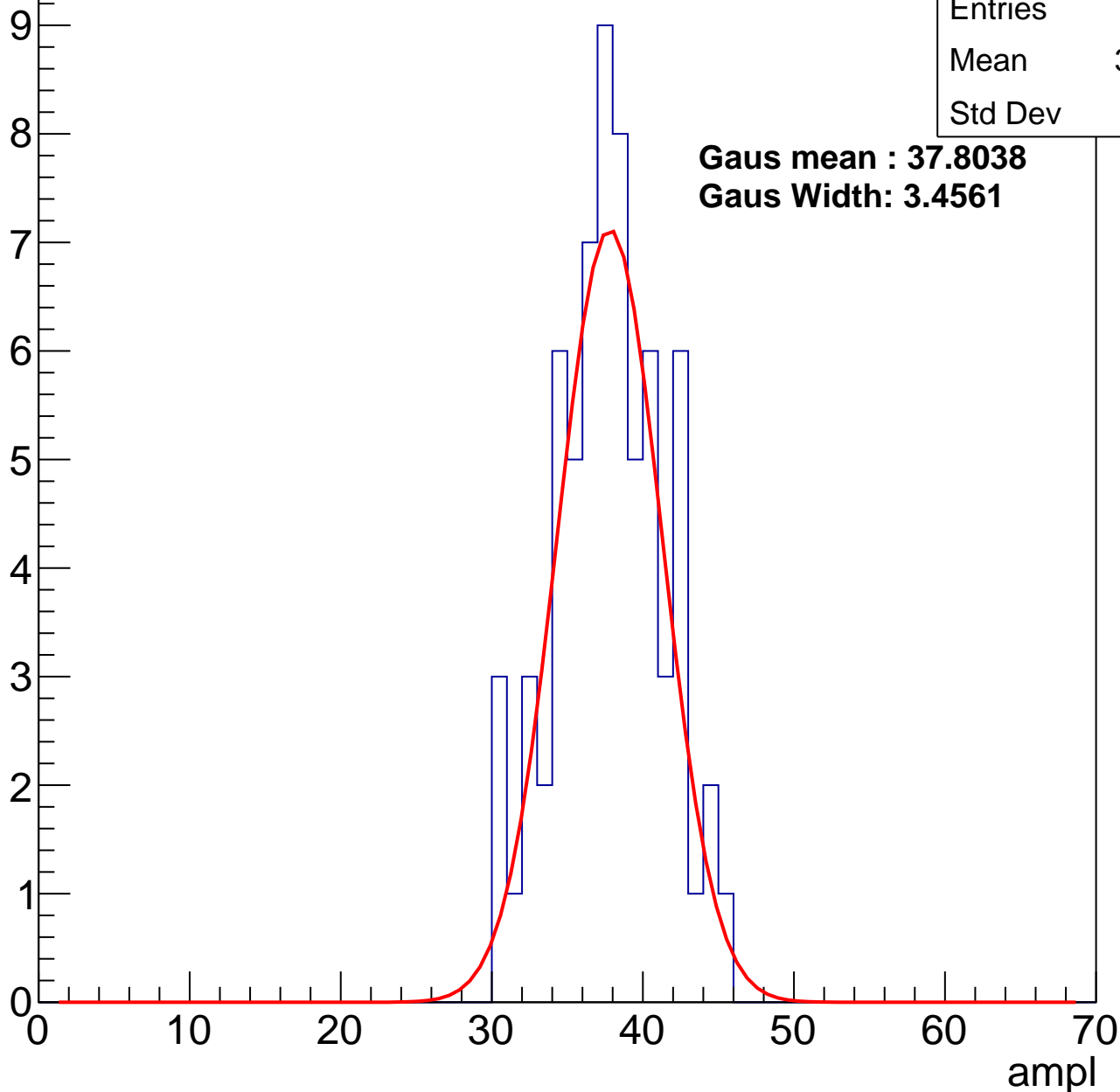
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.31
Std Dev	3.52

Gaus mean : 37.8038

Gaus Width: 3.4561



B1L100S, U12-ch0, adc2

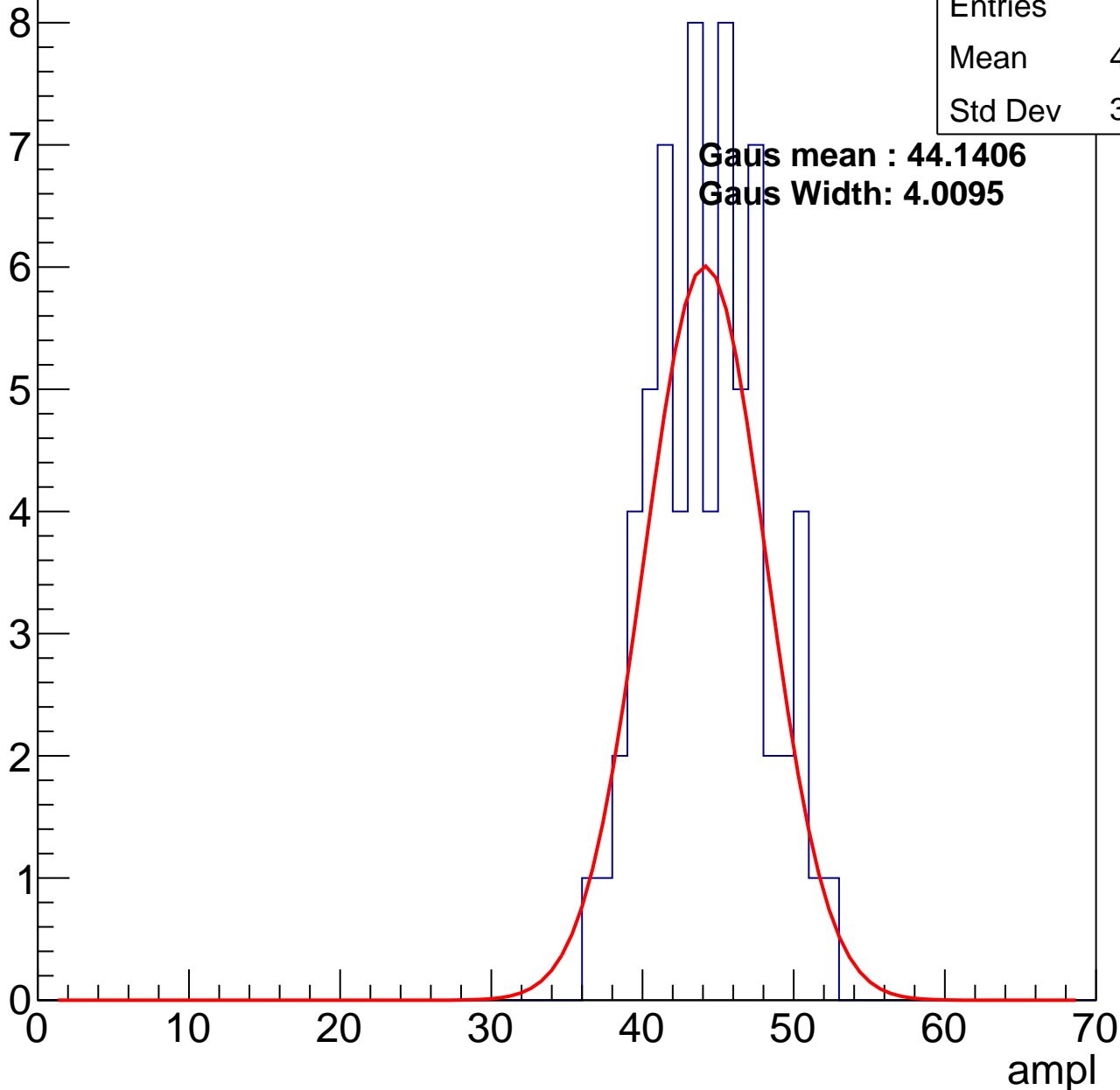
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.88
Std Dev	3.654

Gaus mean : 44.1406

Gaus Width: 4.0095

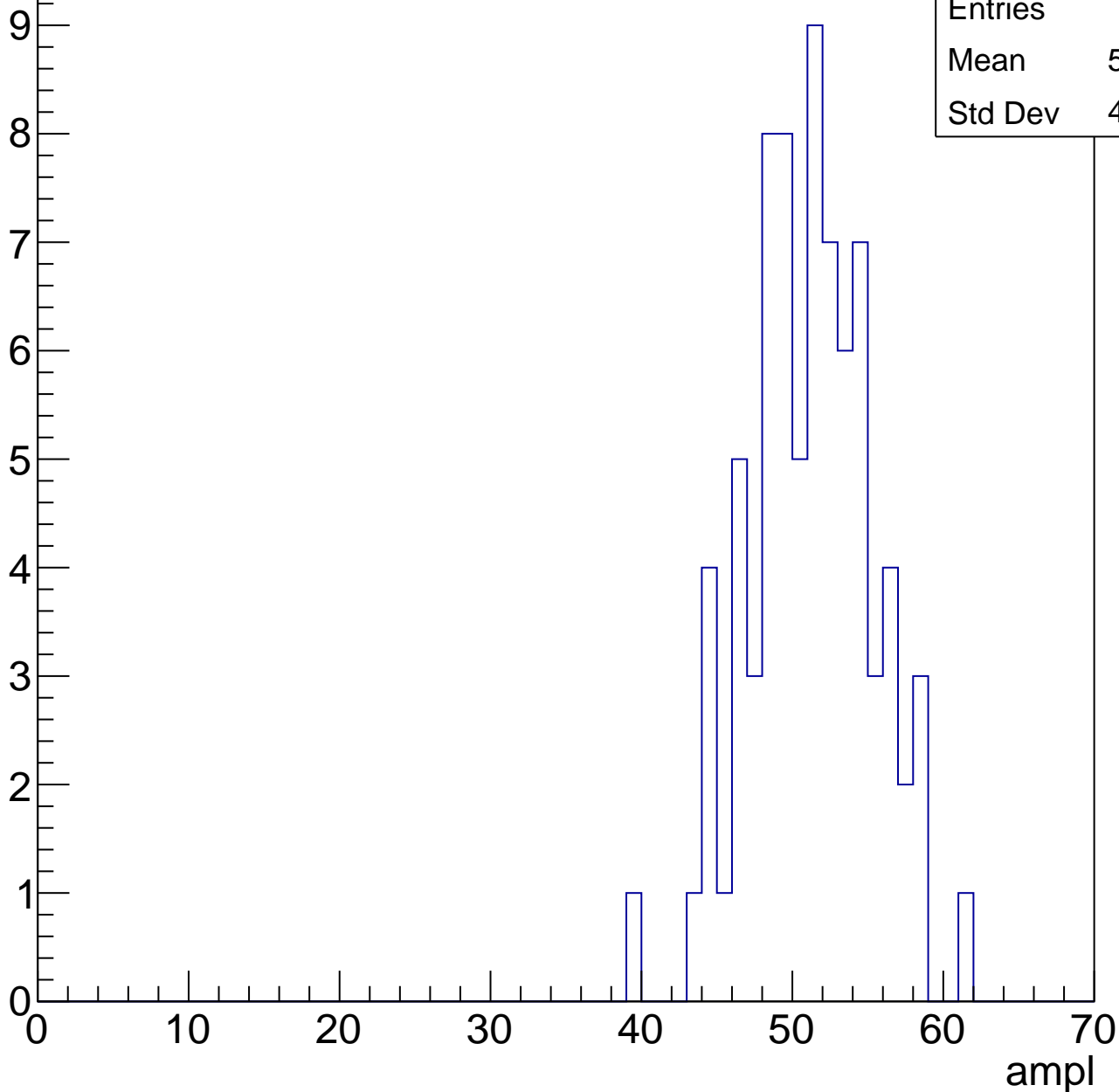


B1L100S, U12-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	50.73
Std Dev	4.047

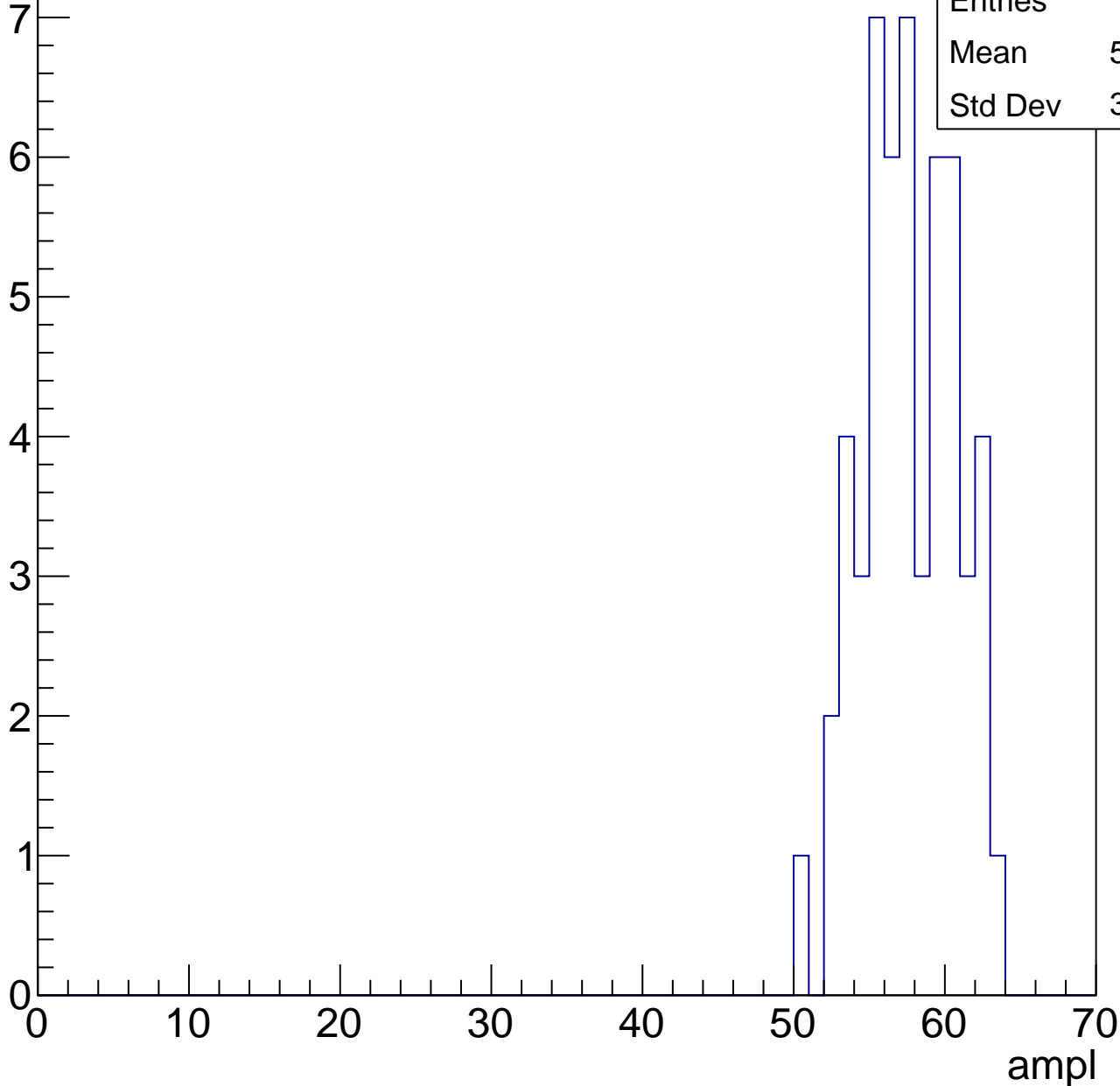


B1L100S, U12-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.17
Std Dev	3.033

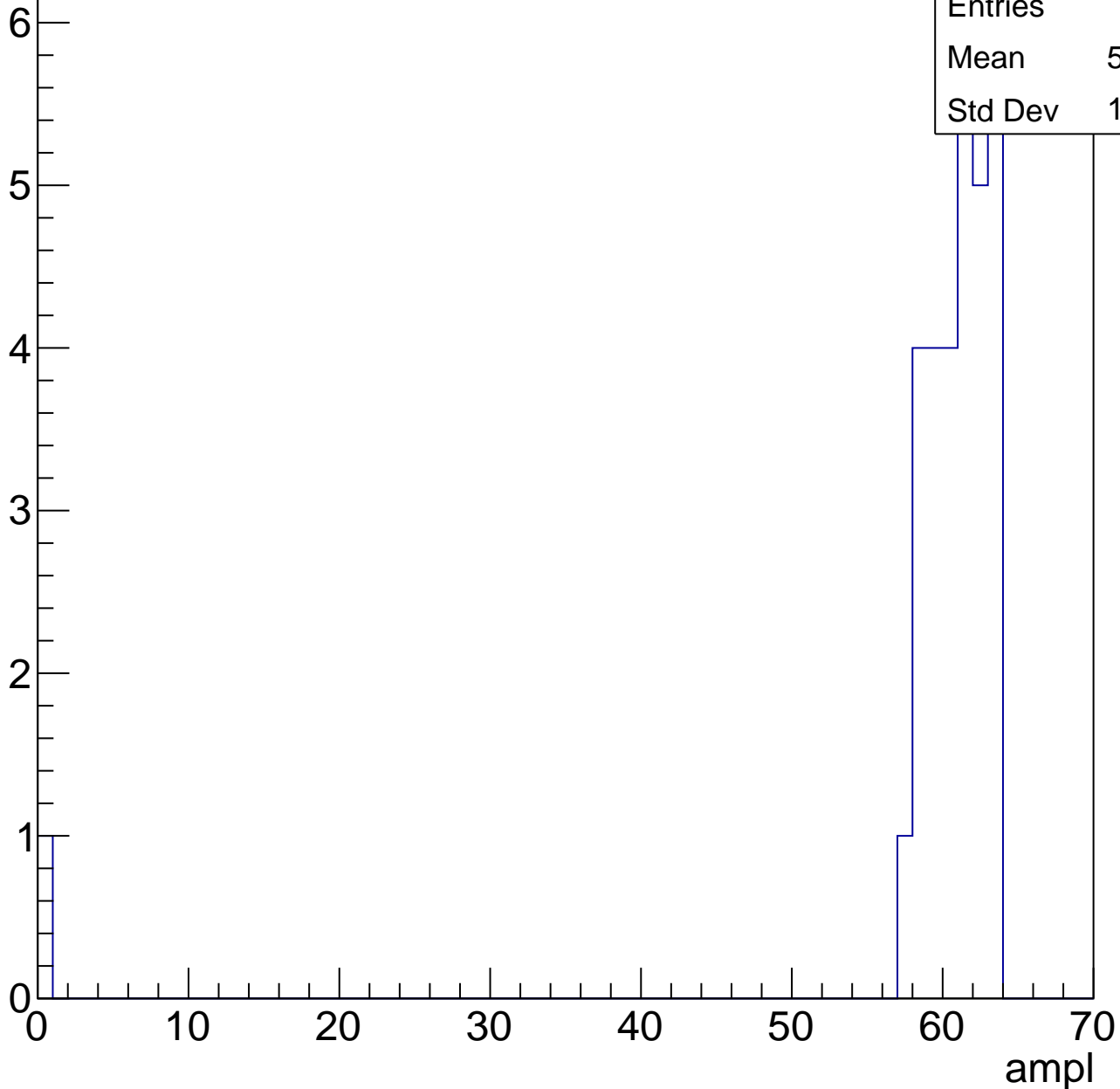


B1L100S, U12-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.68
Std Dev	10.86



B1L100S, U12-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U12-ch1, adc0

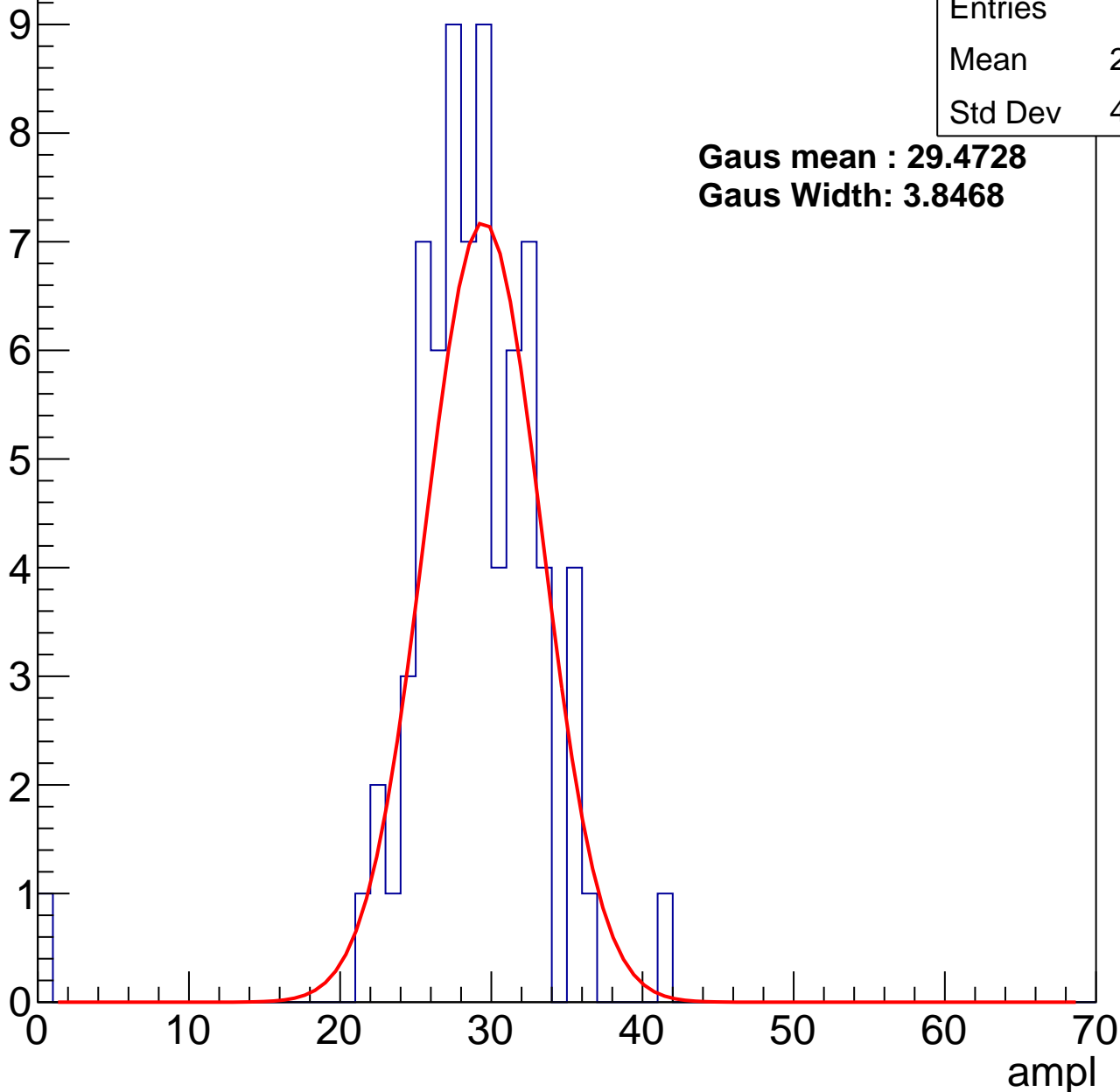
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.36
Std Dev	4.945

Gaus mean : 29.4728

Gaus Width: 3.8468



B1L100S, U12-ch1, adc1

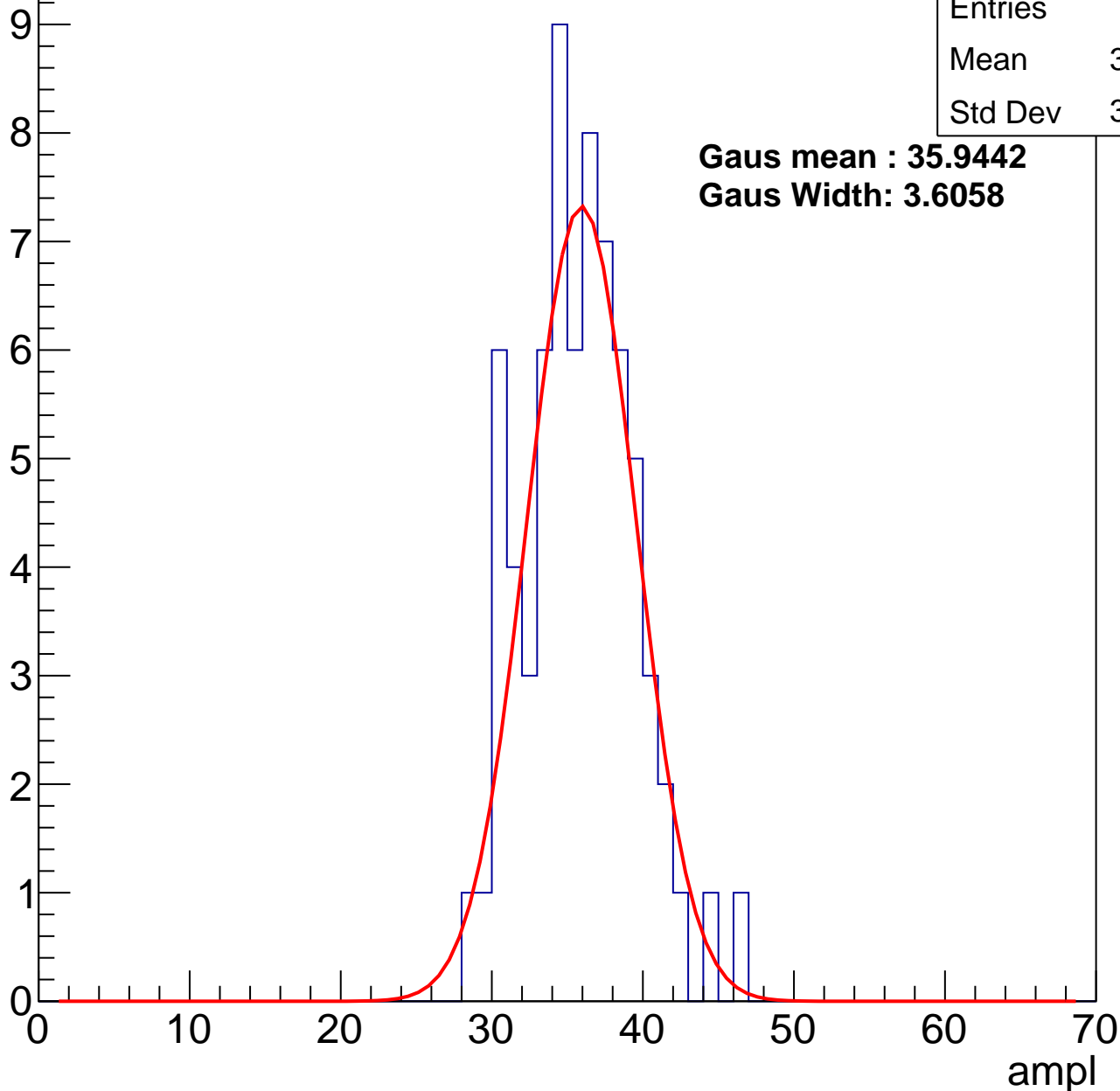
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.36
Std Dev	3.622

Gaus mean : 35.9442

Gaus Width: 3.6058



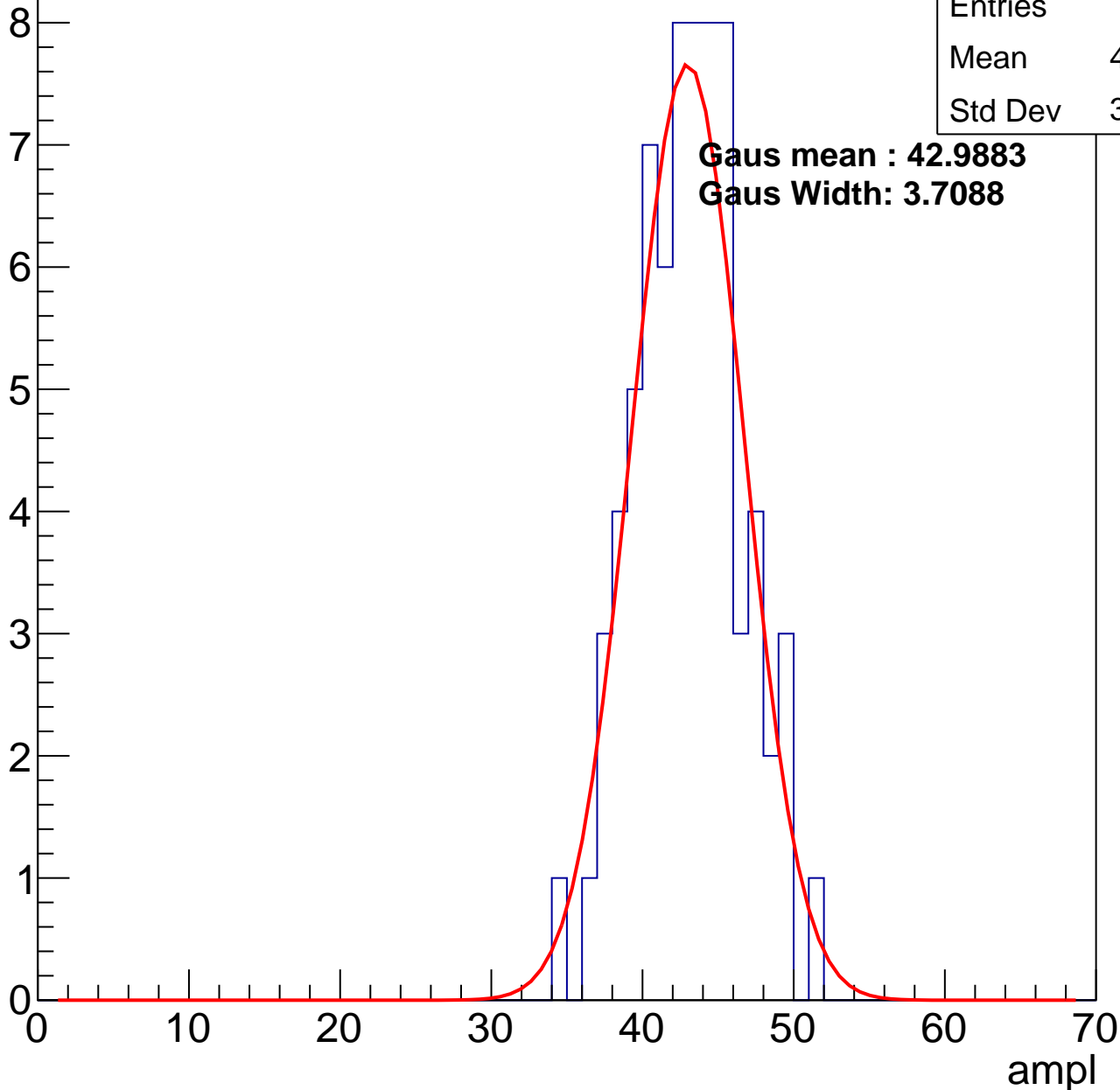
B1L100S, U12-ch1, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42.58
Std Dev	3.443

Gaus mean : 42.9883
Gaus Width: 3.7088

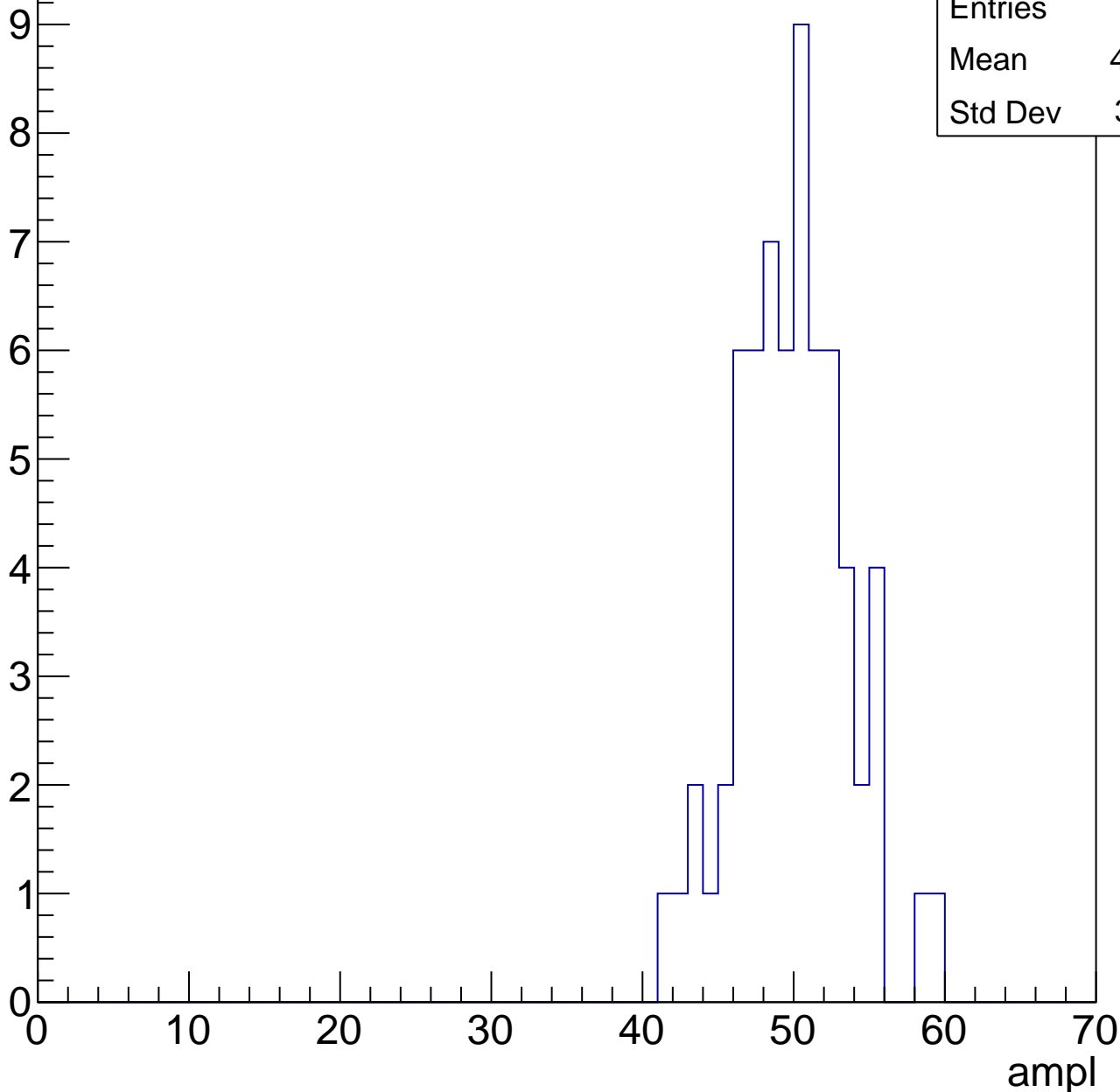


B1L100S, U12-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

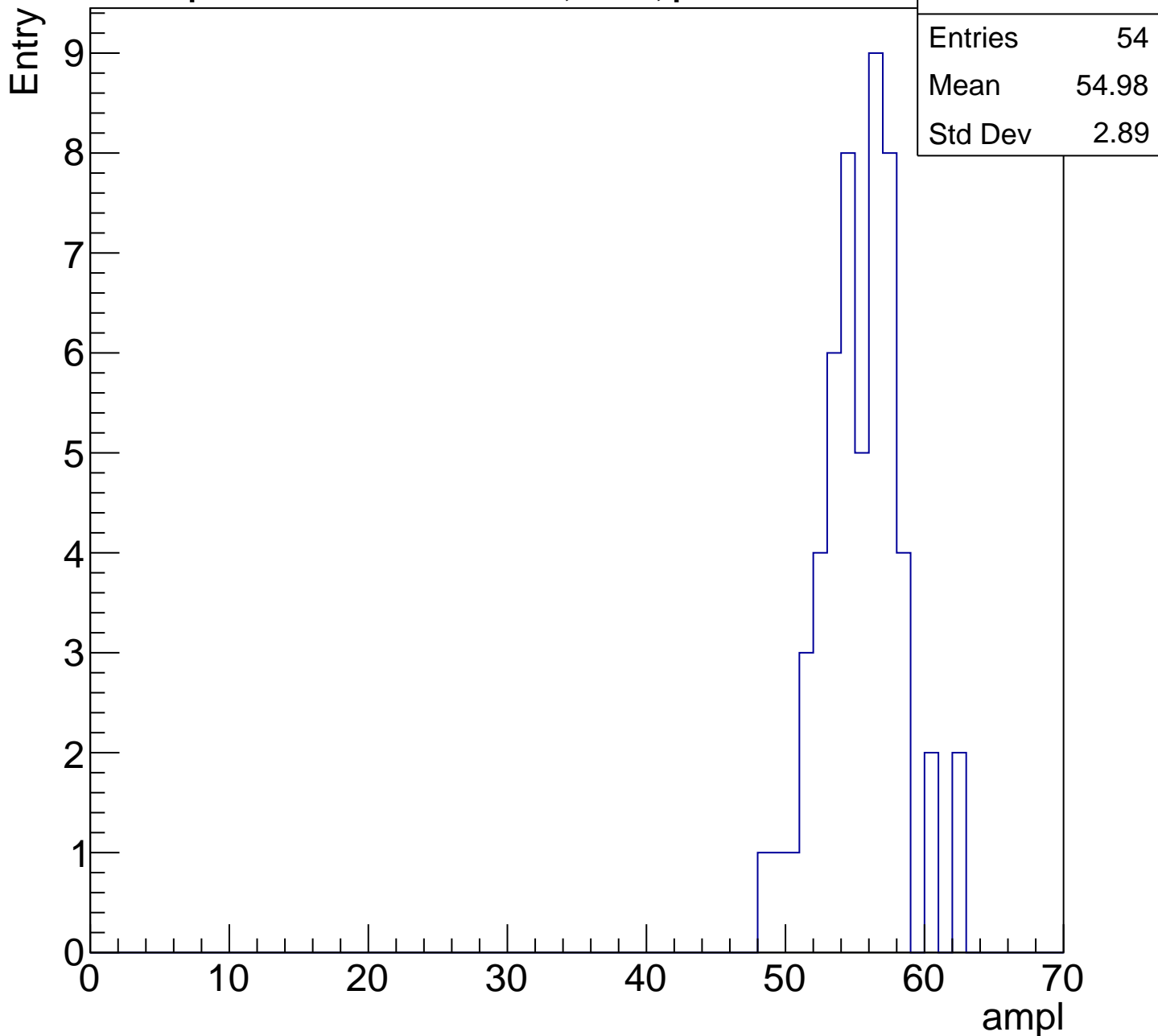
Entry

Entries	65
Mean	49.48
Std Dev	3.591



B1L100S, U12-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.06
Std Dev	8.657

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

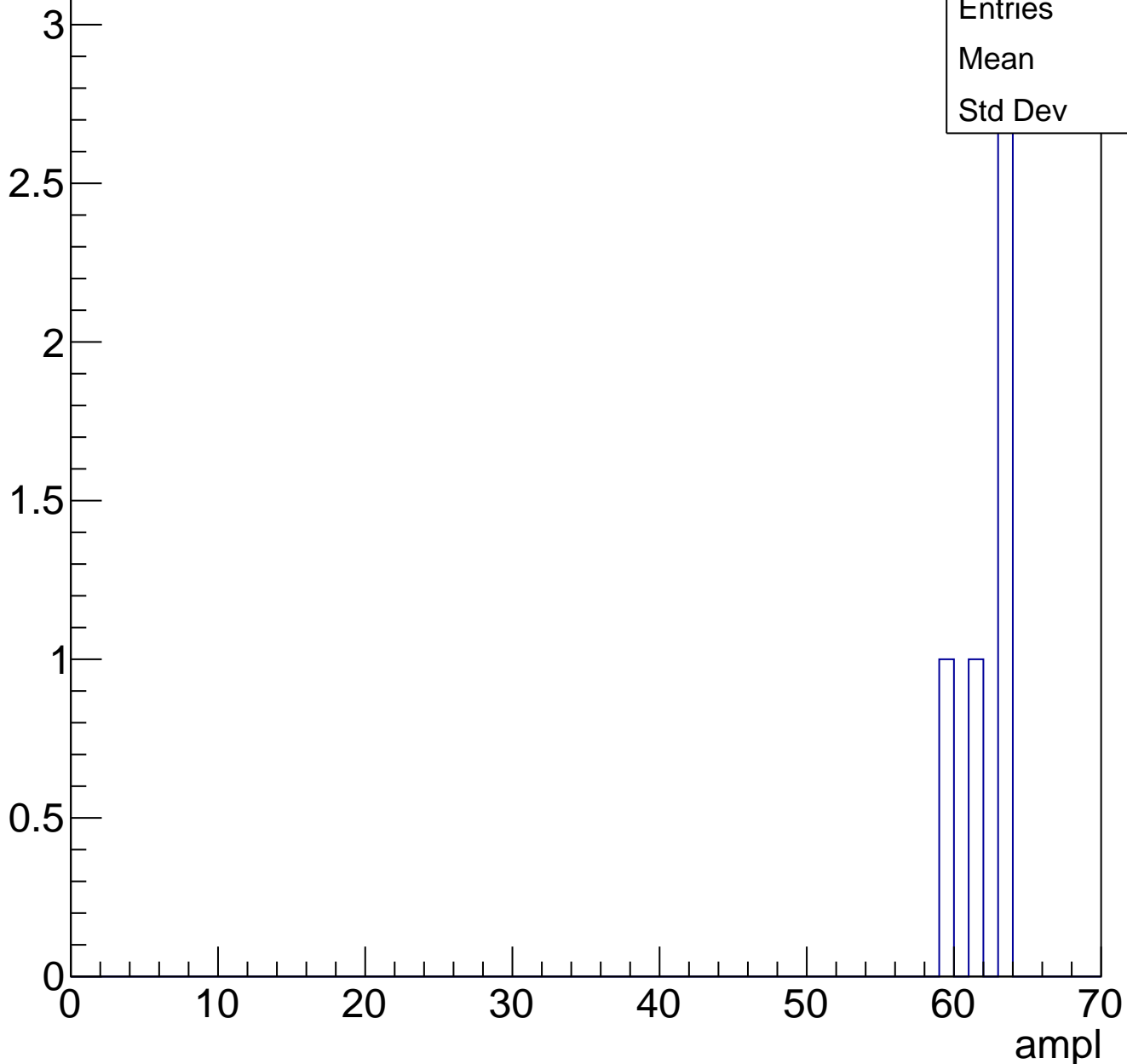
8

9

B1L100S, U12-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	61.8
Std Dev	1.6

B1L100S, U12-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch2, adc0

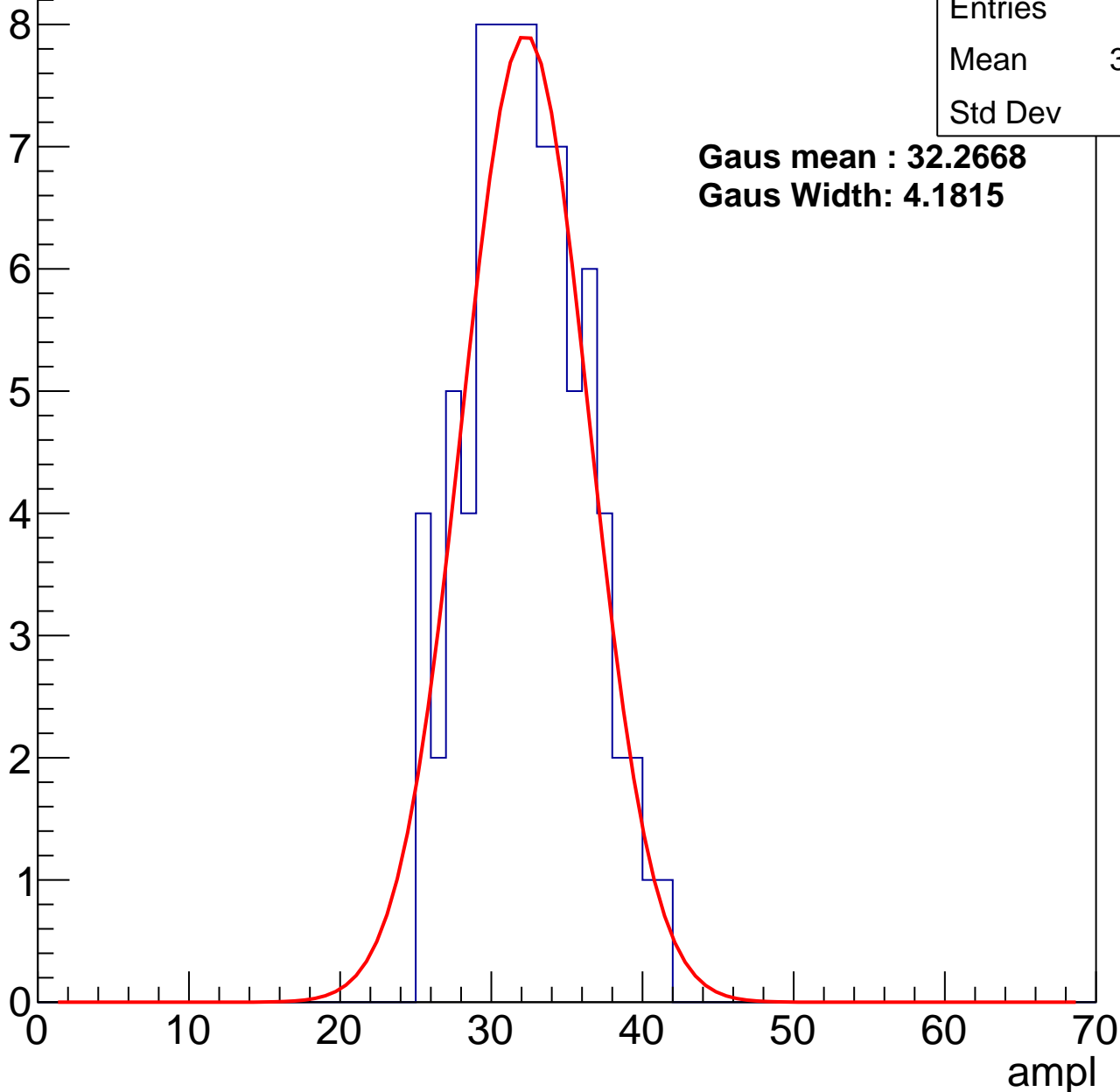
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	31.93
Std Dev	3.76

Gaus mean : 32.2668

Gaus Width: 4.1815



B1L100S, U12-ch2, adc1

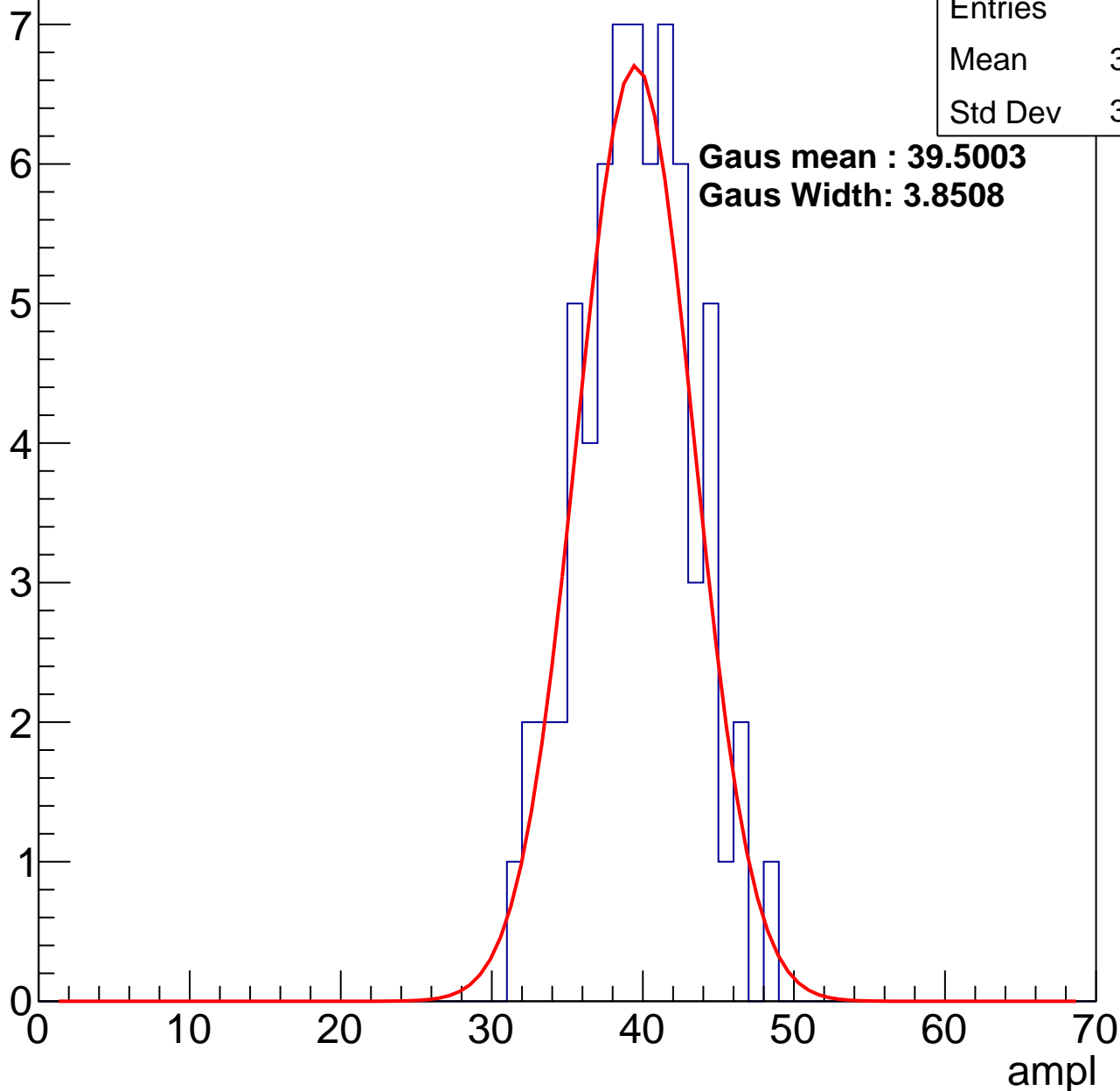
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	39.13
Std Dev	3.677

Gaus mean : 39.5003

Gaus Width: 3.8508



B1L100S, U12-ch2, adc2

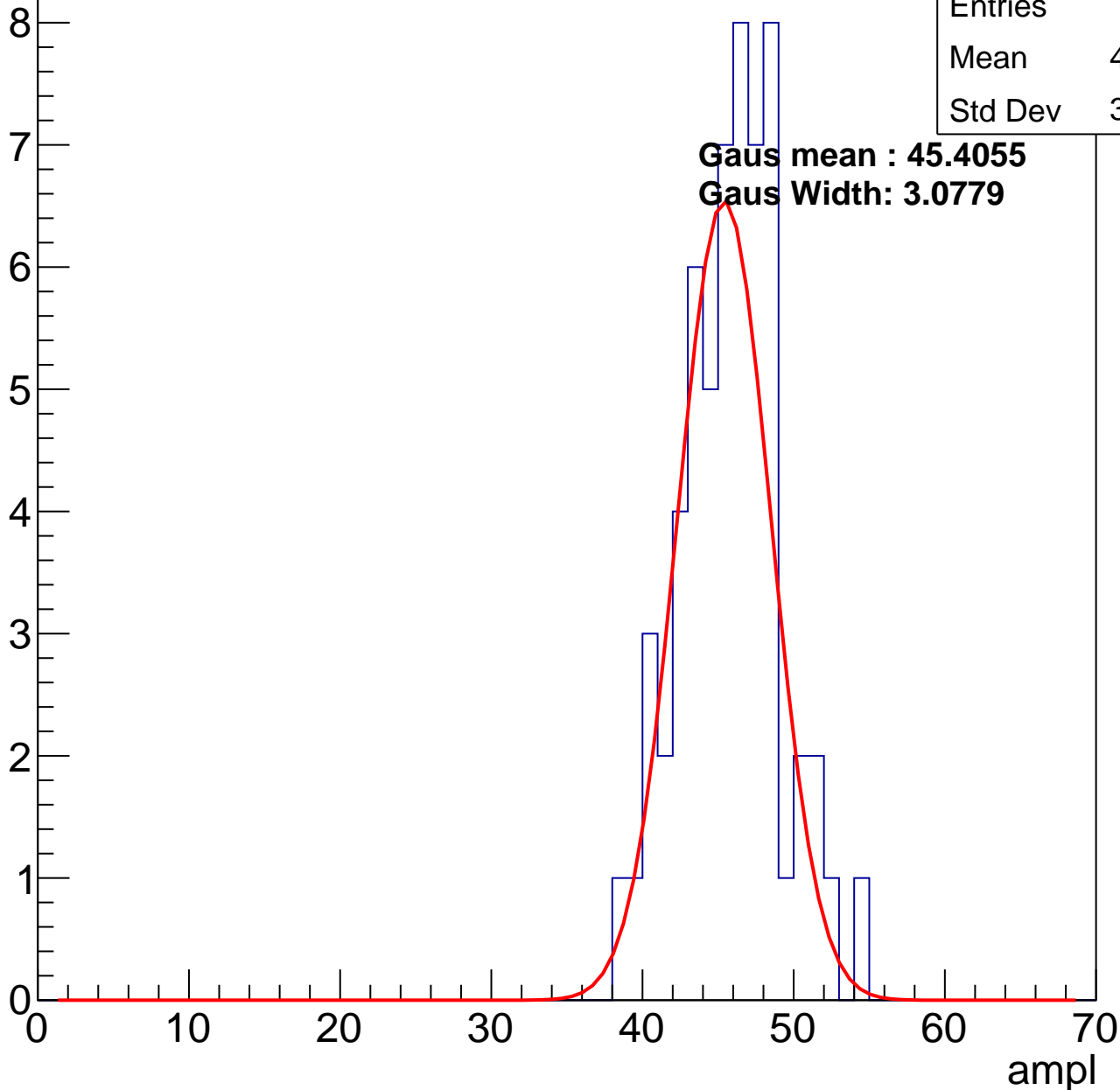
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	45.39
Std Dev	3.252

Gaus mean : 45.4055

Gaus Width: 3.0779

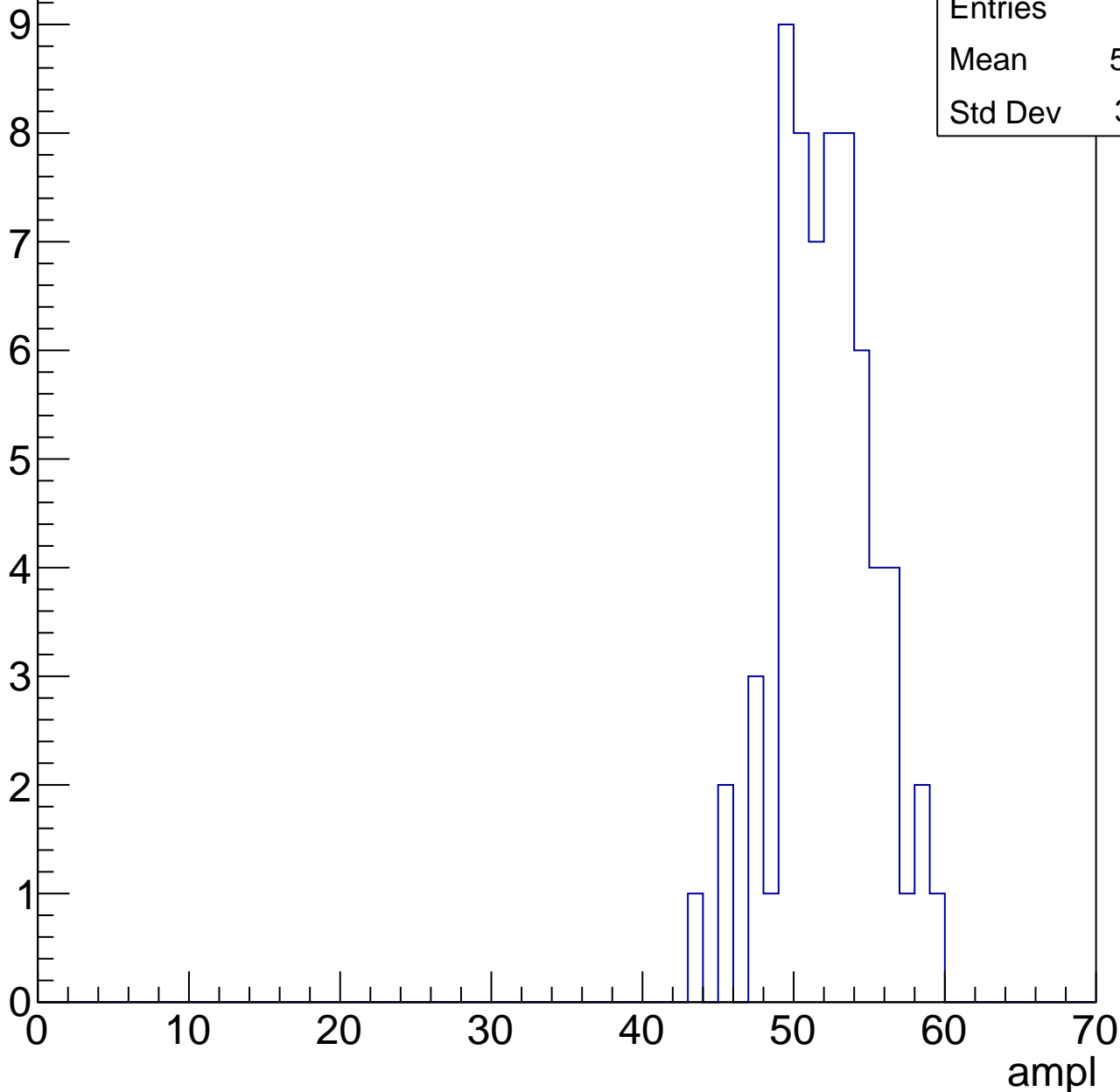


B1L100S, U12-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	51.69
Std Dev	3.191



B1L100S, U12-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

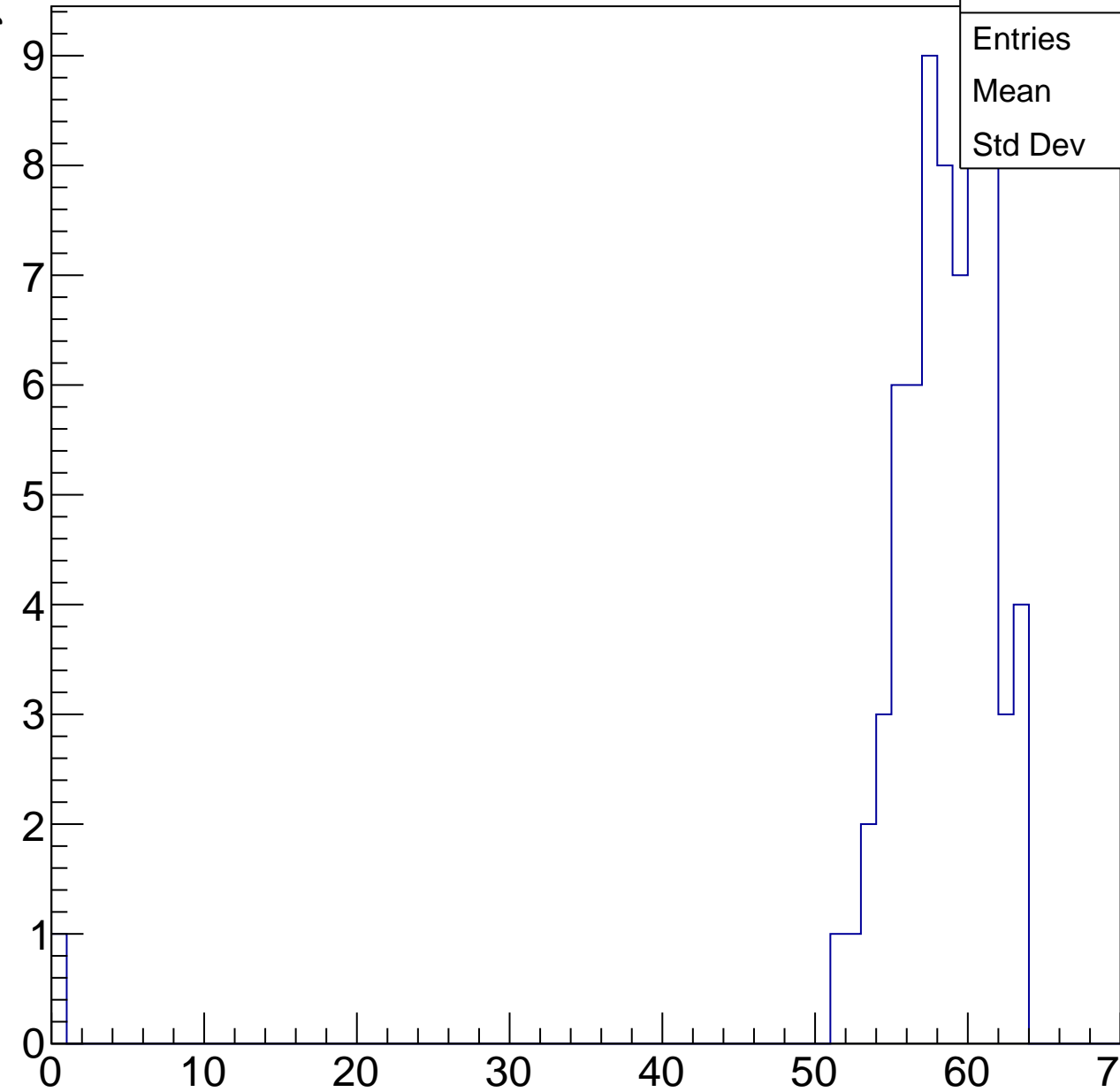
Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	57.3
Std Dev	7.492

ampl

0 10 20 30 40 50 60 70

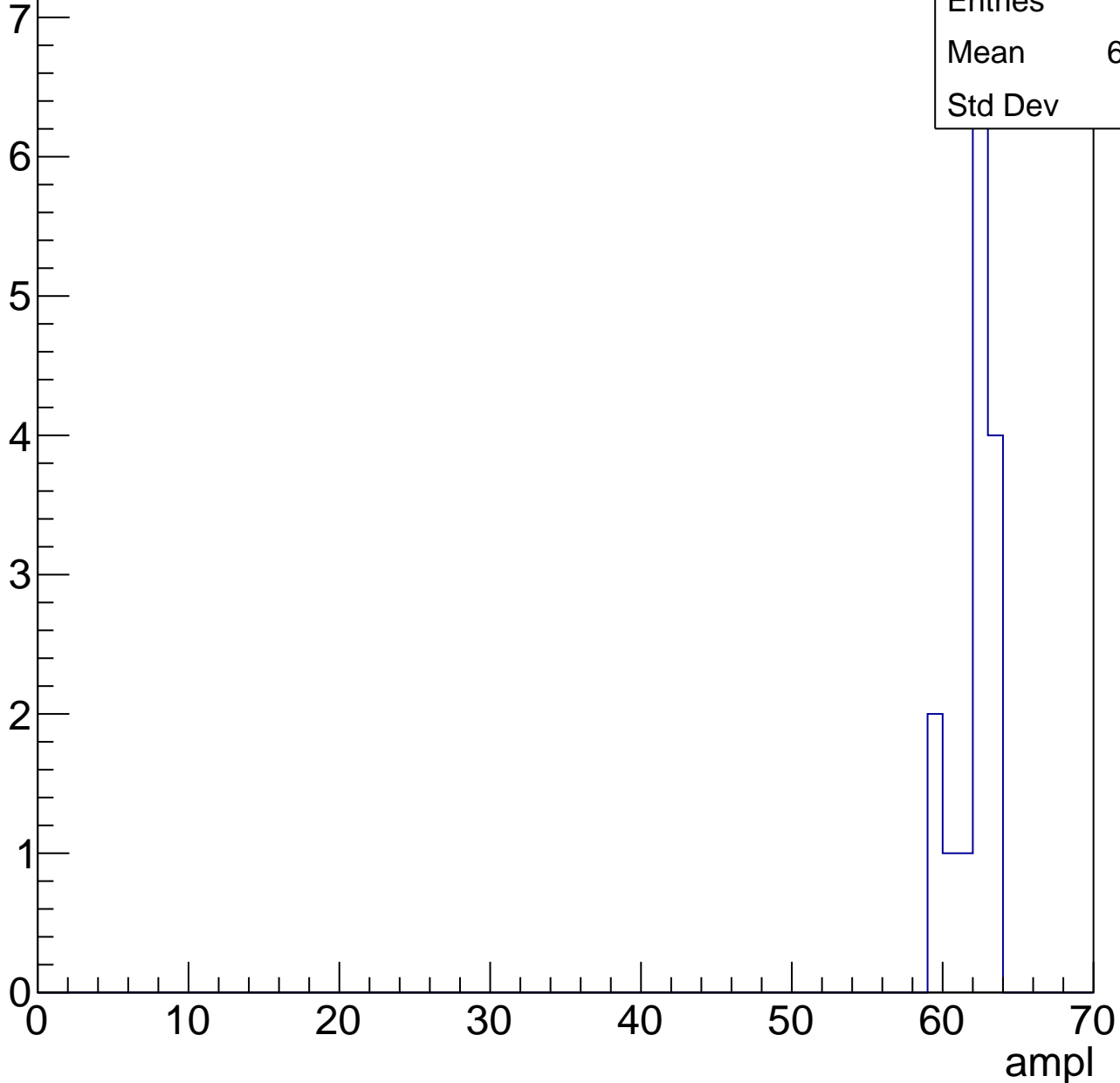


B1L100S, U12-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.67
Std Dev	1.3



B1L100S, U12-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L100S, U12-ch3, adc0

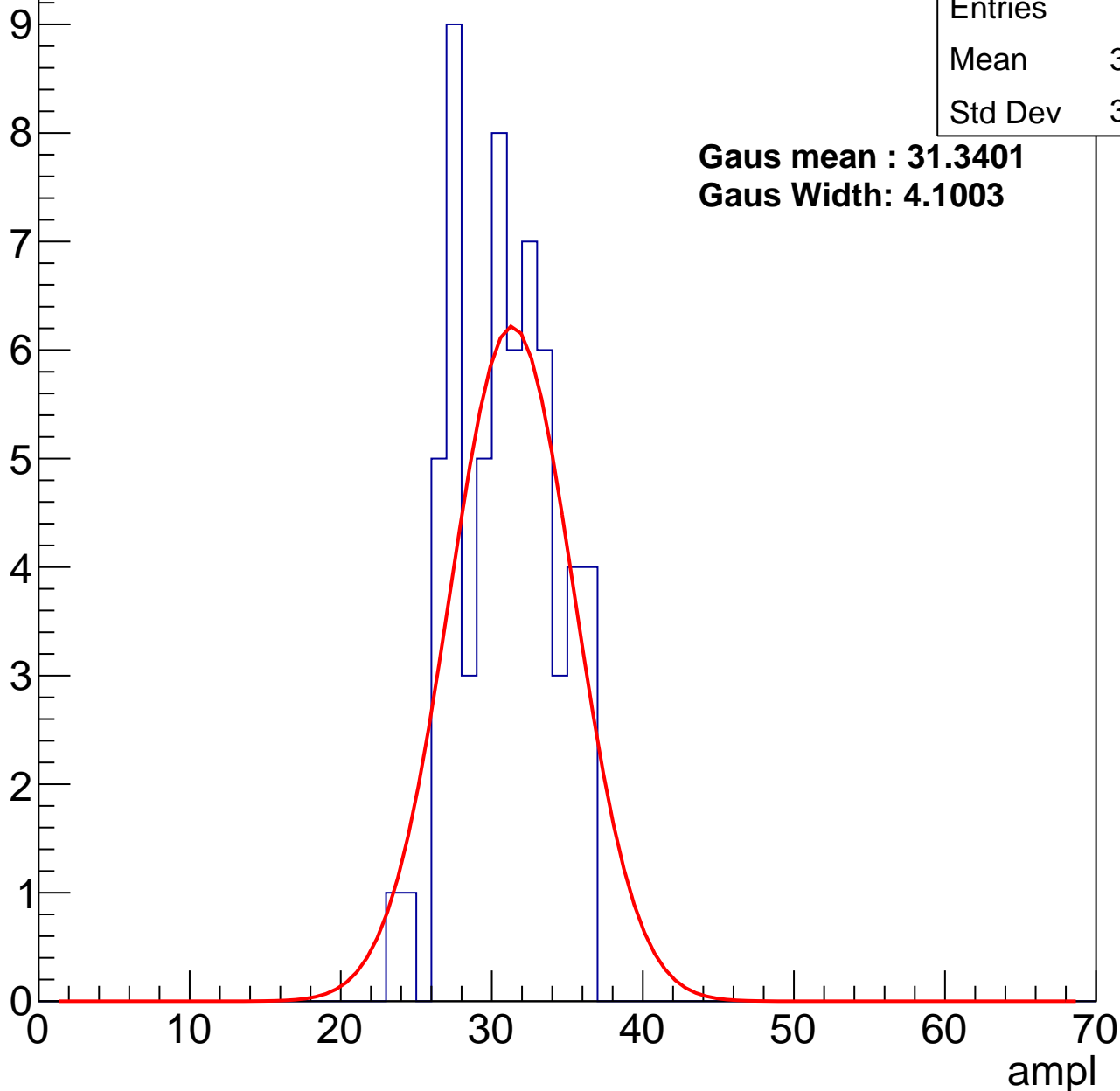
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.37
Std Dev	3.199

Gaus mean : 31.3401

Gaus Width: 4.1003



B1L100S, U12-ch3, adc1

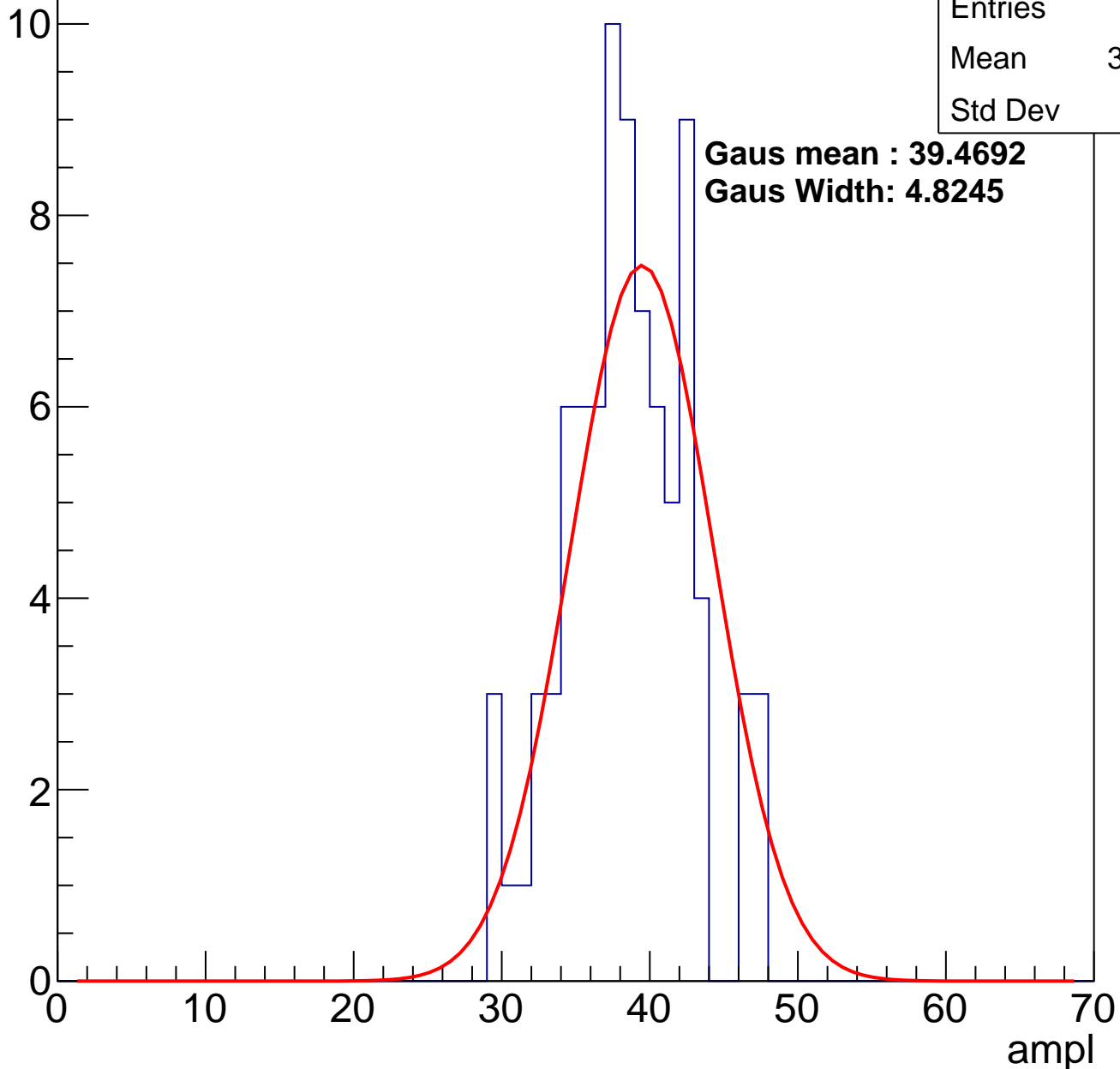
calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	38.02
Std Dev	4.17

Gaus mean : 39.4692

Gaus Width: 4.8245

Entry



B1L100S, U12-ch3, adc2

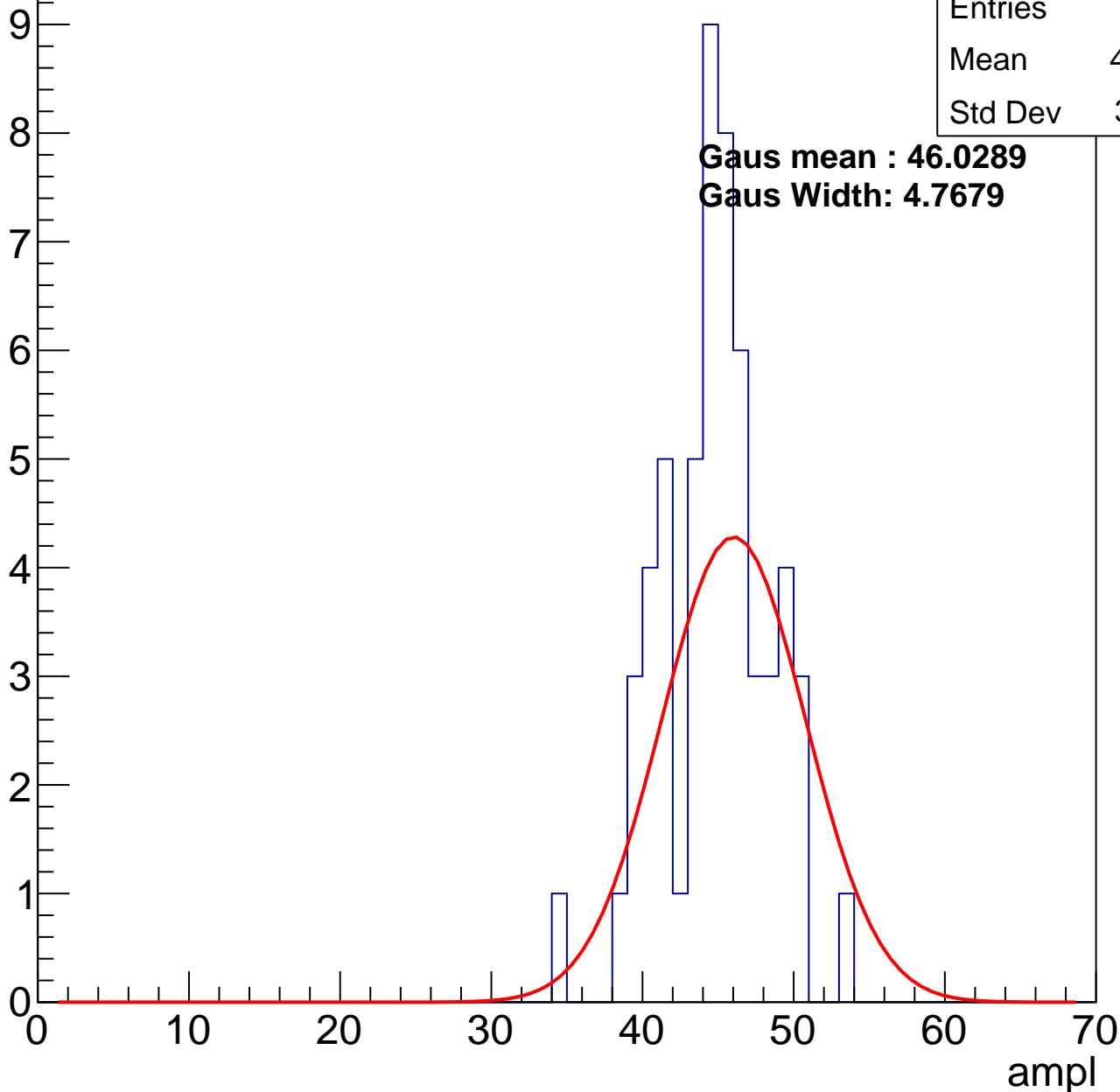
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.33
Std Dev	3.551

Gaus mean : 46.0289

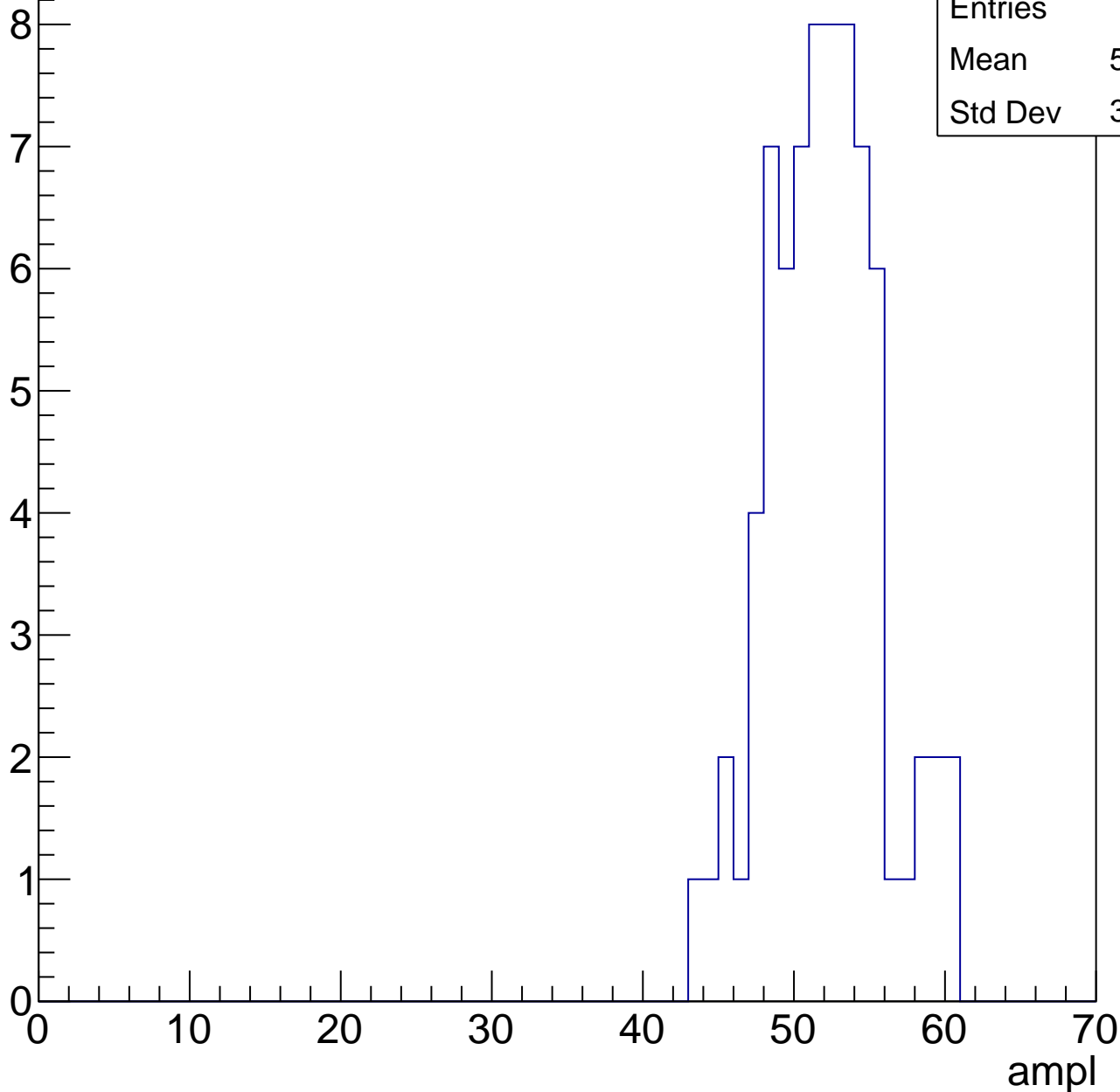
Gaus Width: 4.7679



B1L100S, U12-ch3, adc3

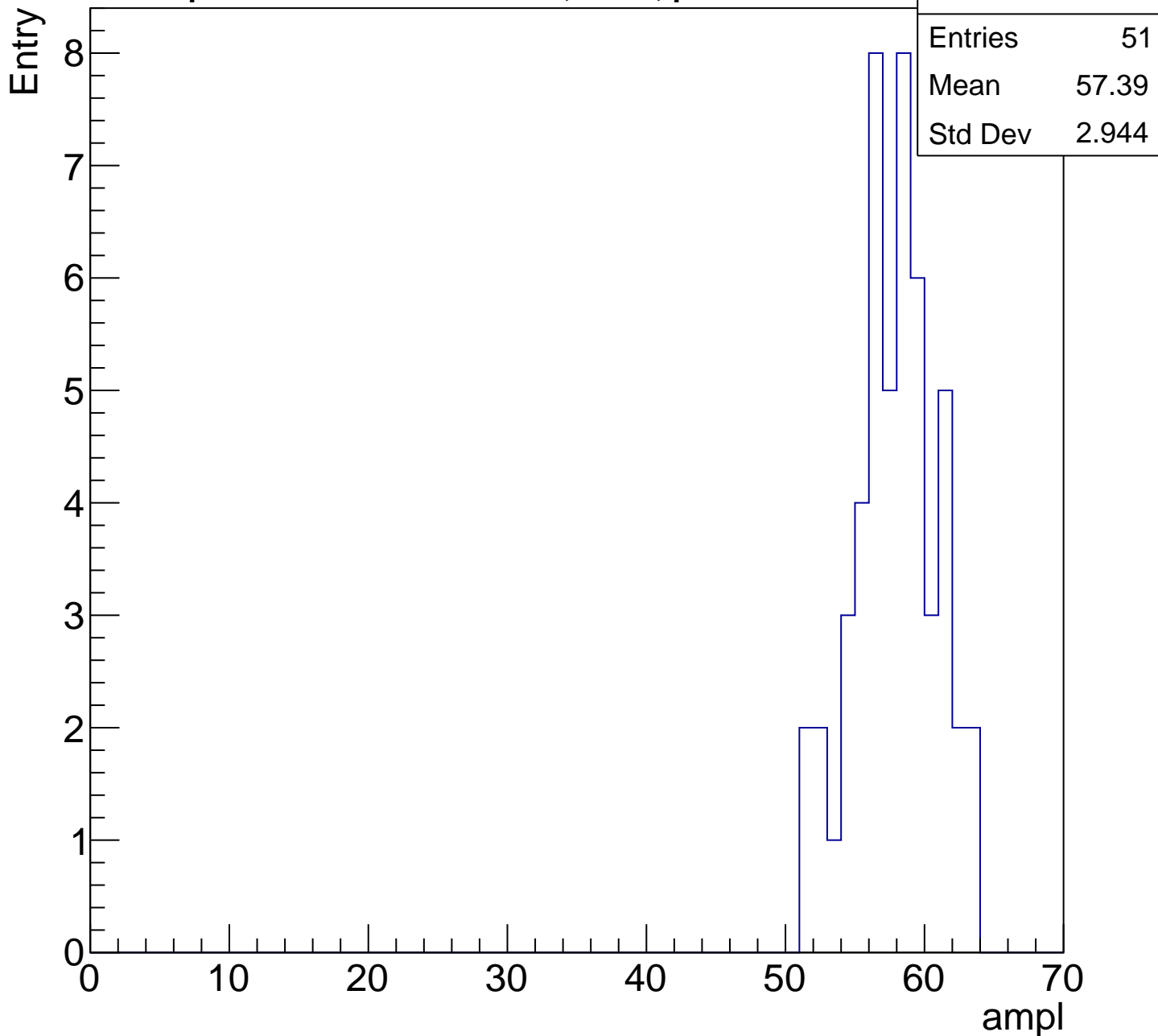
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch3, adc4

calib_packv5_042523_0143.root, FC#4, port A2

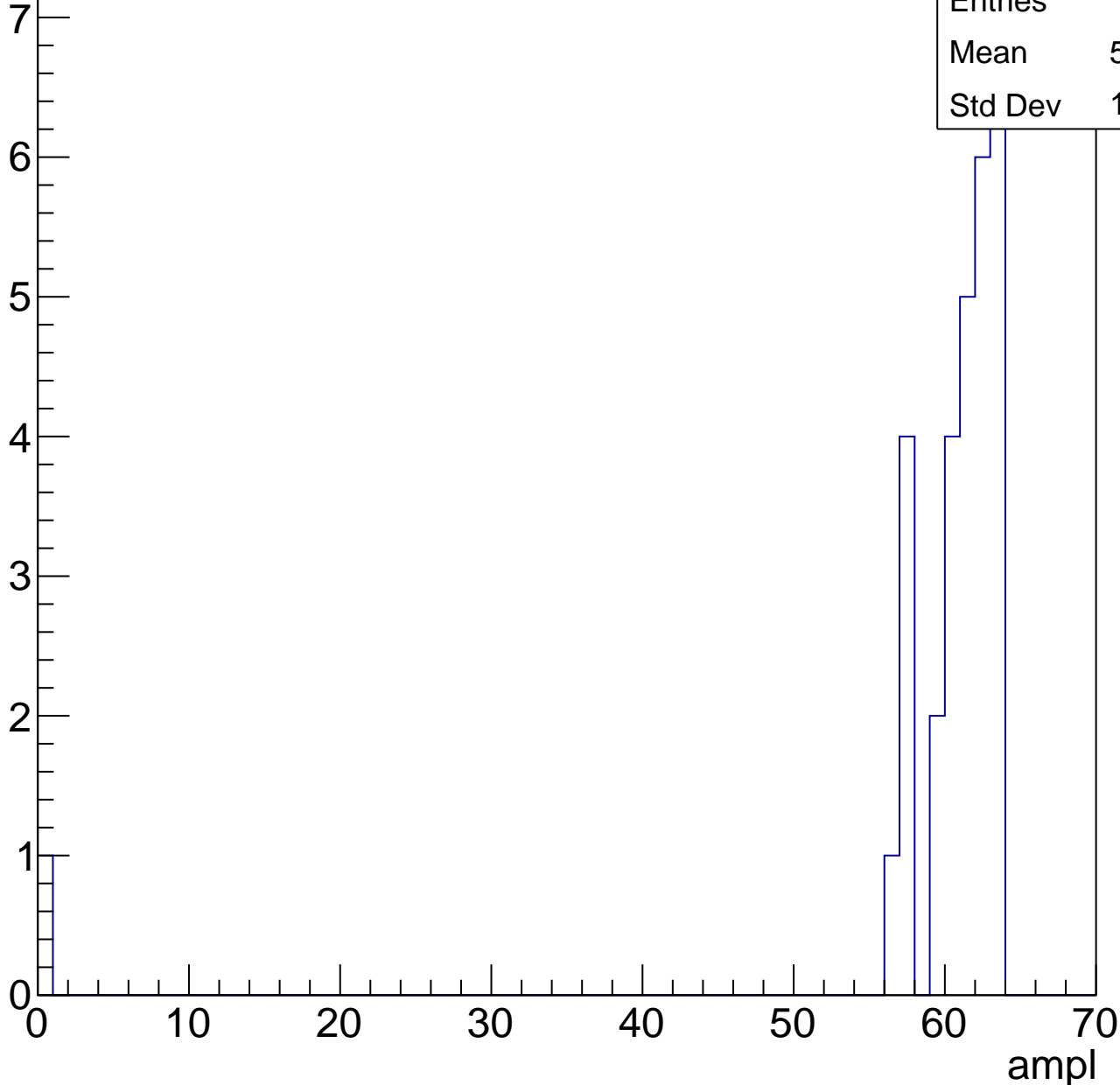


B1L100S, U12-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.67
Std Dev	11.09



B1L100S, U12-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B1L100S, U12-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch4, adc0

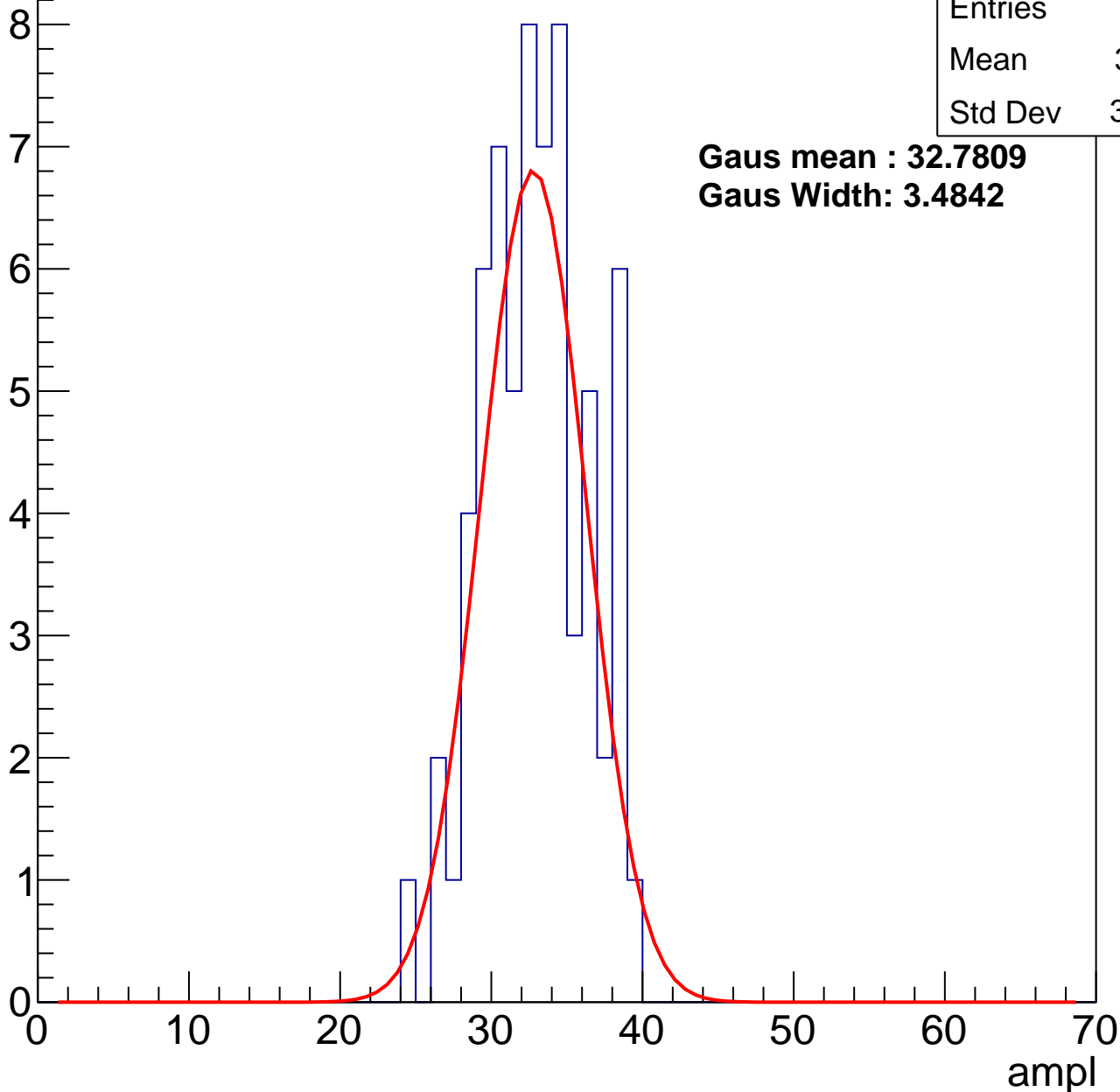
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	32.41
Std Dev	3.398

Gaus mean : 32.7809

Gaus Width: 3.4842



B1L100S, U12-ch4, adc1

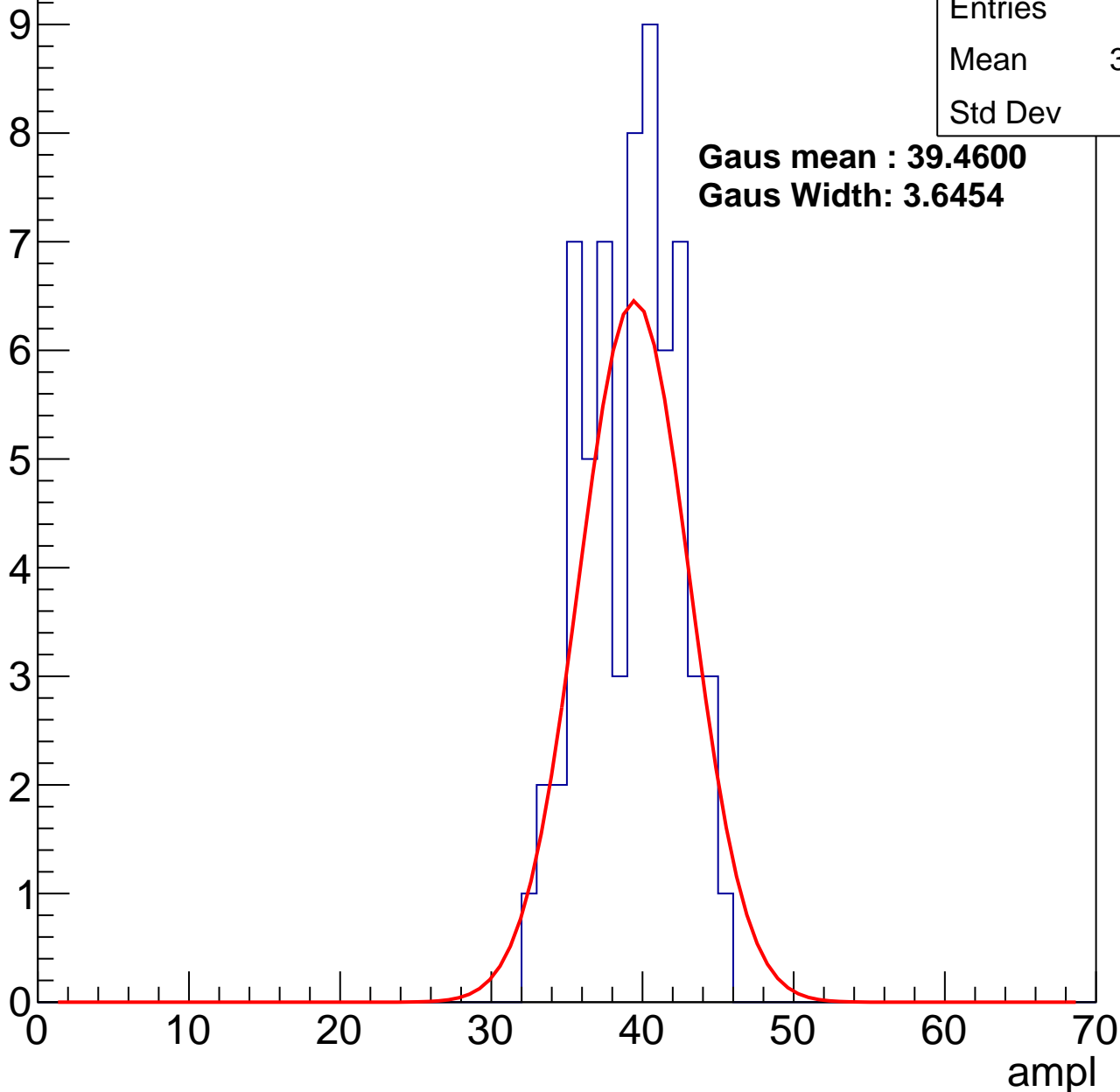
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	38.78
Std Dev	3.09

Gaus mean : 39.4600

Gaus Width: 3.6454



B1L100S, U12-ch4, adc2

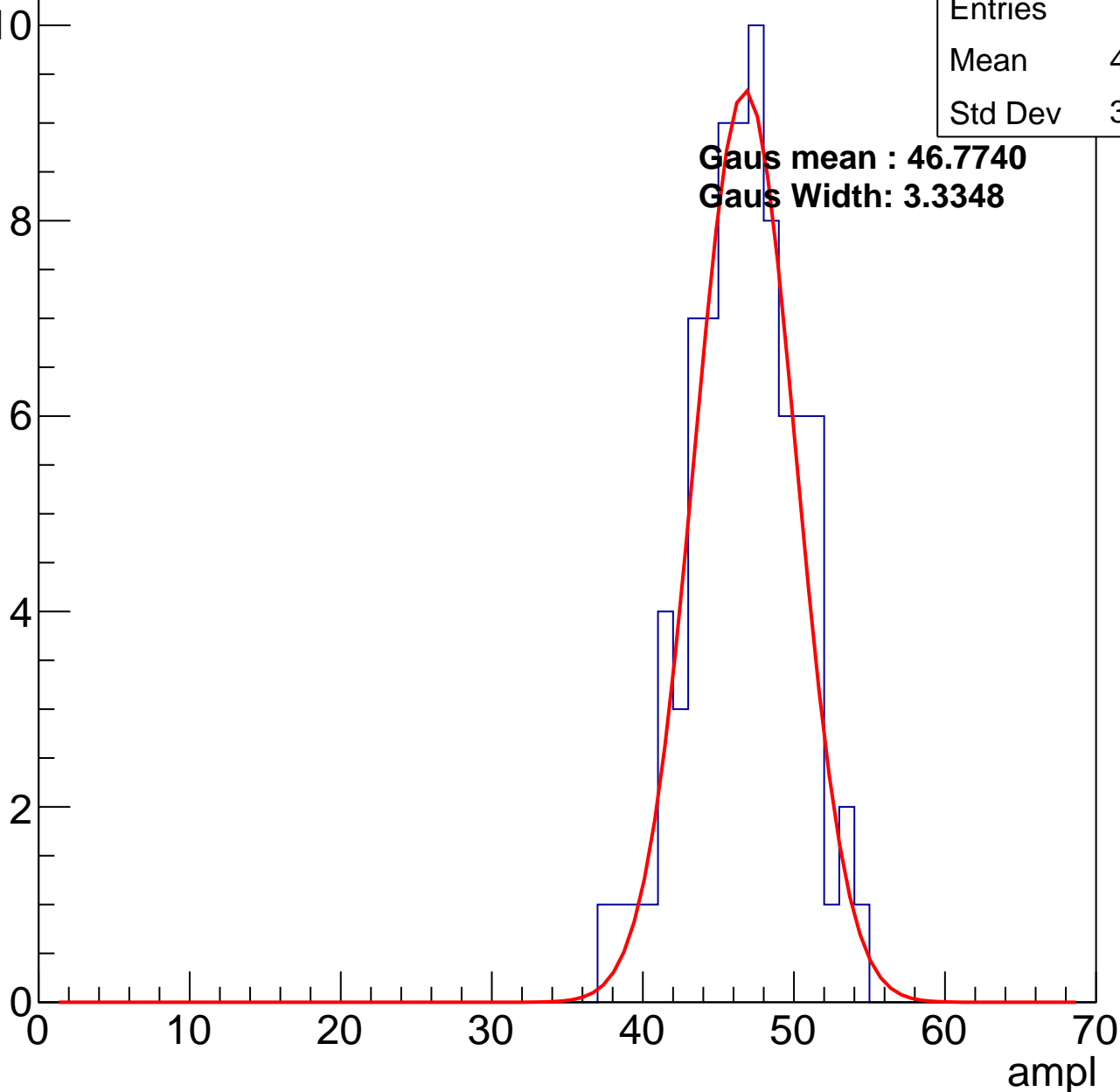
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	46.24
Std Dev	3.494

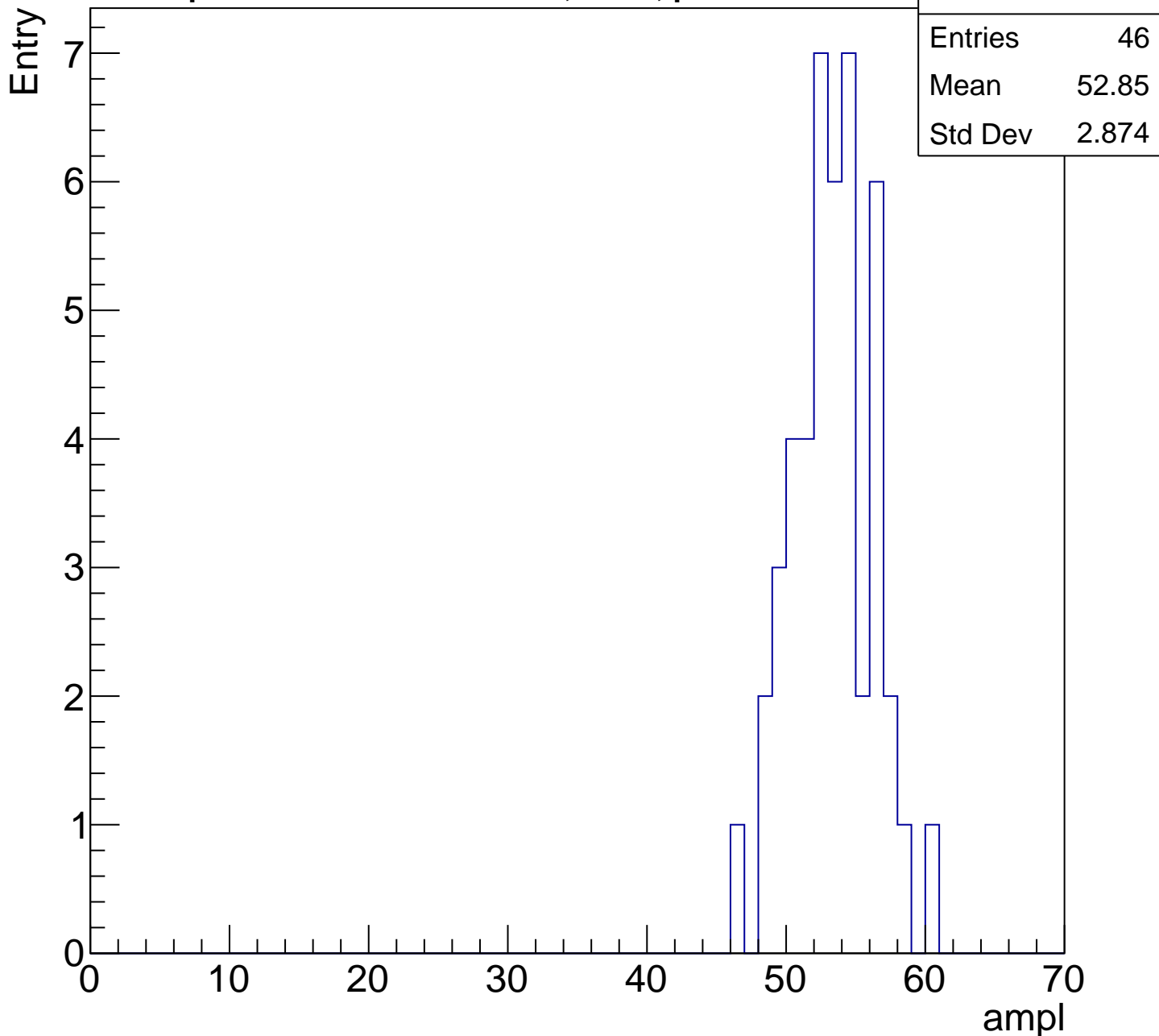
Gaus mean : 46.7740

Gaus Width: 3.3348



B1L100S, U12-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

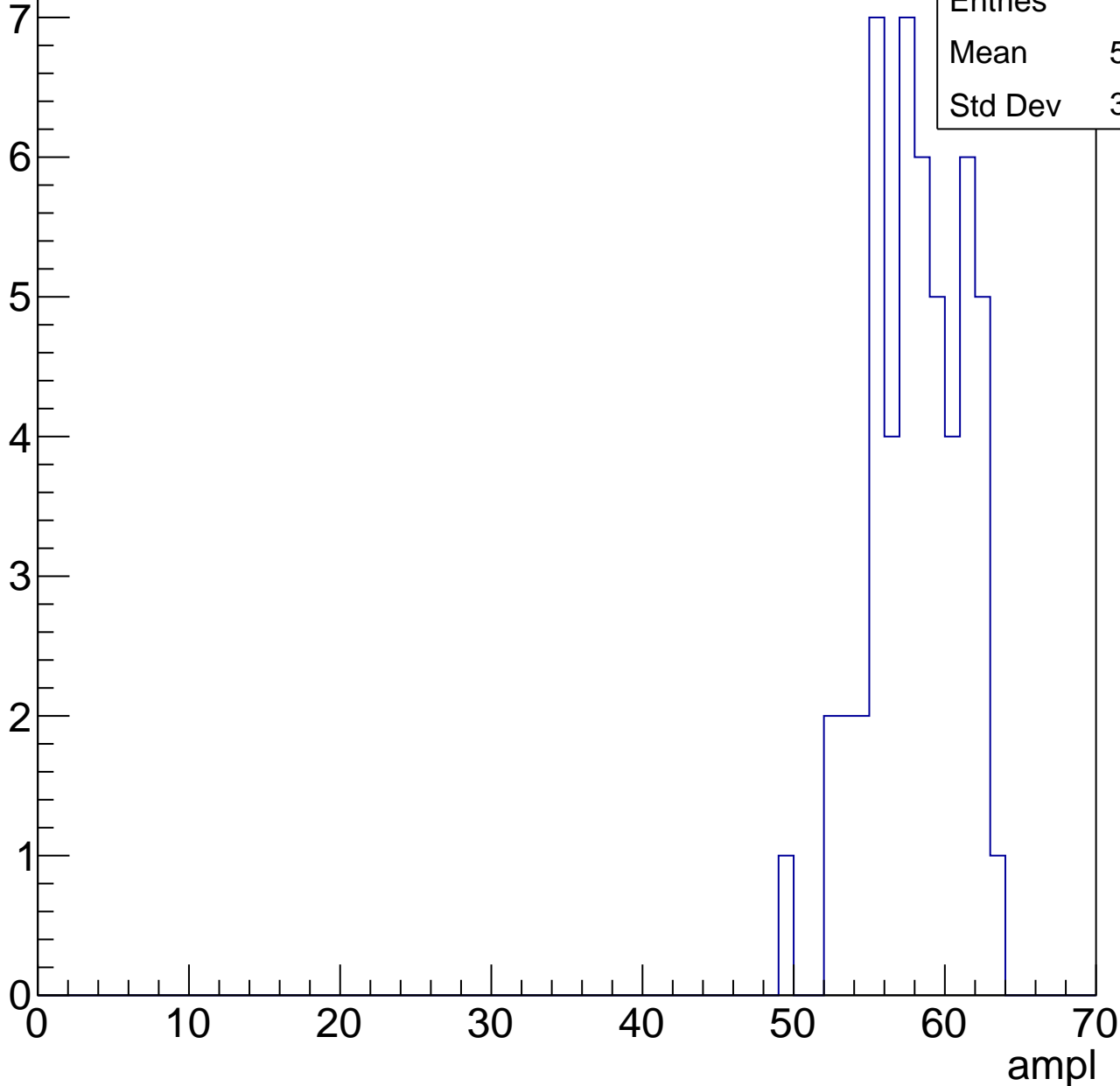


B1L100S, U12-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	57.63
Std Dev	3.082



B1L100S, U12-ch4, adc5

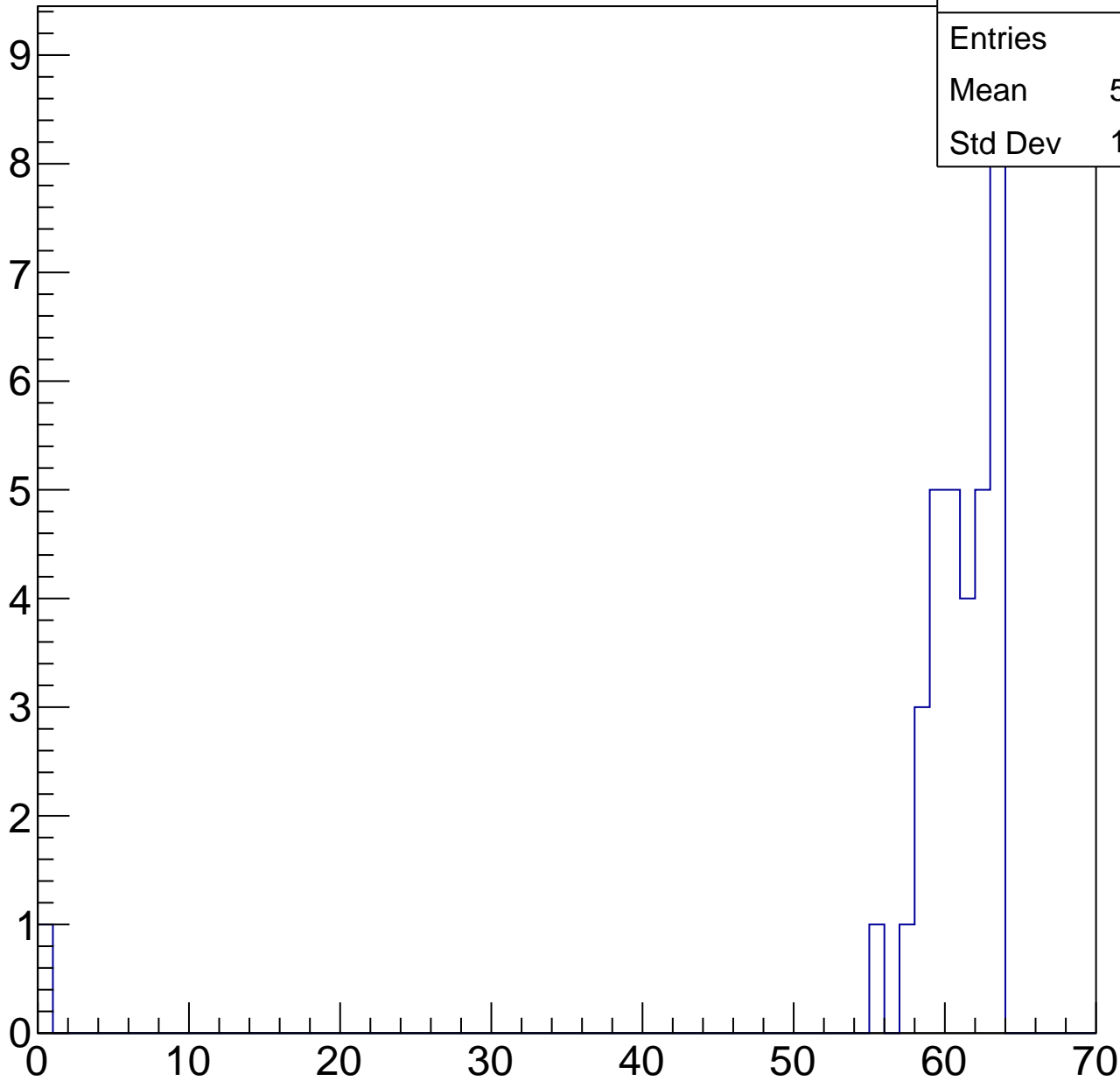
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	34
Mean	58.88
Std Dev	10.45

ampl



B1L100S, U12-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch5, adc0

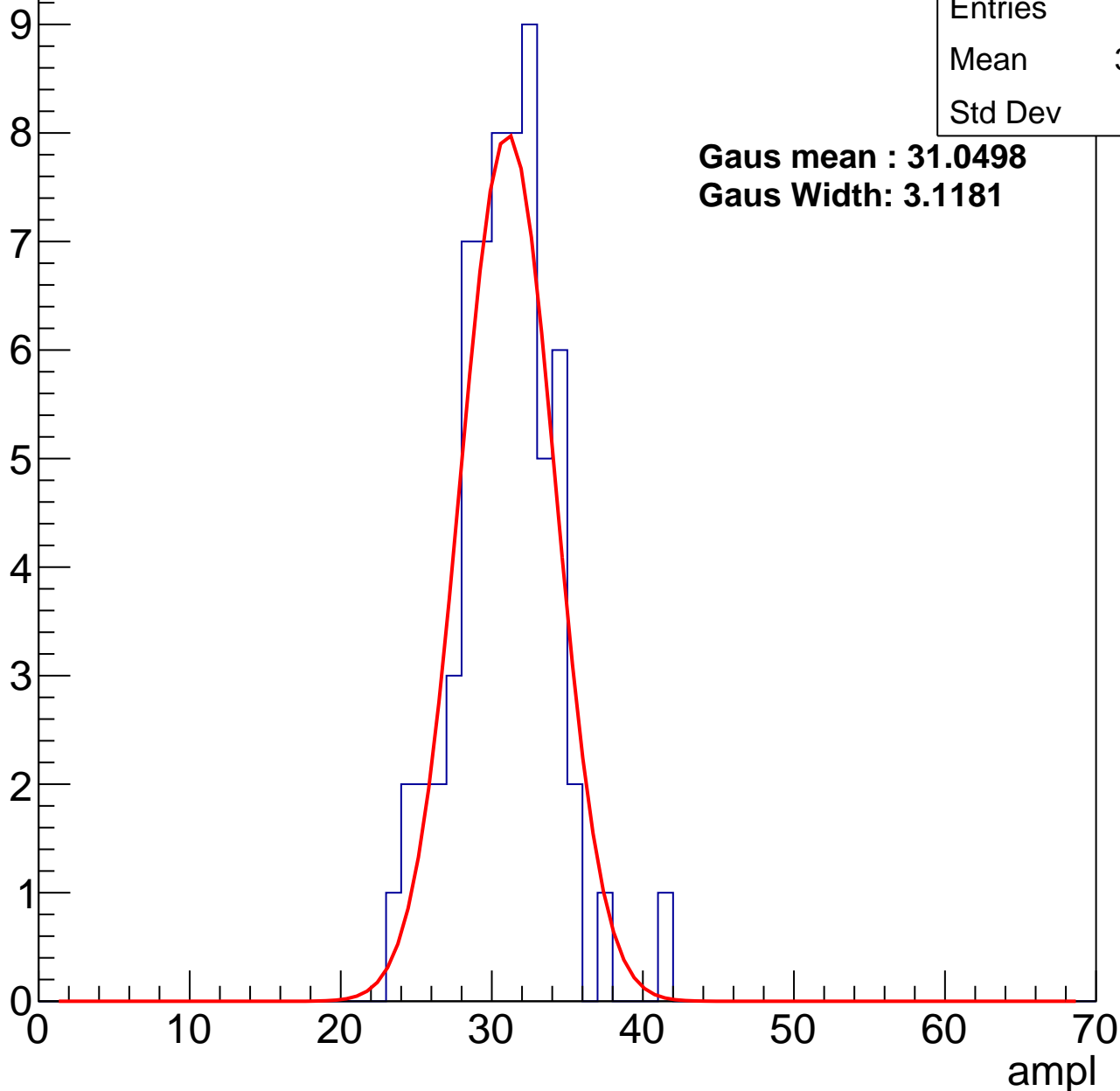
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.41
Std Dev	3.21

Gaus mean : 31.0498

Gaus Width: 3.1181



B1L100S, U12-ch5, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	37.51
Std Dev	3.226

Gaus mean : 38.3420

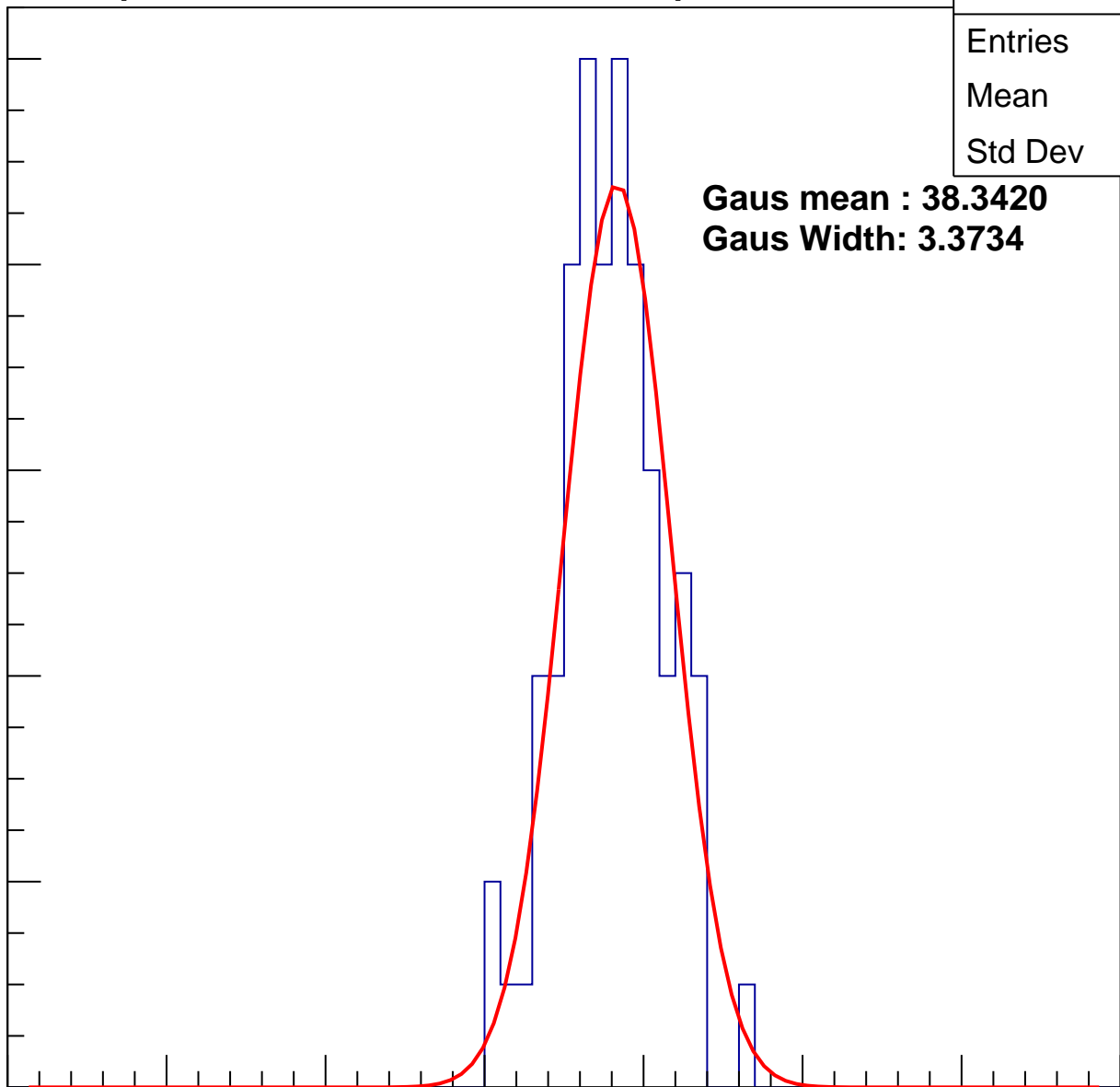
Gaus Width: 3.3734

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch5, adc2

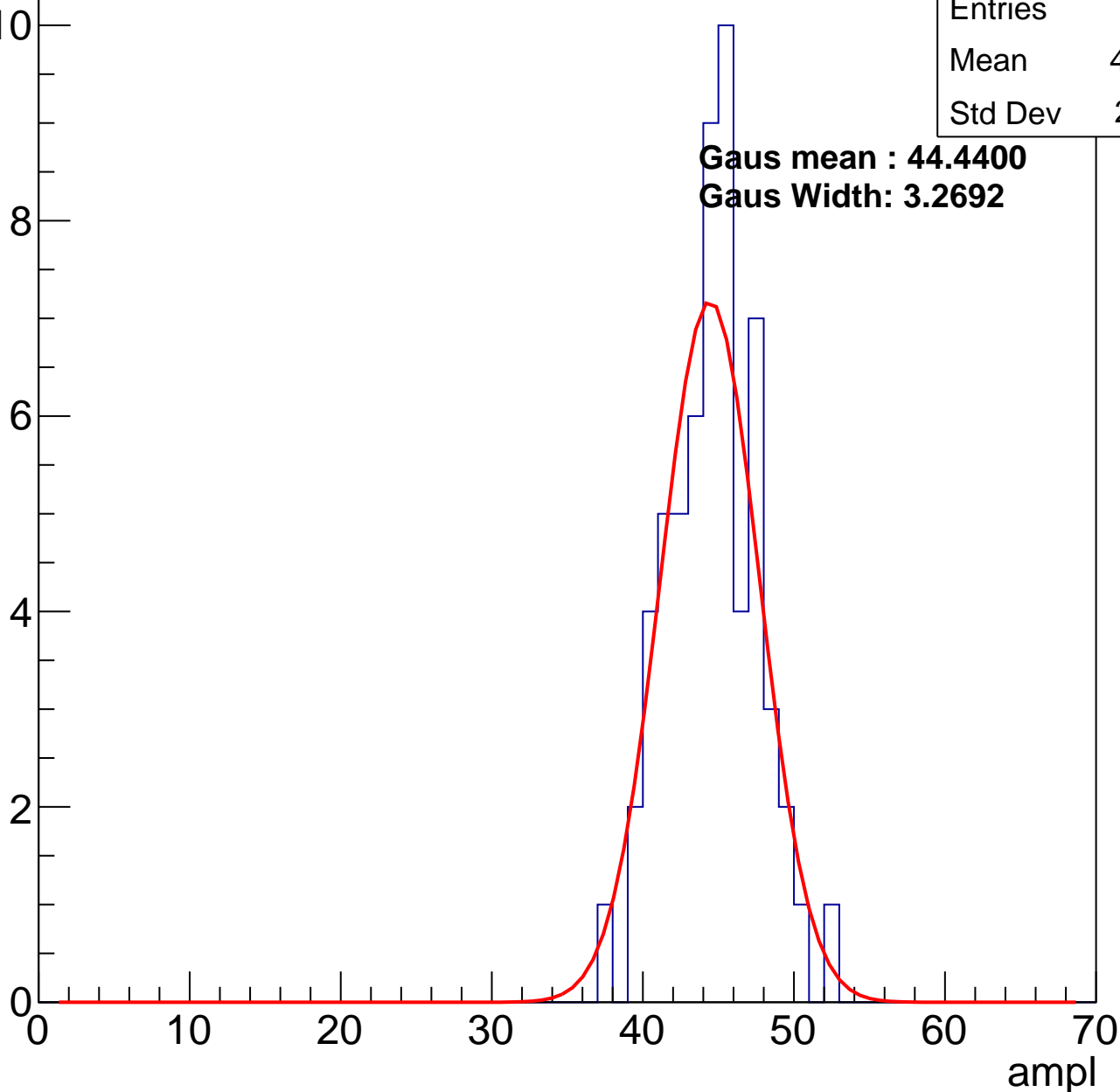
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.18
Std Dev	2.941

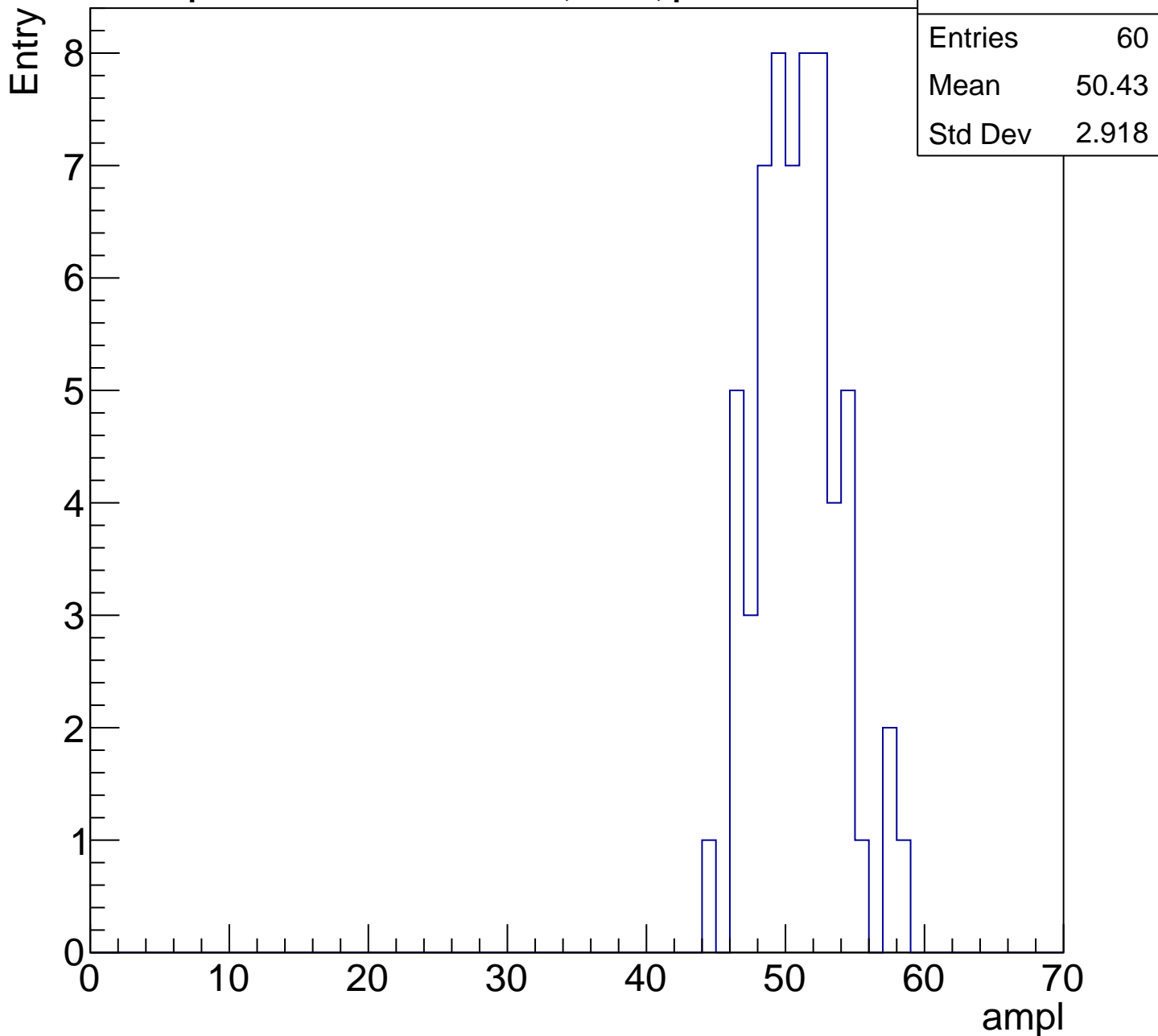
Gaus mean : 44.4400

Gaus Width: 3.2692



B1L100S, U12-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

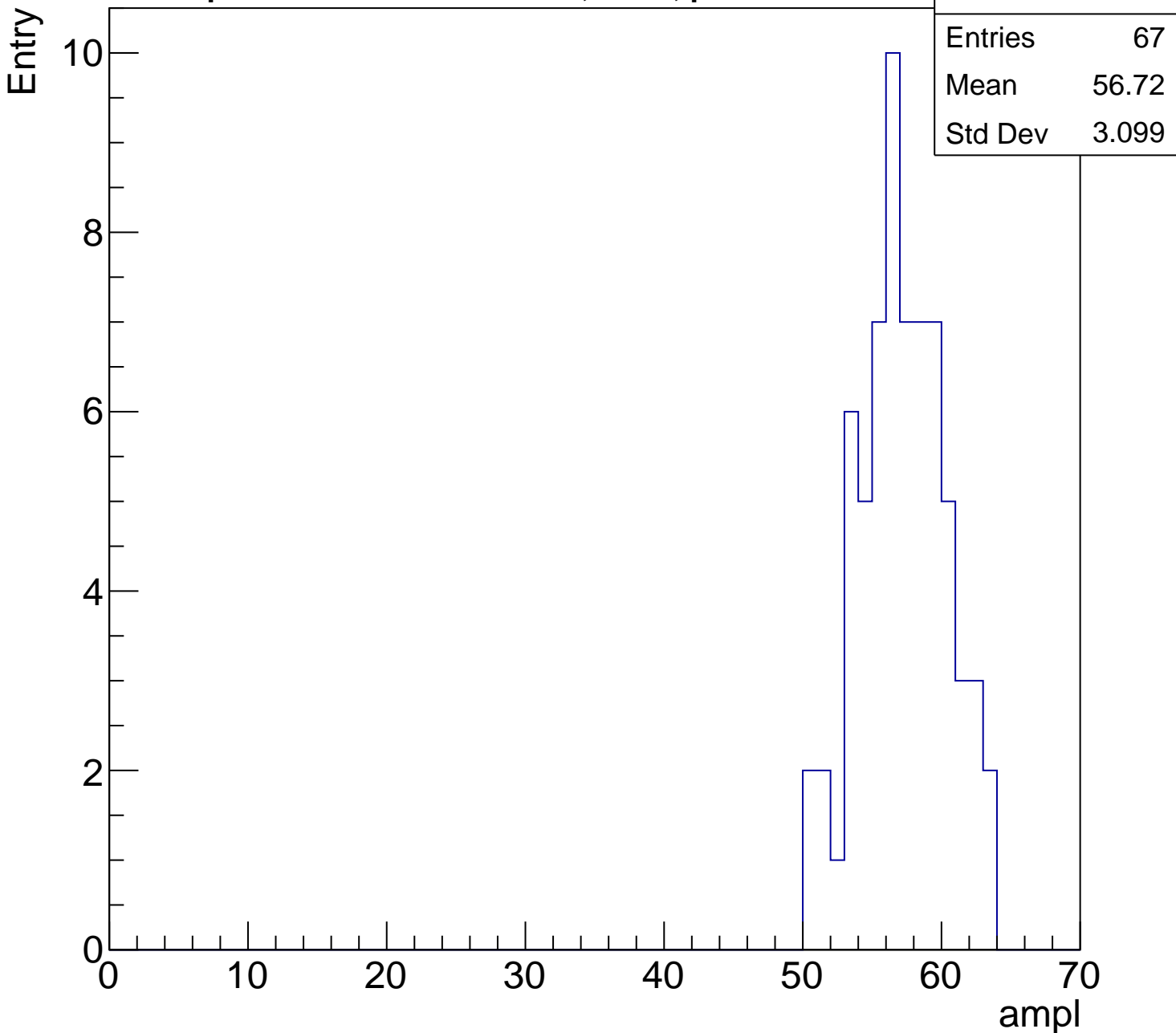
Entries	67
Mean	56.72
Std Dev	3.099

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

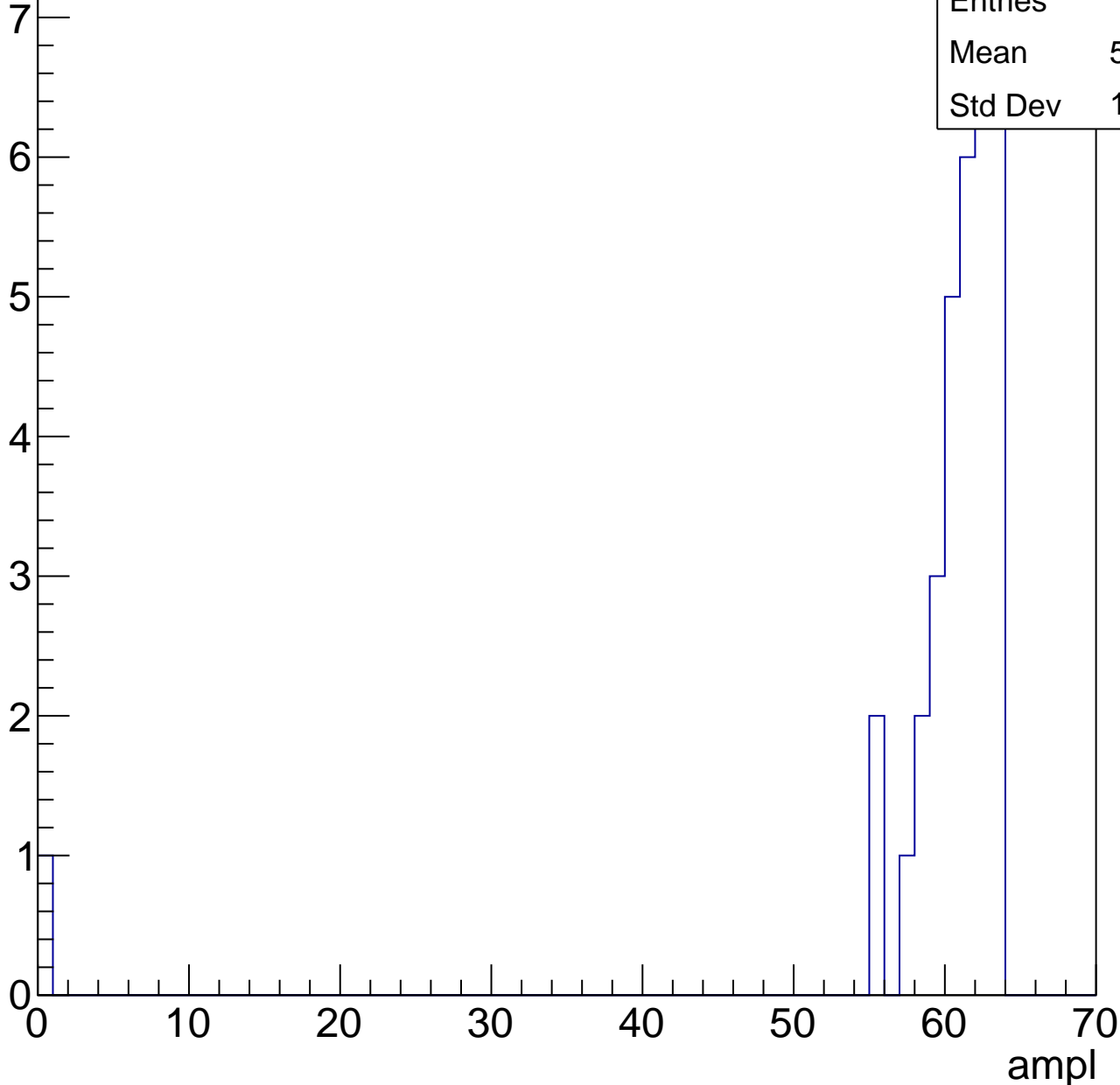


B1L100S, U12-ch5, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.85
Std Dev	10.46



B1L100S, U12-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch6, adc0

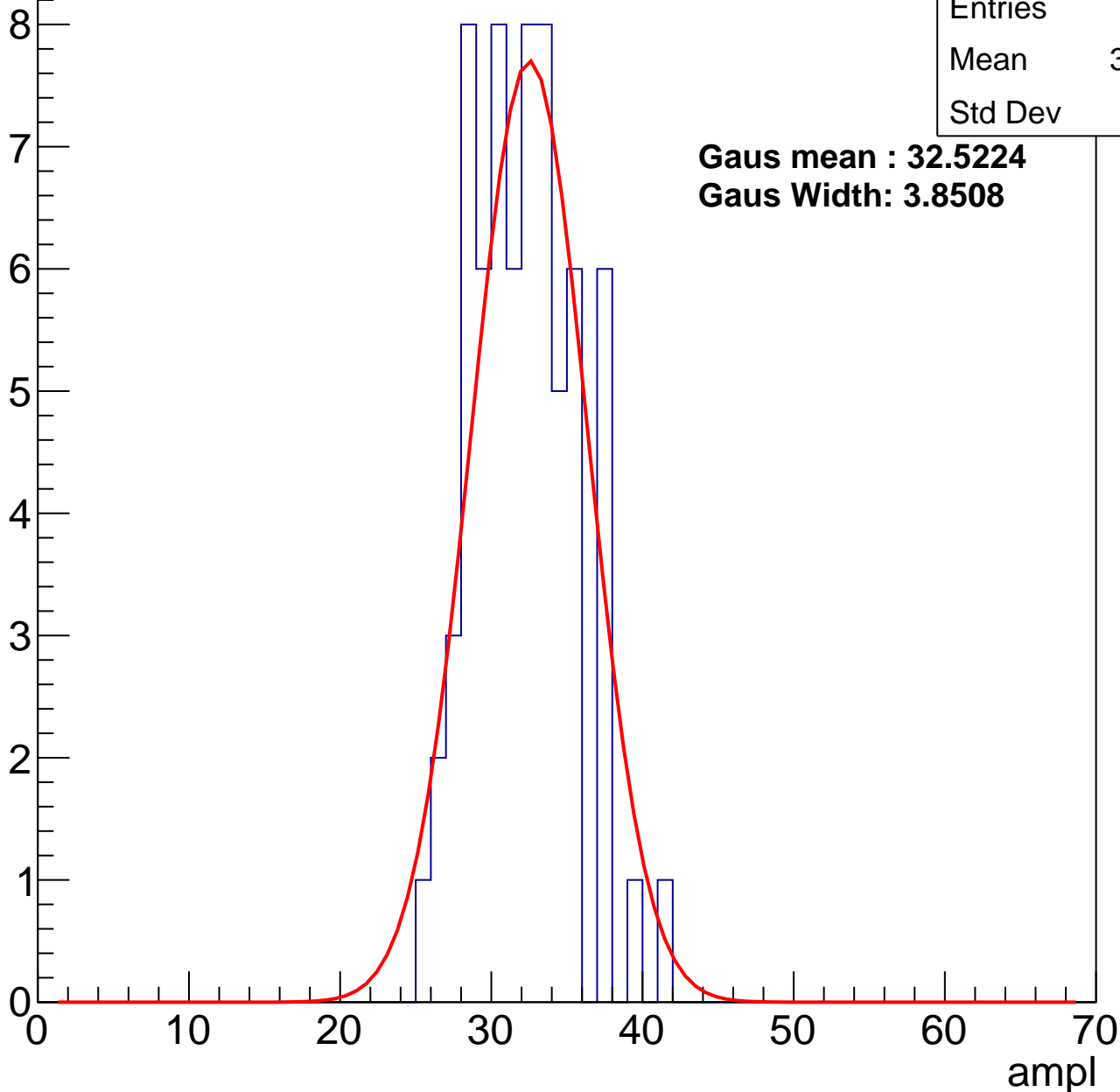
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	31.65
Std Dev	3.34

Gaus mean : 32.5224

Gaus Width: 3.8508



B1L100S, U12-ch6, adc1

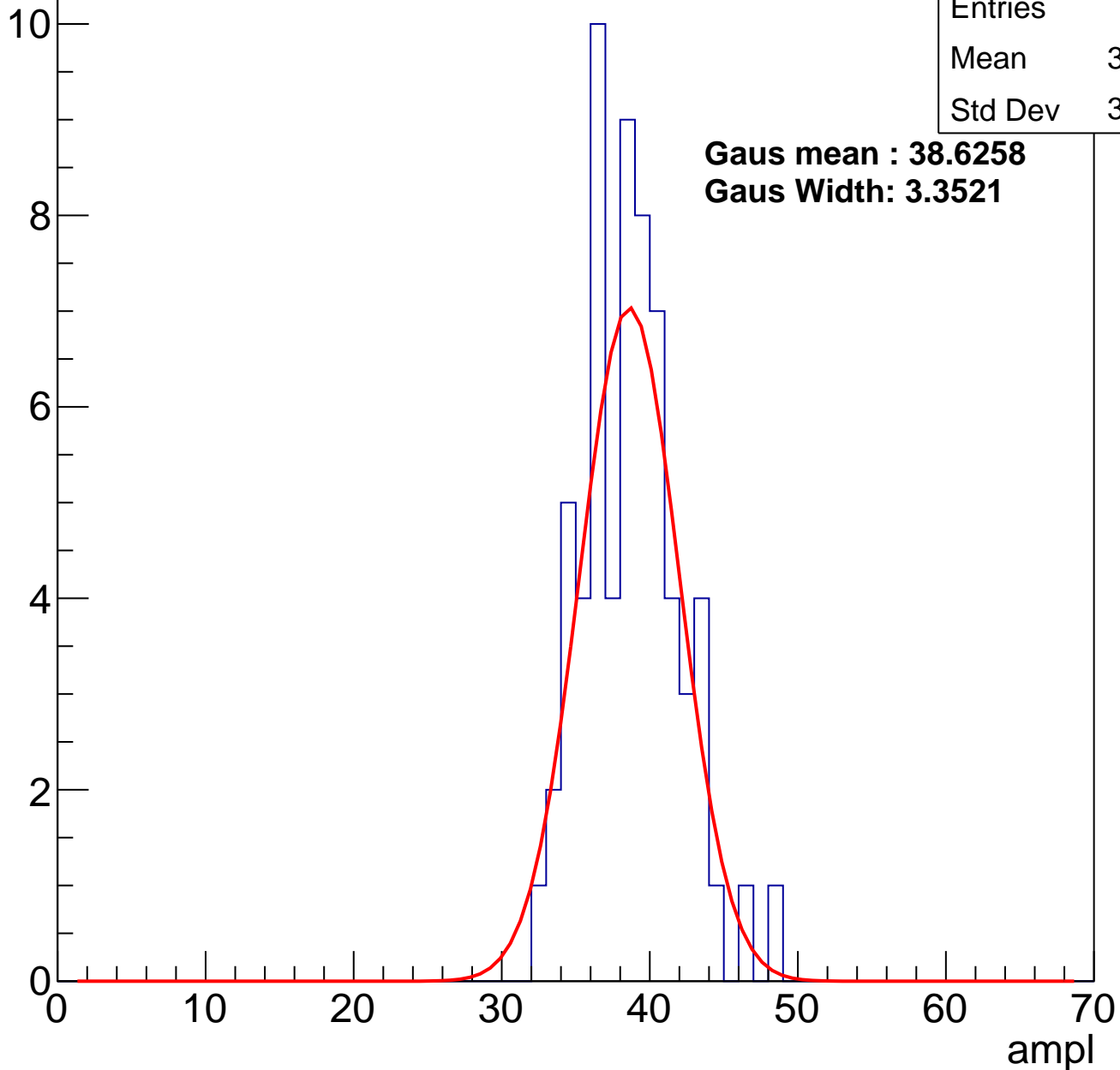
calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	38.28
Std Dev	3.214

Gaus mean : 38.6258

Gaus Width: 3.3521

Entry



B1L100S, U12-ch6, adc2

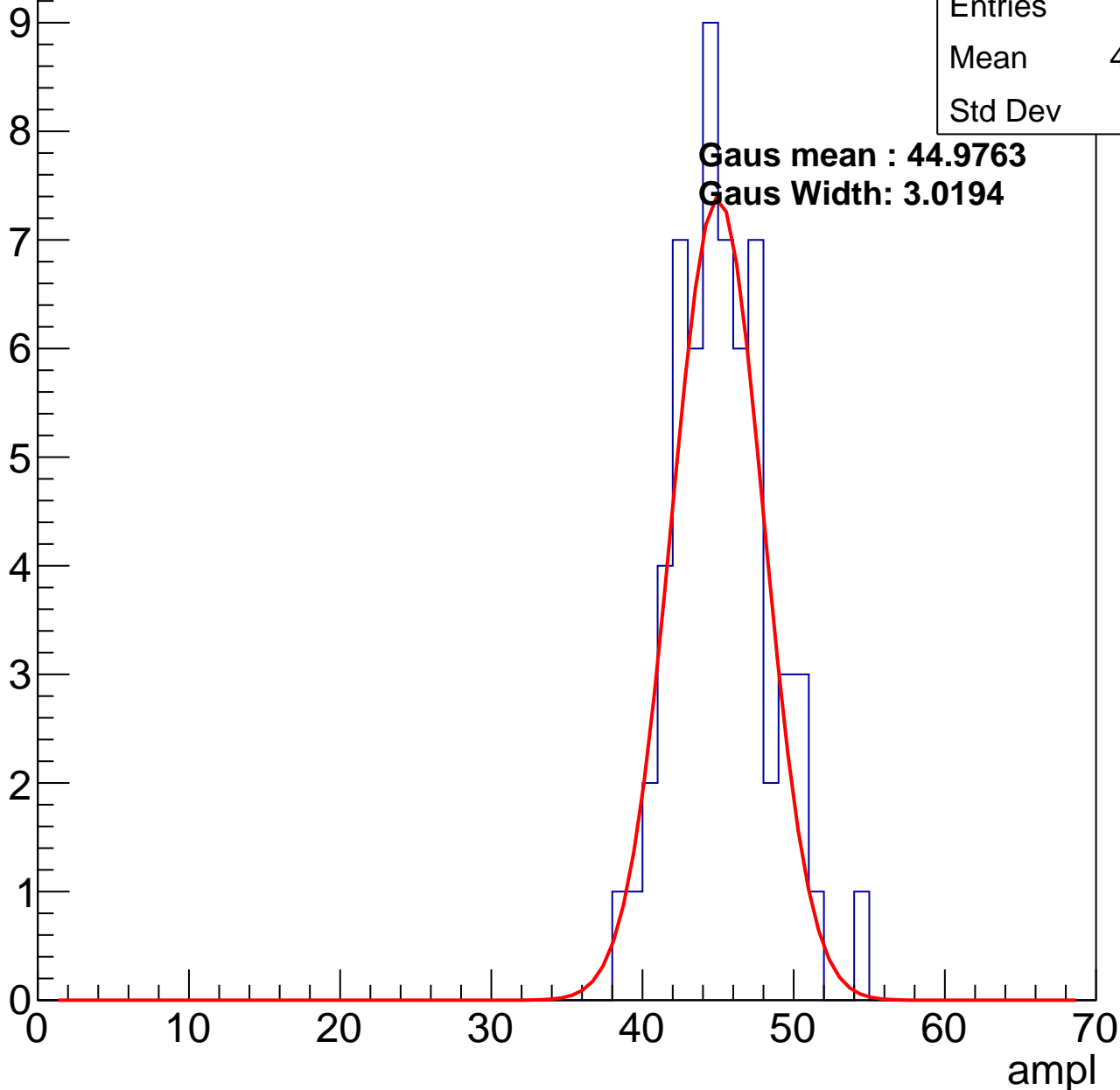
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.78
Std Dev	3.12

Gaus mean : 44.9763

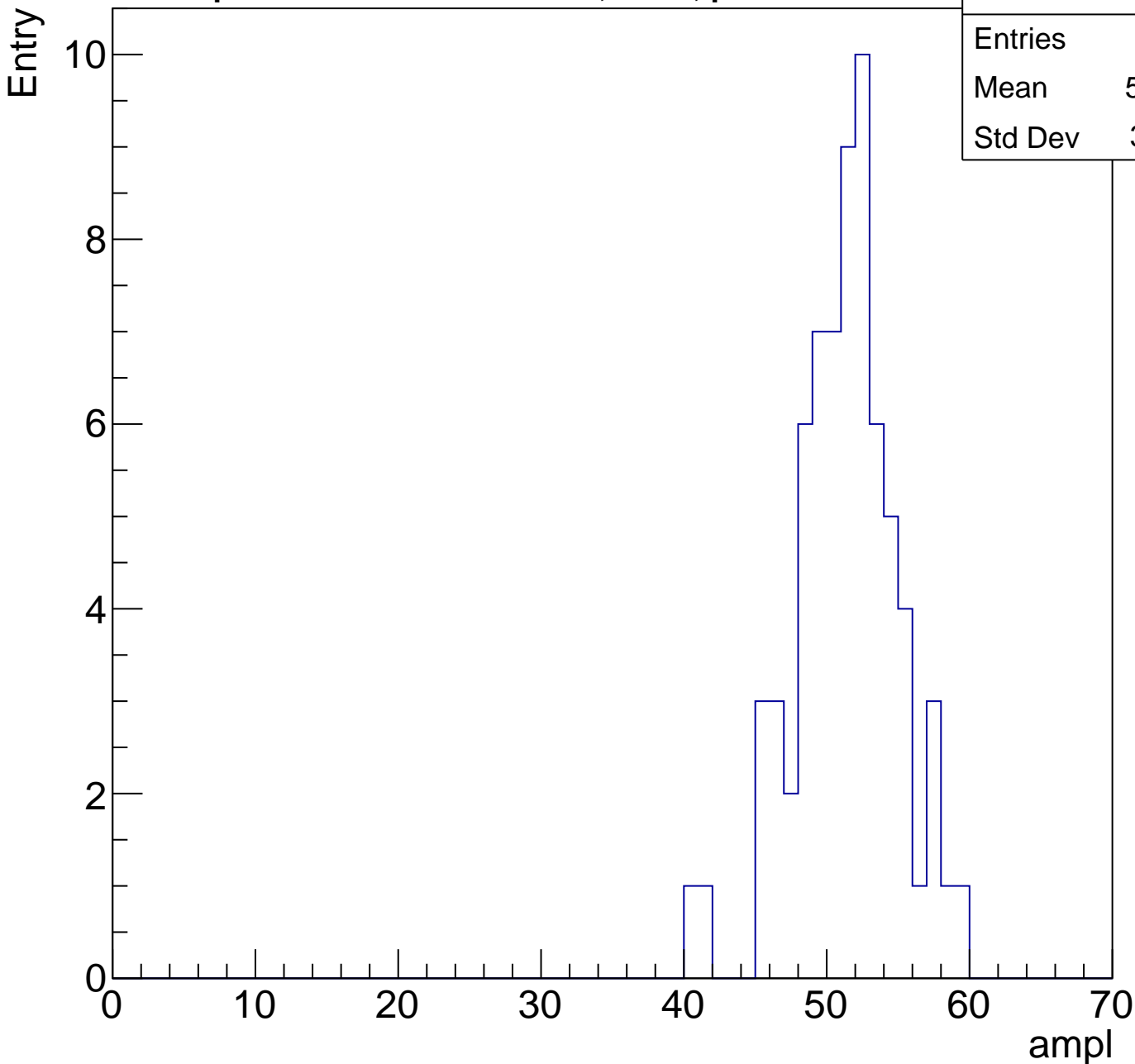
Gaus Width: 3.0194



B1L100S, U12-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	50.86
Std Dev	3.611

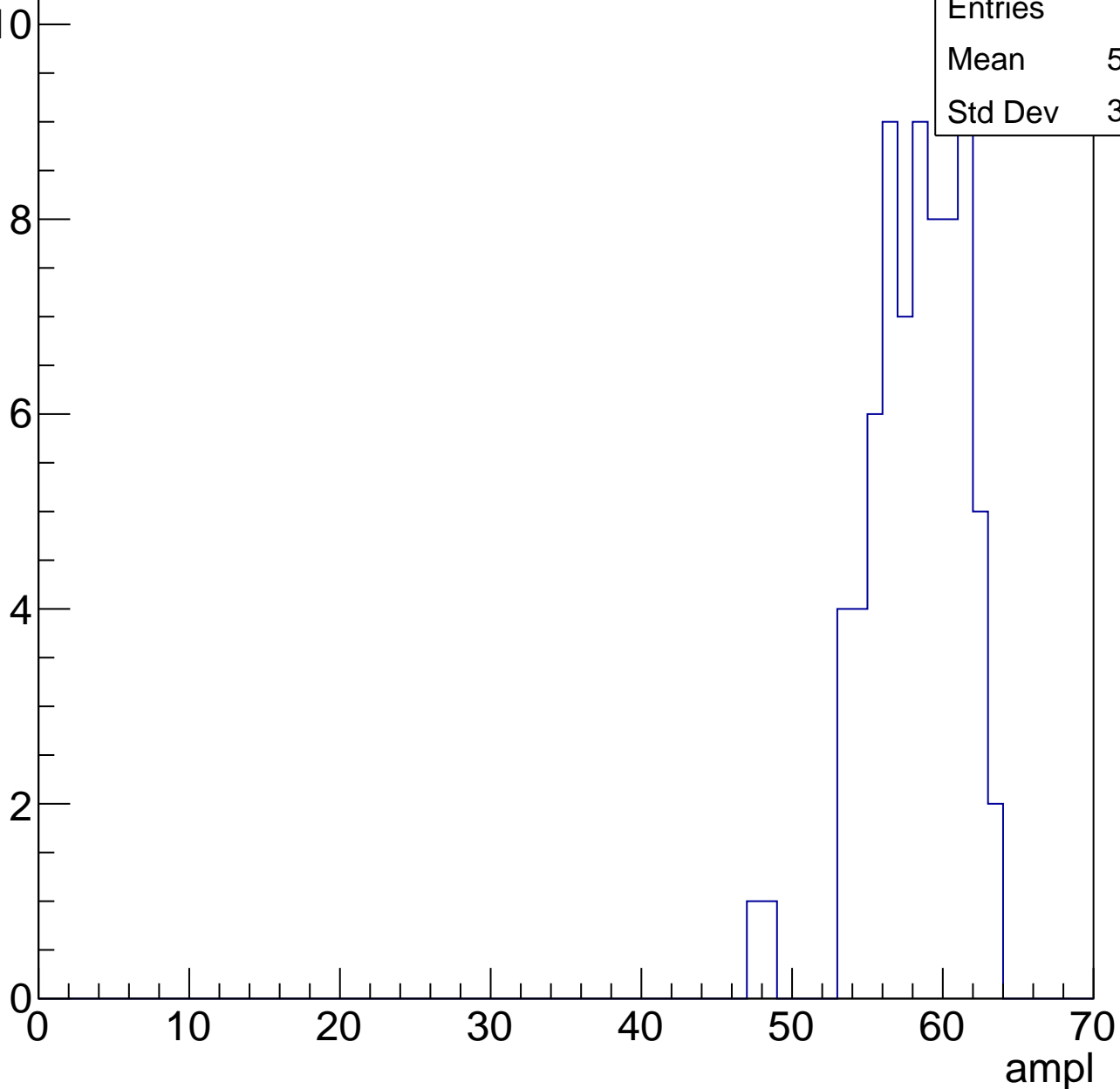


B1L100S, U12-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	57.78
Std Dev	3.159

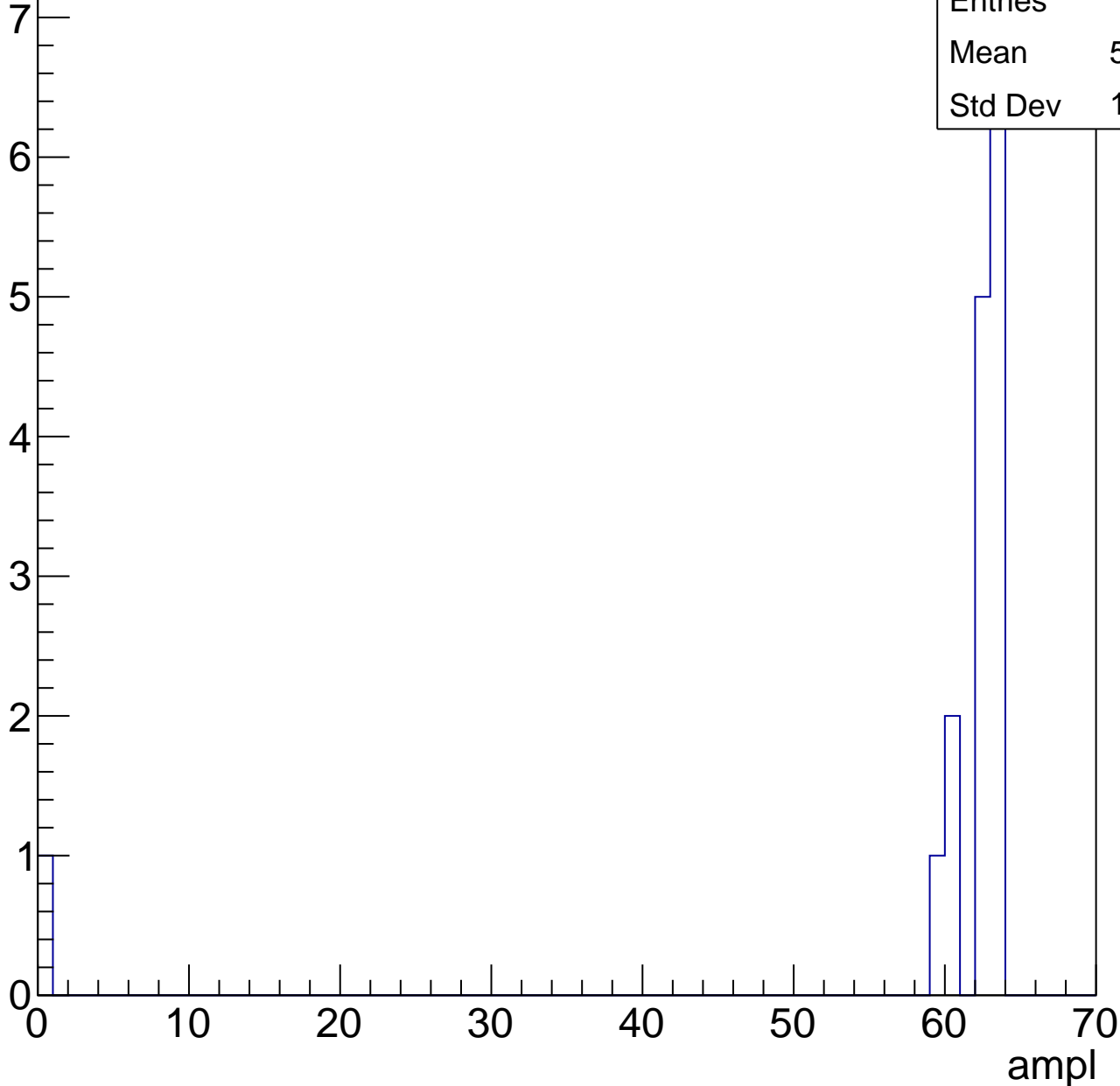


B1L100S, U12-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	58.12
Std Dev	15.06



B1L100S, U12-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch7, adc0

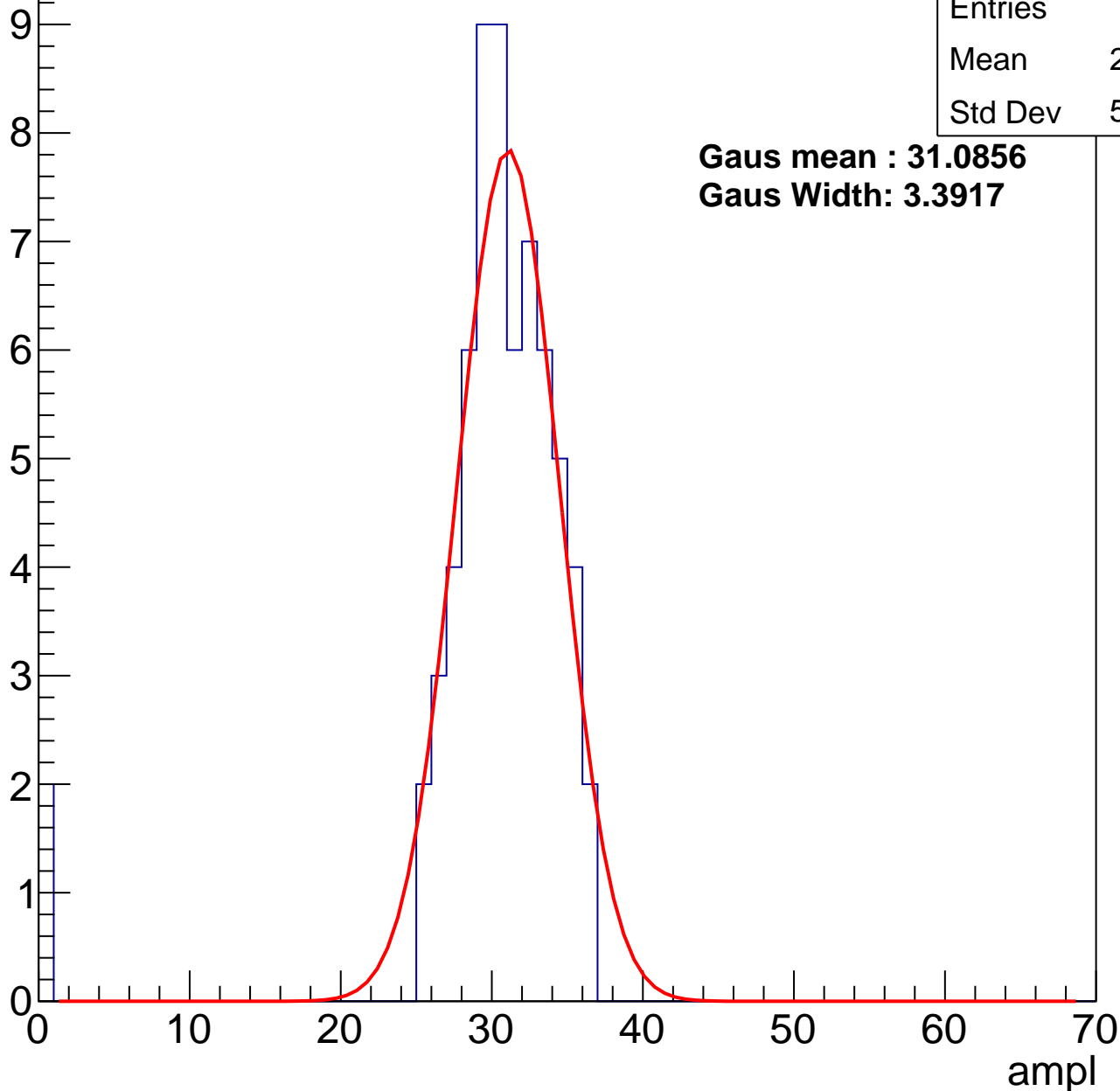
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.62
Std Dev	5.945

Gaus mean : 31.0856

Gaus Width: 3.3917



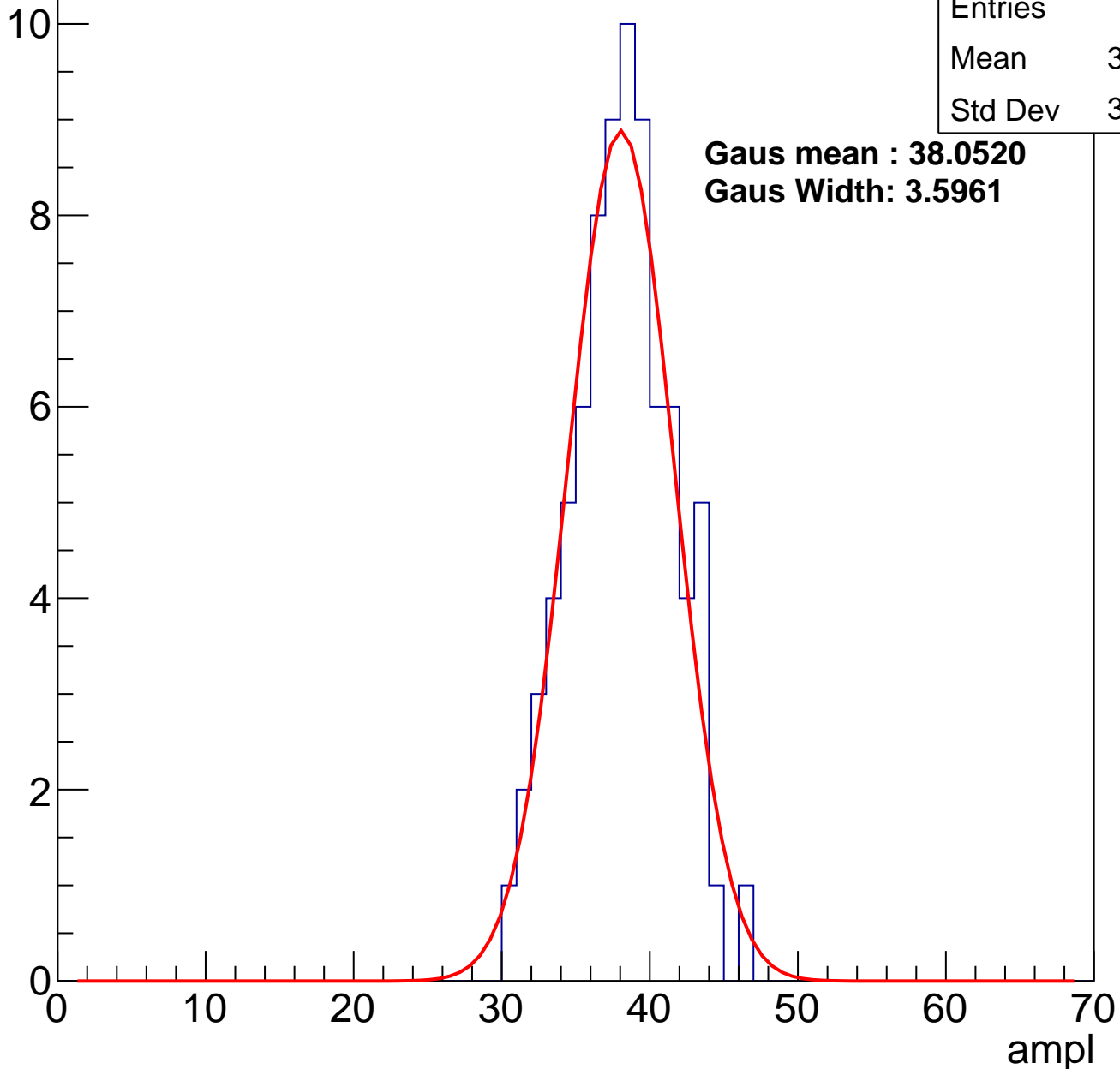
B1L100S, U12-ch7, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	37.64
Std Dev	3.374

Gaus mean : 38.0520
Gaus Width: 3.5961

Entry



B1L100S, U12-ch7, adc2

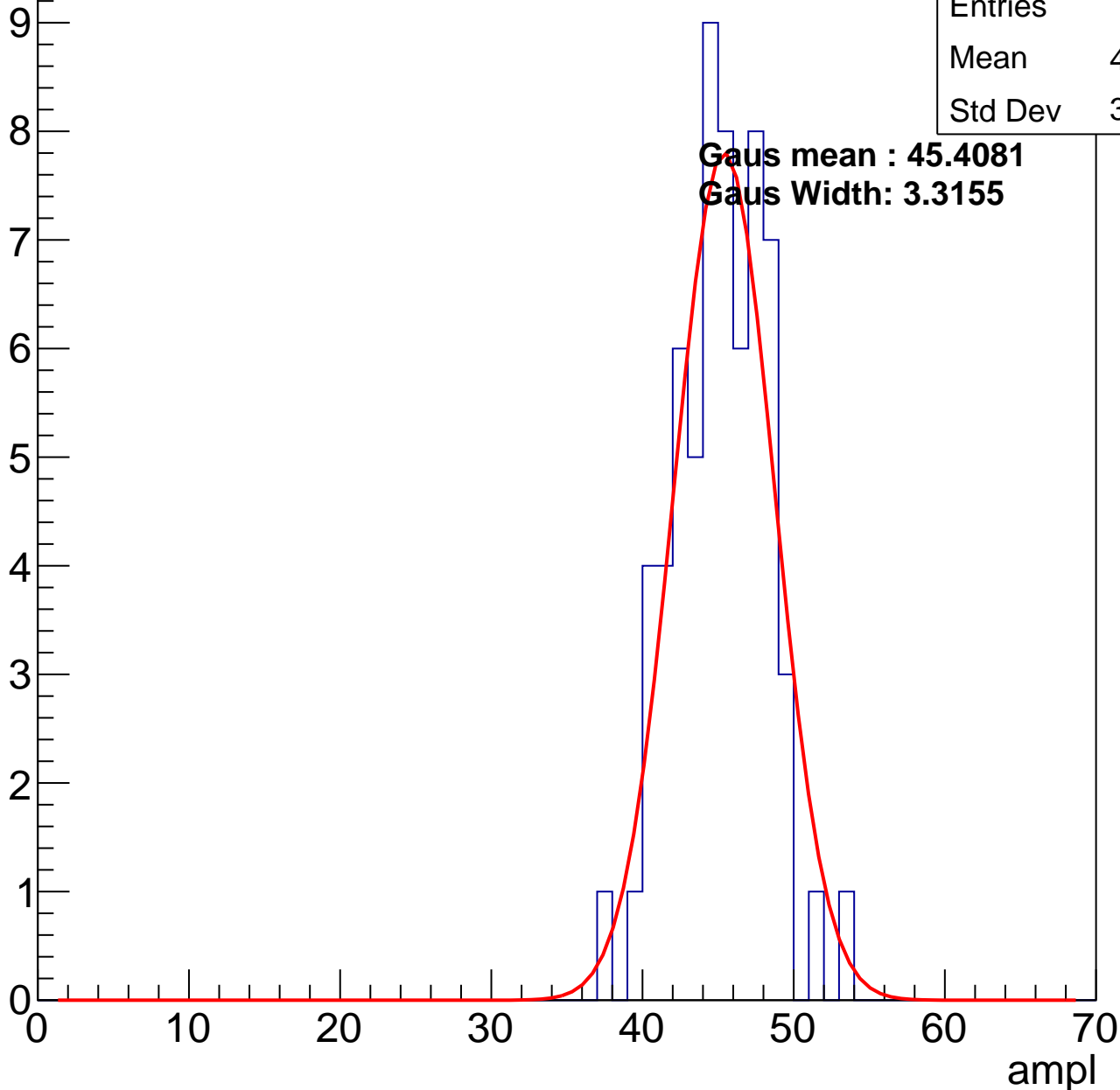
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.72
Std Dev	3.039

Gaus mean : 45.4081

Gaus Width: 3.3155

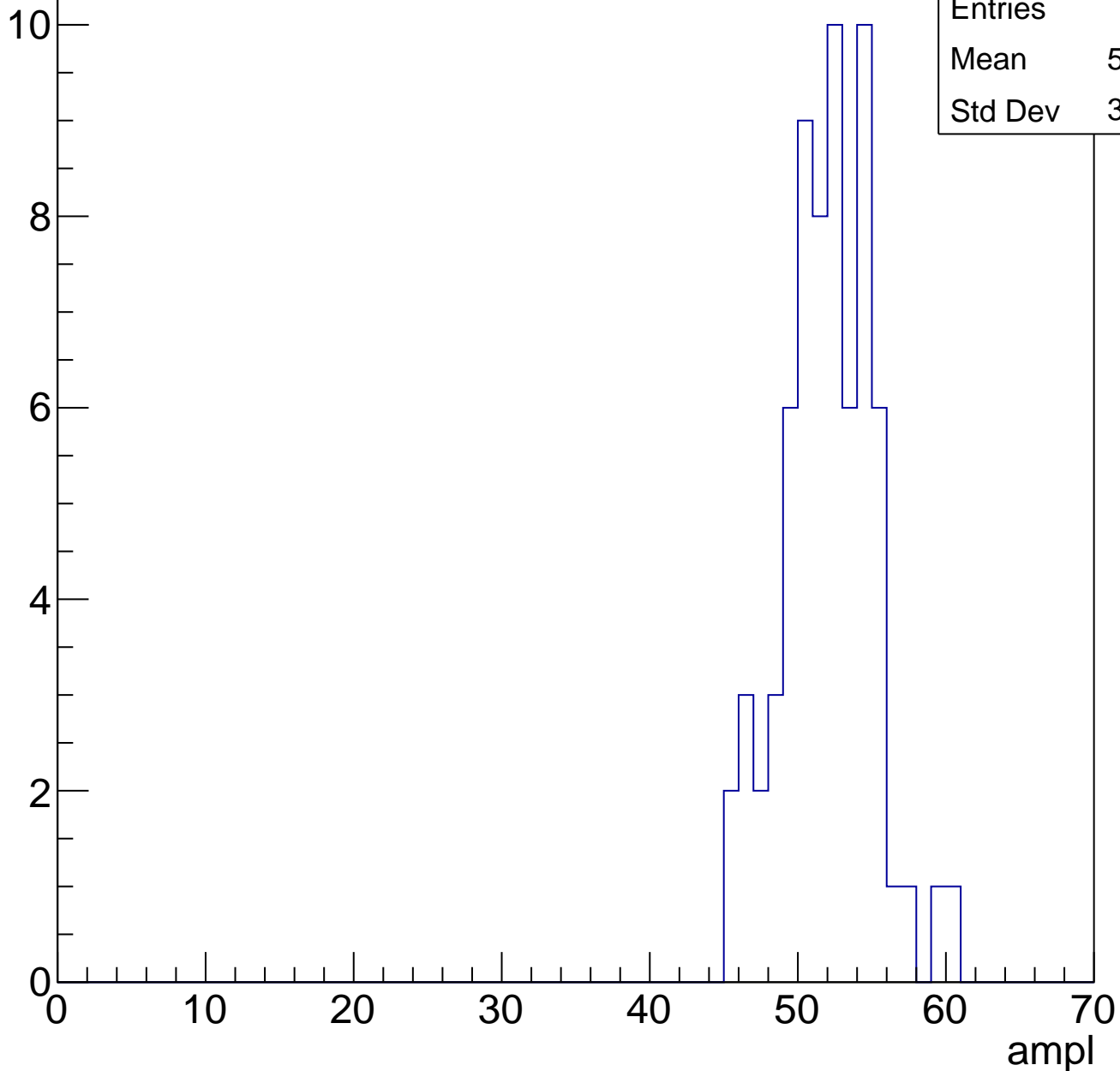


B1L100S, U12-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	51.57
Std Dev	3.053

Entry

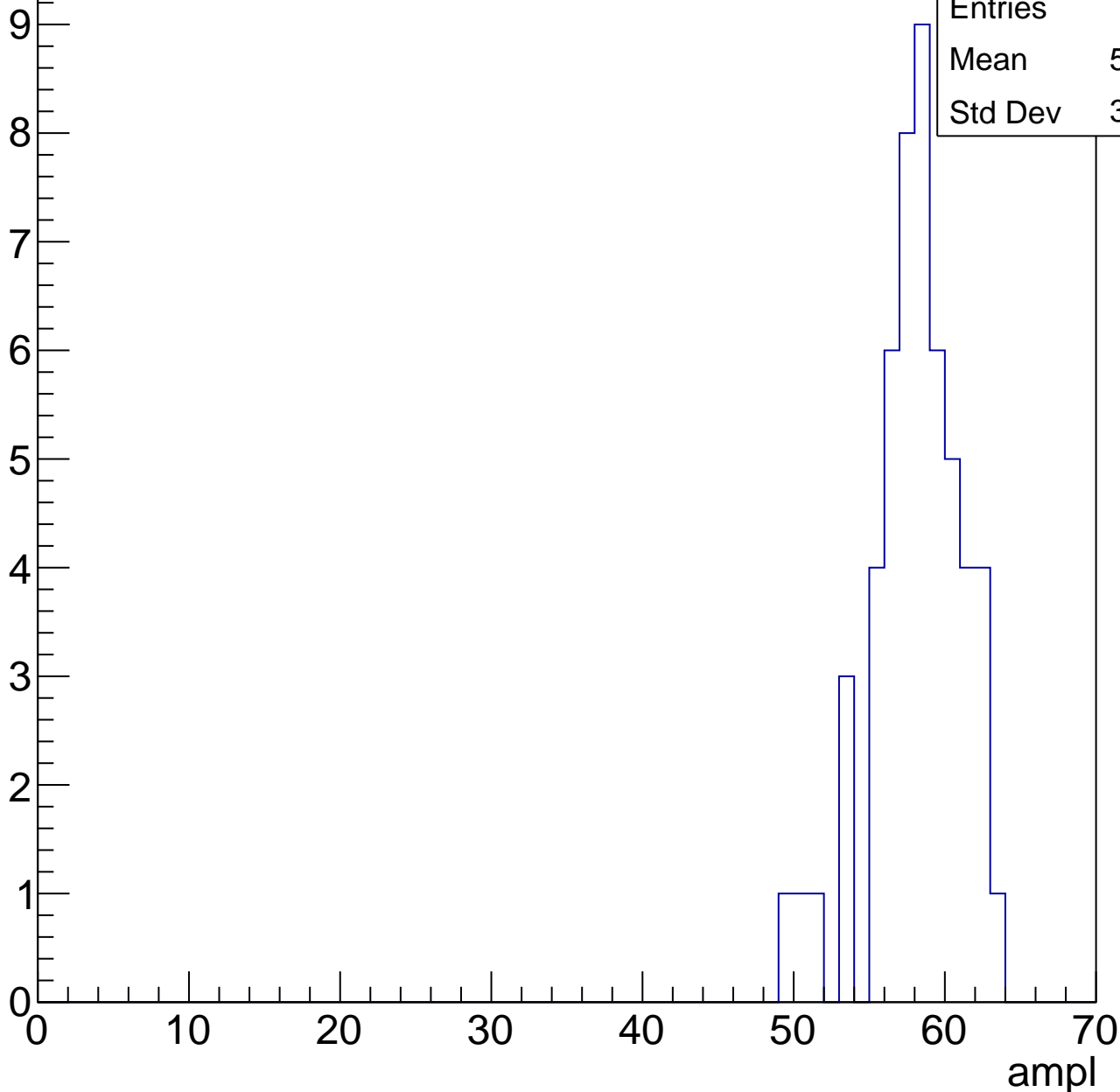


B1L100S, U12-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.58
Std Dev	3.006

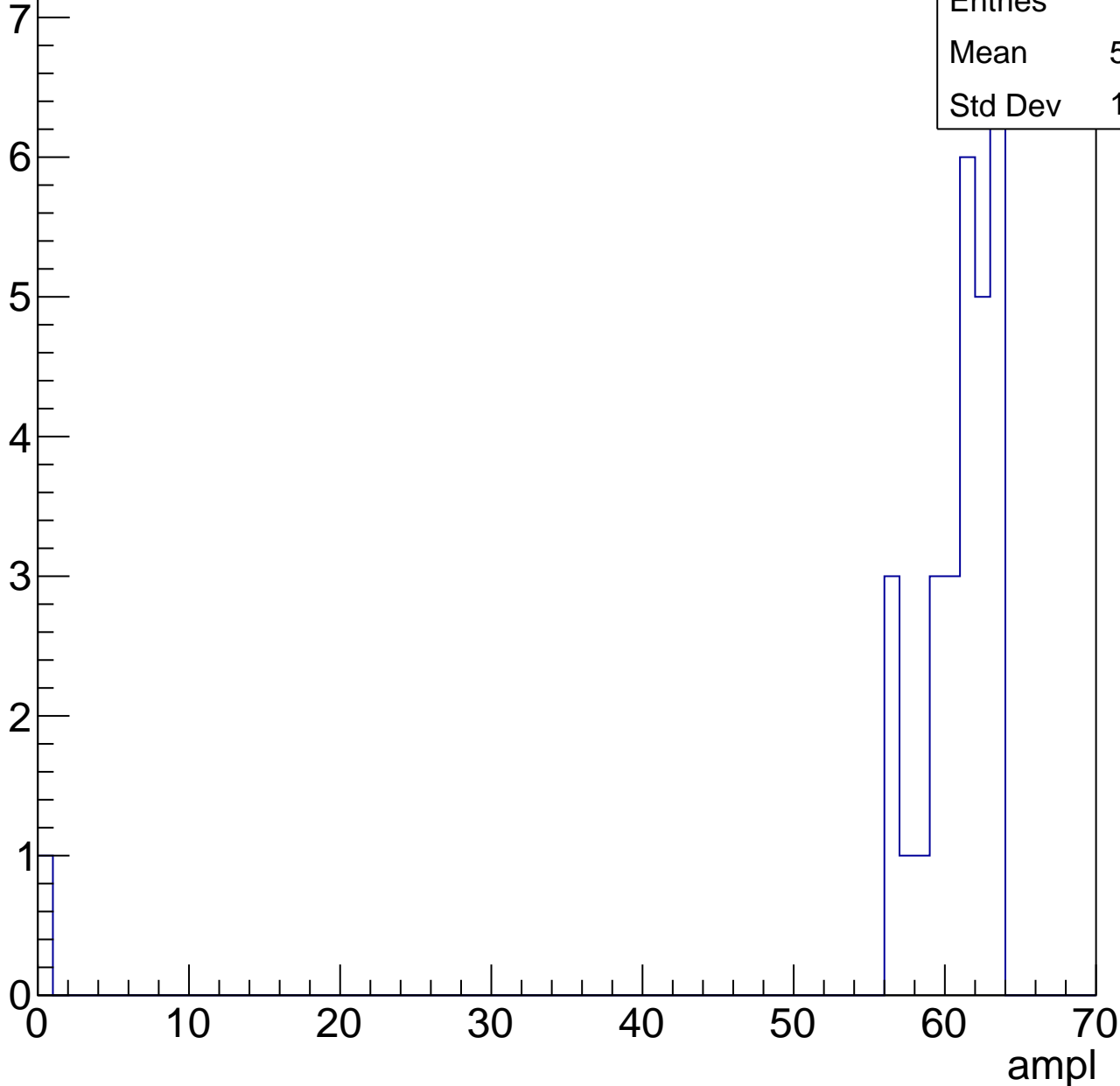


B1L100S, U12-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.57
Std Dev	11.09



B1L100S, U12-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch8, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	29.75
Std Dev	4.814

Gaus mean : 30.5620

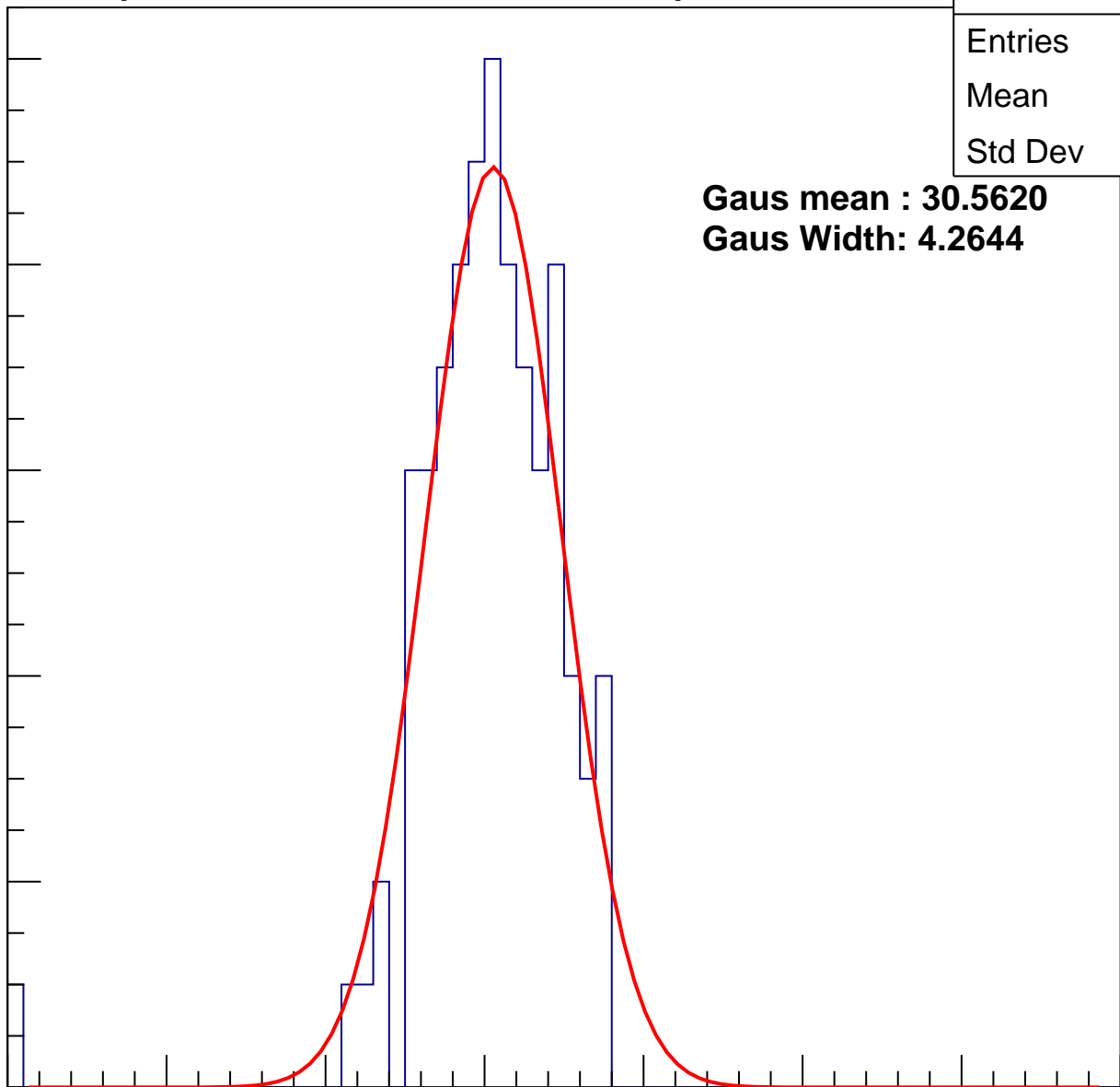
Gaus Width: 4.2644

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch8, adc1

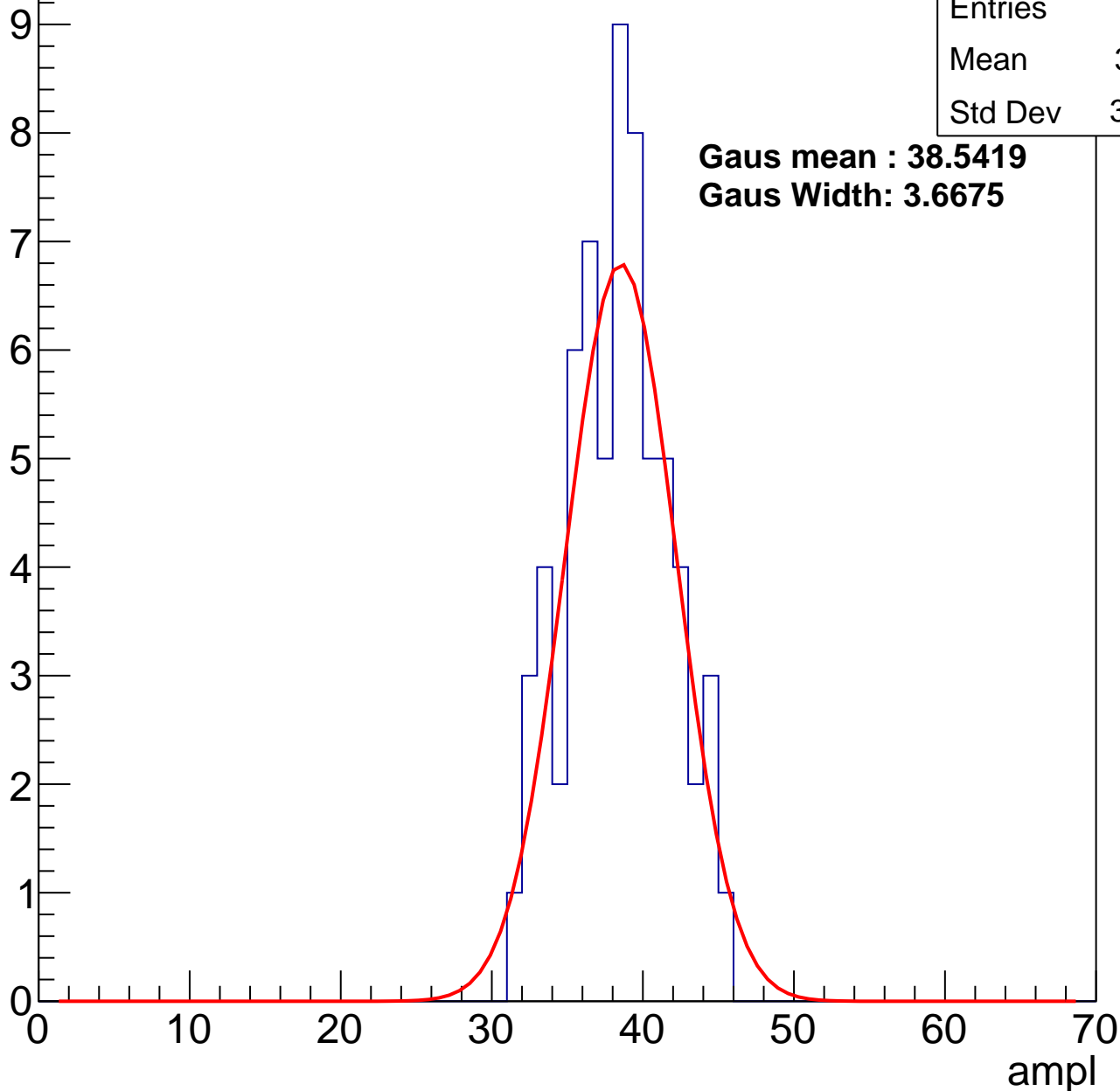
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.91
Std Dev	3.327

Gaus mean : 38.5419

Gaus Width: 3.6675



B1L100S, U12-ch8, adc2

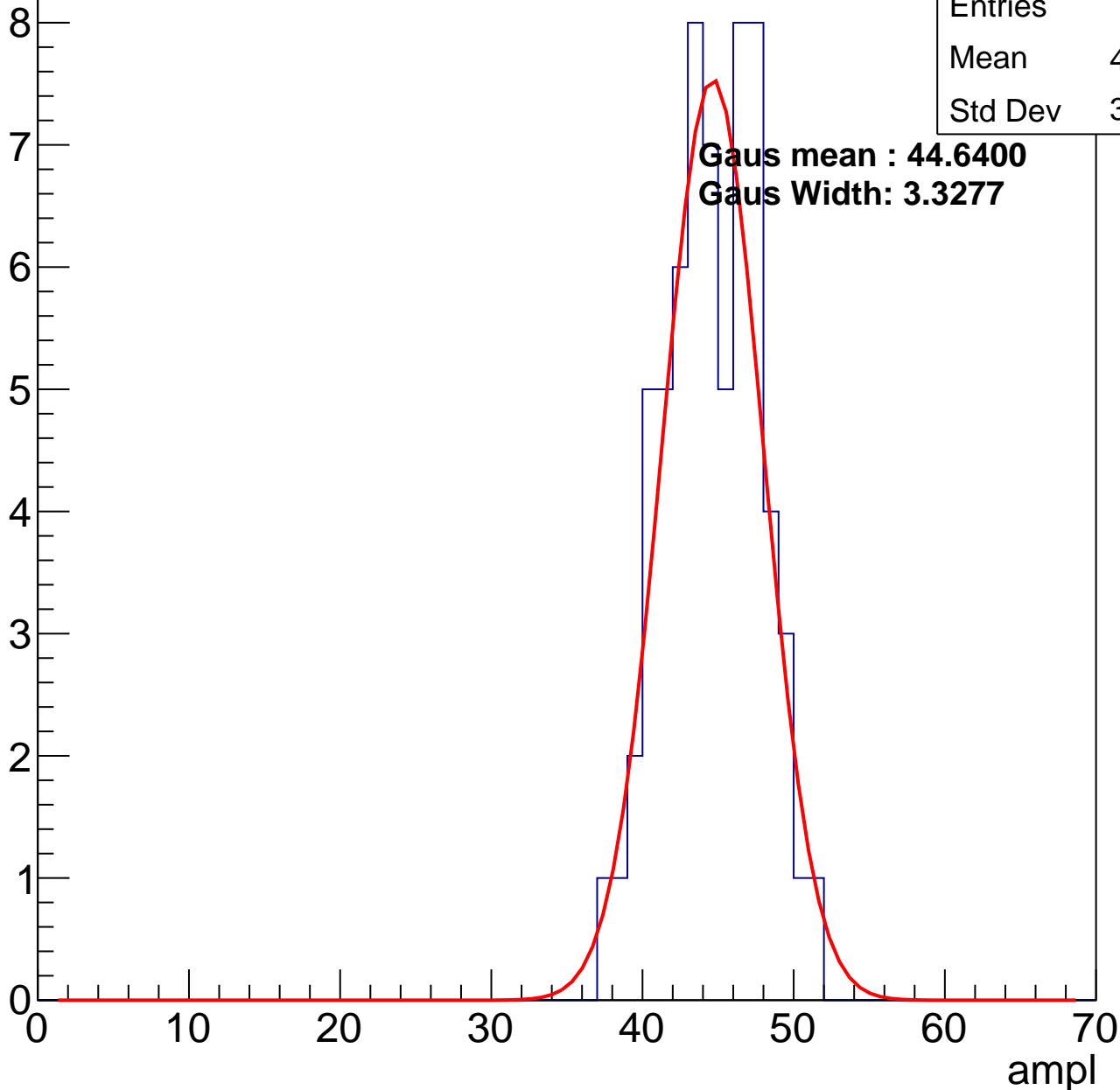
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	44.17
Std Dev	3.096

Gaus mean : 44.6400

Gaus Width: 3.3277

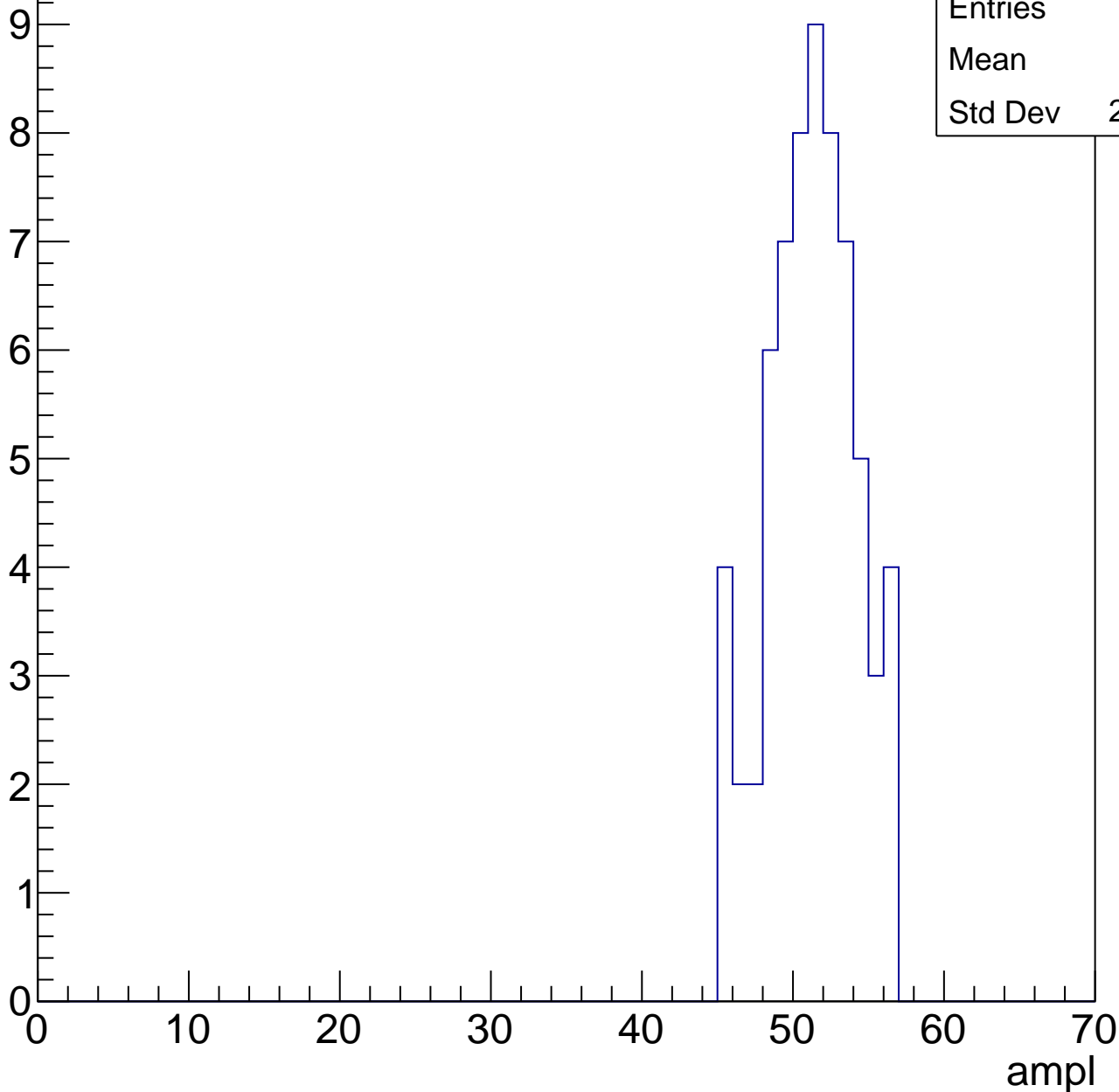


B1L100S, U12-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.8
Std Dev	2.889

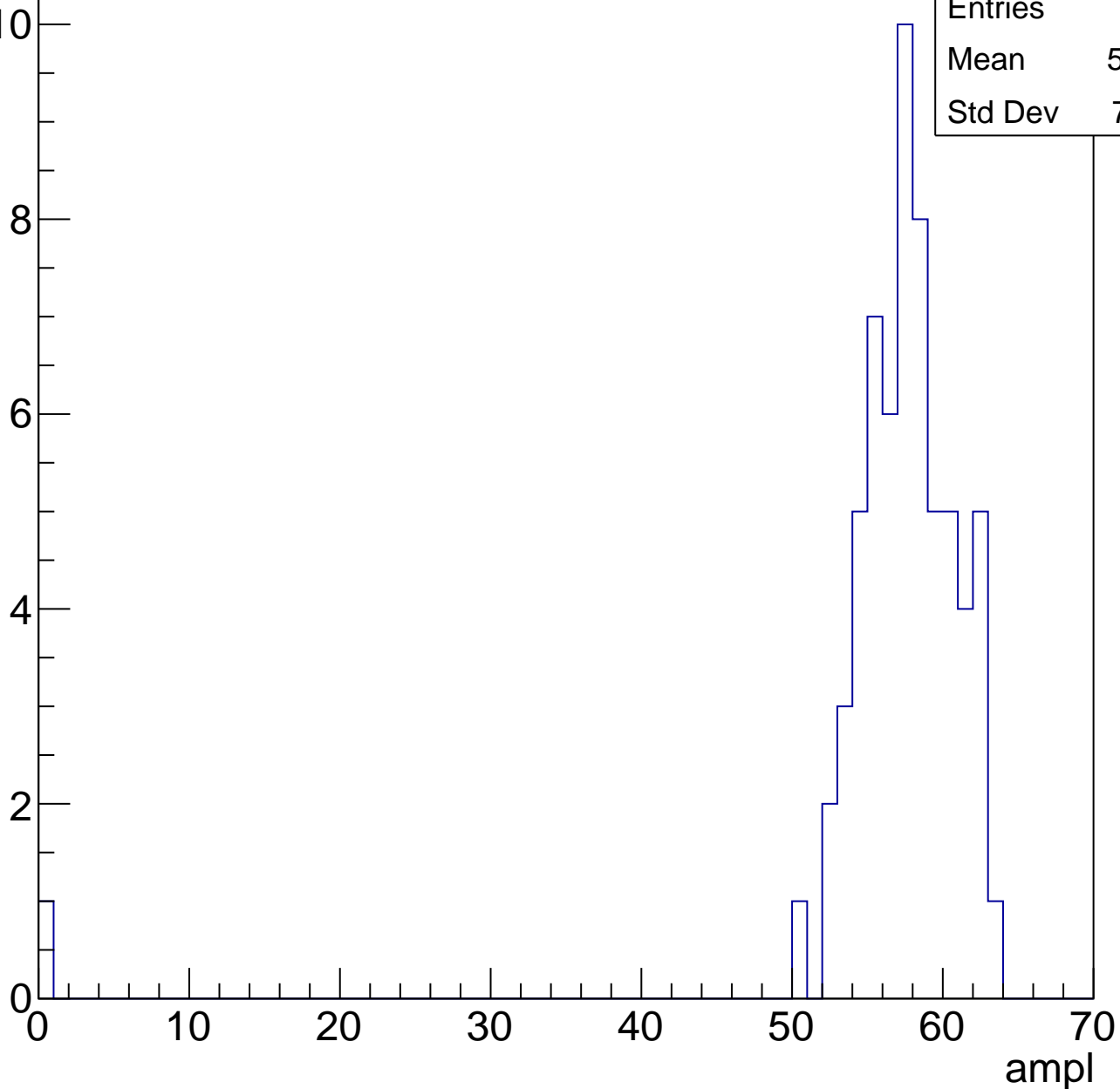


B1L100S, U12-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

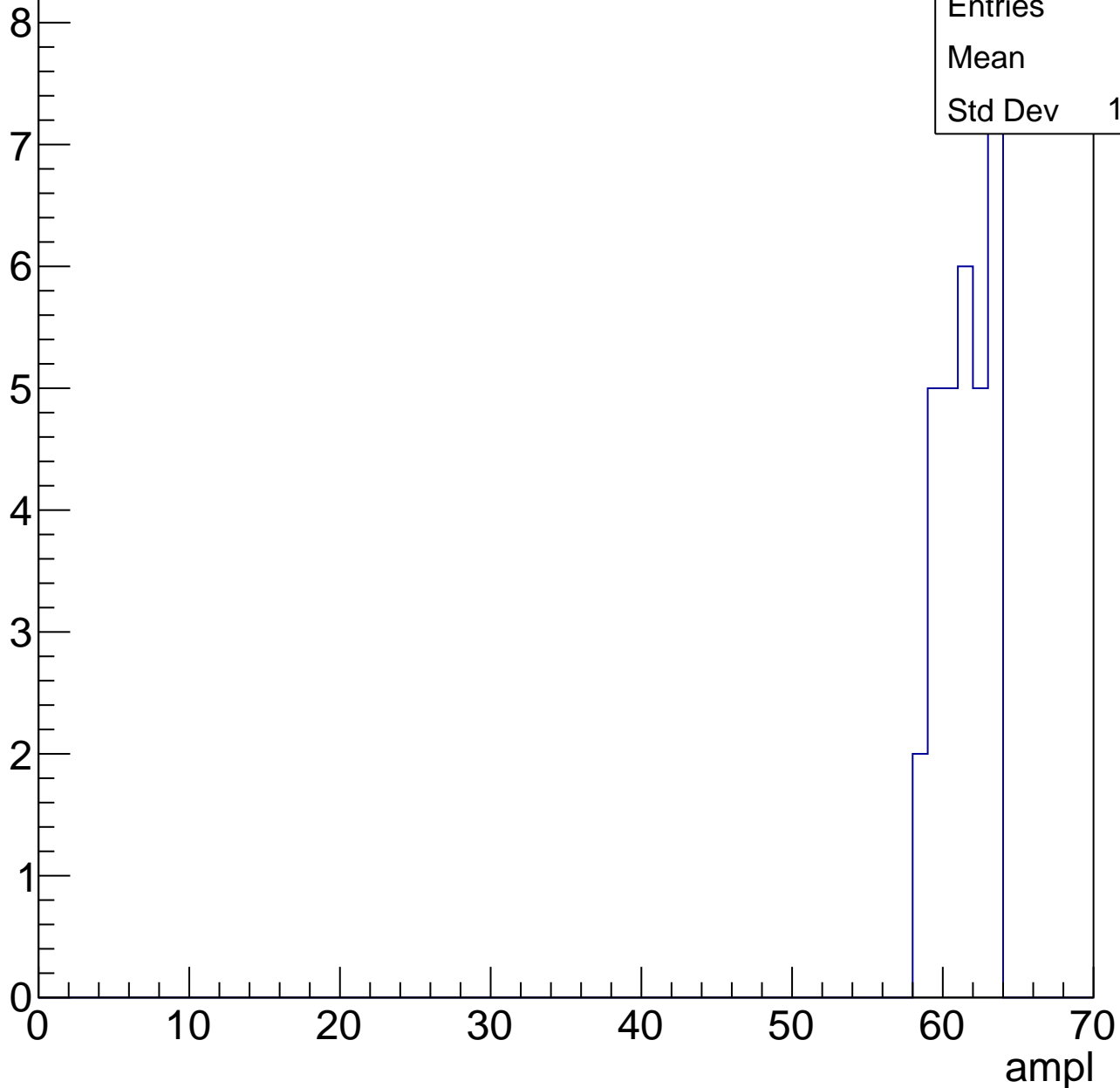
Entries	63
Mean	56.35
Std Dev	7.711



B1L100S, U12-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	31
Mean	61
Std Dev	1.606

B1L100S, U12-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch9, adc0

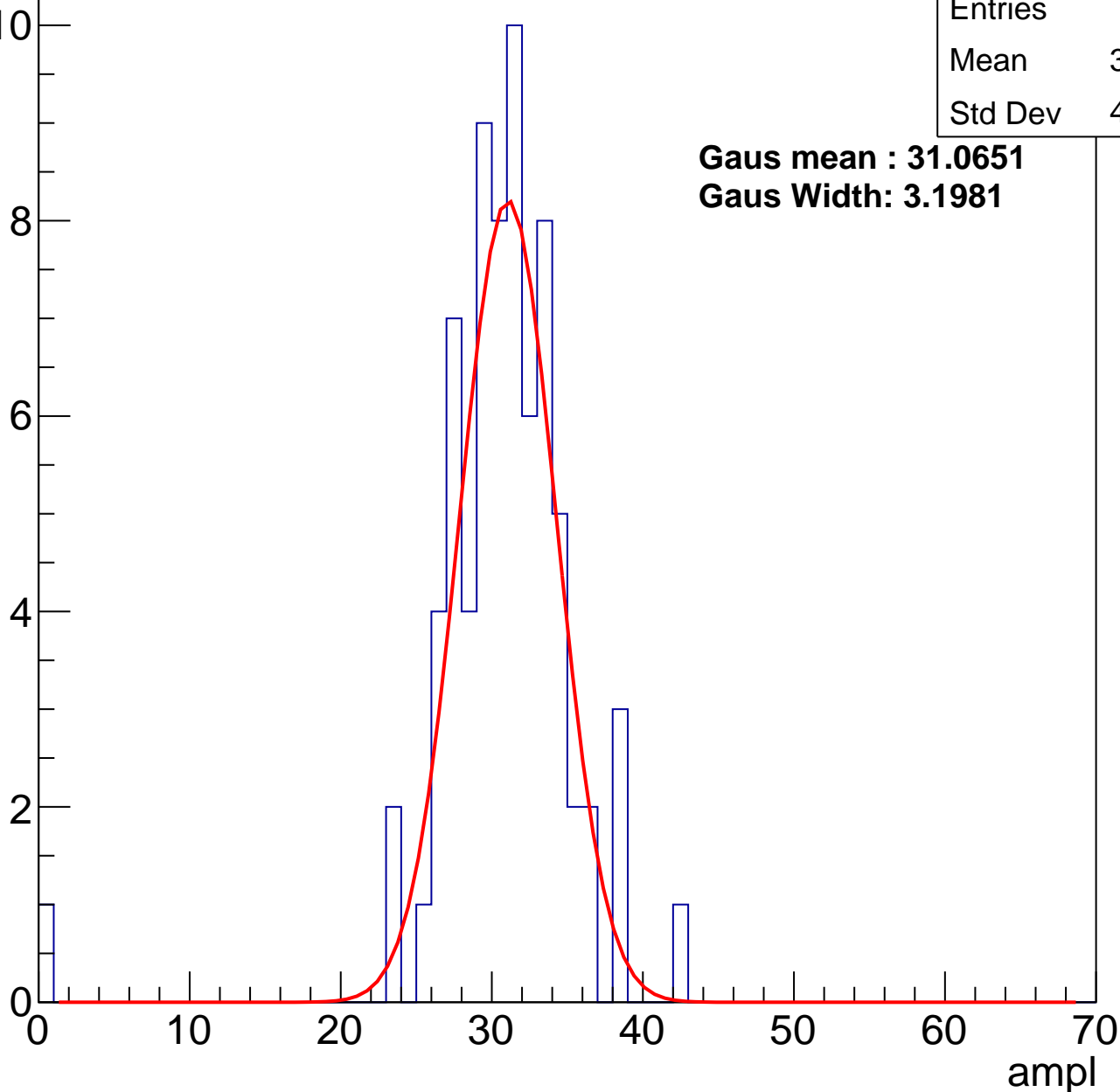
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.29
Std Dev	4.983

Gaus mean : 31.0651

Gaus Width: 3.1981



B1L100S, U12-ch9, adc1

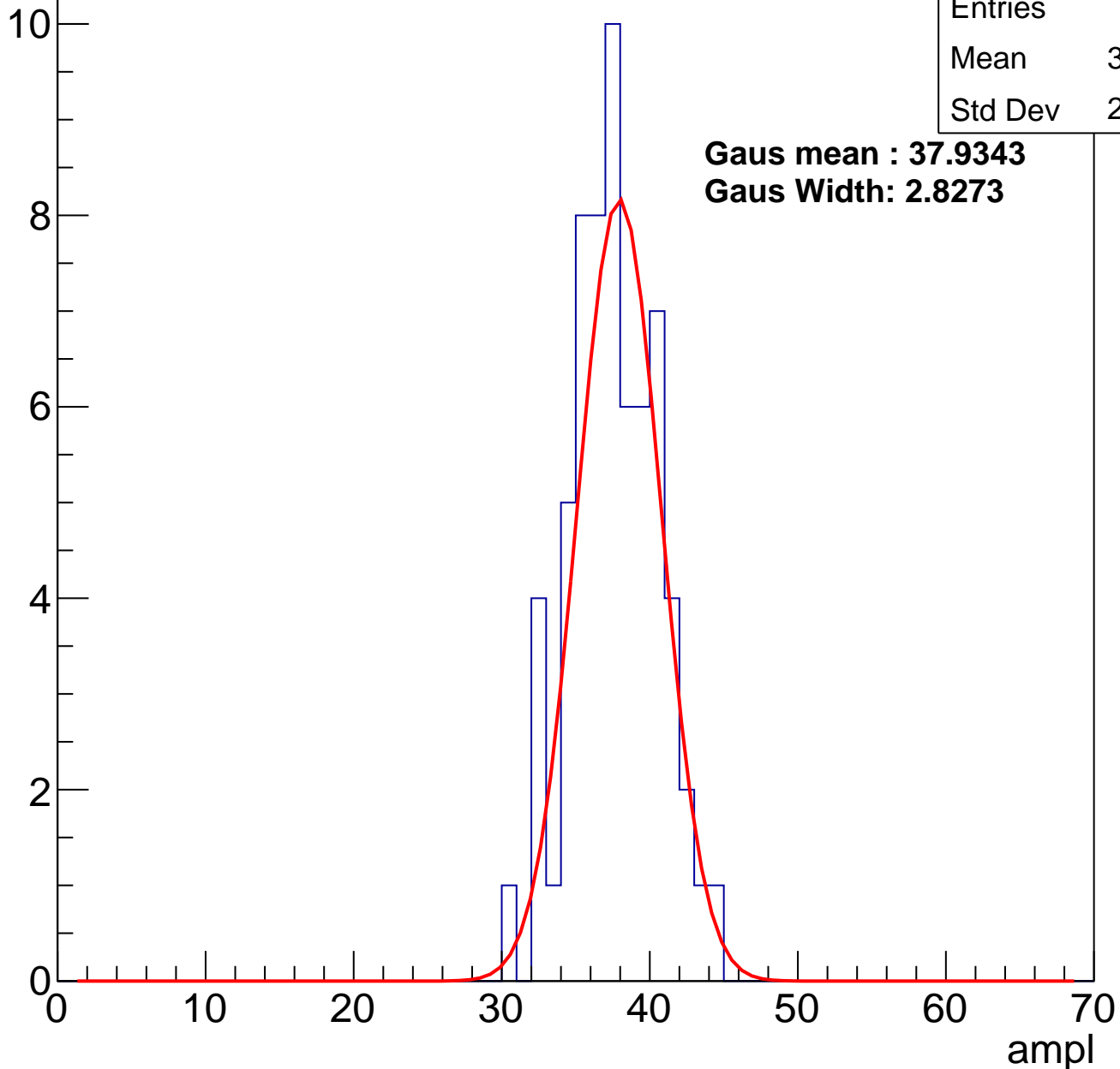
calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	37.12
Std Dev	2.907

Gaus mean : 37.9343

Gaus Width: 2.8273

Entry



B1L100S, U12-ch9, adc2

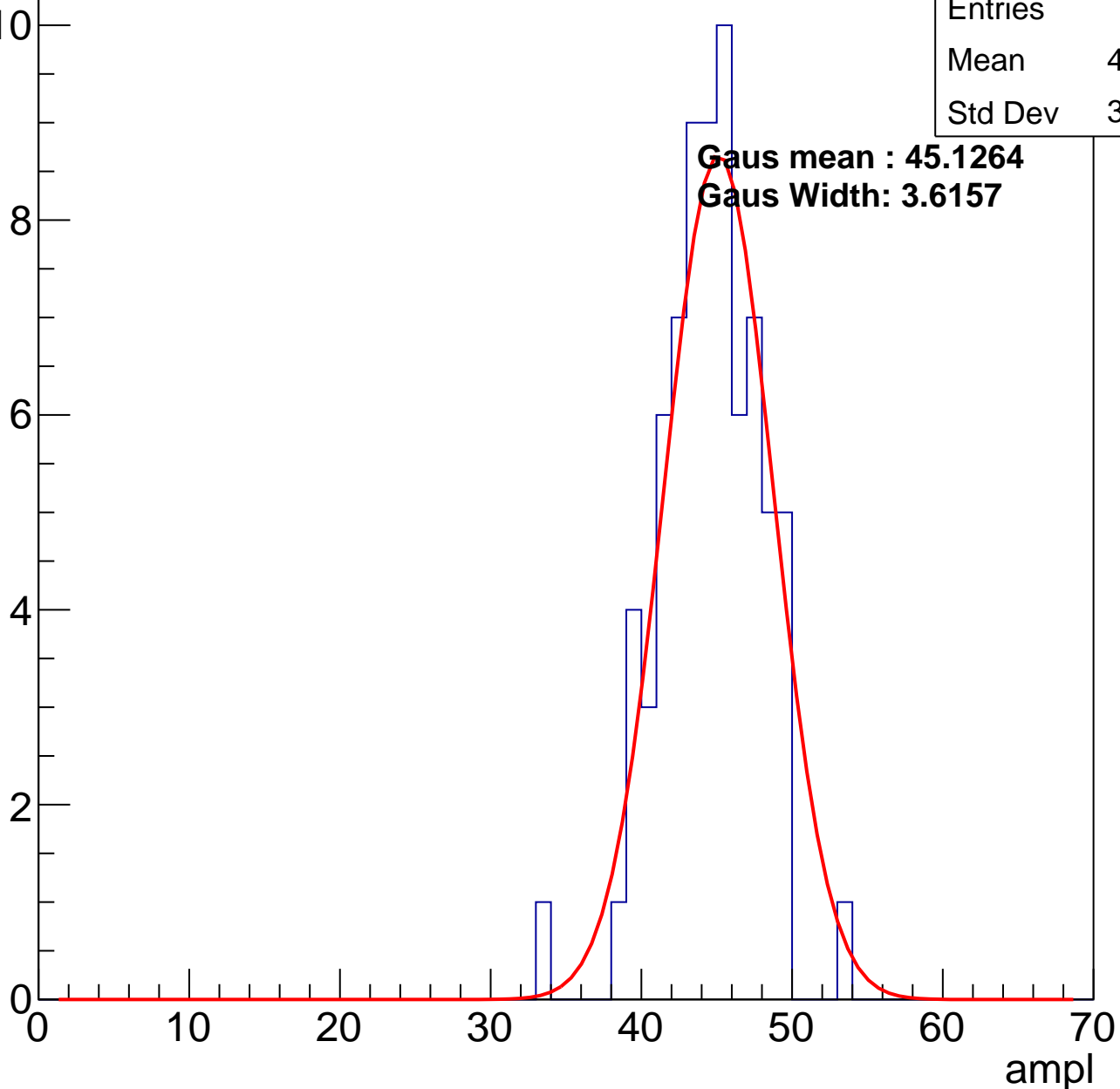
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	44.09
Std Dev	3.243

Gaus mean : 45.1264

Gaus Width: 3.6157

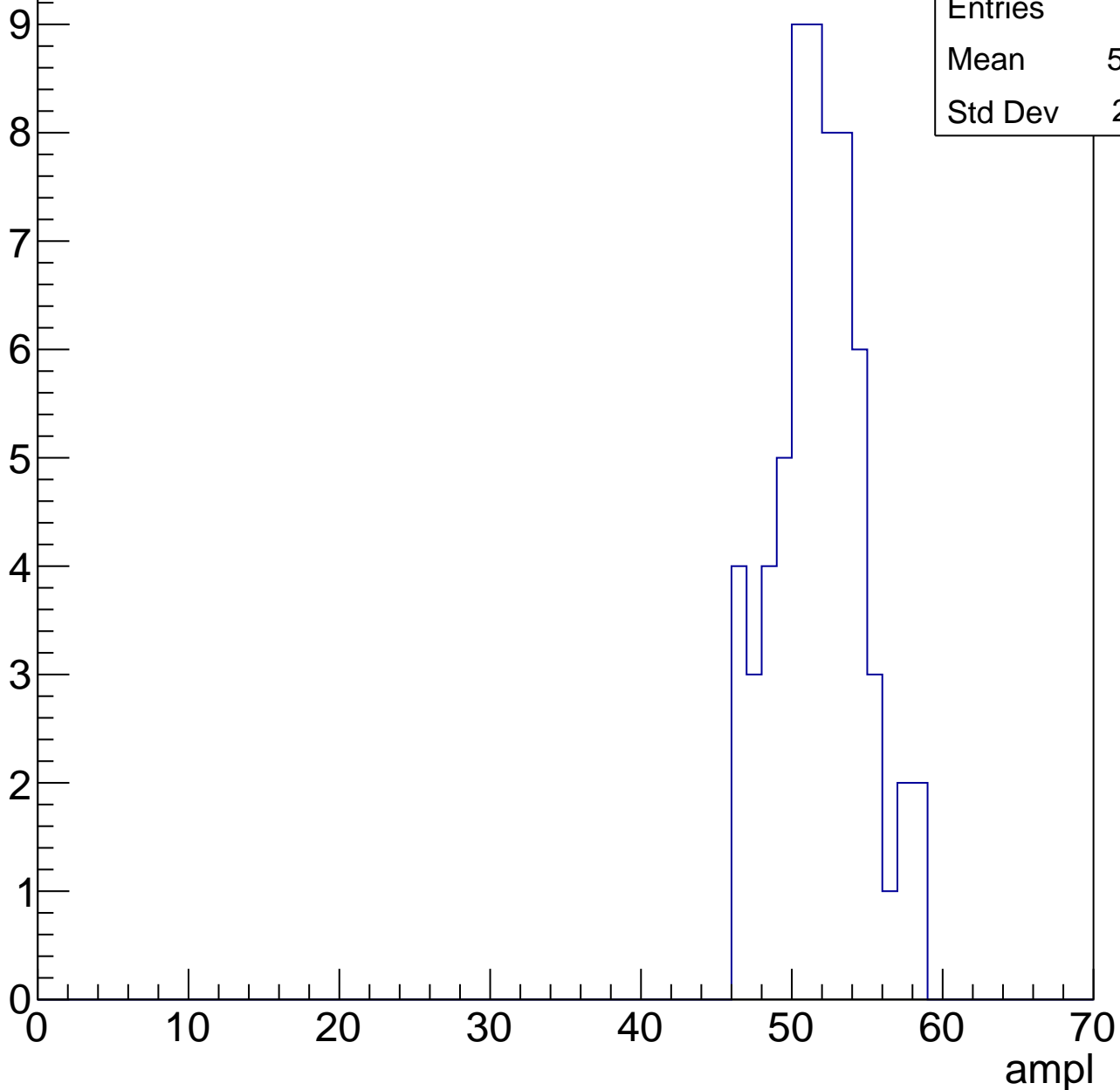


B1L100S, U12-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	51.34
Std Dev	2.911

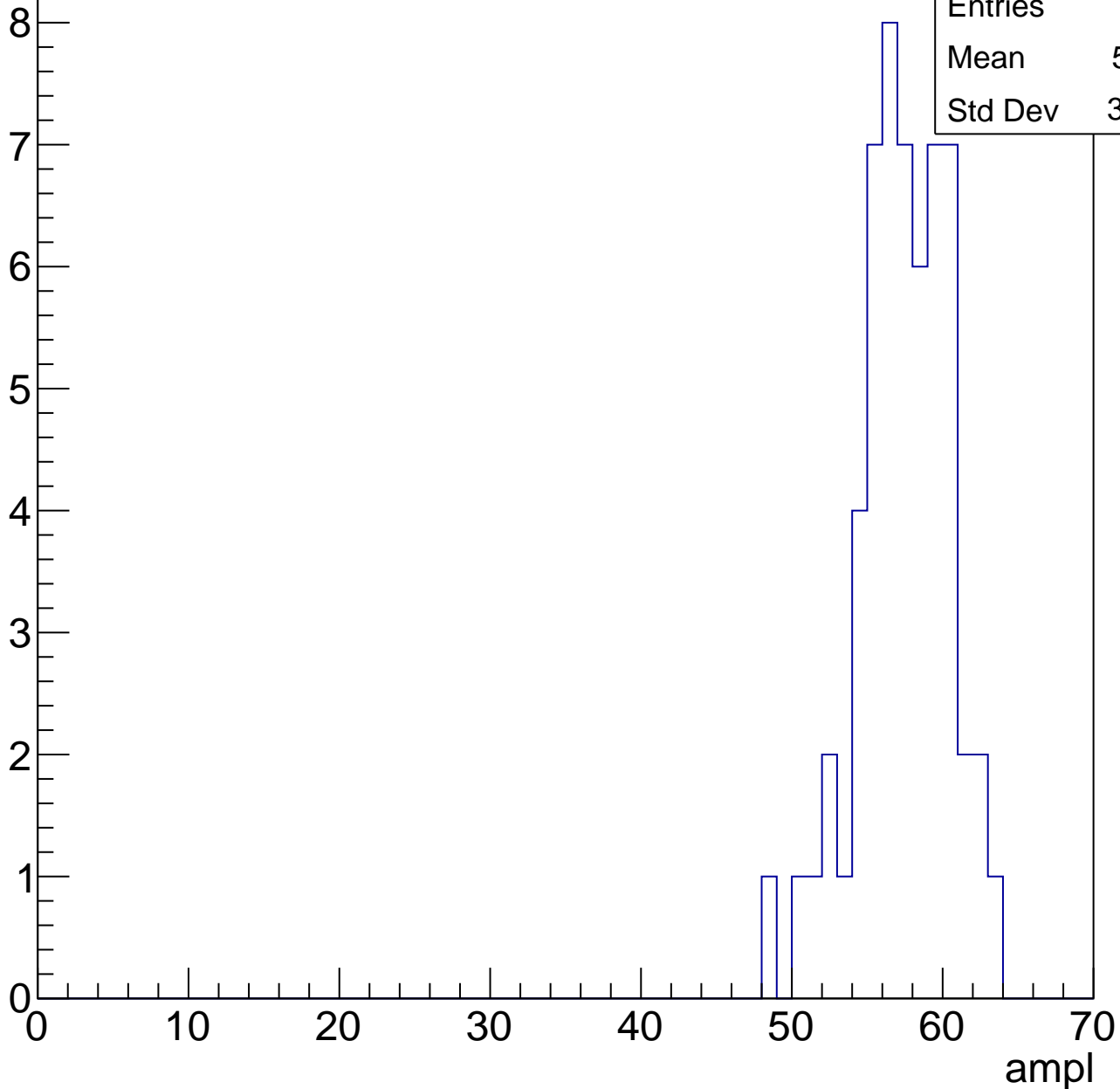


B1L100S, U12-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

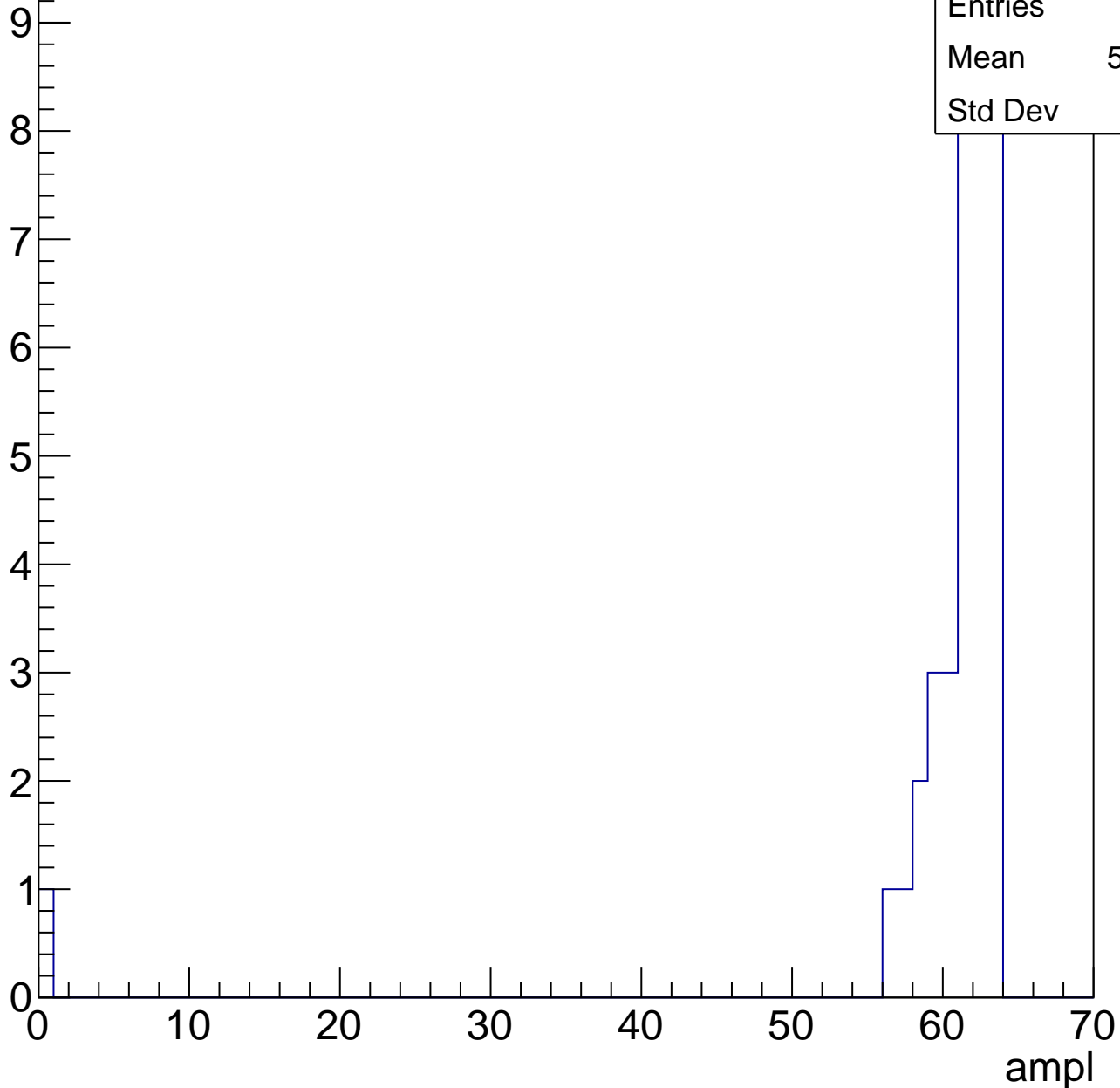
Entries	57
Mean	56.91
Std Dev	3.016



B1L100S, U12-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch10, adc0

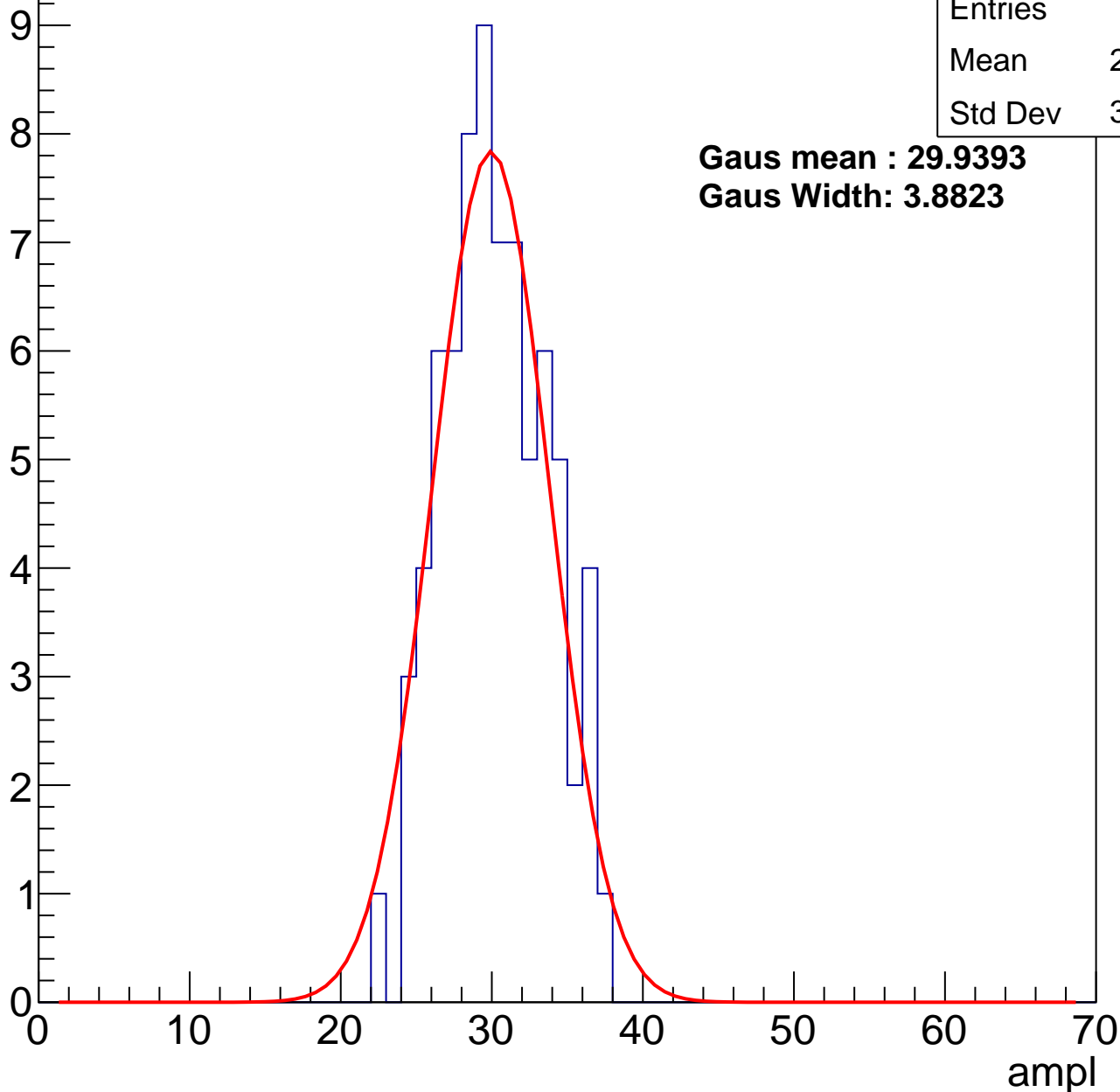
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.77
Std Dev	3.415

Gaus mean : 29.9393

Gaus Width: 3.8823



B1L100S, U12-ch10, adc1

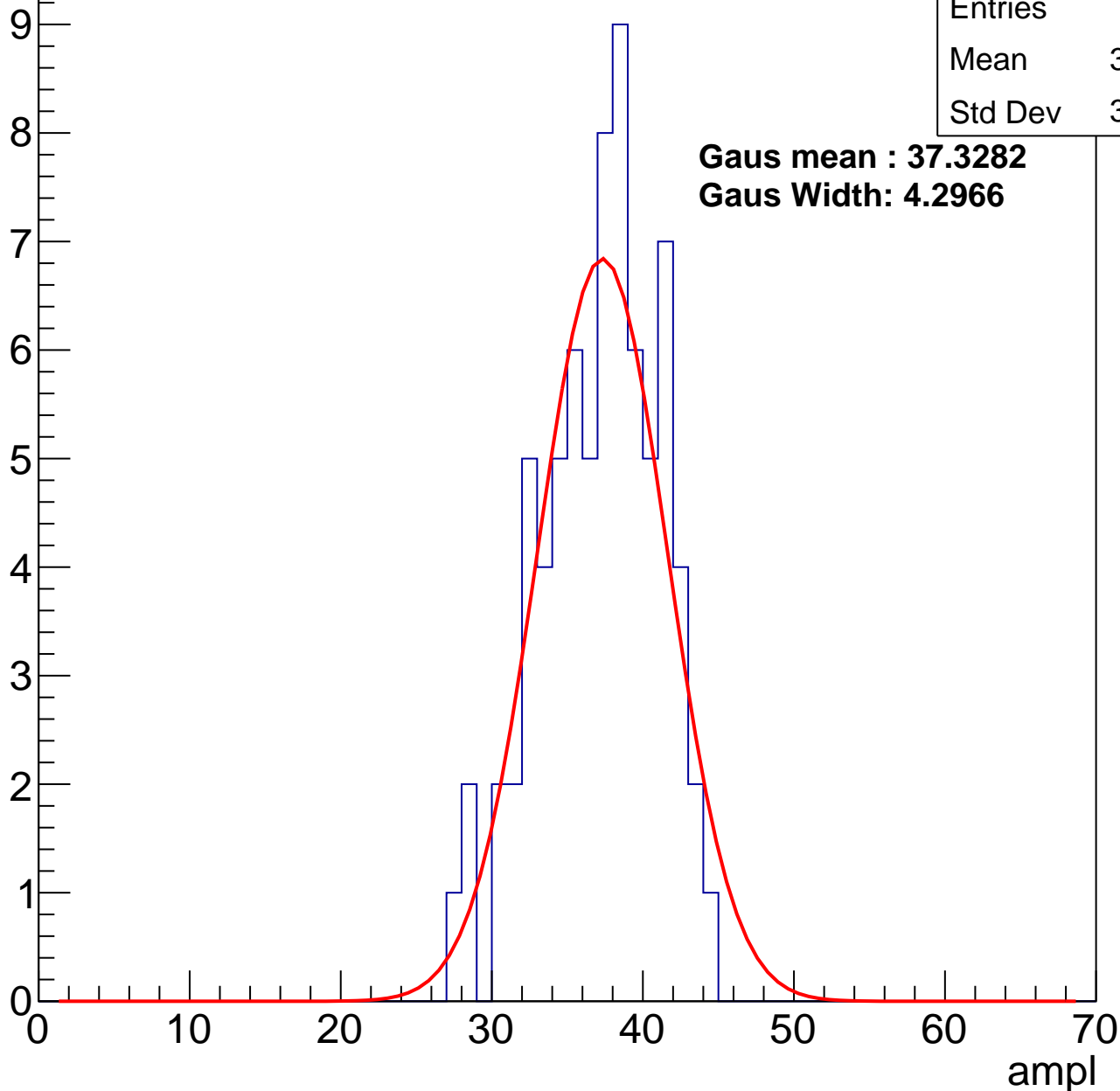
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.68
Std Dev	3.852

Gaus mean : 37.3282

Gaus Width: 4.2966



B1L100S, U12-ch10, adc2

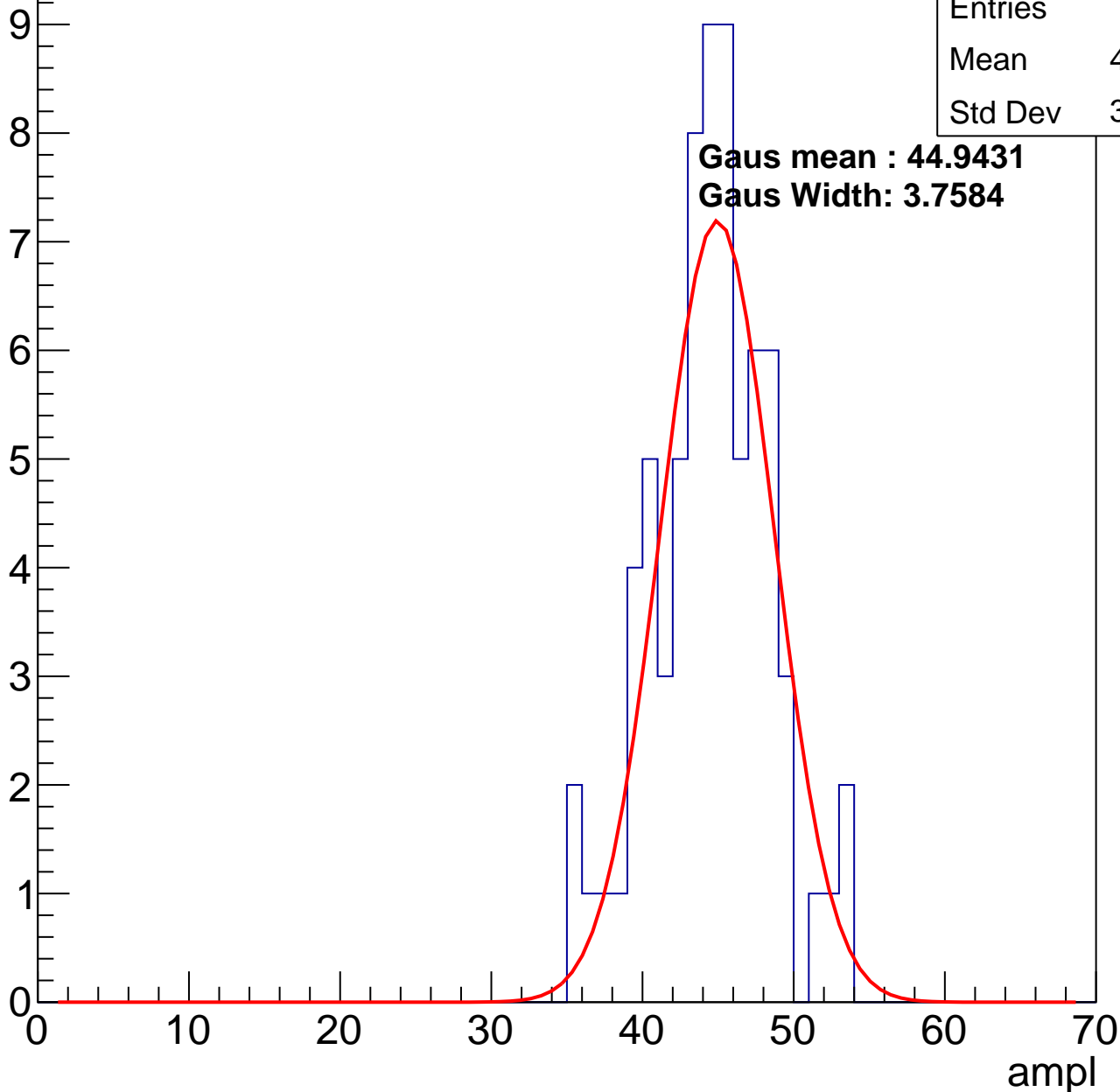
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.04
Std Dev	3.864

Gaus mean : 44.9431

Gaus Width: 3.7584



B1L100S, U12-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	51.06
Std Dev	3.53

Entry

10

8

6

4

2

0

0

10

20

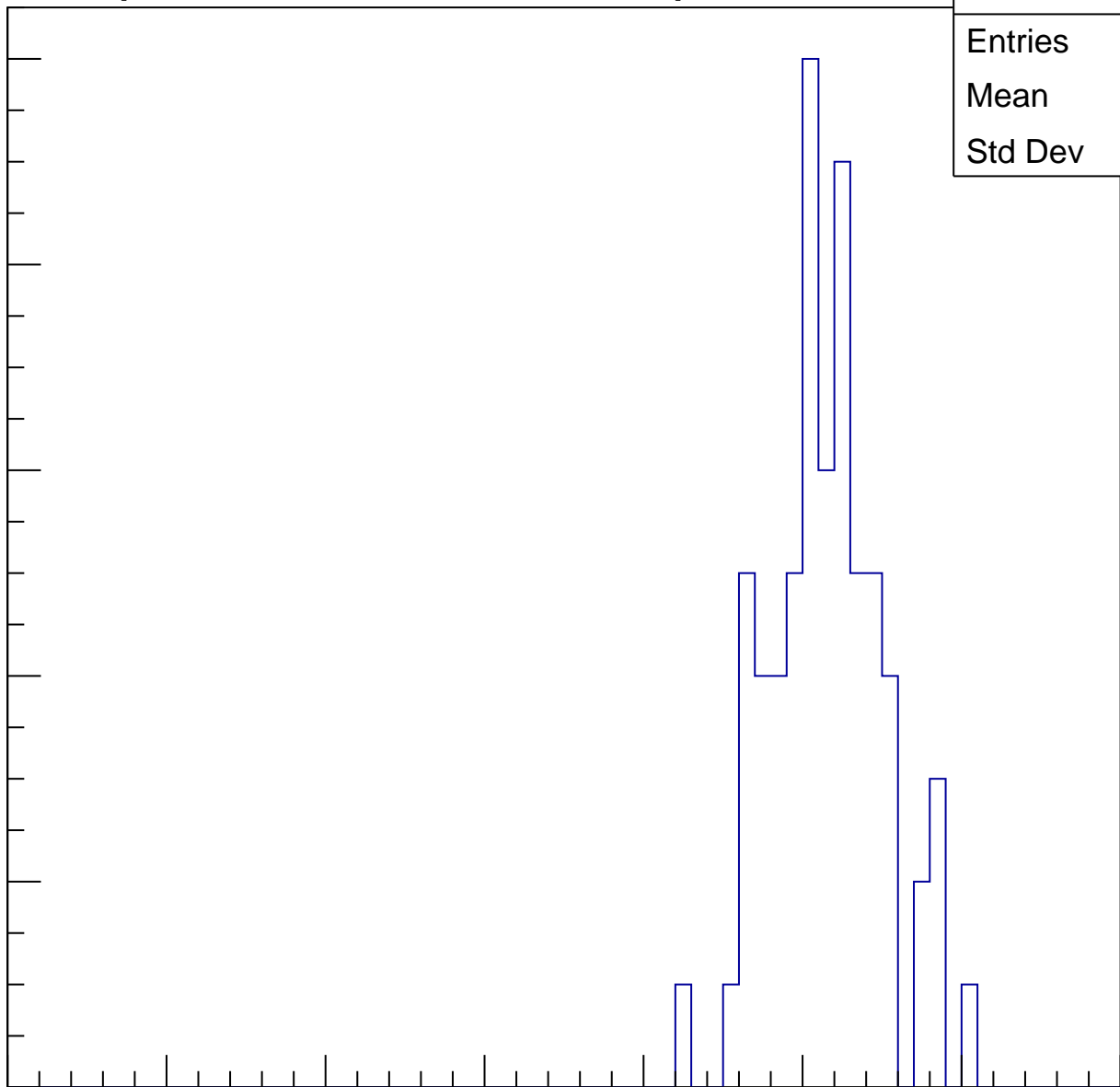
30

40

50

60

ampl

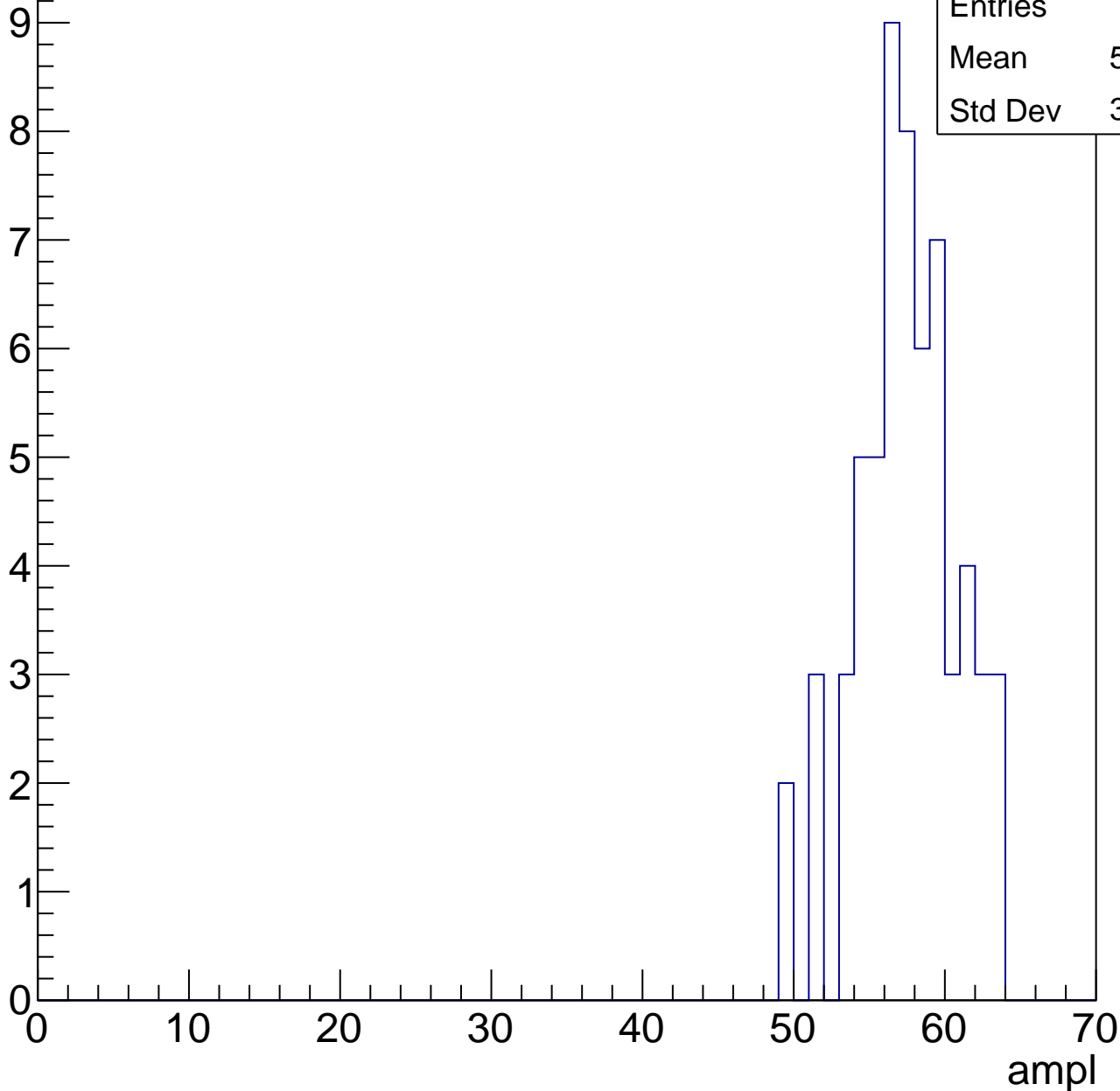


B1L100S, U12-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.97
Std Dev	3.304

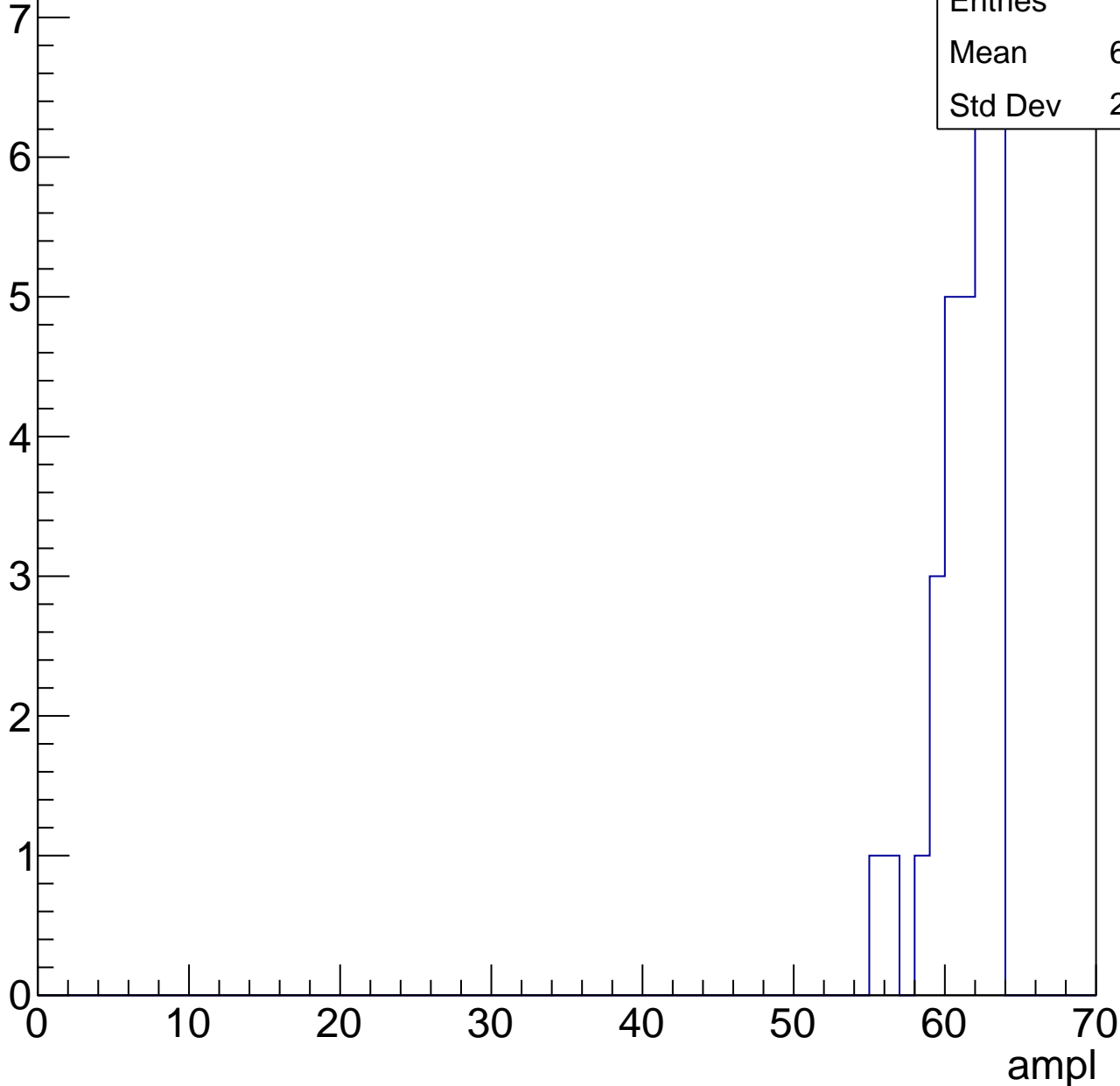


B1L100S, U12-ch10, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

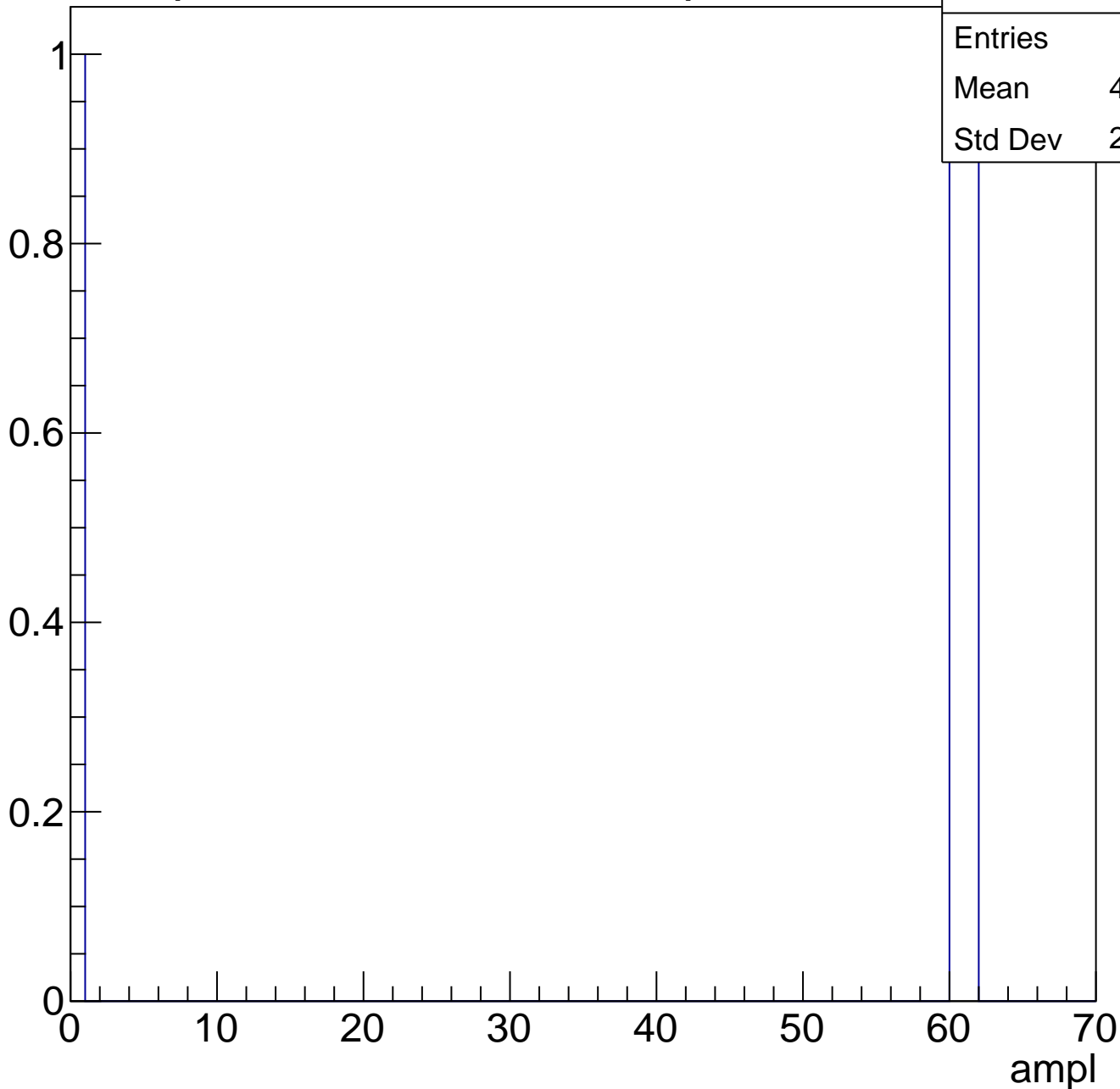
Entries	30
Mean	60.87
Std Dev	2.012



B1L100S, U12-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch11, adc0

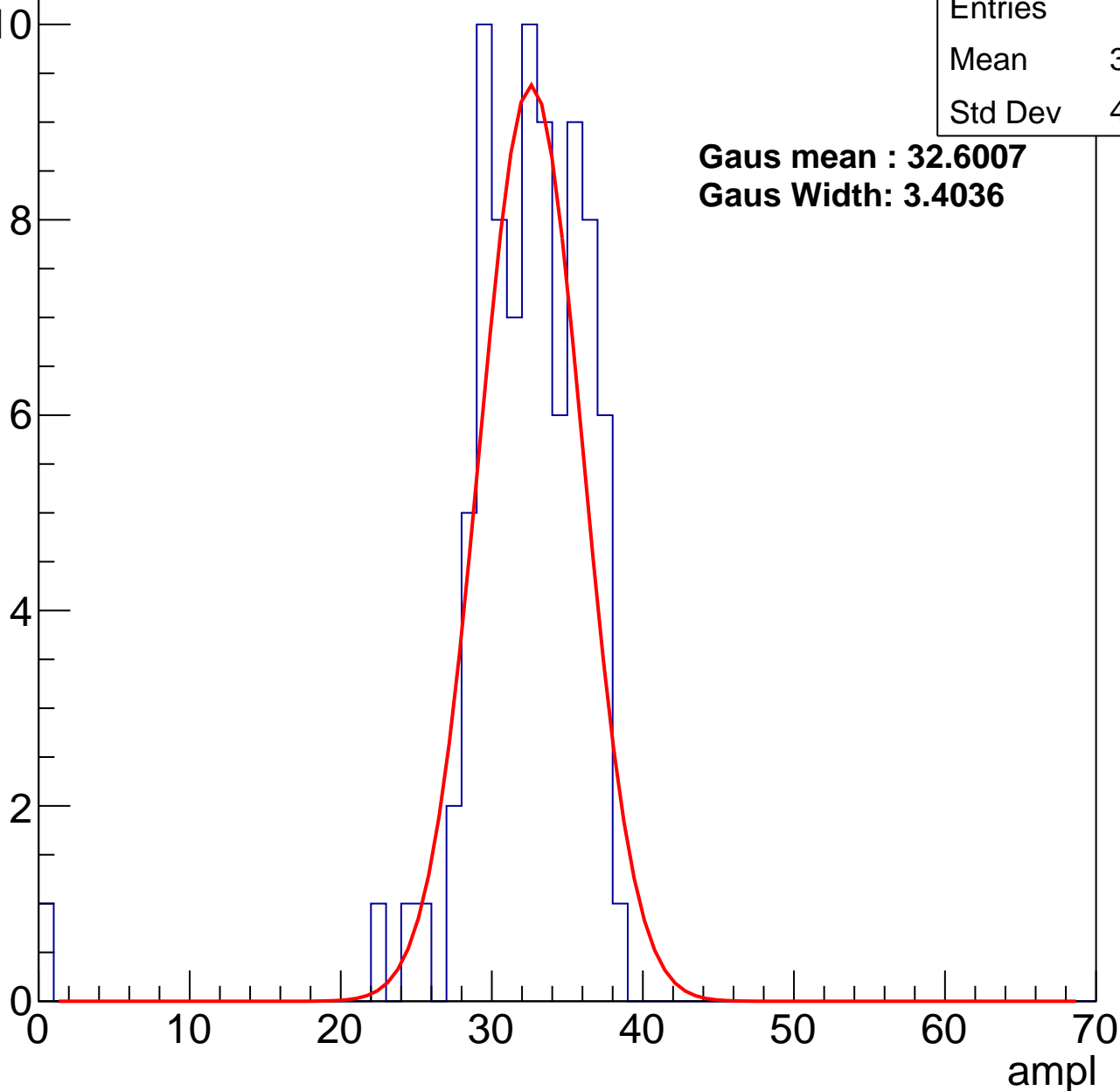
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	31.72
Std Dev	4.749

Gaus mean : 32.6007

Gaus Width: 3.4036



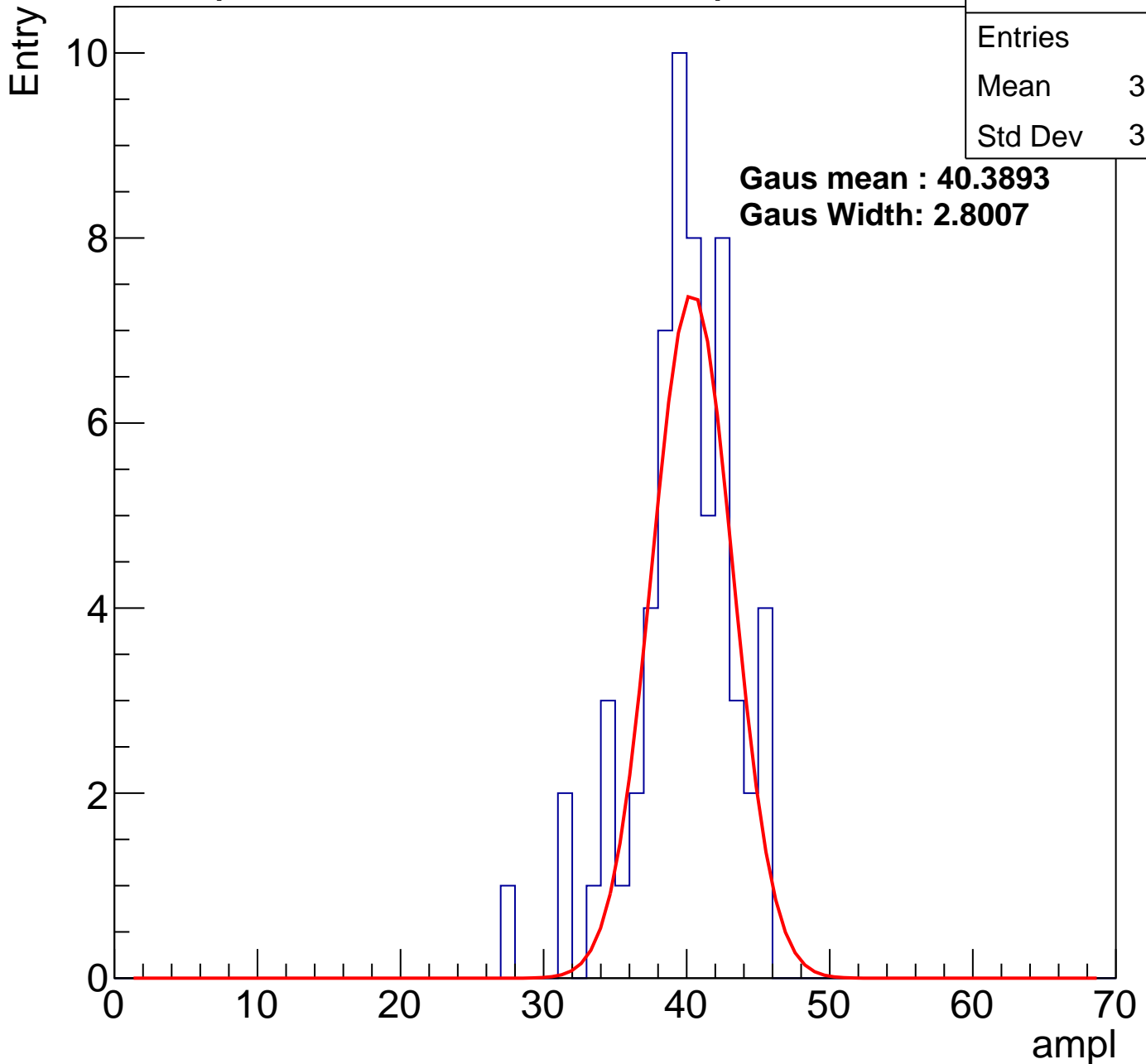
B1L100S, U12-ch11, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	39.23
Std Dev	3.573

Gaus mean : 40.3893

Gaus Width: 2.8007



B1L100S, U12-ch11, adc2

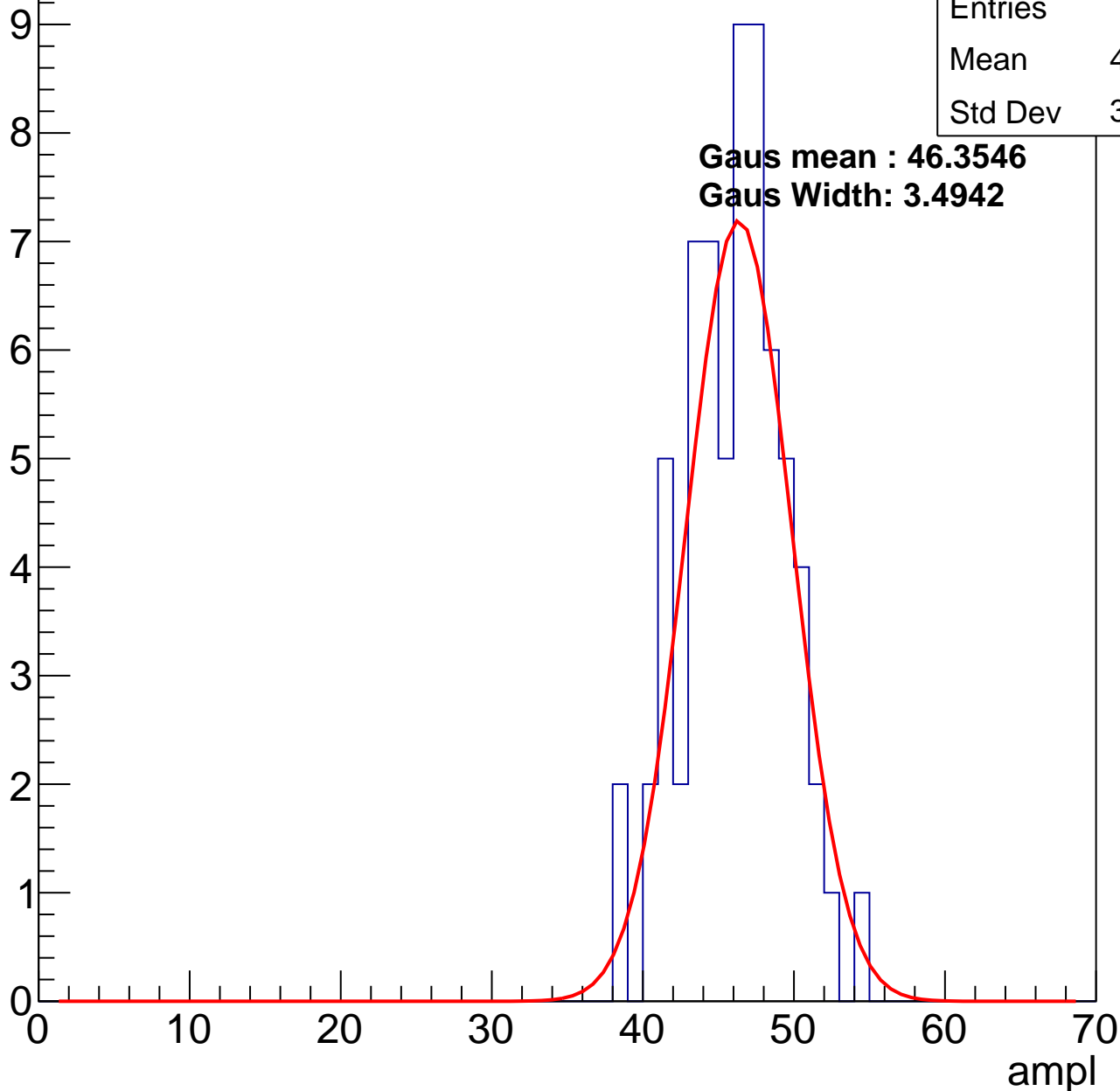
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	45.63
Std Dev	3.314

Gaus mean : 46.3546

Gaus Width: 3.4942

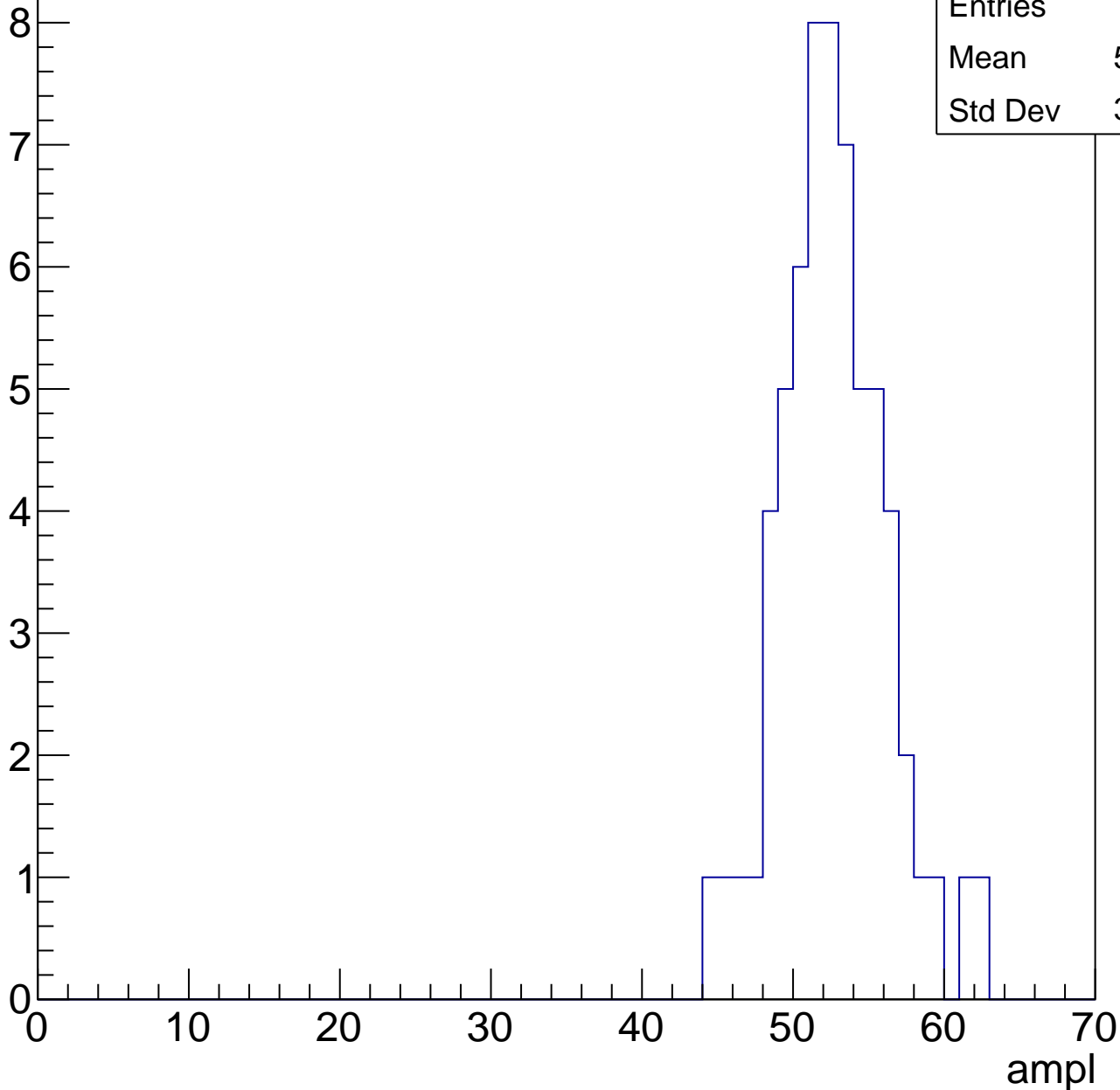


B1L100S, U12-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

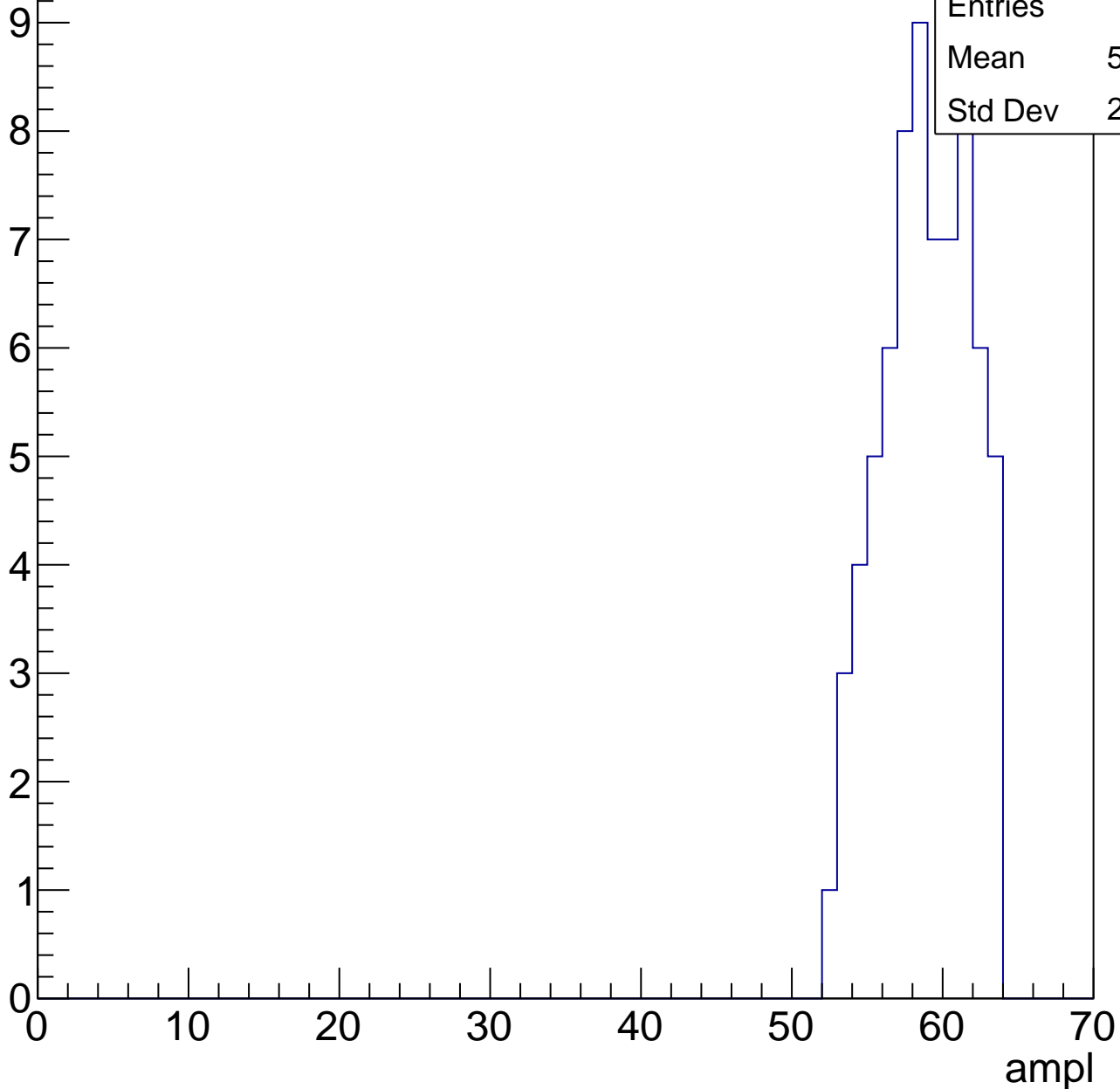
Entries	62
Mean	52.21
Std Dev	3.511



B1L100S, U12-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



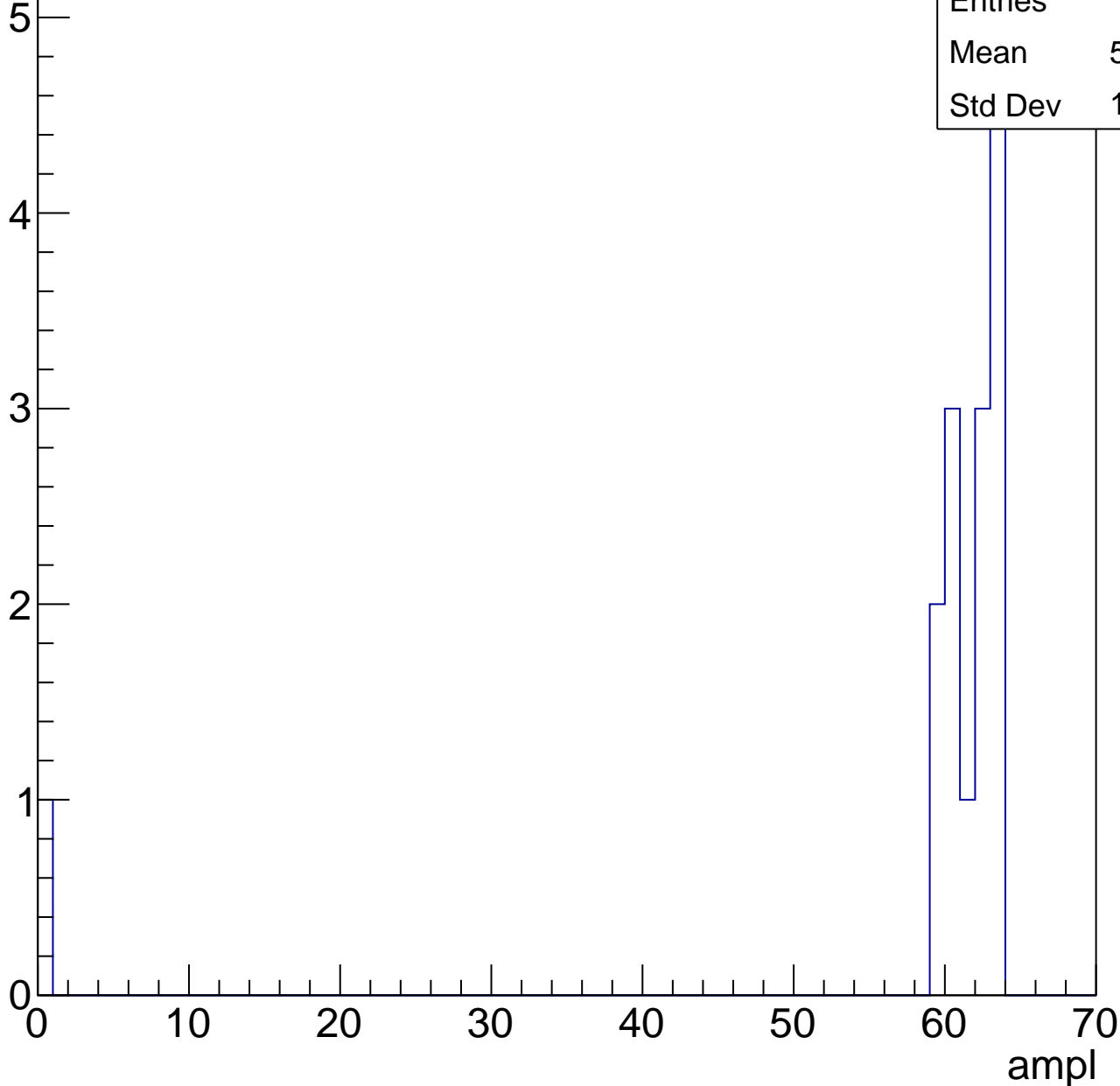
Entries	69
Mean	58.32
Std Dev	2.882

B1L100S, U12-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	57.33
Std Dev	15.39



B1L100S, U12-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch12, adc0

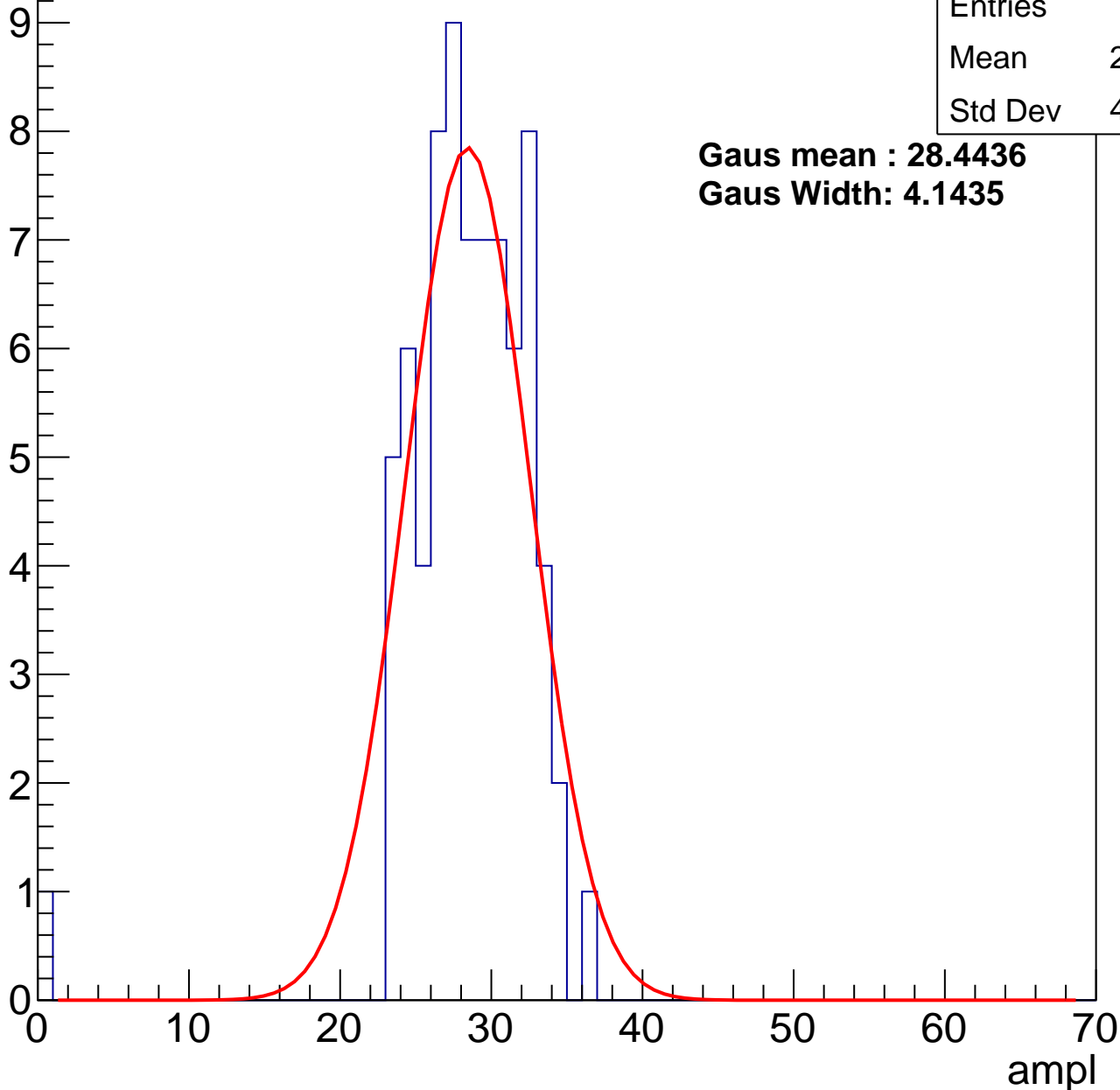
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	27.96
Std Dev	4.524

Gaus mean : 28.4436

Gaus Width: 4.1435



B1L100S, U12-ch12, adc1

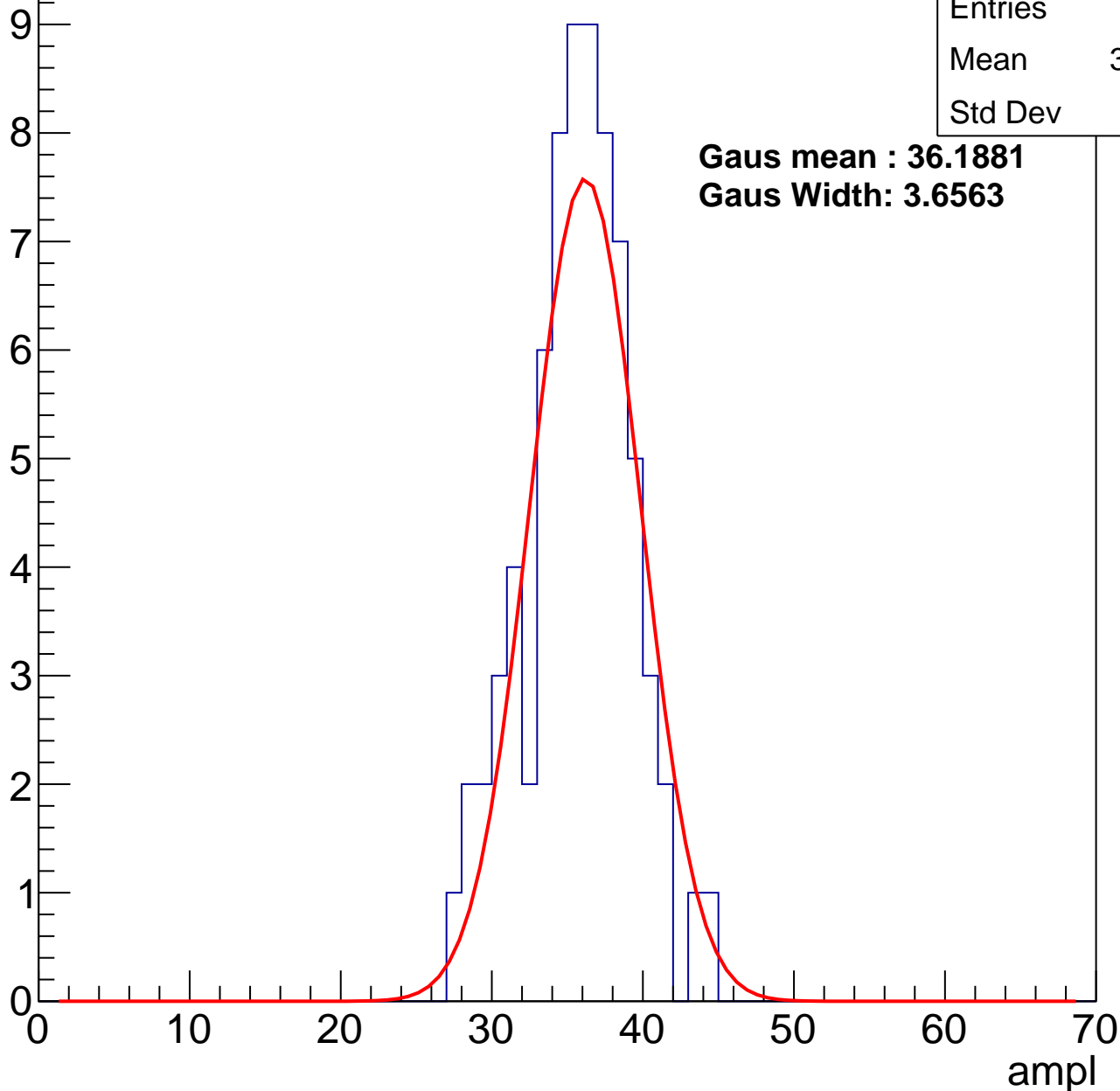
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	35.26
Std Dev	3.5

Gaus mean : 36.1881

Gaus Width: 3.6563



B1L100S, U12-ch12, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	42.64
Std Dev	3.272

Gaus mean : 42.7448

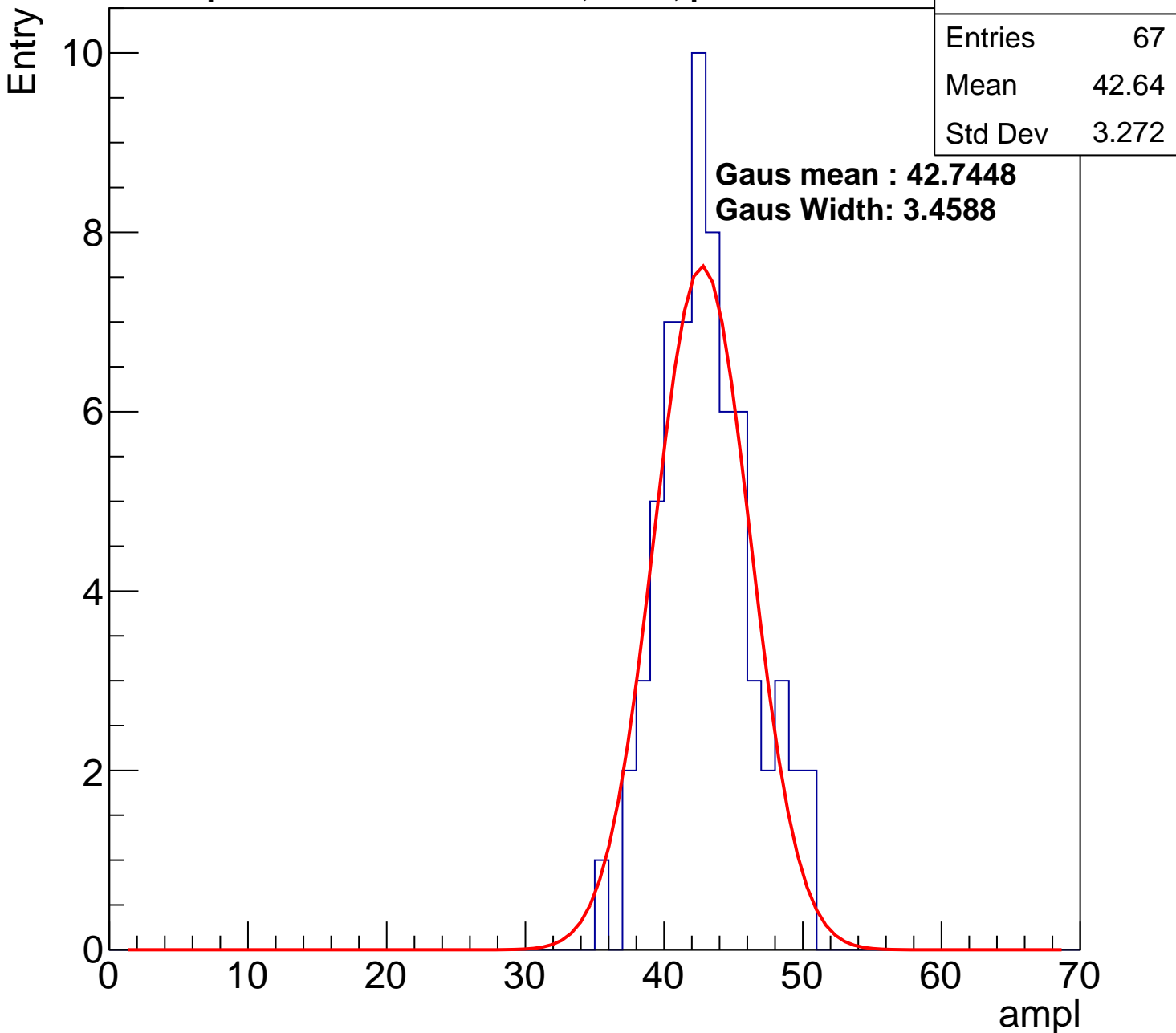
Gaus Width: 3.4588

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

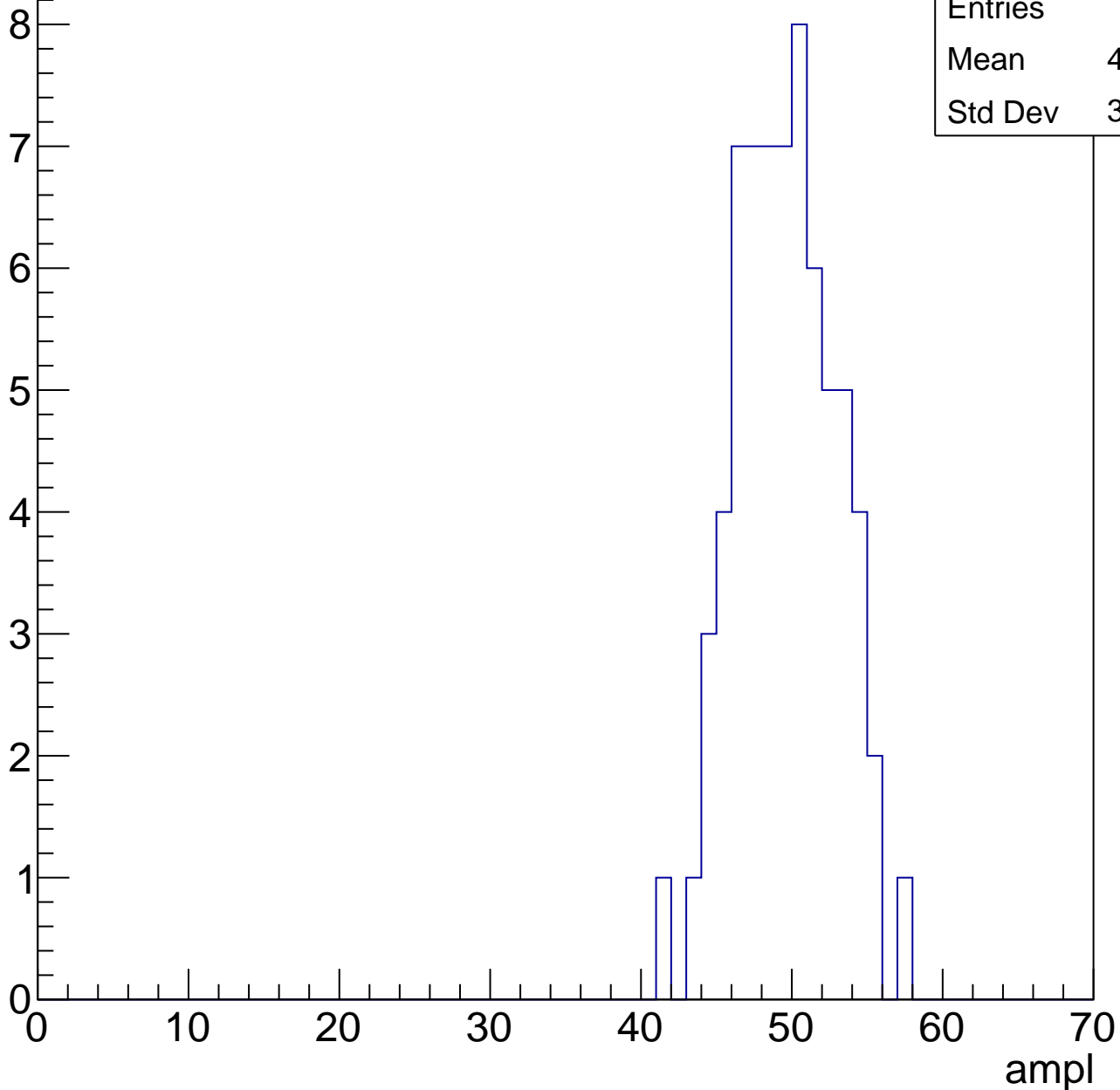


B1L100S, U12-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.12
Std Dev	3.274

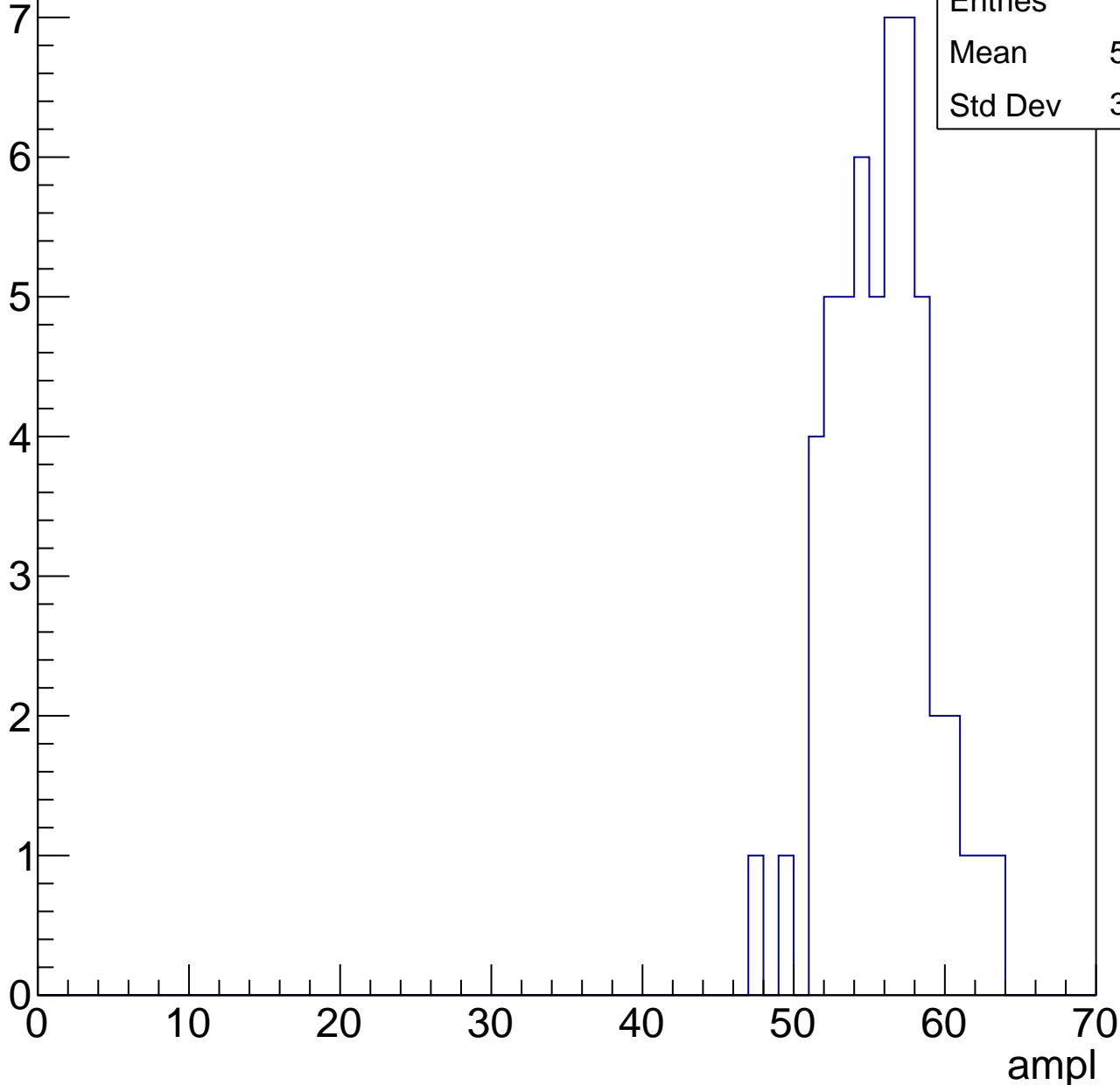


B1L100S, U12-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

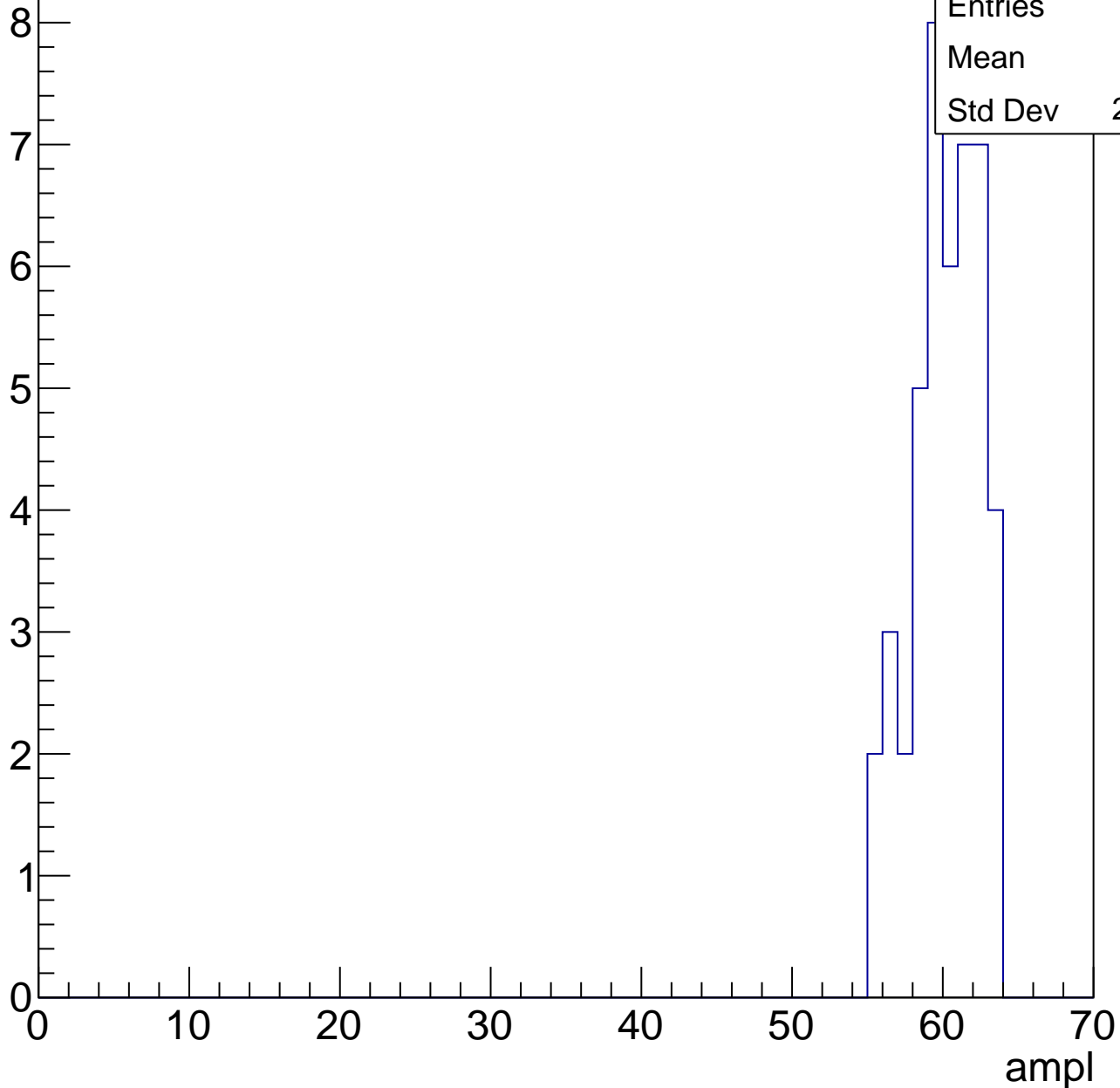
Entries	53
Mean	55.26
Std Dev	3.199



B1L100S, U12-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

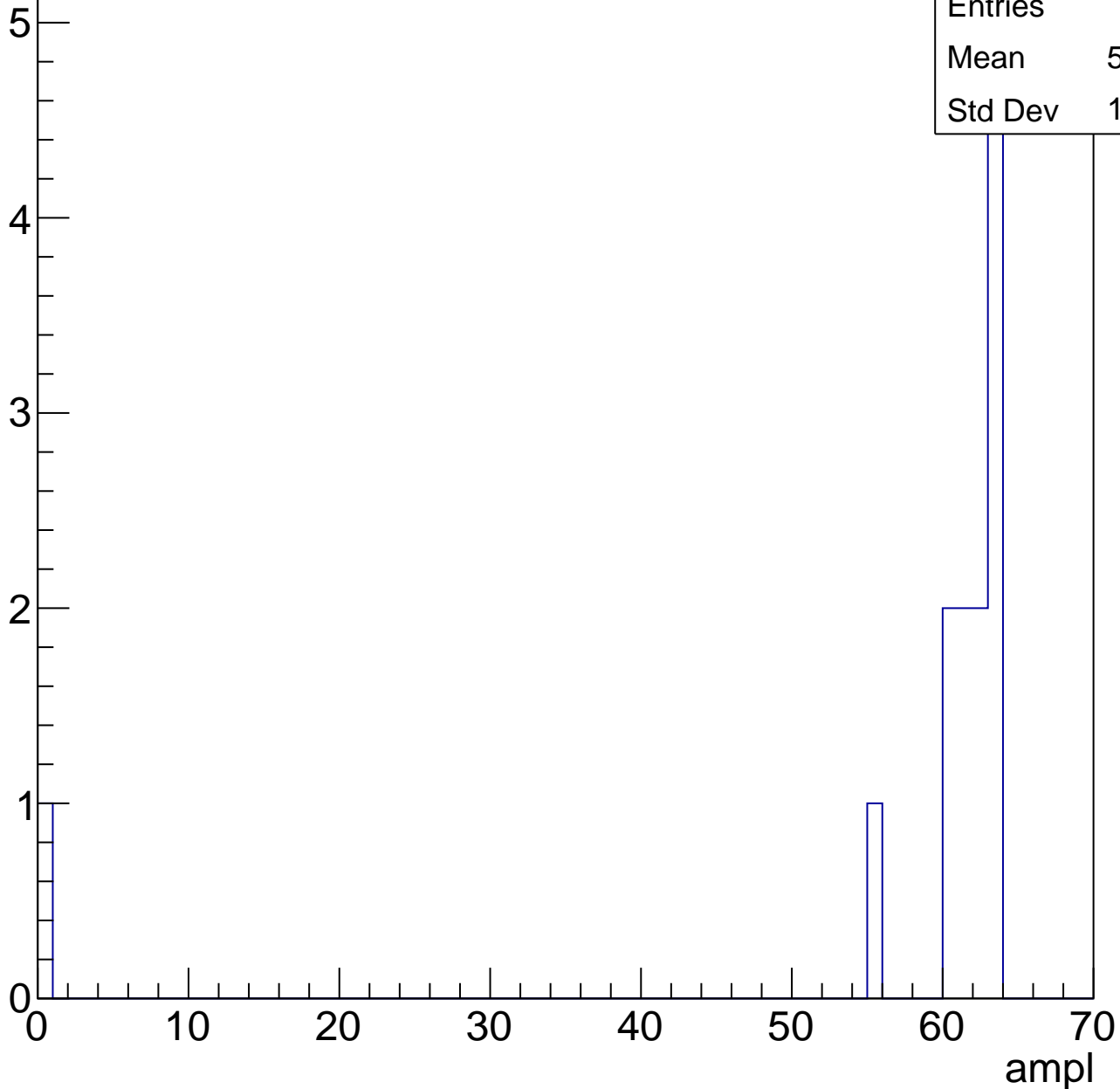


B1L100S, U12-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

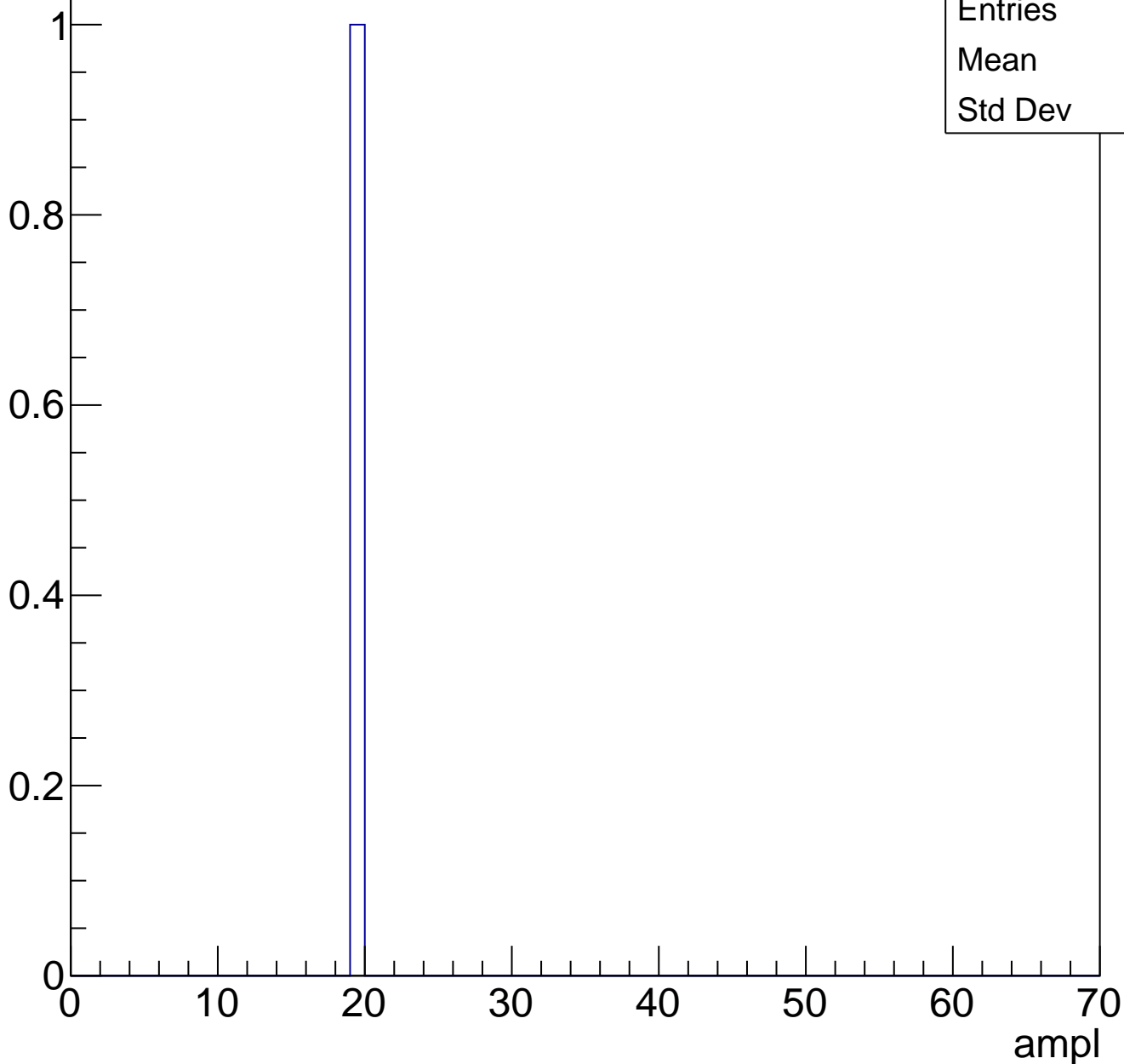
Entries	13
Mean	56.62
Std Dev	16.48



B1L100S, U12-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch13, adc0

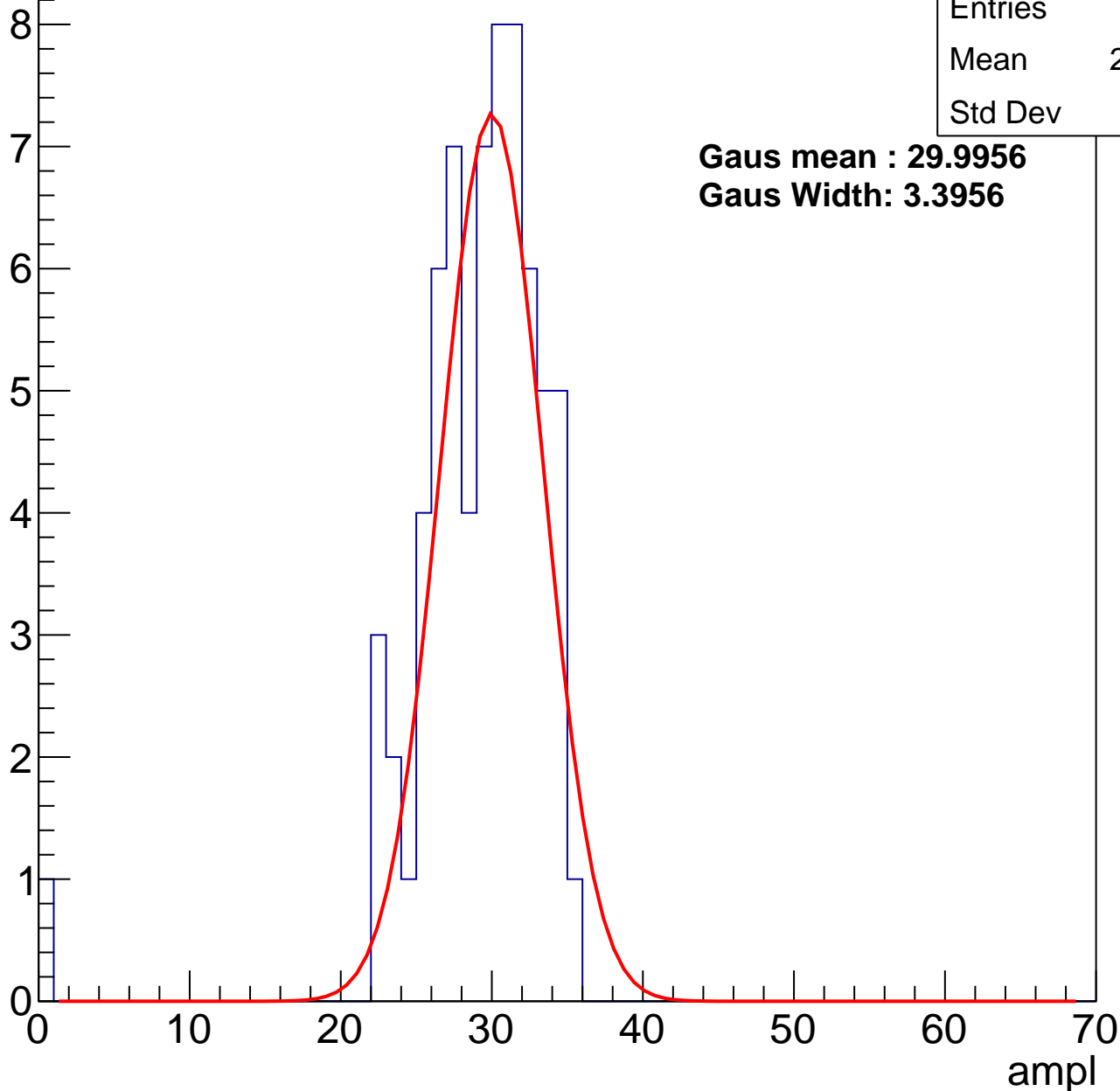
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.62
Std Dev	4.79

Gaus mean : 29.9956

Gaus Width: 3.3956



B1L100S, U12-ch13, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	36.95
Std Dev	3.793

Gaus mean : 37.6333

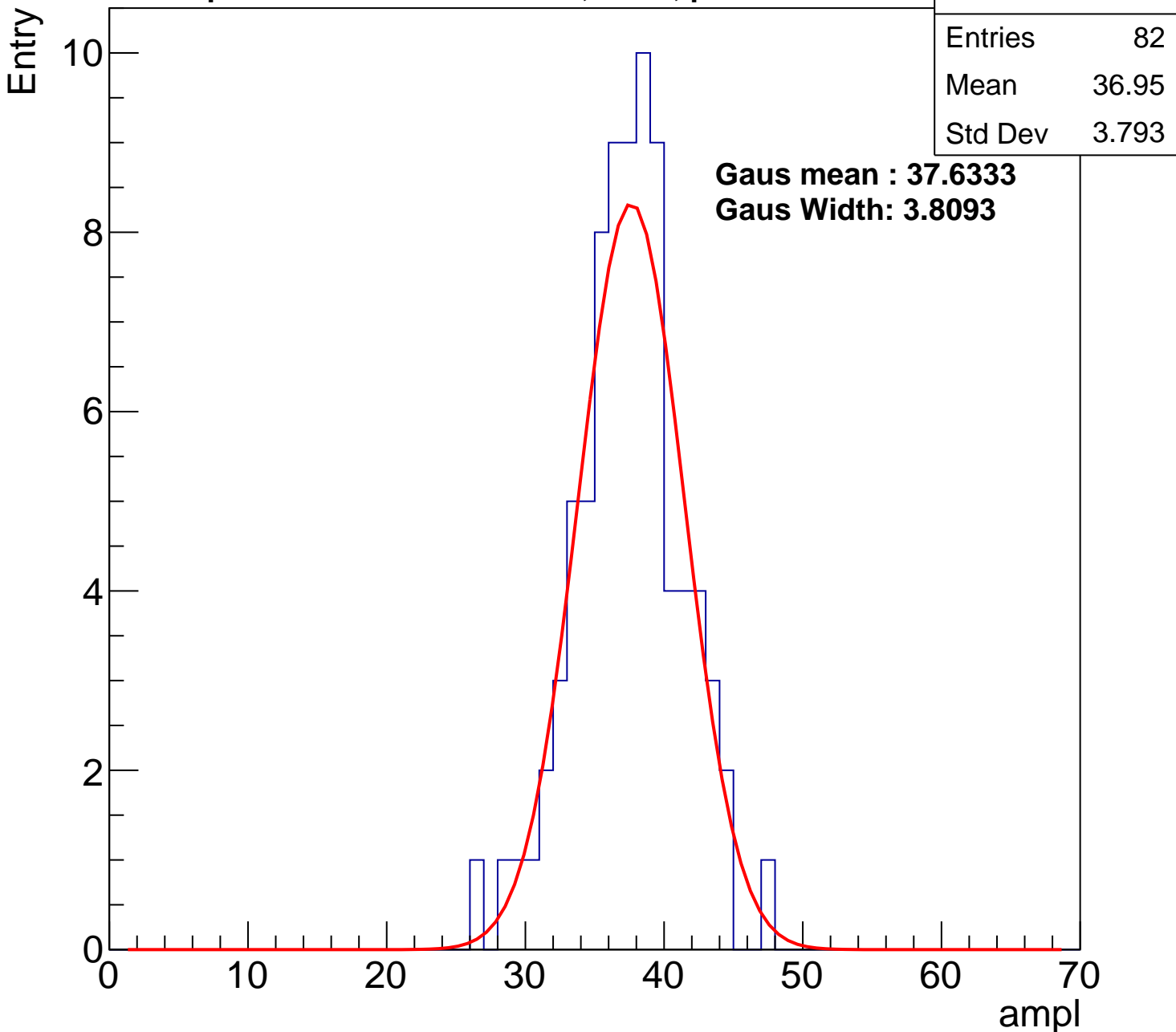
Gaus Width: 3.8093

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch13, adc2

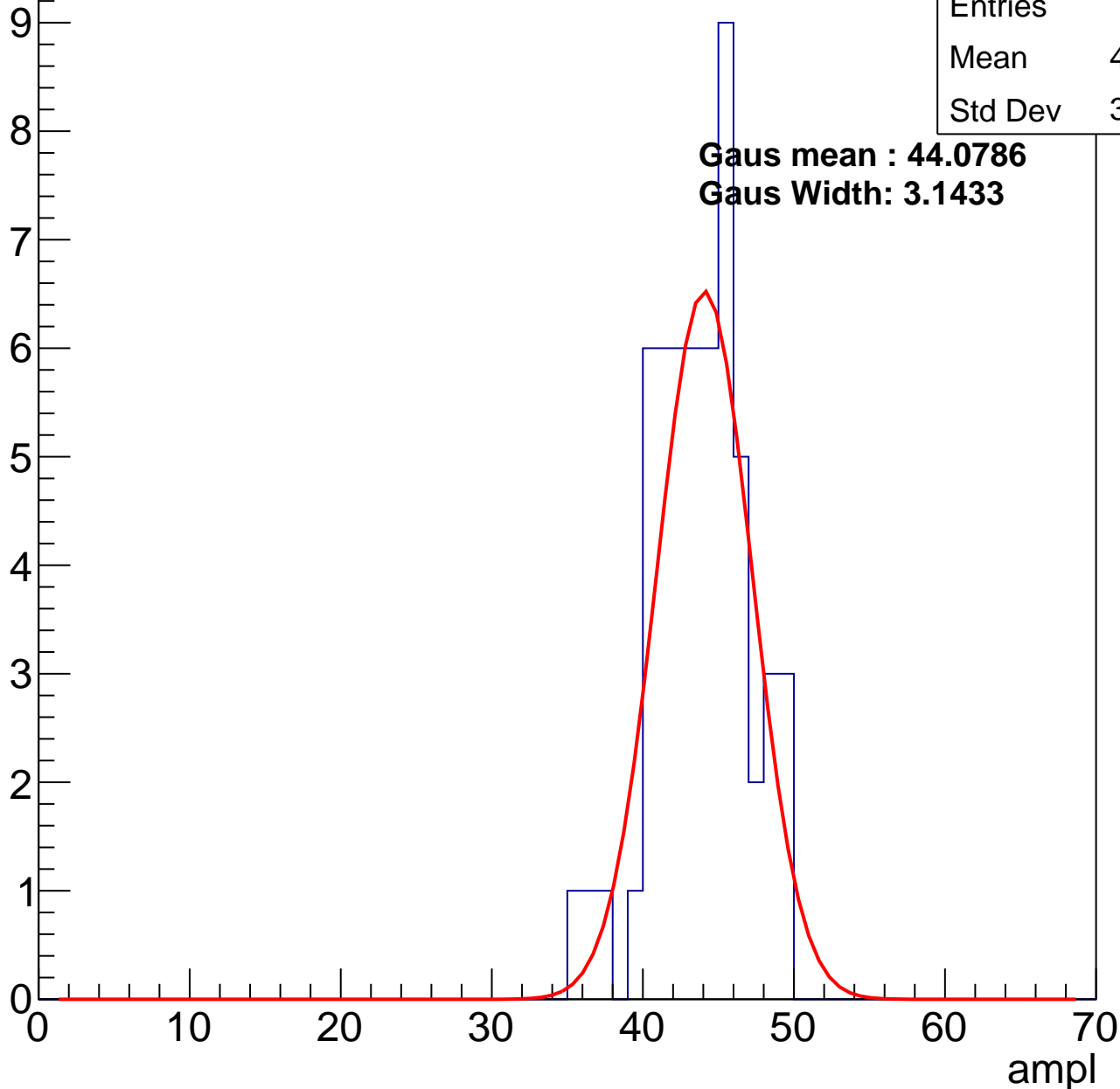
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.34
Std Dev	3.118

Gaus mean : 44.0786

Gaus Width: 3.1433

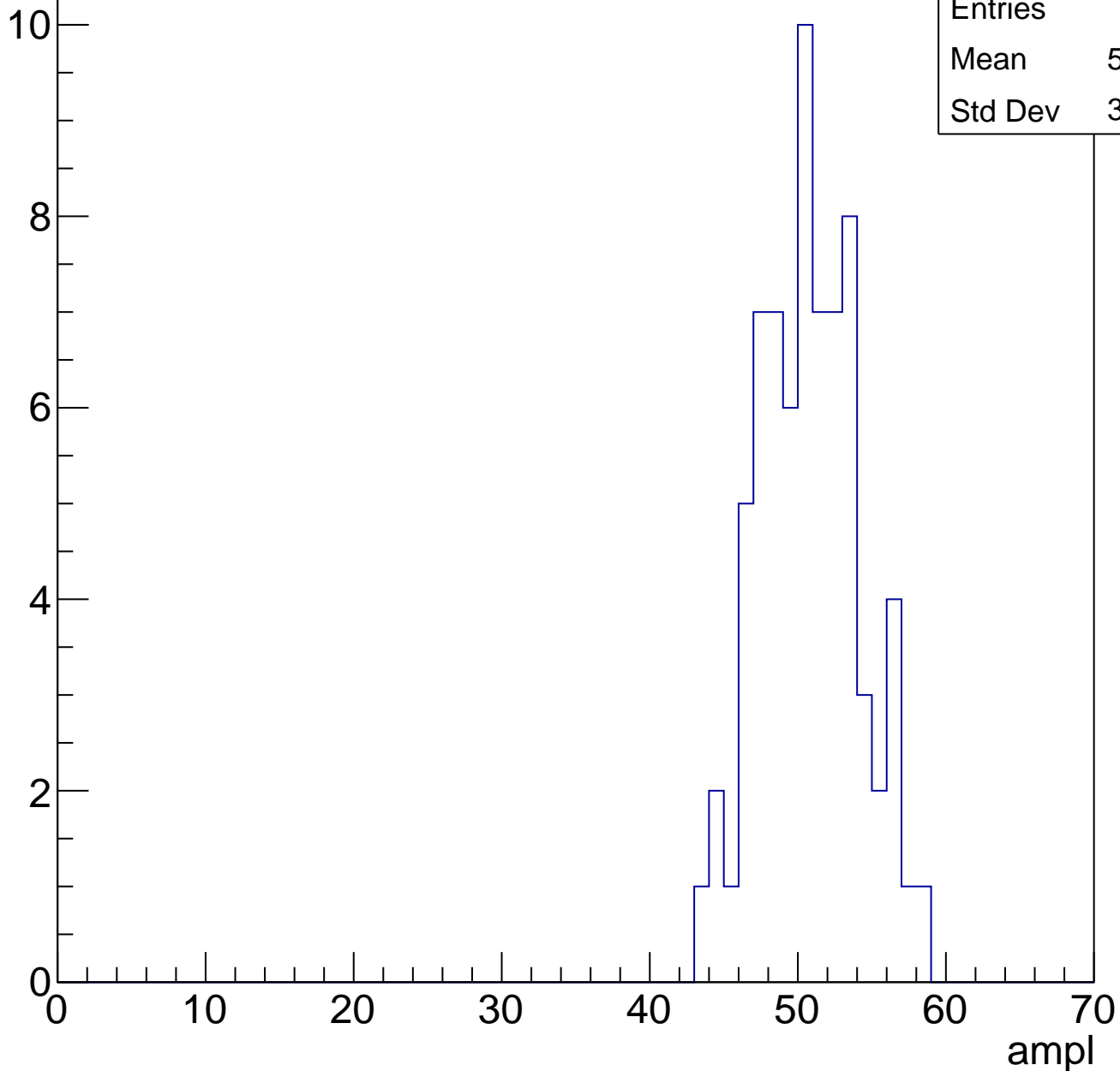


B1L100S, U12-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	50.29
Std Dev	3.289

Entry

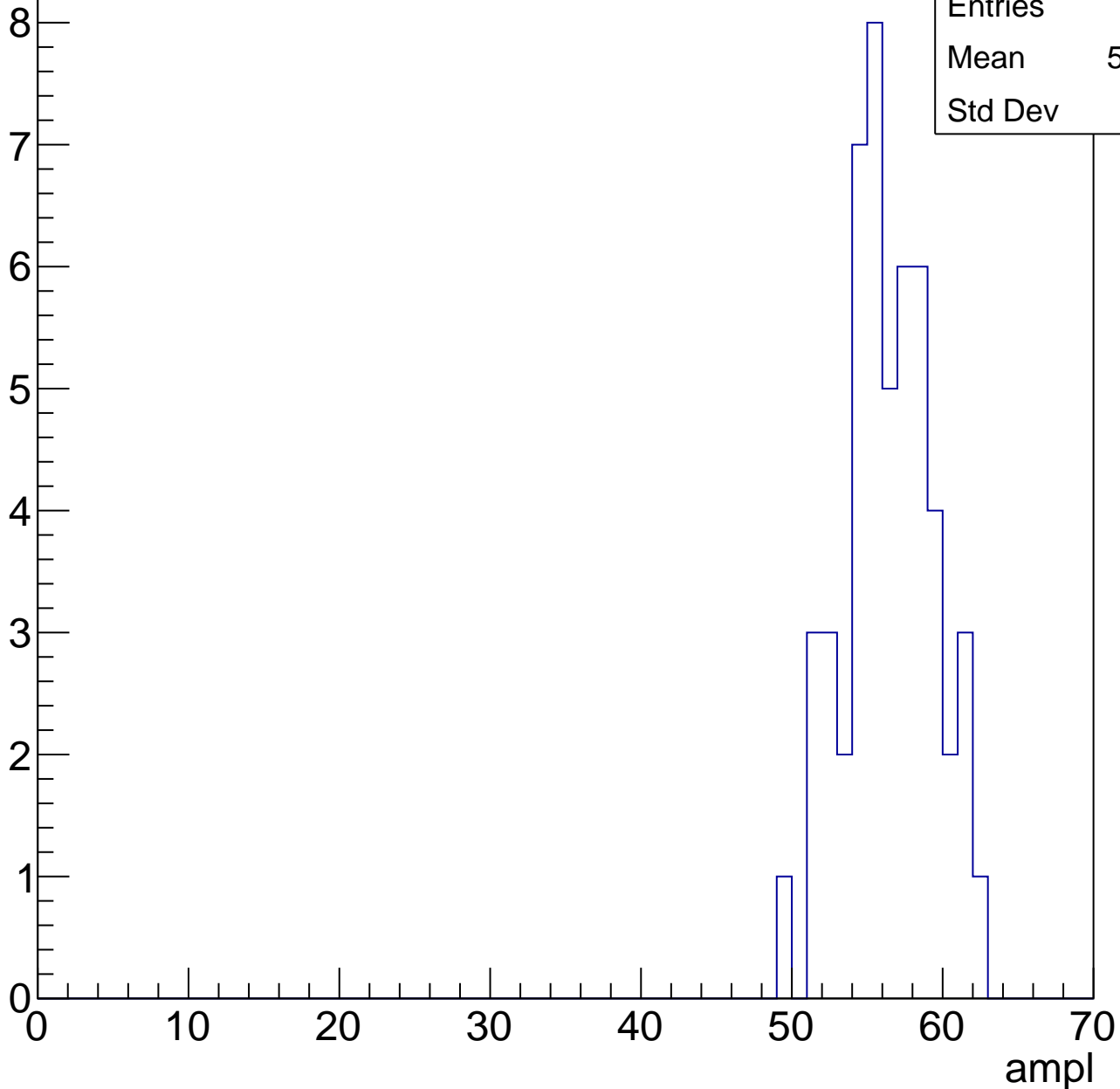


B1L100S, U12-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	55.94
Std Dev	2.92

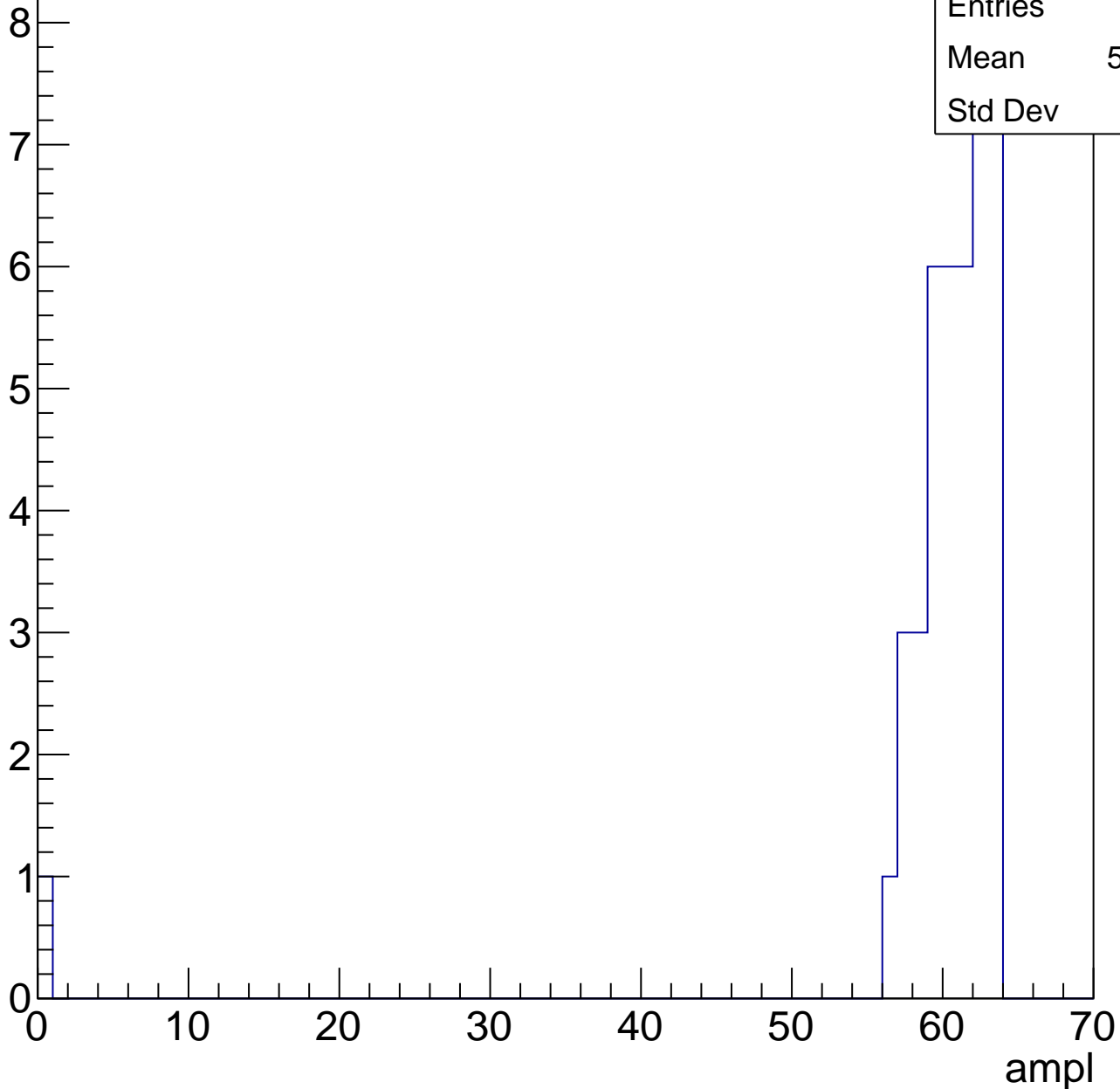


B1L100S, U12-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	59.07
Std Dev	9.43



B1L100S, U12-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	6
Mean	61.5
Std Dev	1.258

B1L100S, U12-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch14, adc0

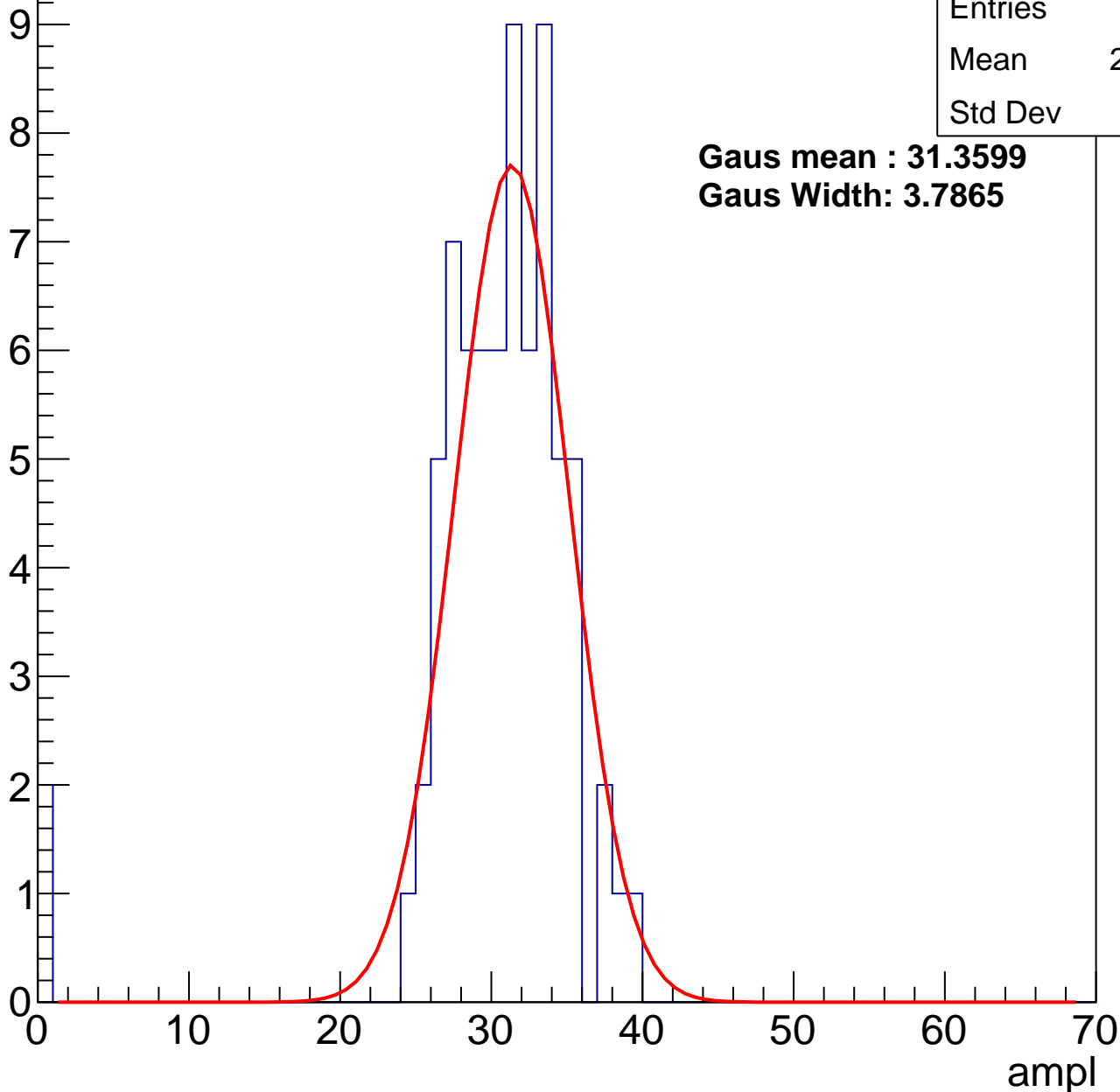
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.85
Std Dev	5.99

Gaus mean : 31.3599

Gaus Width: 3.7865



B1L100S, U12-ch14, adc1

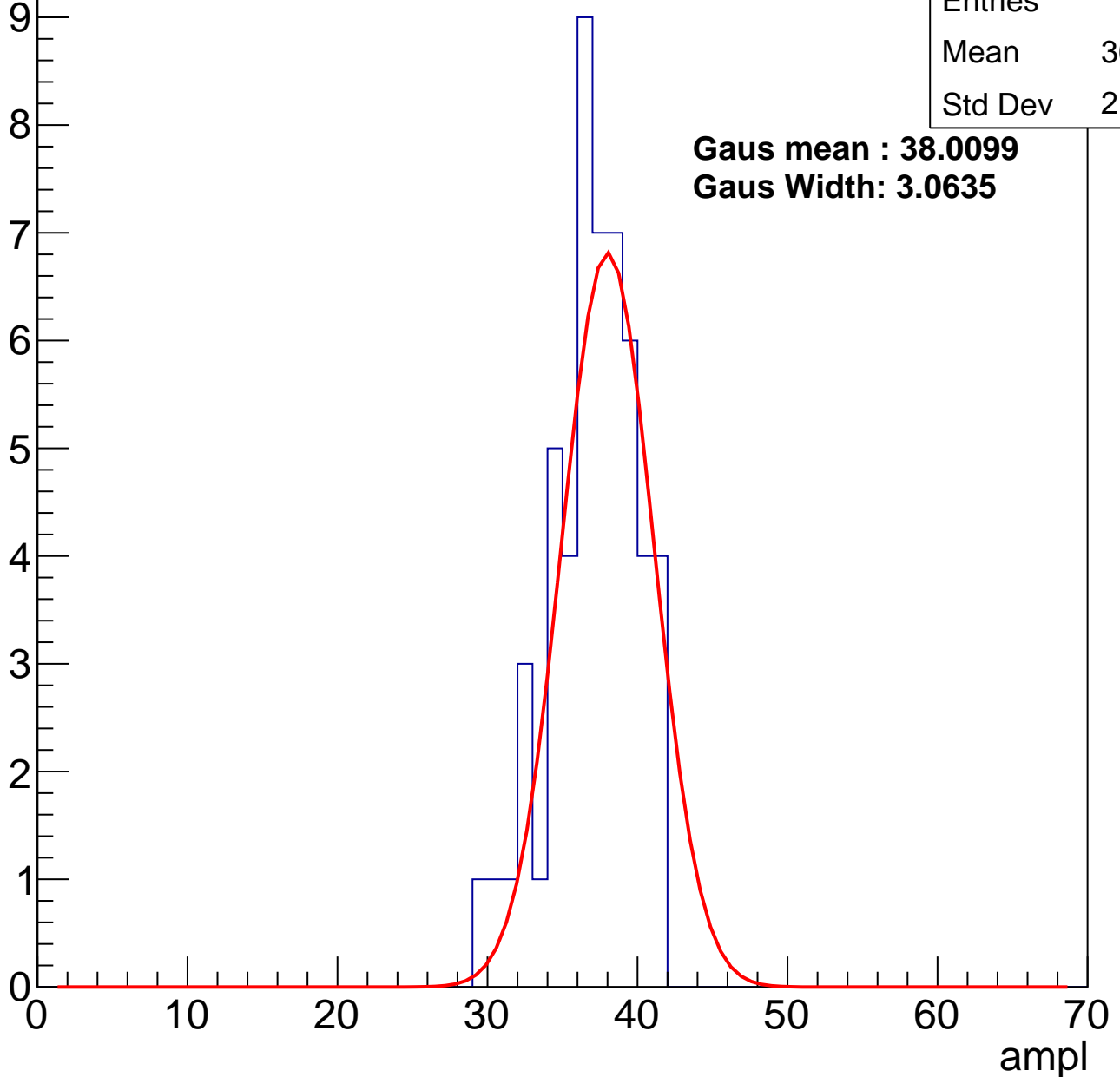
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	36.53
Std Dev	2.846

Gaus mean : 38.0099

Gaus Width: 3.0635



B1L100S, U12-ch14, adc2

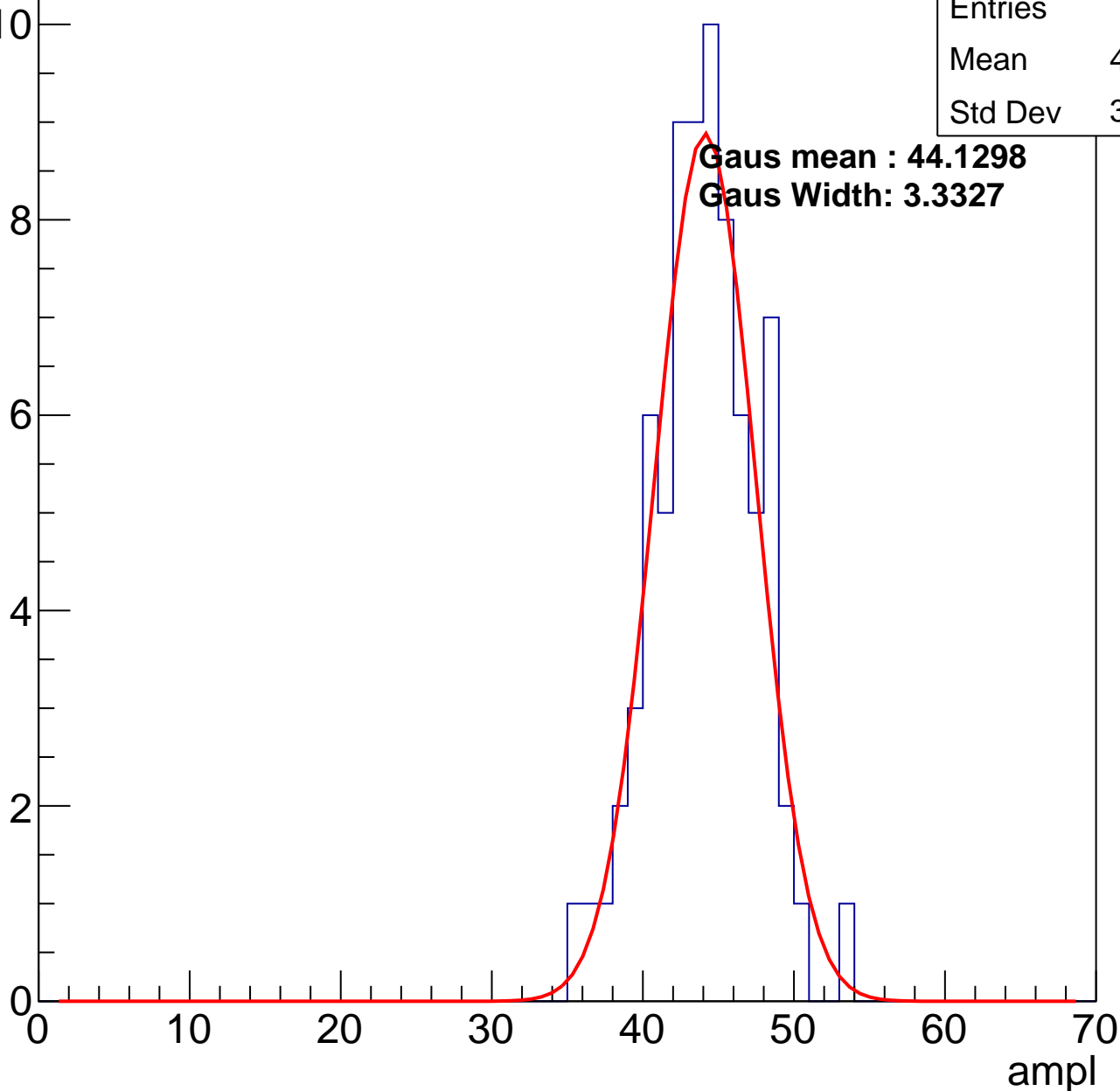
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	43.62
Std Dev	3.365

Gaus mean : 44.1298

Gaus Width: 3.3327

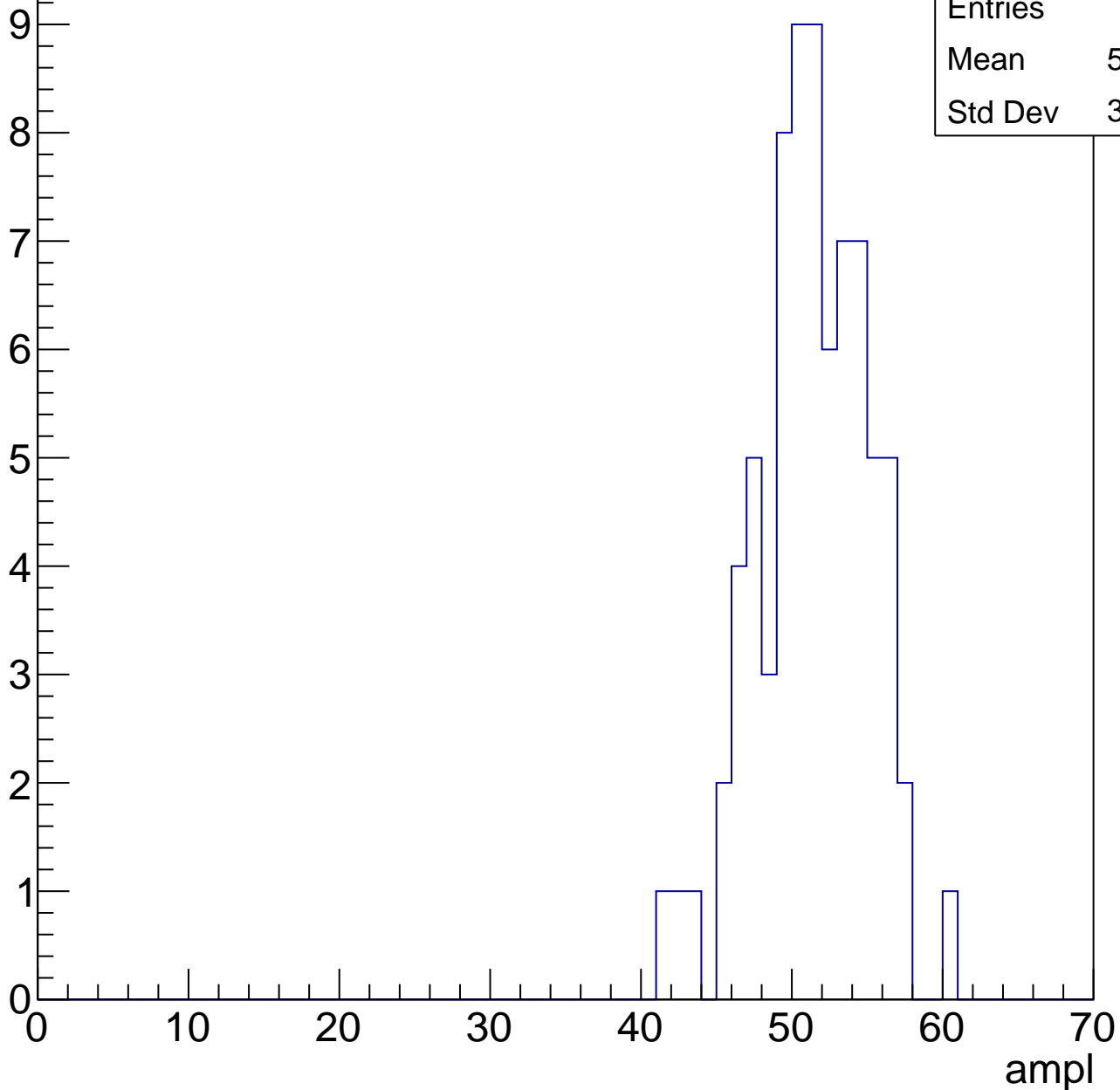


B1L100S, U12-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	50.92
Std Dev	3.663

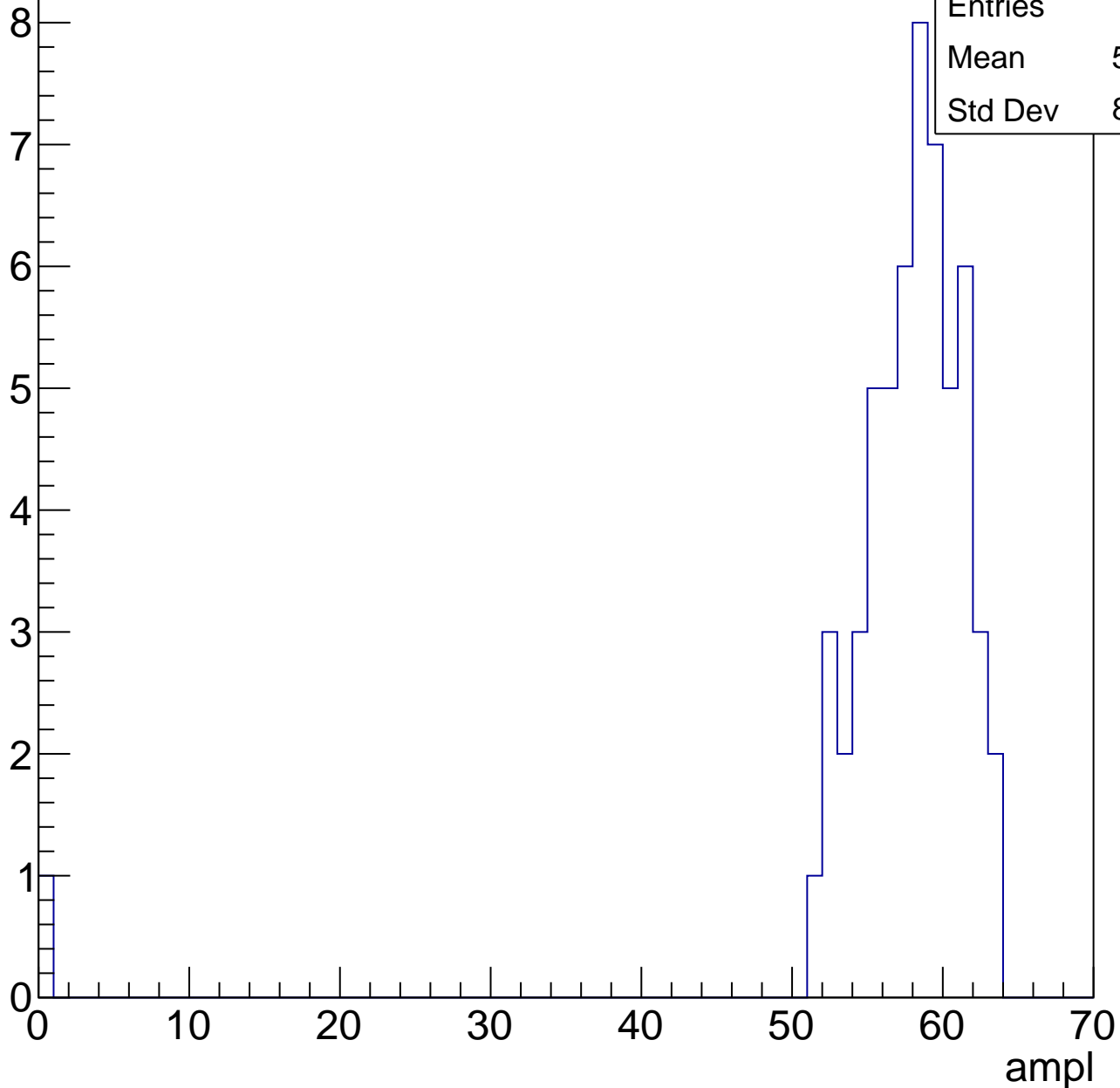


B1L100S, U12-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	56.61
Std Dev	8.121

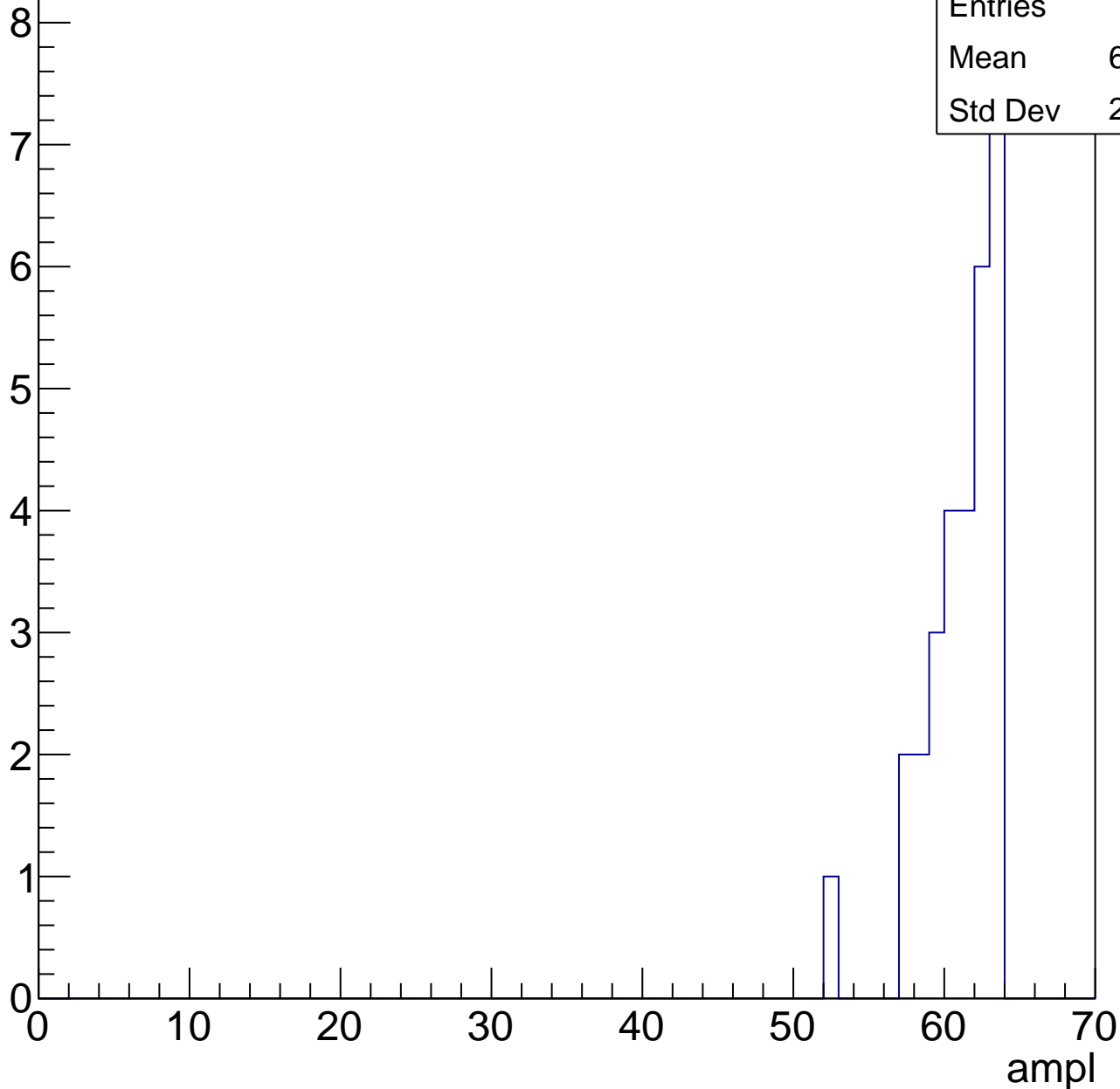


B1L100S, U12-ch14, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.63
Std Dev	2.456



B1L100S, U12-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L100S, U12-ch15, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	30.6
Std Dev	3.774

Gaus mean : 30.8424

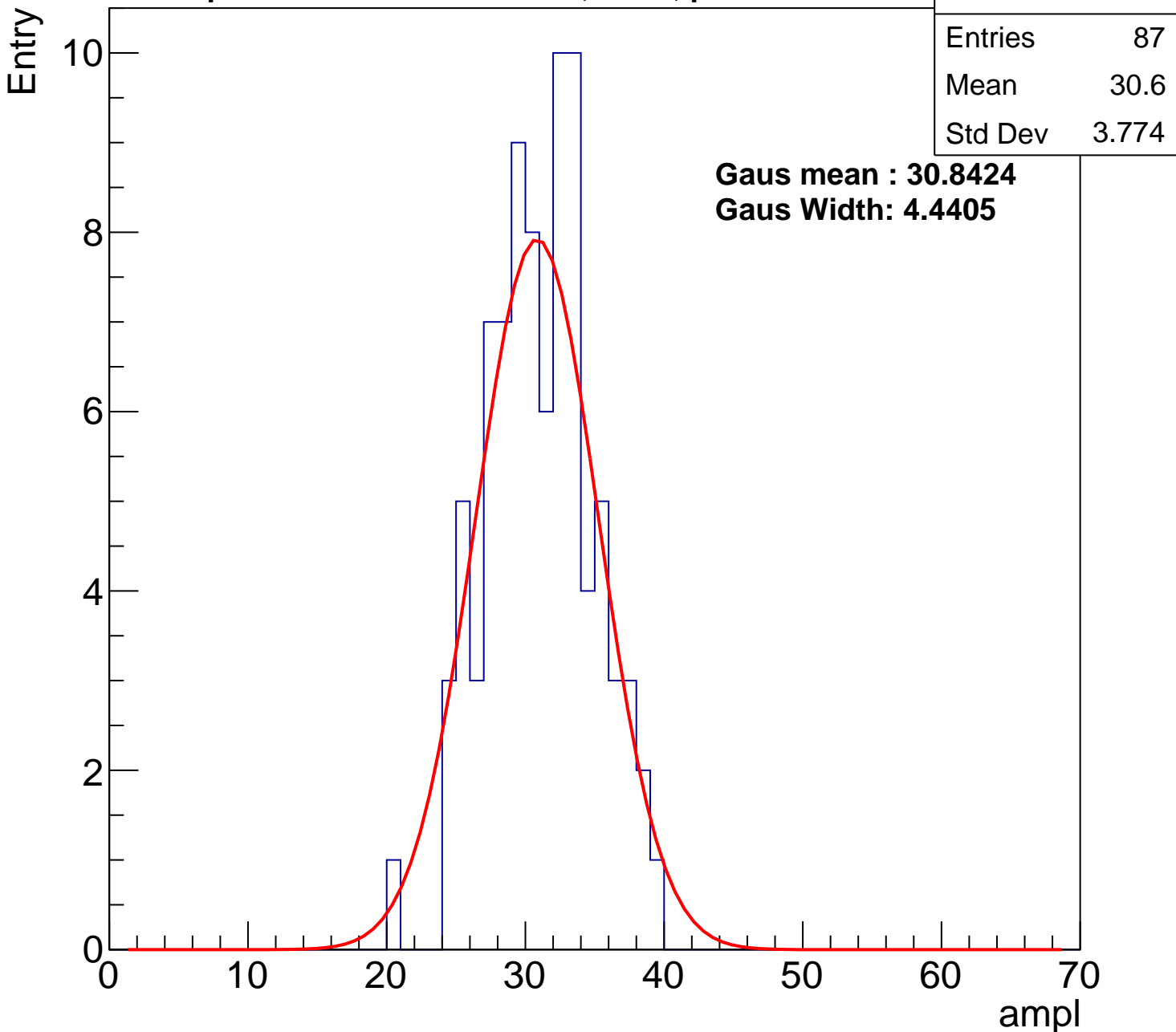
Gaus Width: 4.4405

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch15, adc1

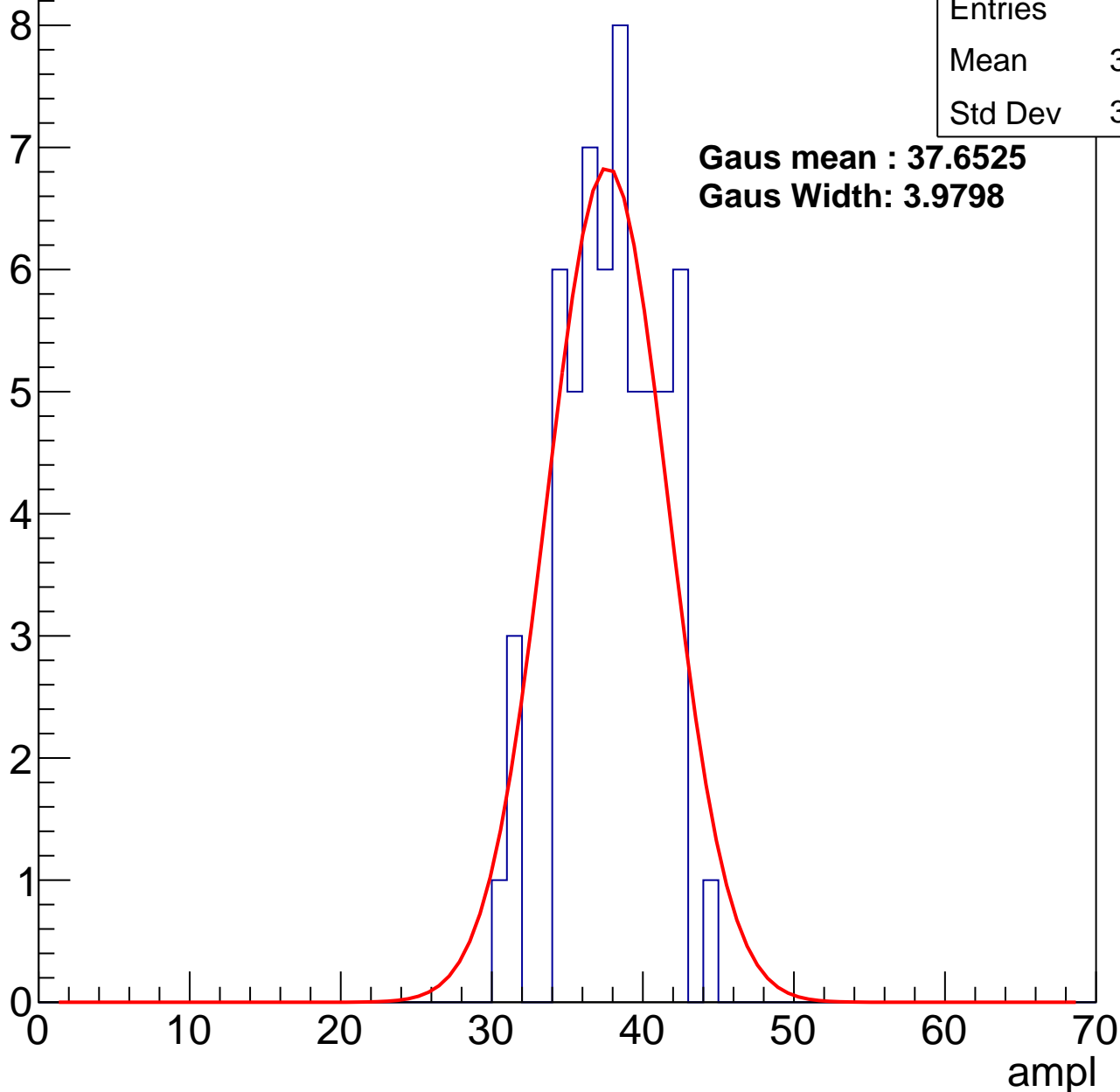
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	37.52
Std Dev	3.147

Gaus mean : 37.6525

Gaus Width: 3.9798



B1L100S, U12-ch15, adc2

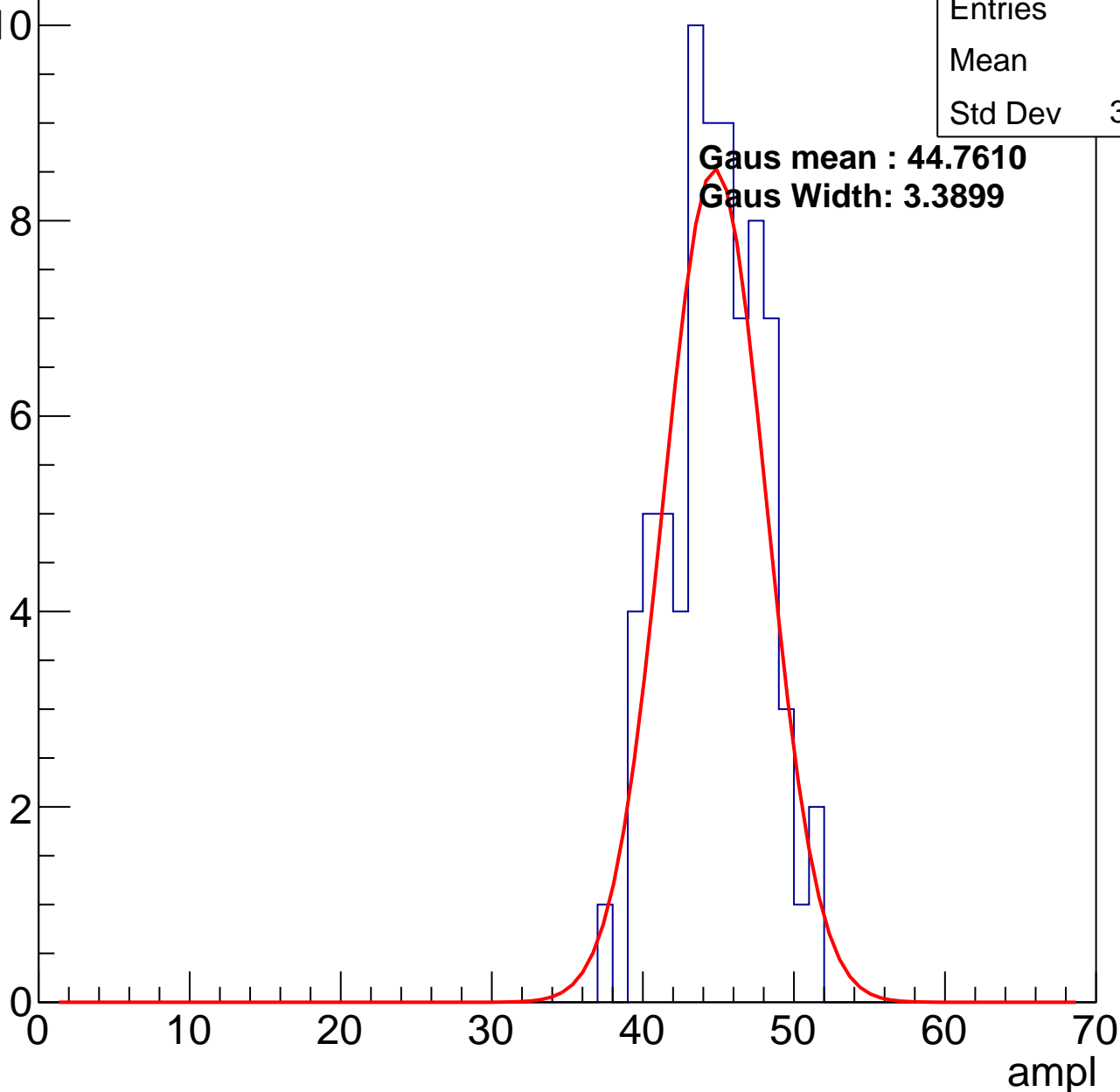
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.4
Std Dev	3.094

Gaus mean : 44.7610

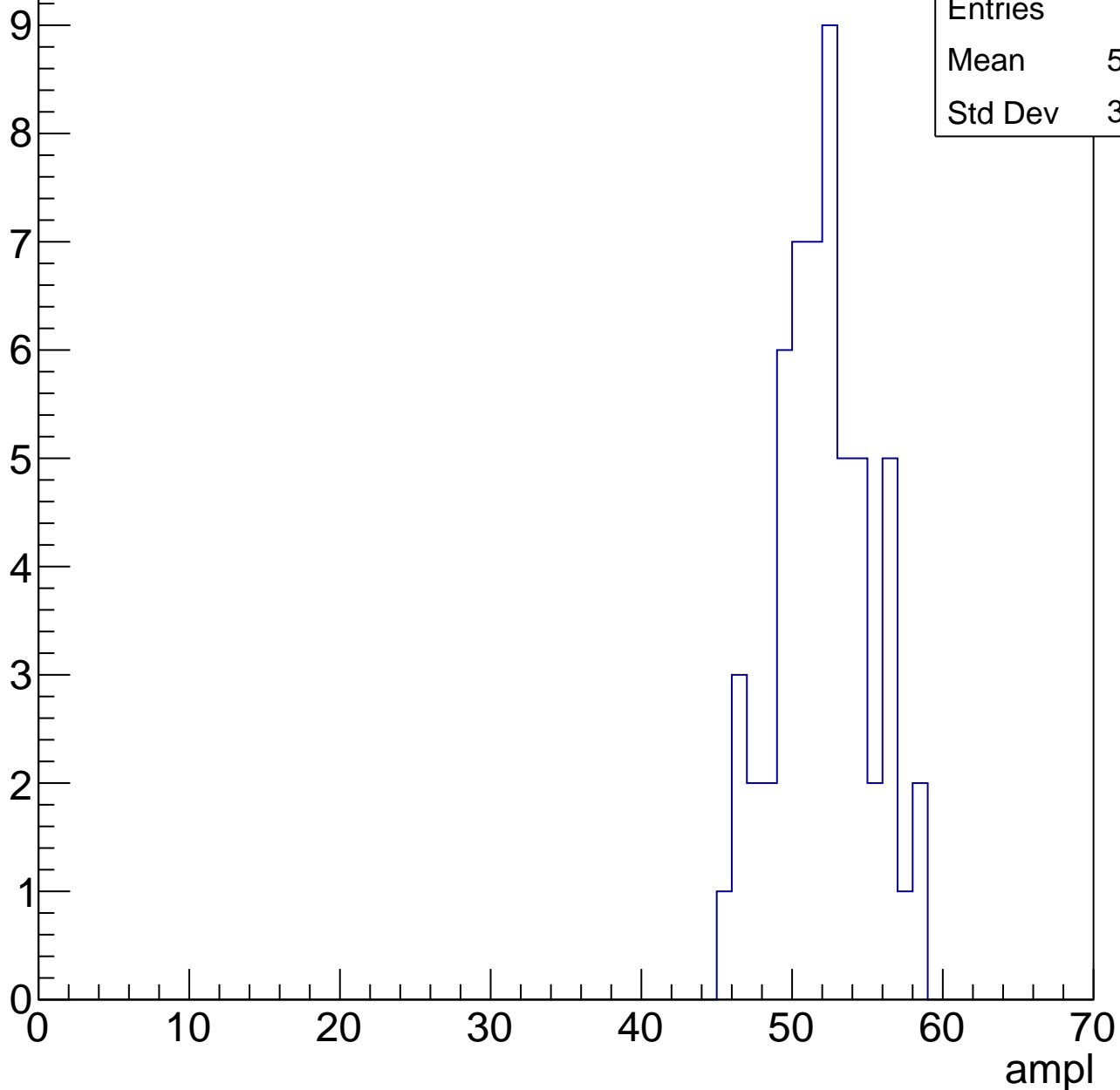
Gaus Width: 3.3899



B1L100S, U12-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

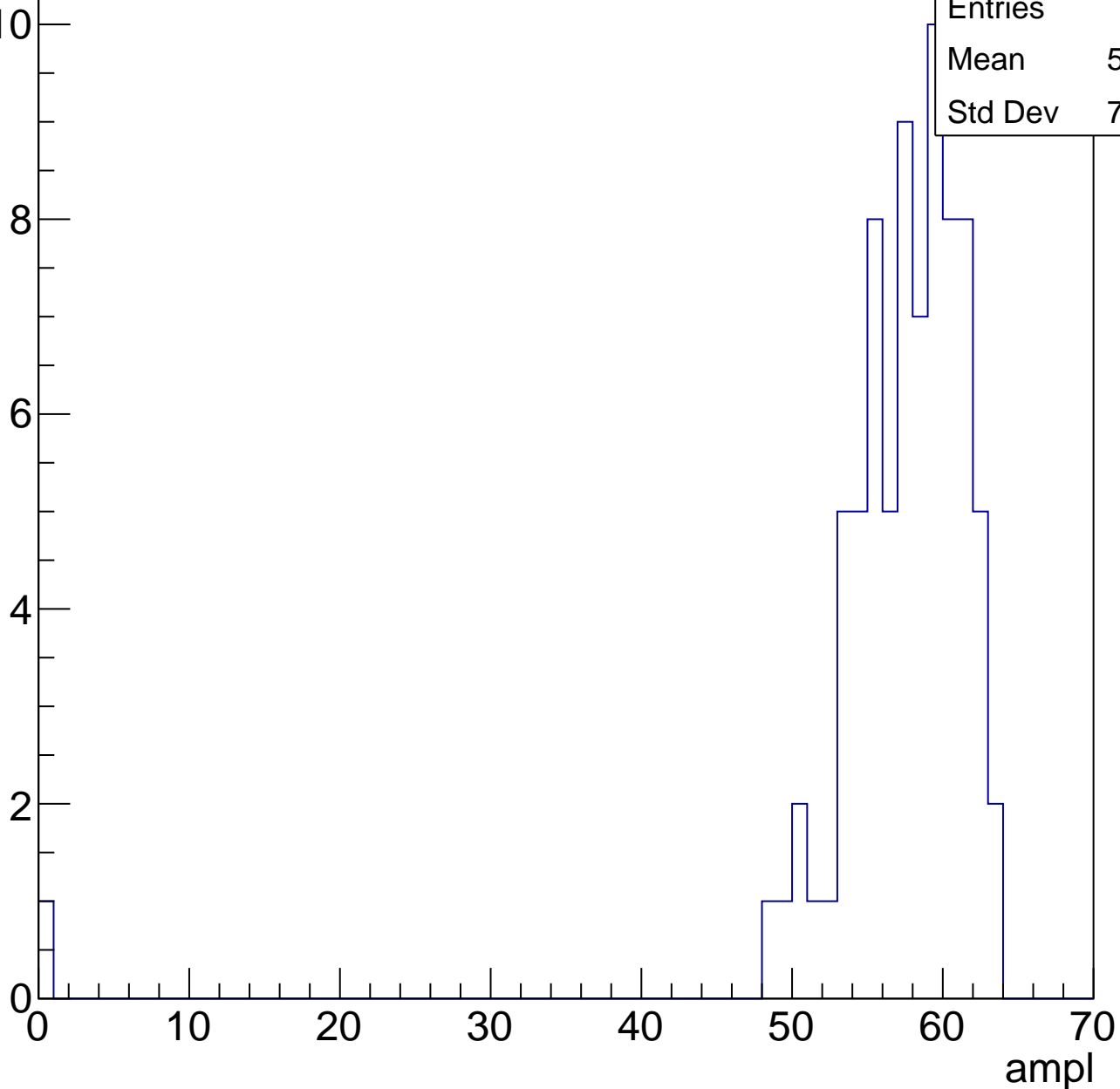


B1L100S, U12-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	56.56
Std Dev	7.242

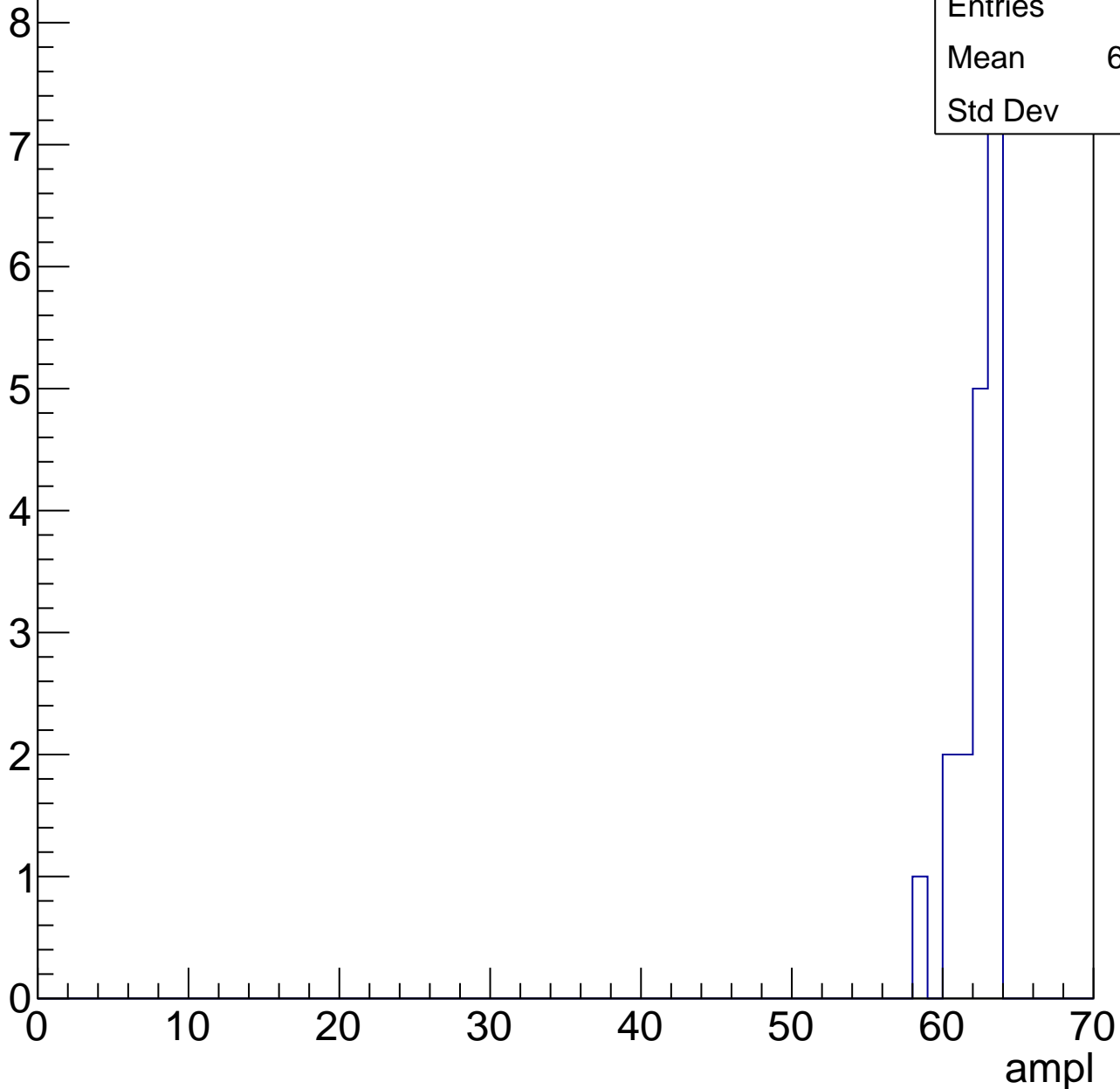


B1L100S, U12-ch15, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	61.89
Std Dev	1.37



B1L100S, U12-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch16, adc0

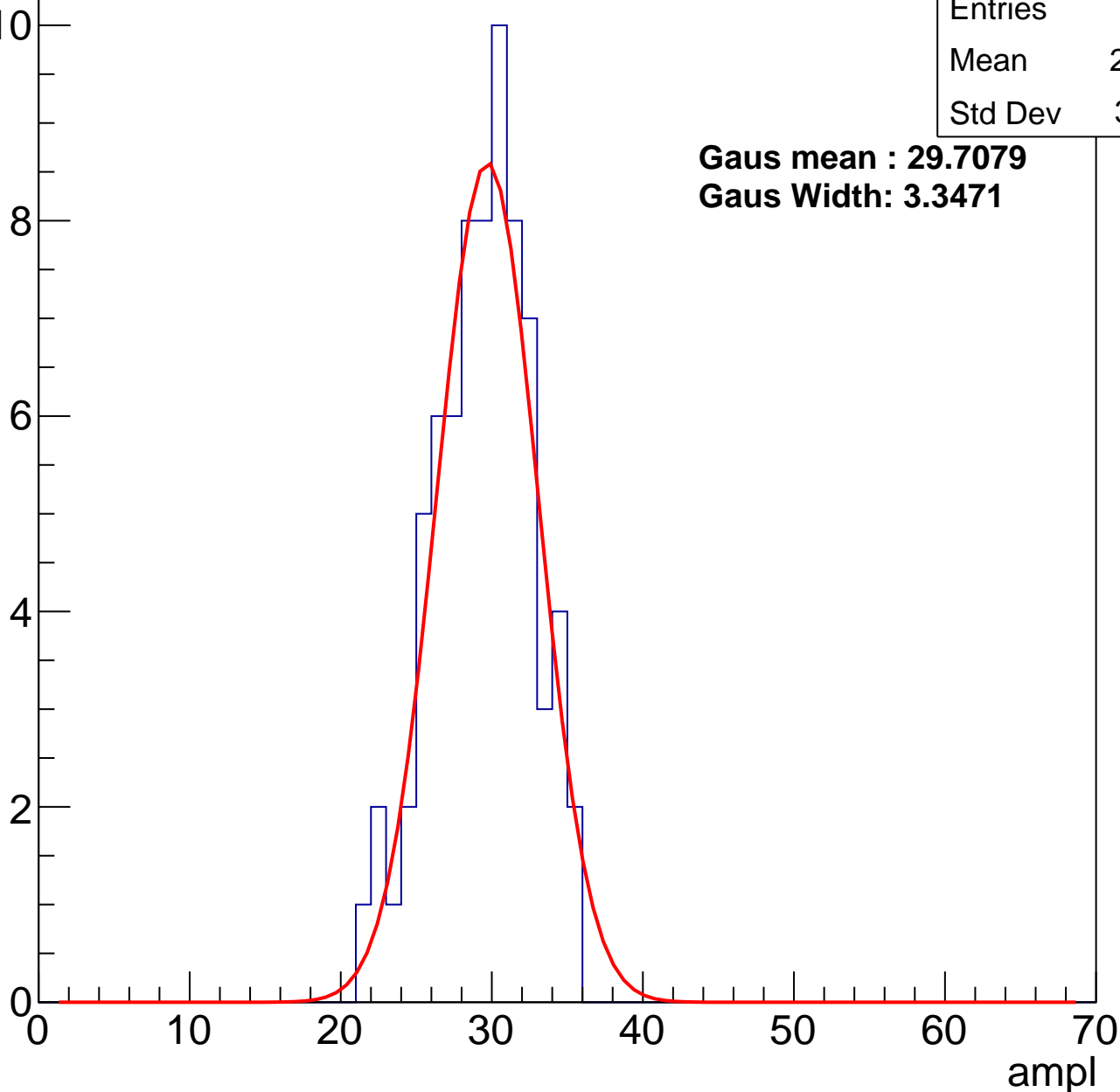
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.93
Std Dev	3.181

Gaus mean : 29.7079

Gaus Width: 3.3471



B1L100S, U12-ch16, adc1

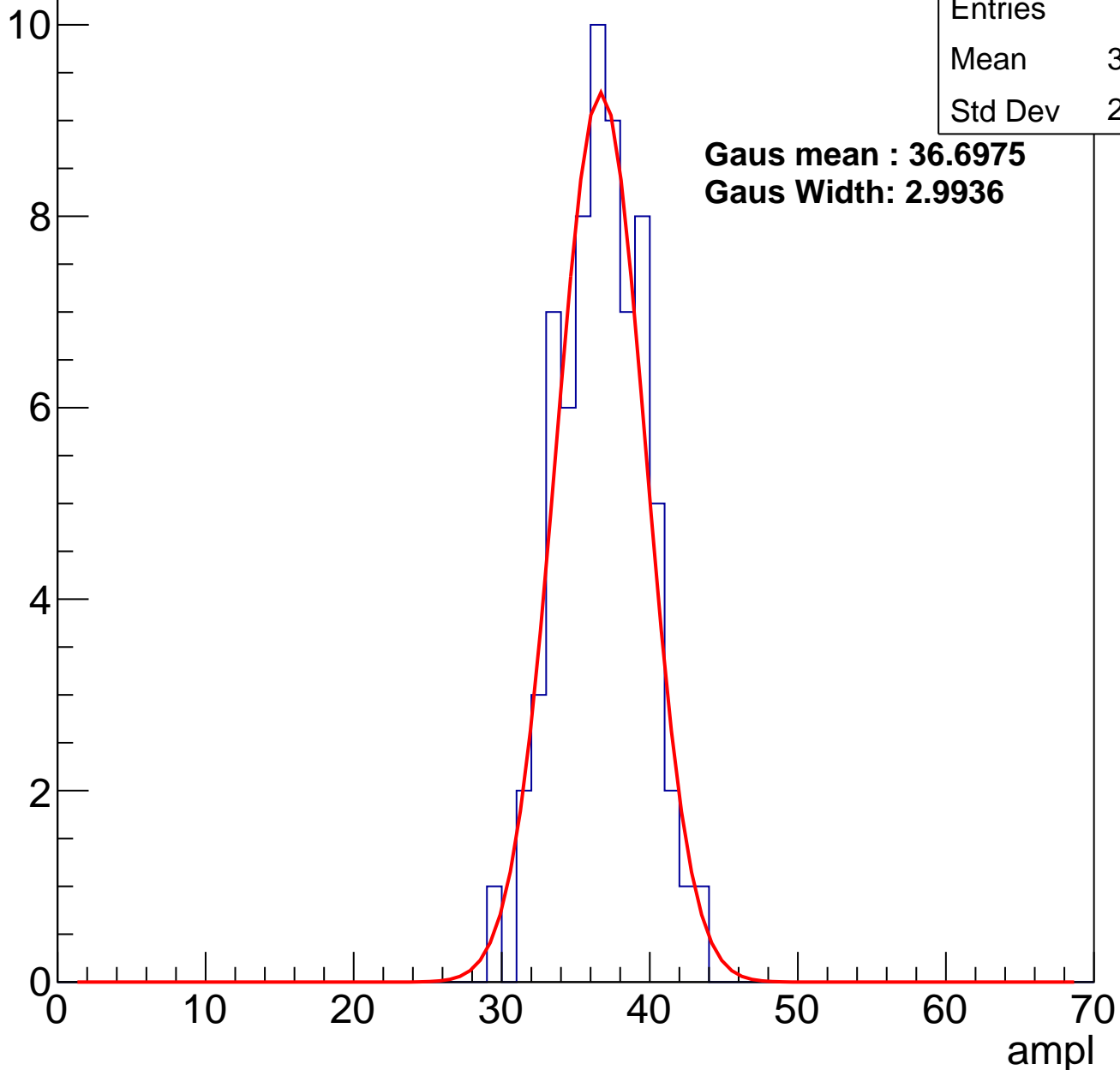
calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	36.29
Std Dev	2.829

Gaus mean : 36.6975

Gaus Width: 2.9936

Entry



B1L100S, U12-ch16, adc2

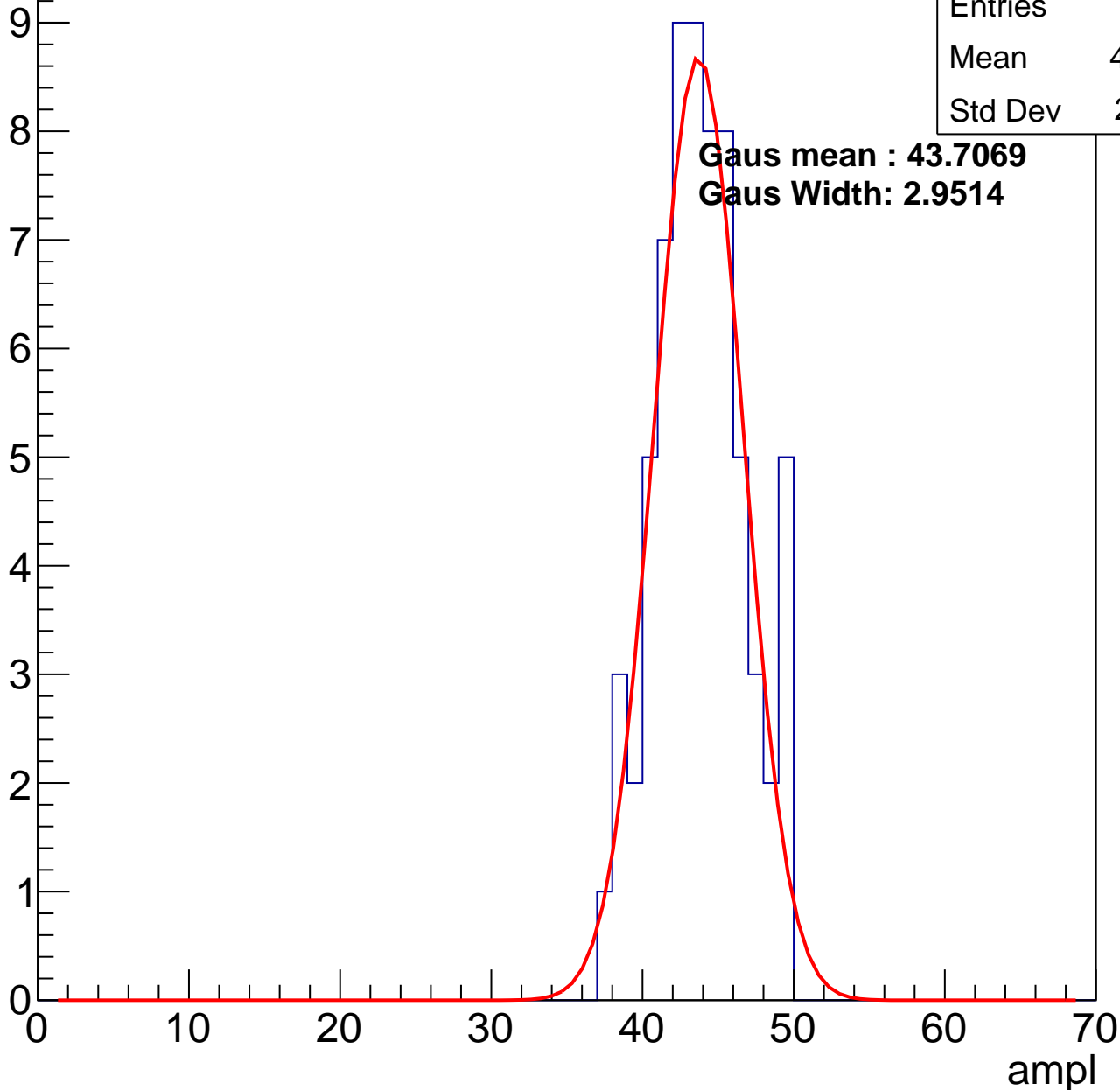
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.36
Std Dev	2.941

Gaus mean : 43.7069

Gaus Width: 2.9514

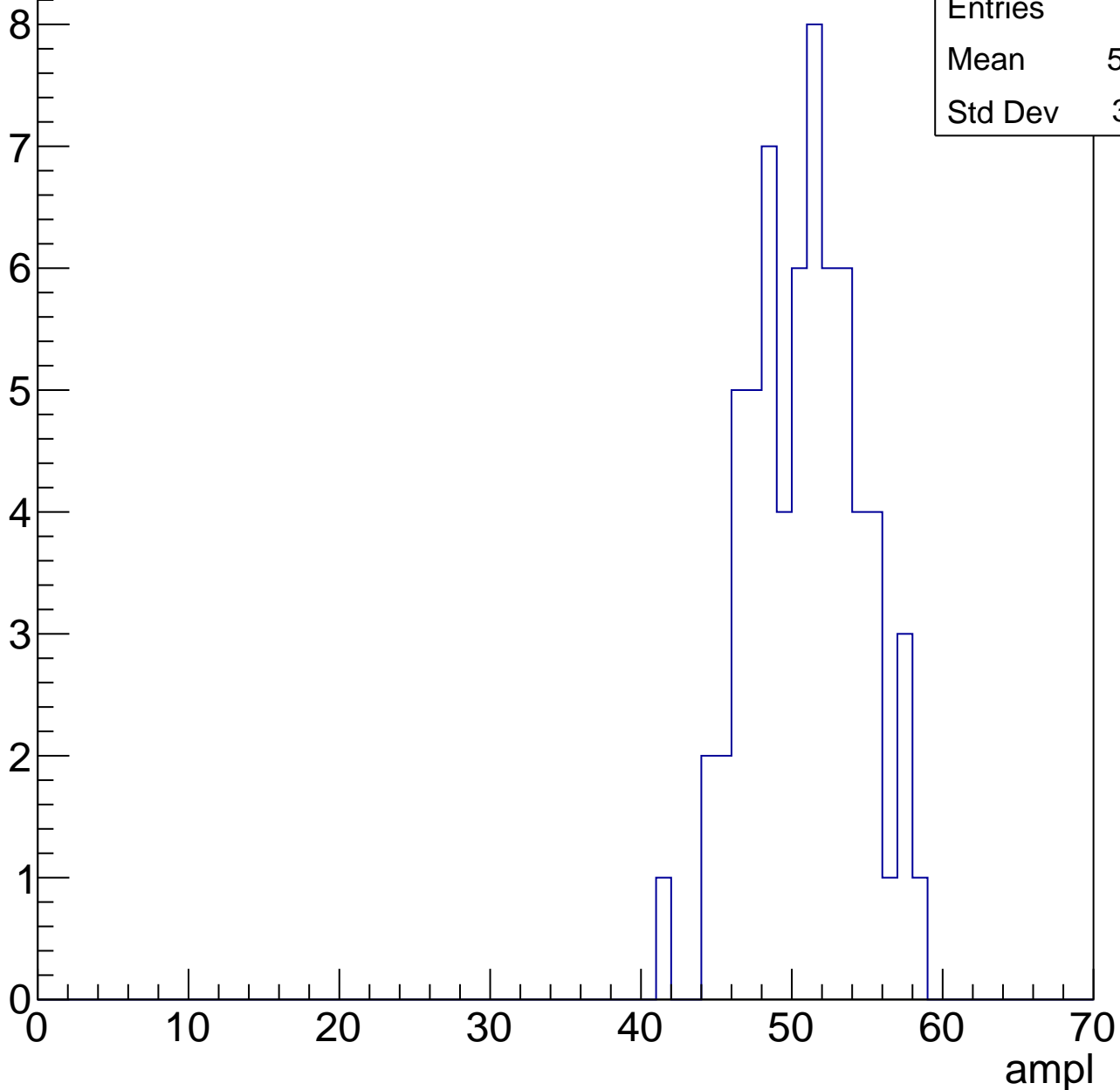


B1L100S, U12-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

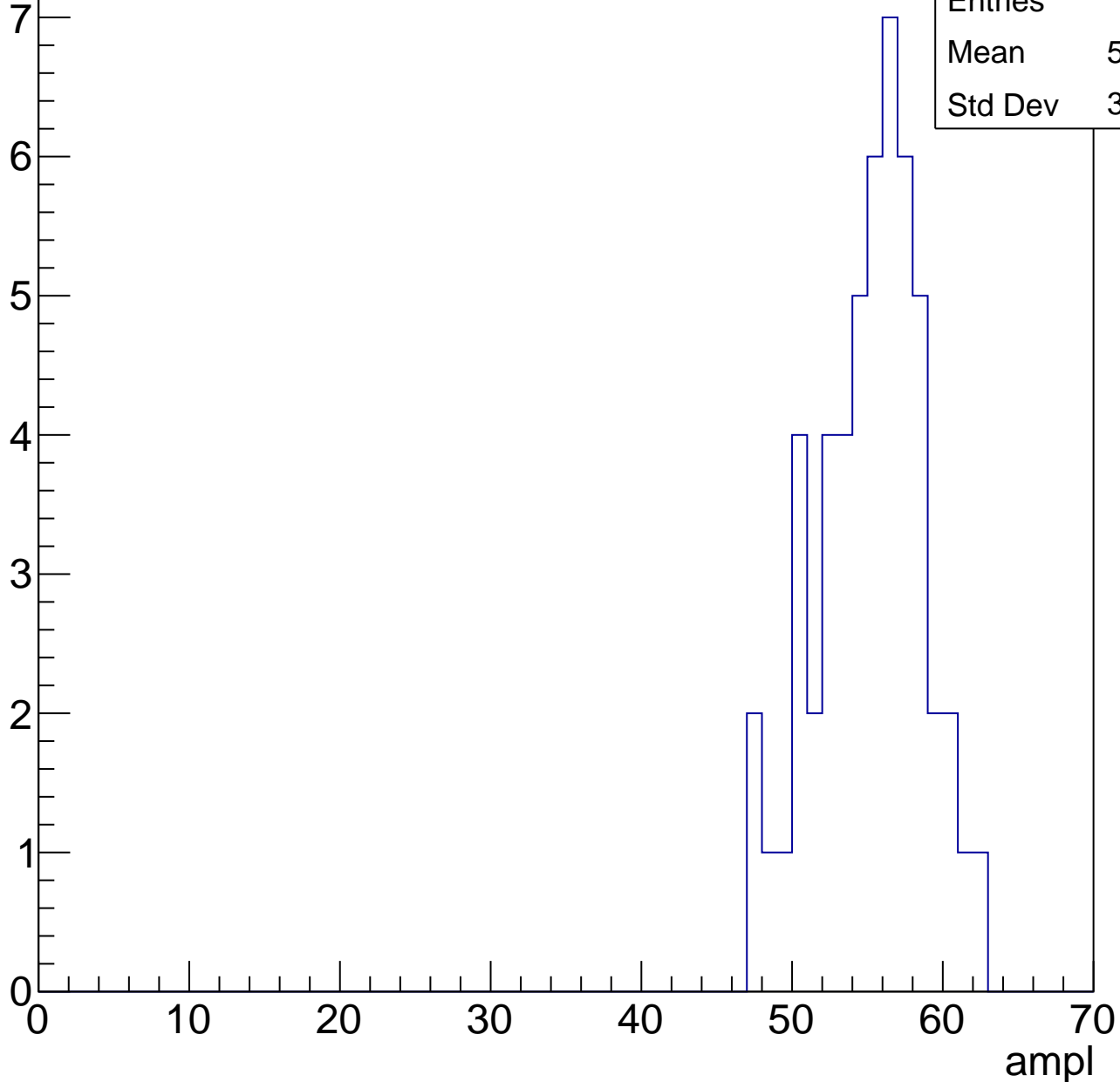
Entries	65
Mean	50.38
Std Dev	3.611



B1L100S, U12-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

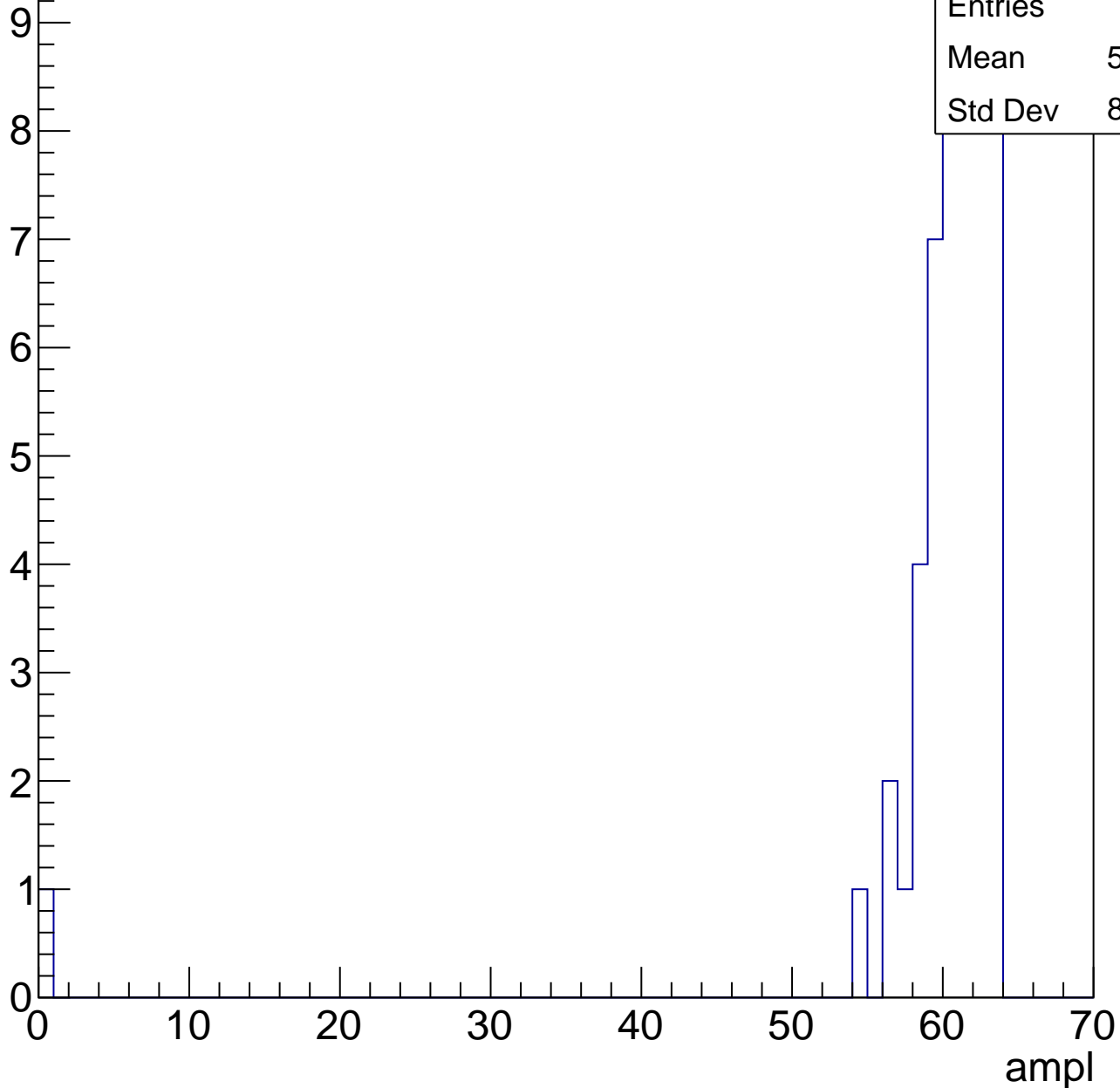
Entry



B1L100S, U12-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

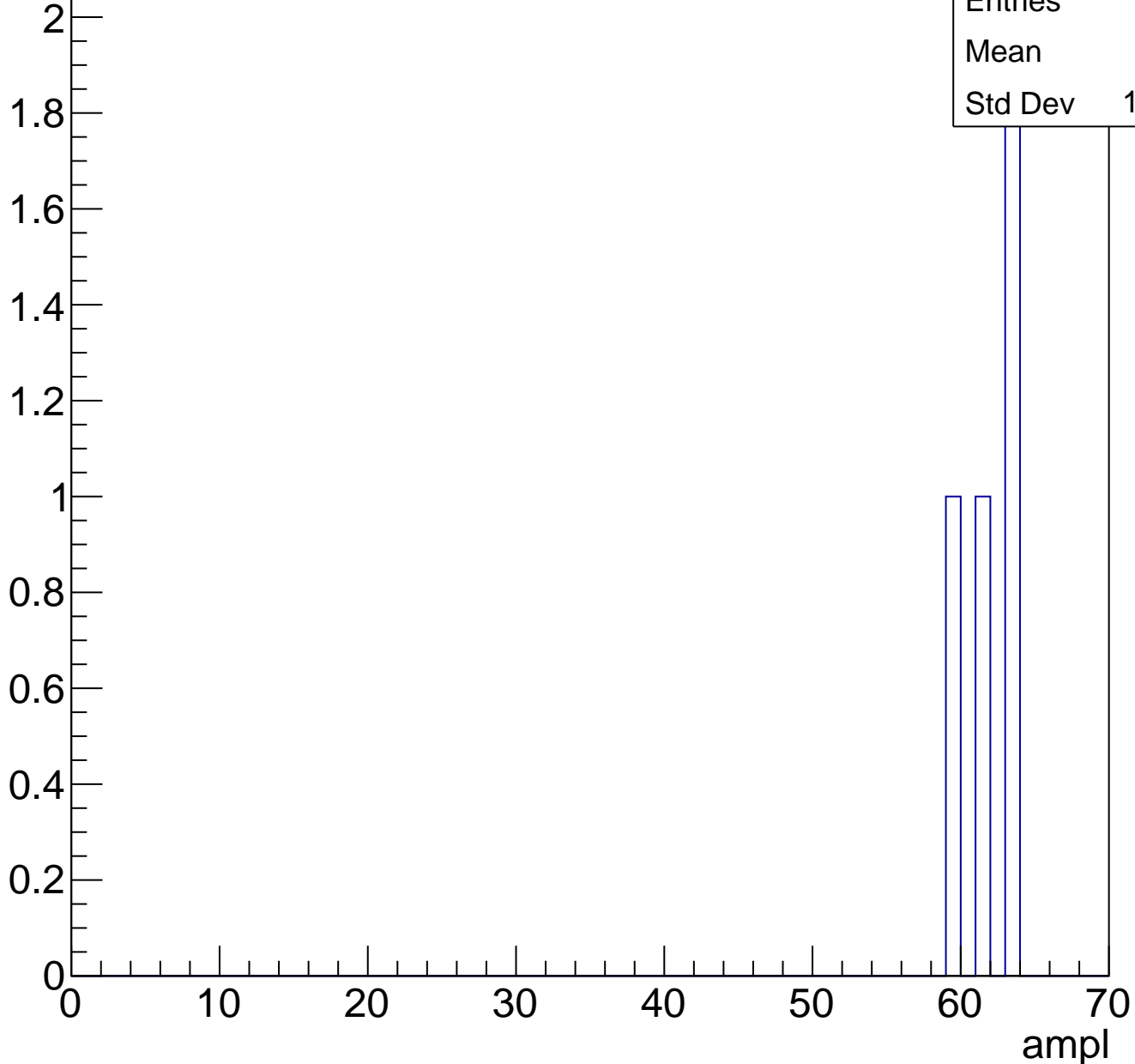
Entry



B1L100S, U12-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	61.5
Std Dev	1.658

B1L100S, U12-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch17, adc0

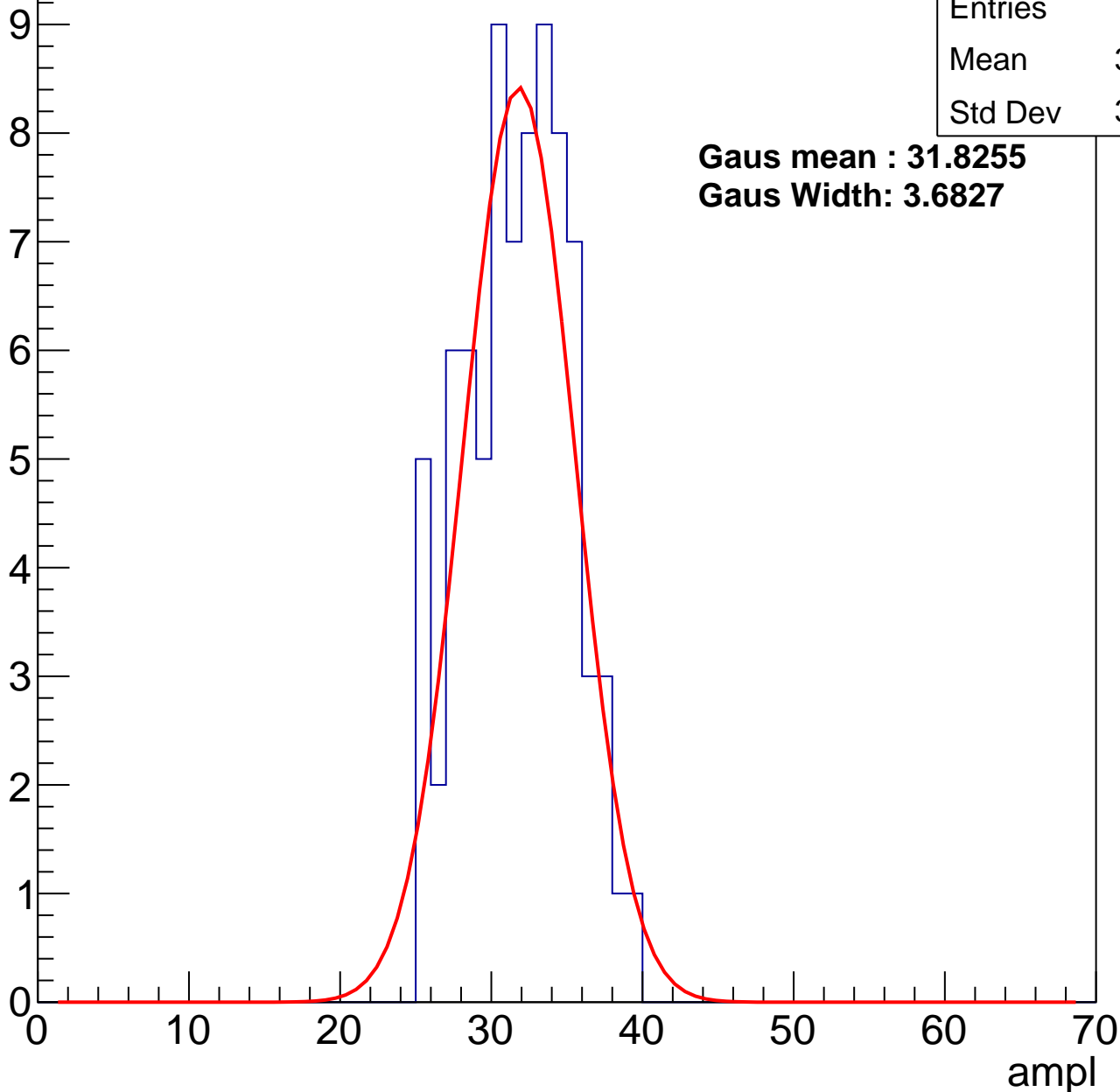
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	31.31
Std Dev	3.401

Gaus mean : 31.8255

Gaus Width: 3.6827



B1L100S, U12-ch17, adc1

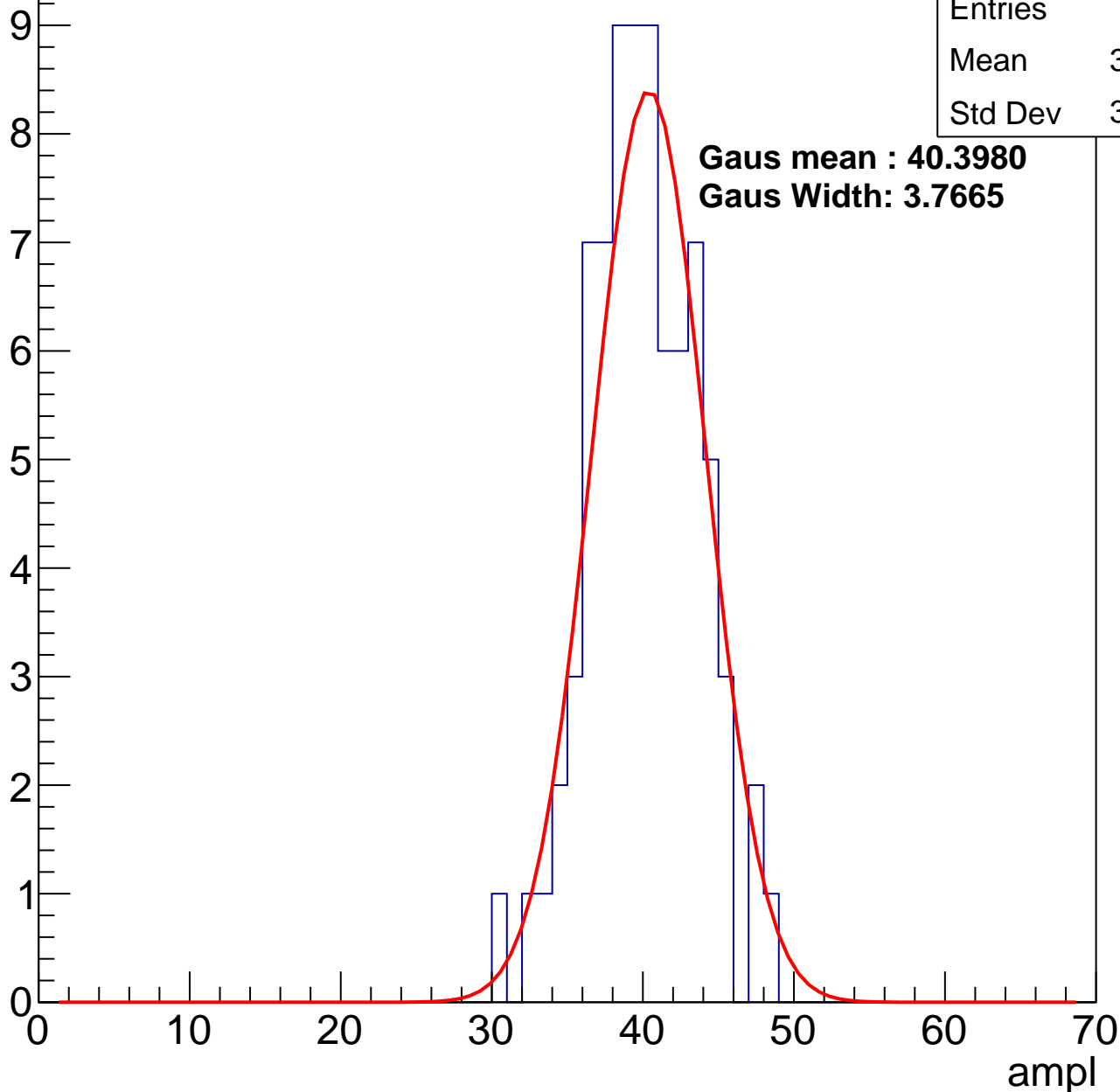
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	39.59
Std Dev	3.517

Gaus mean : 40.3980

Gaus Width: 3.7665



B1L100S, U12-ch17, adc2

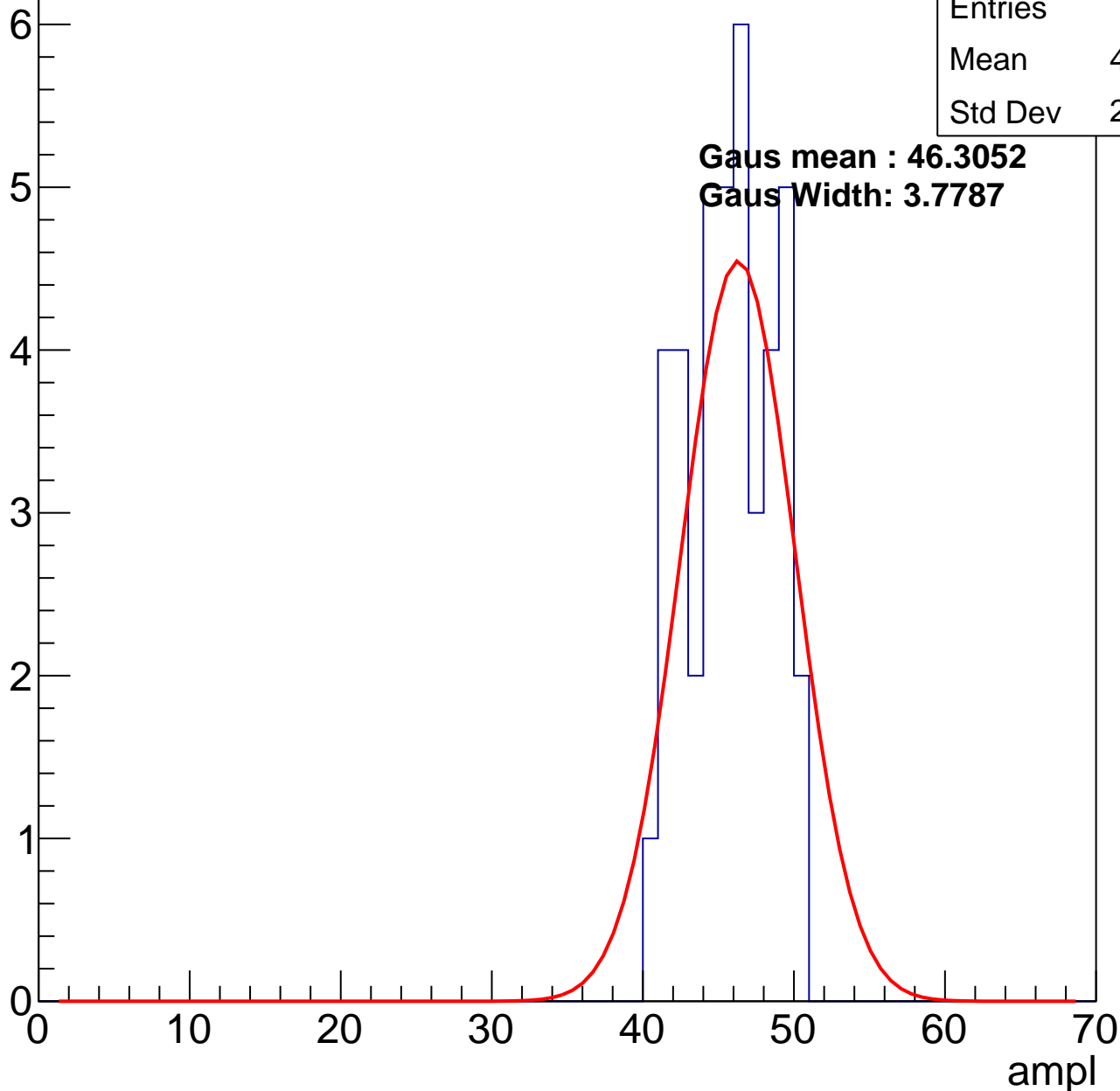
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	45.29
Std Dev	2.787

Gaus mean : 46.3052

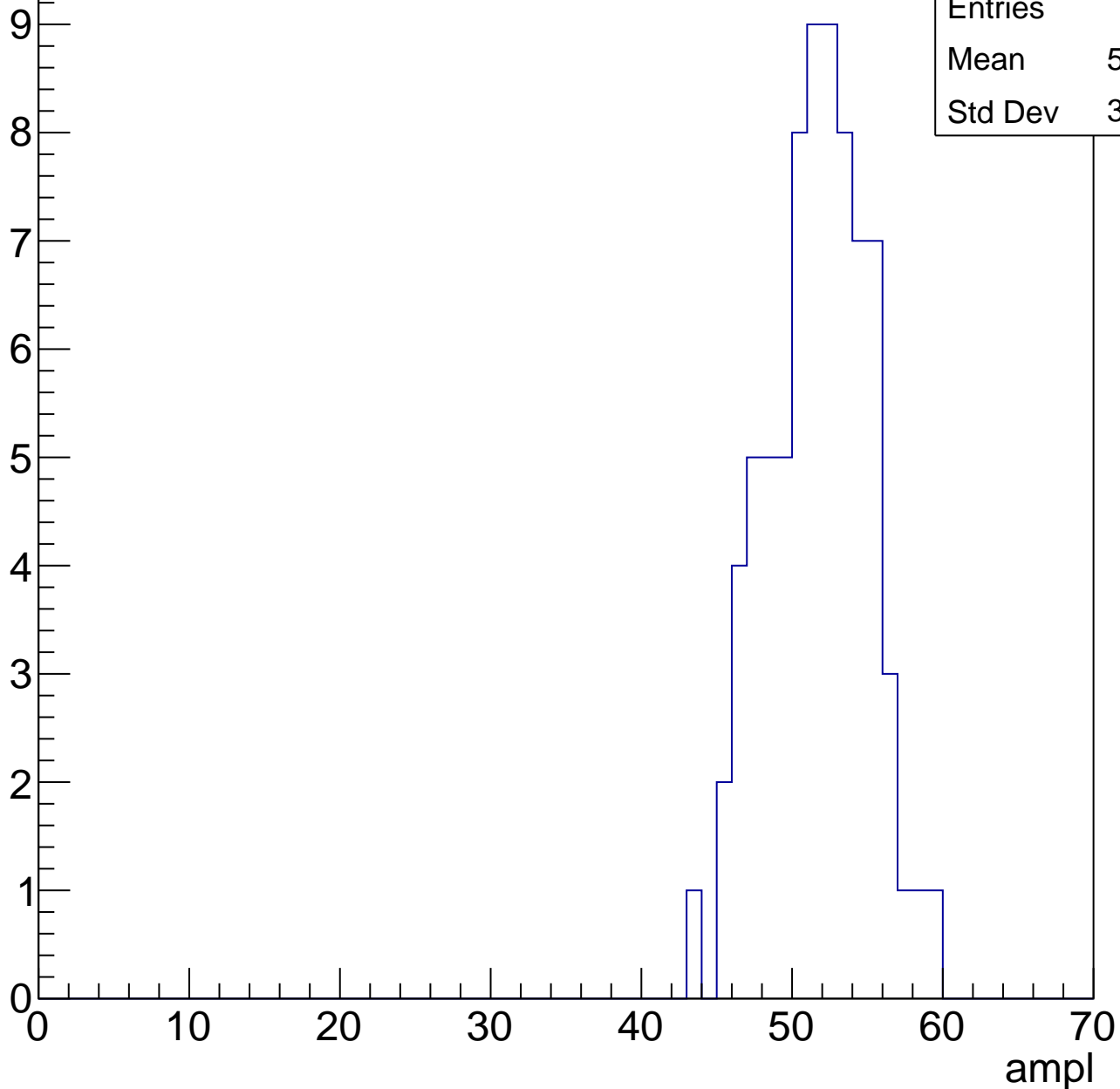
Gaus Width: 3.7787



B1L100S, U12-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

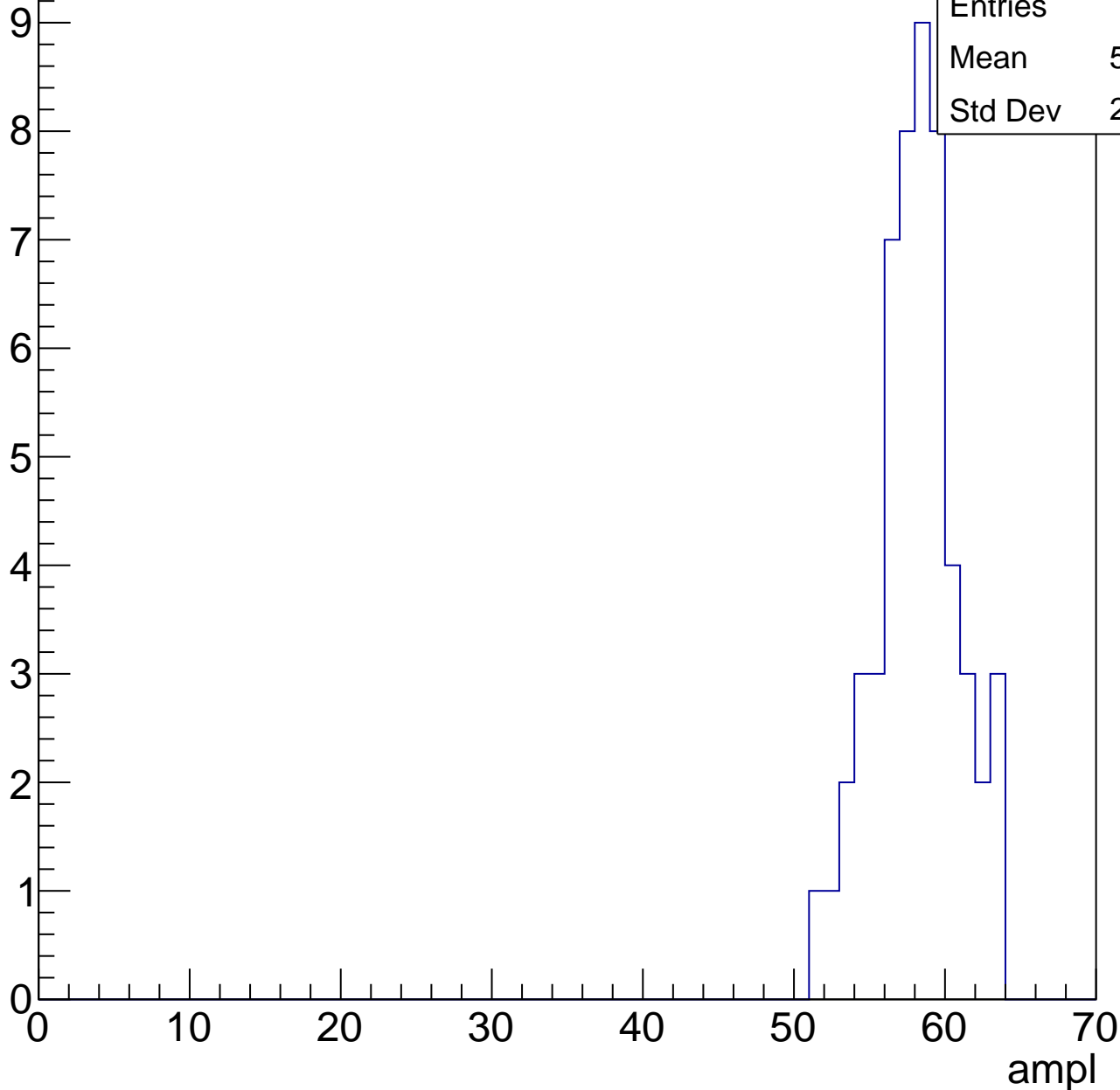
Entry



B1L100S, U12-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

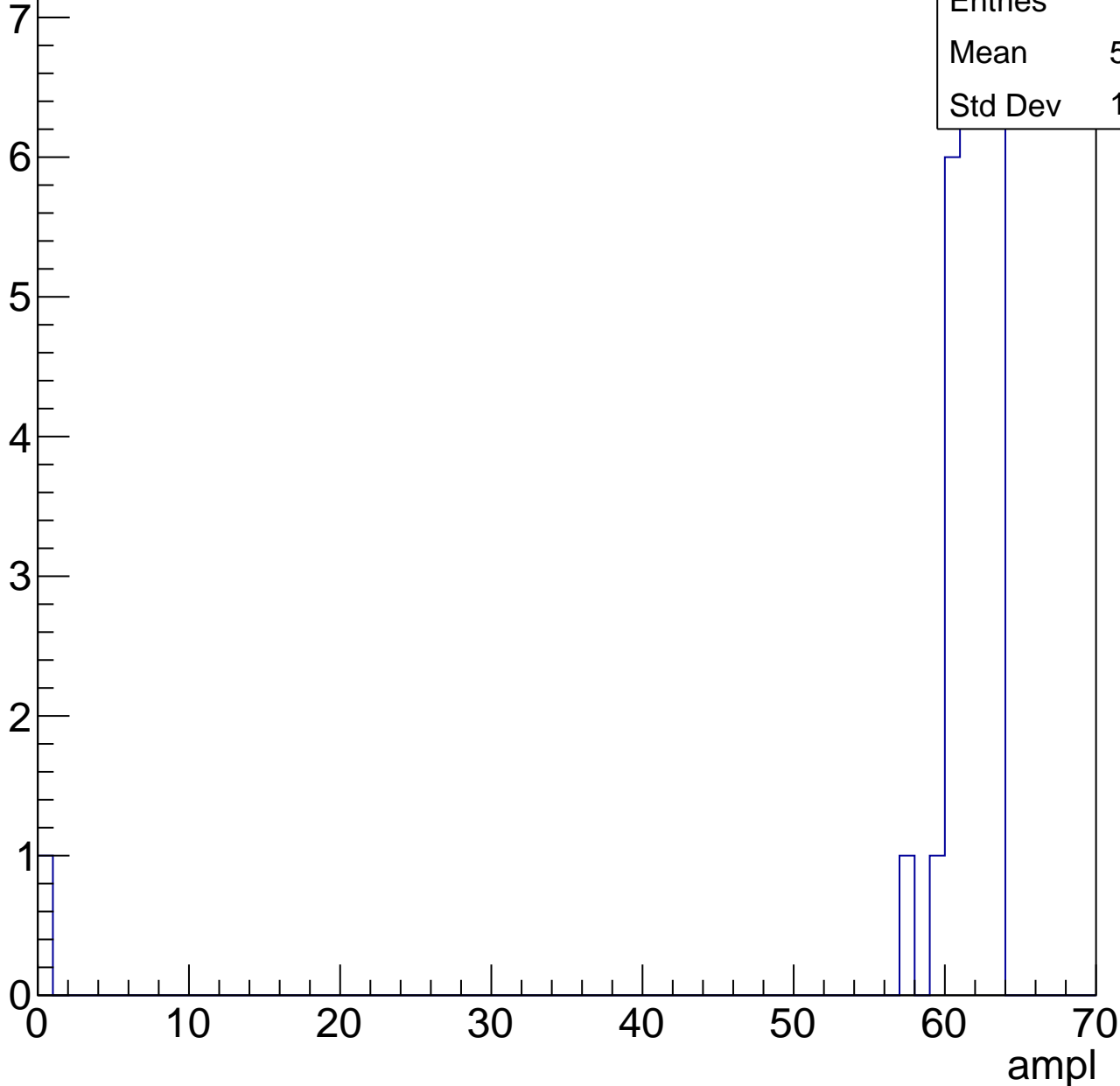


B1L100S, U12-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	59.27
Std Dev	11.09



B1L100S, U12-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U12-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch18, adc0

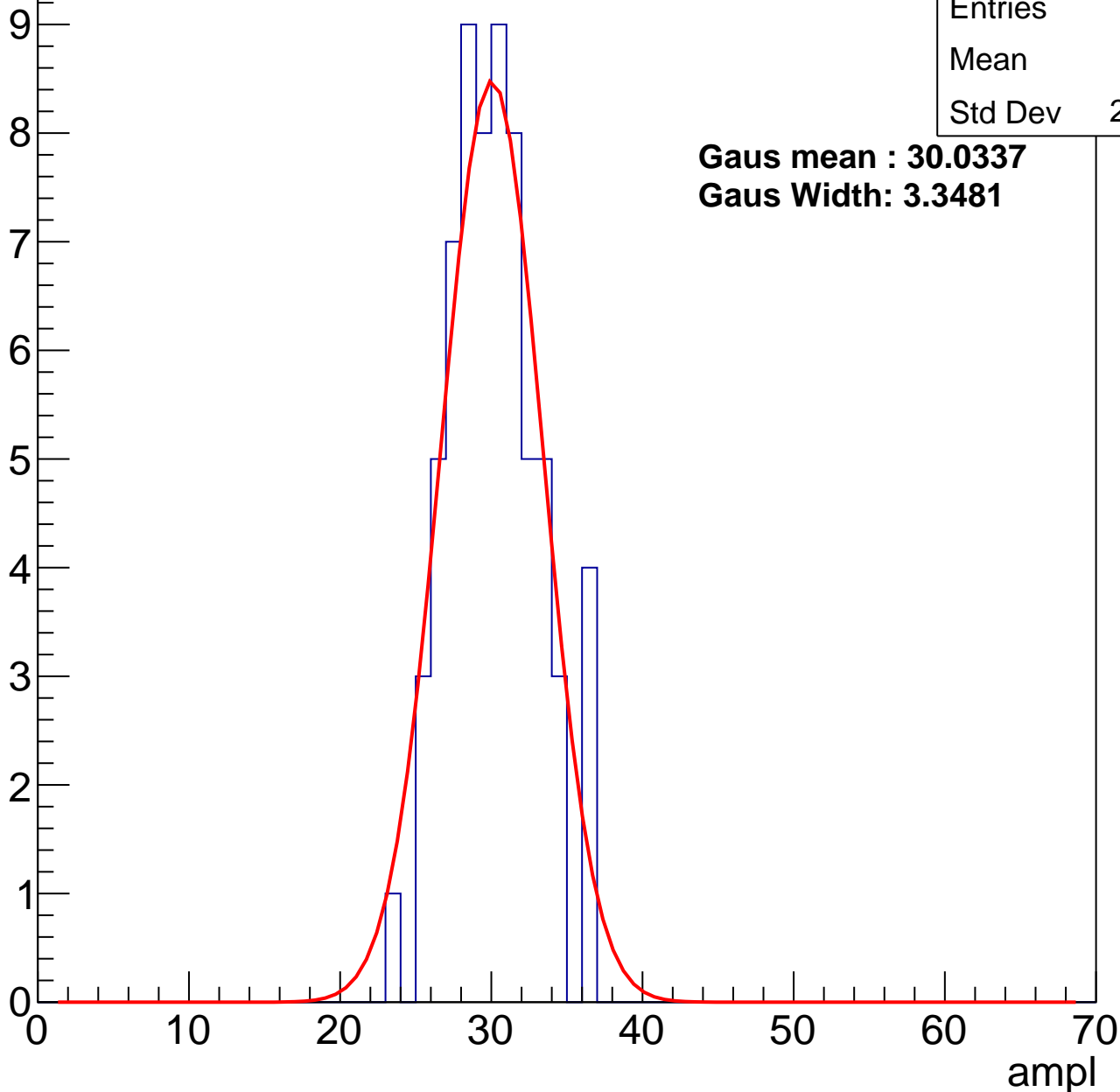
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	29.7
Std Dev	2.917

Gaus mean : 30.0337

Gaus Width: 3.3481



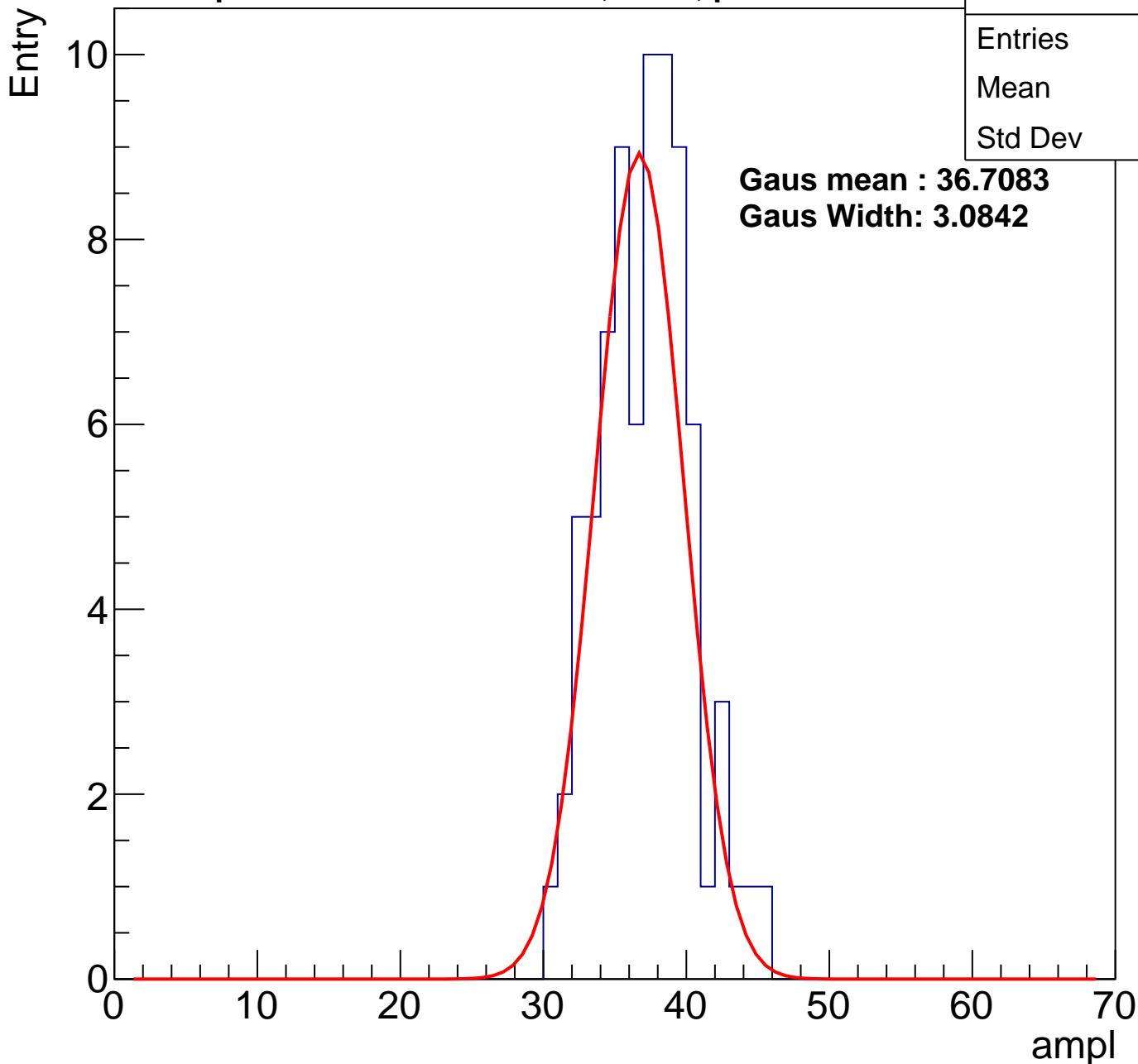
B1L100S, U12-ch18, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	36.7
Std Dev	3.15

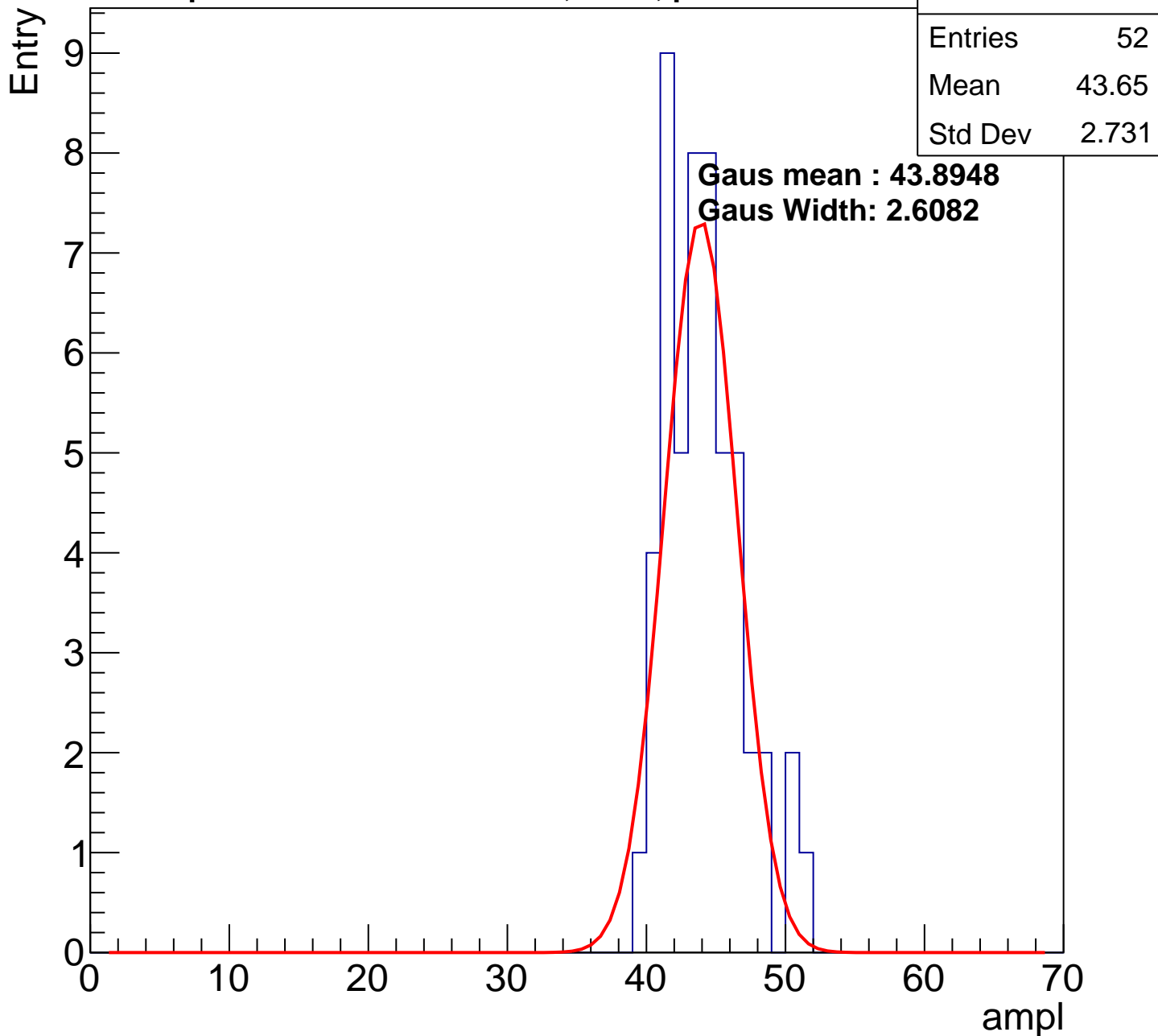
Gaus mean : 36.7083

Gaus Width: 3.0842



B1L100S, U12-ch18, adc2

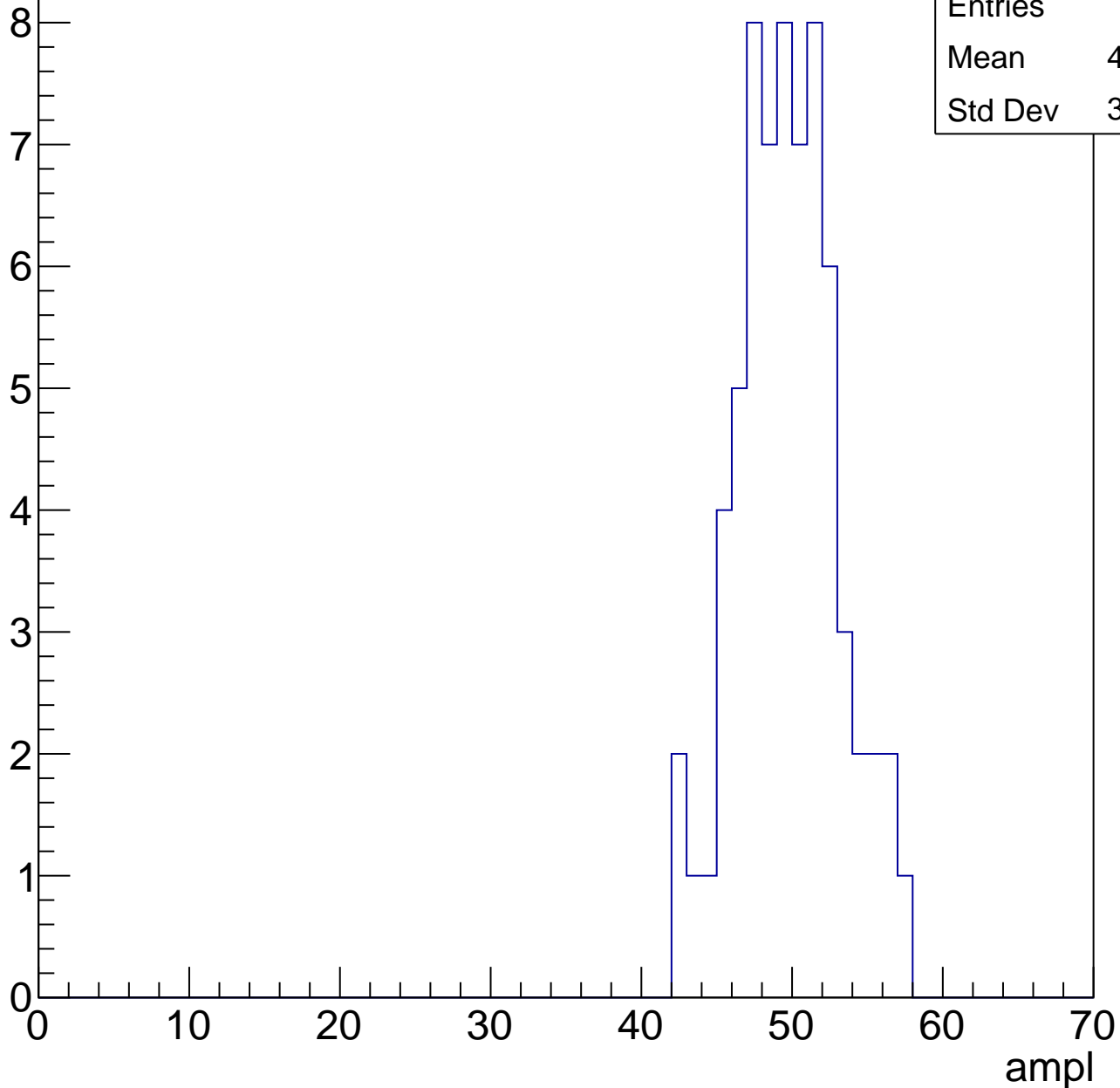
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	67
Mean	49.27
Std Dev	3.294

B1L100S, U12-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

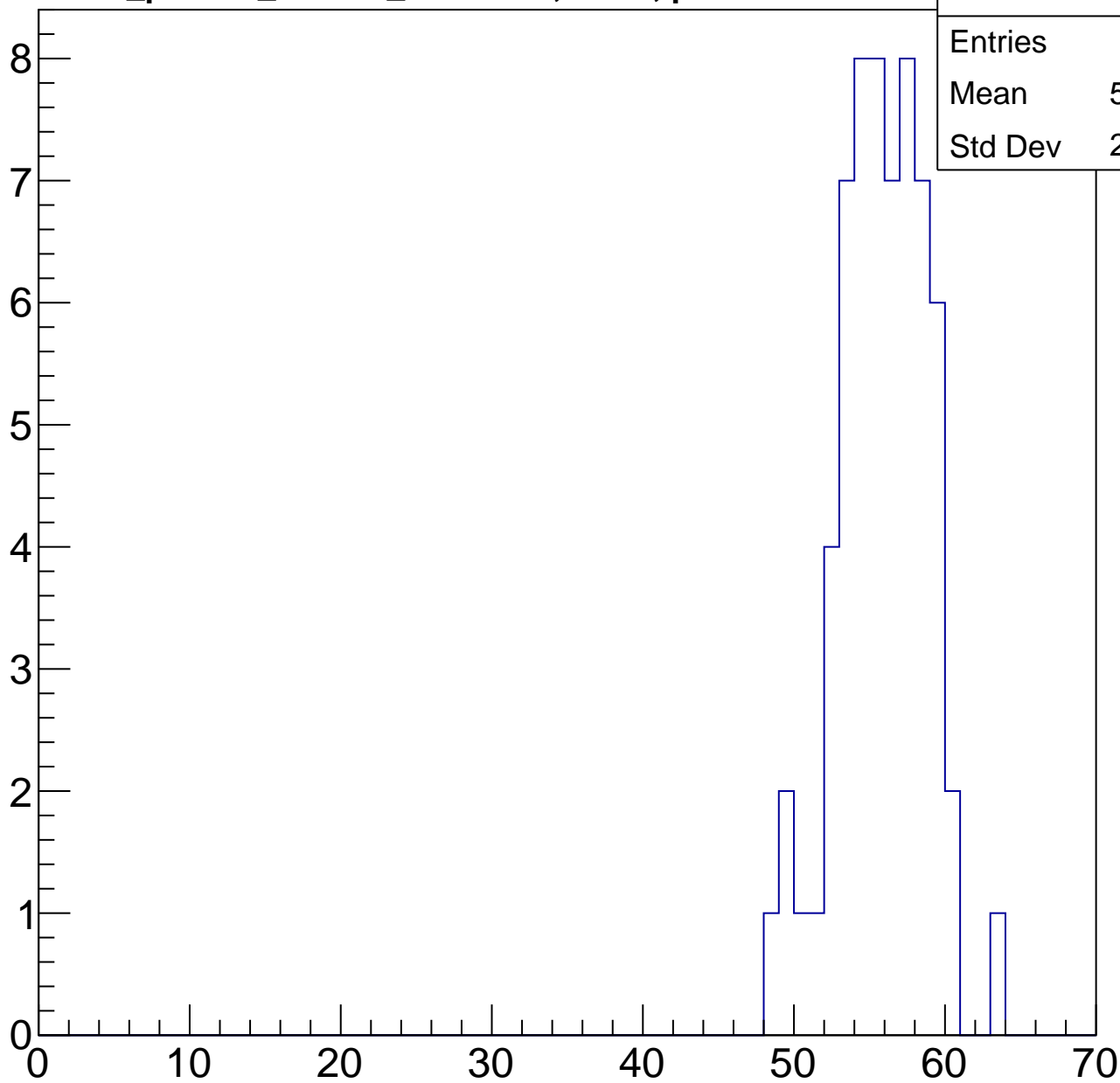
Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	55.38
Std Dev	2.919

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

58.71

Std Dev

10.37

ampl

0

10

20

30

40

50

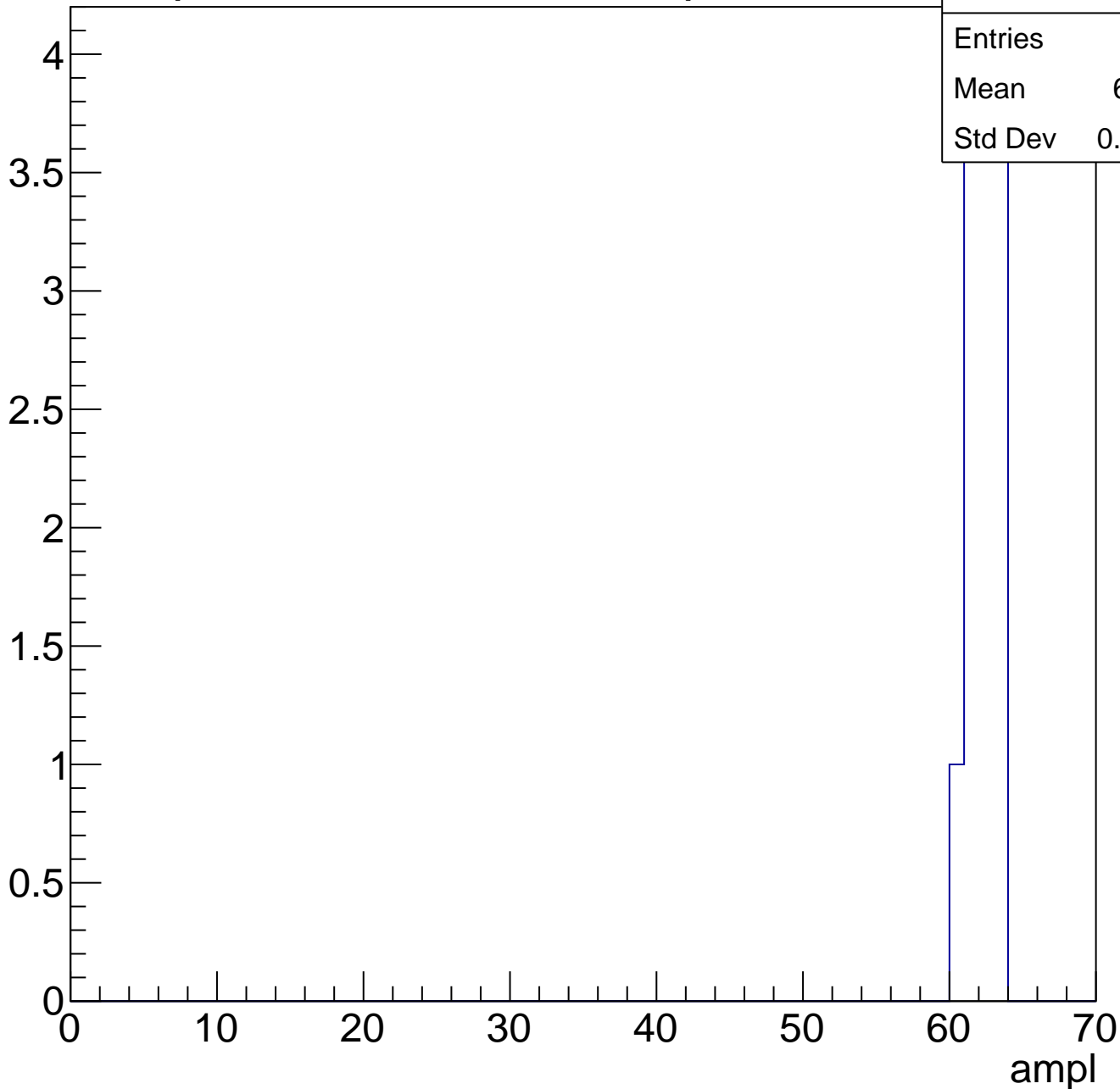
60

70

B1L100S, U12-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch19, adc0

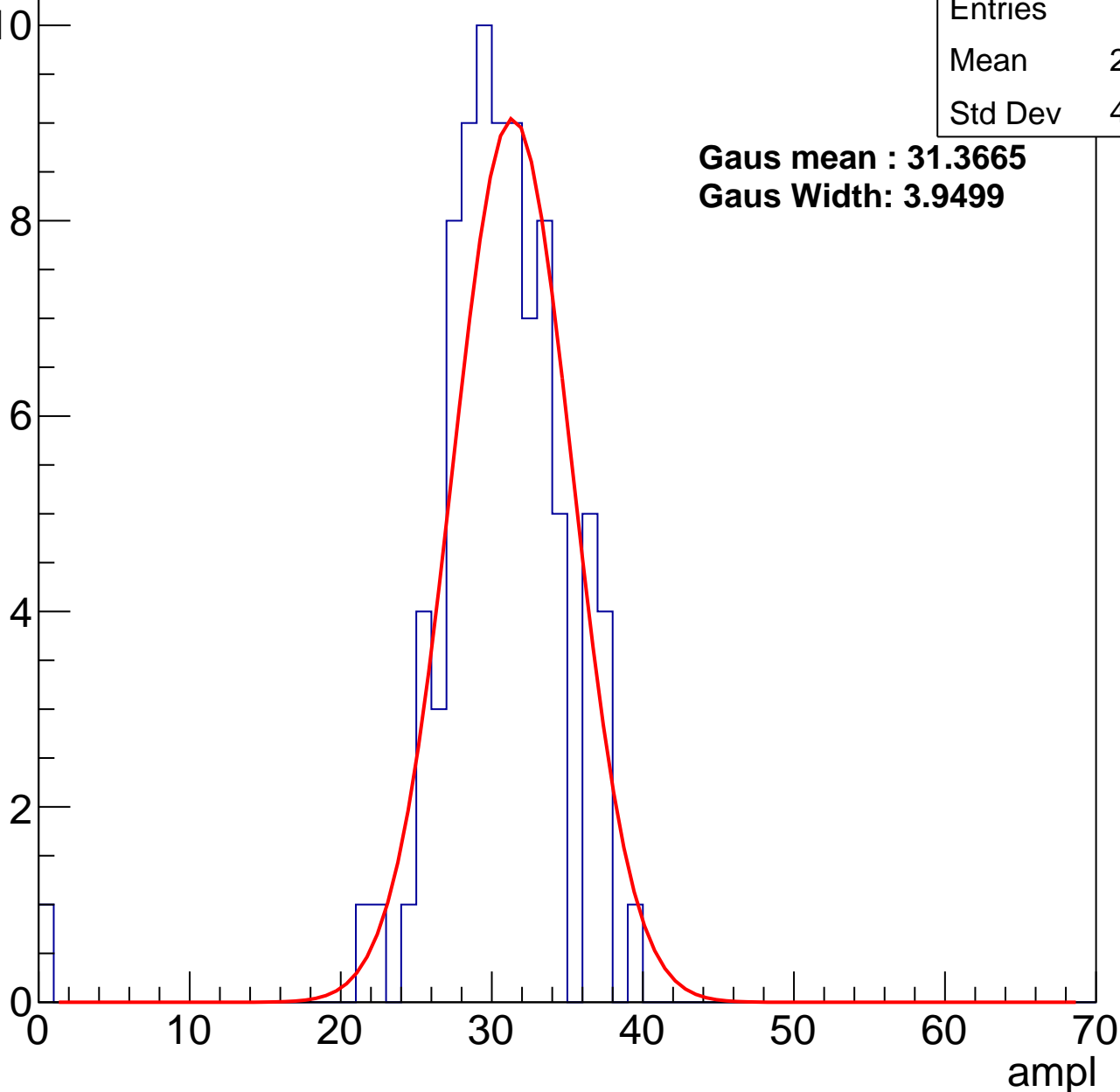
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.97
Std Dev	4.814

Gaus mean : 31.3665

Gaus Width: 3.9499



B1L100S, U12-ch19, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	38.06
Std Dev	3.242

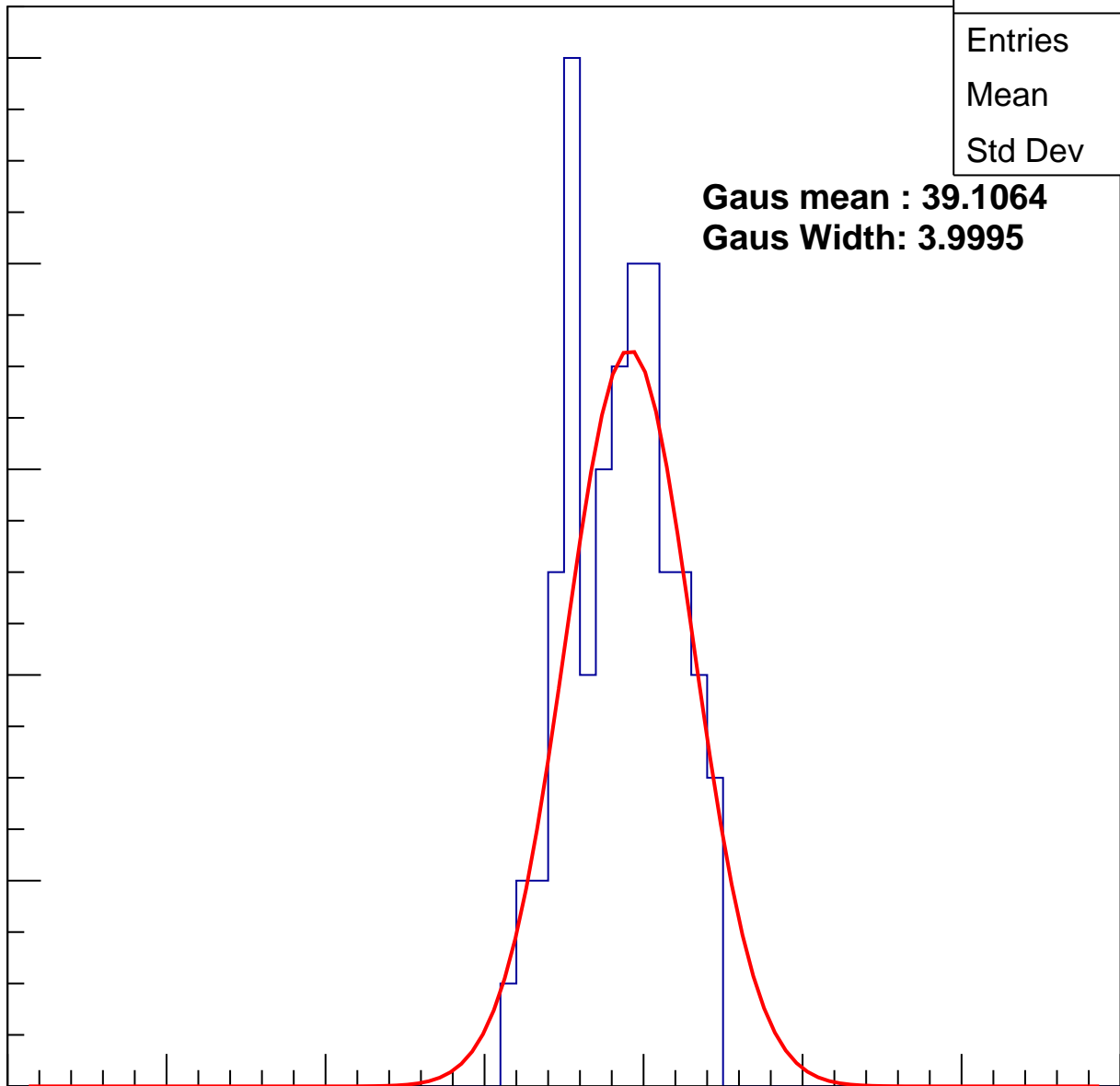
Gaus mean : 39.1064
Gaus Width: 3.9995

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch19, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	45.64
Std Dev	4.124

Gaus mean : 46.3536

Gaus Width: 3.8068

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

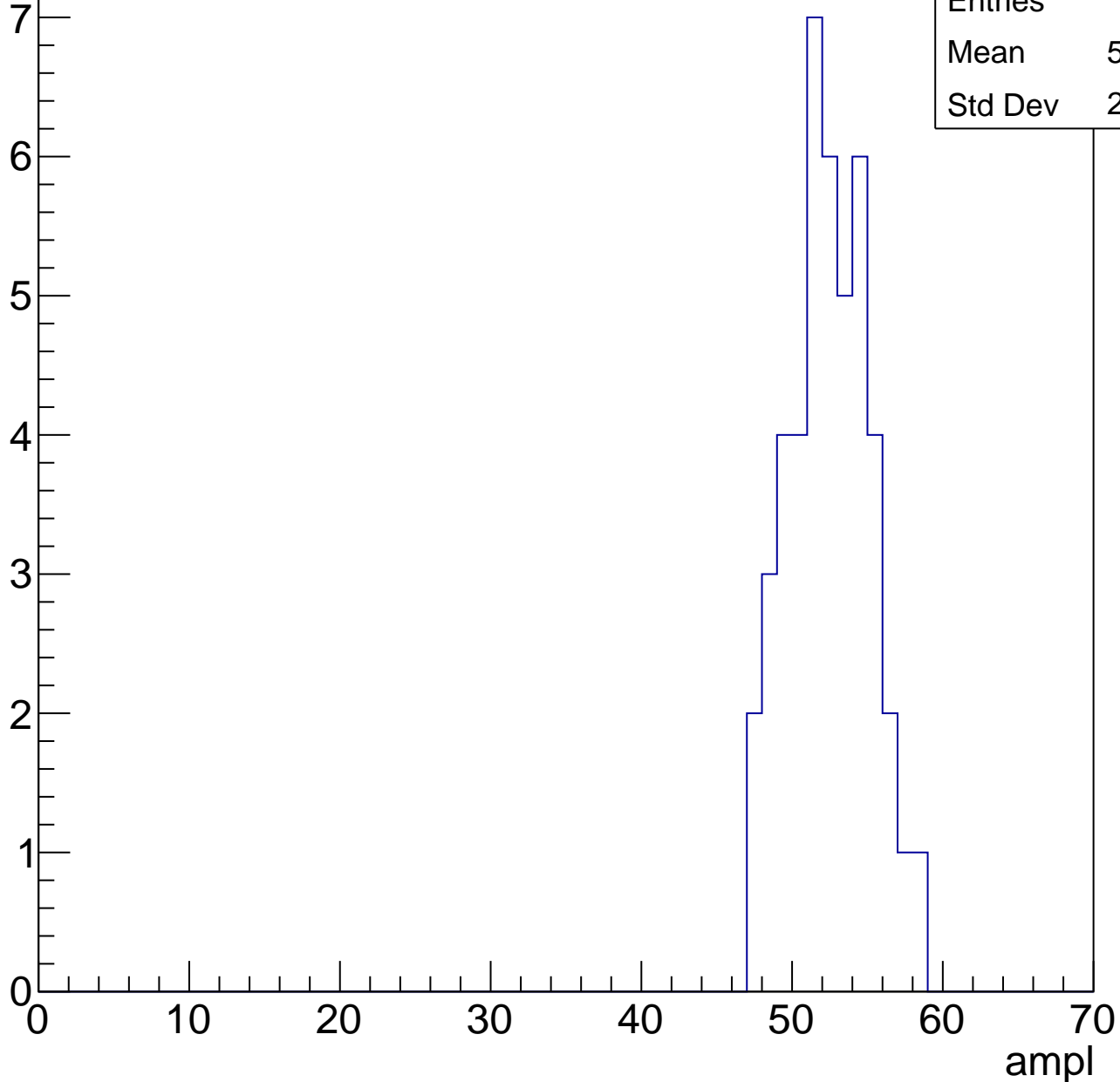
70

B1L100S, U12-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	51.98
Std Dev	2.646

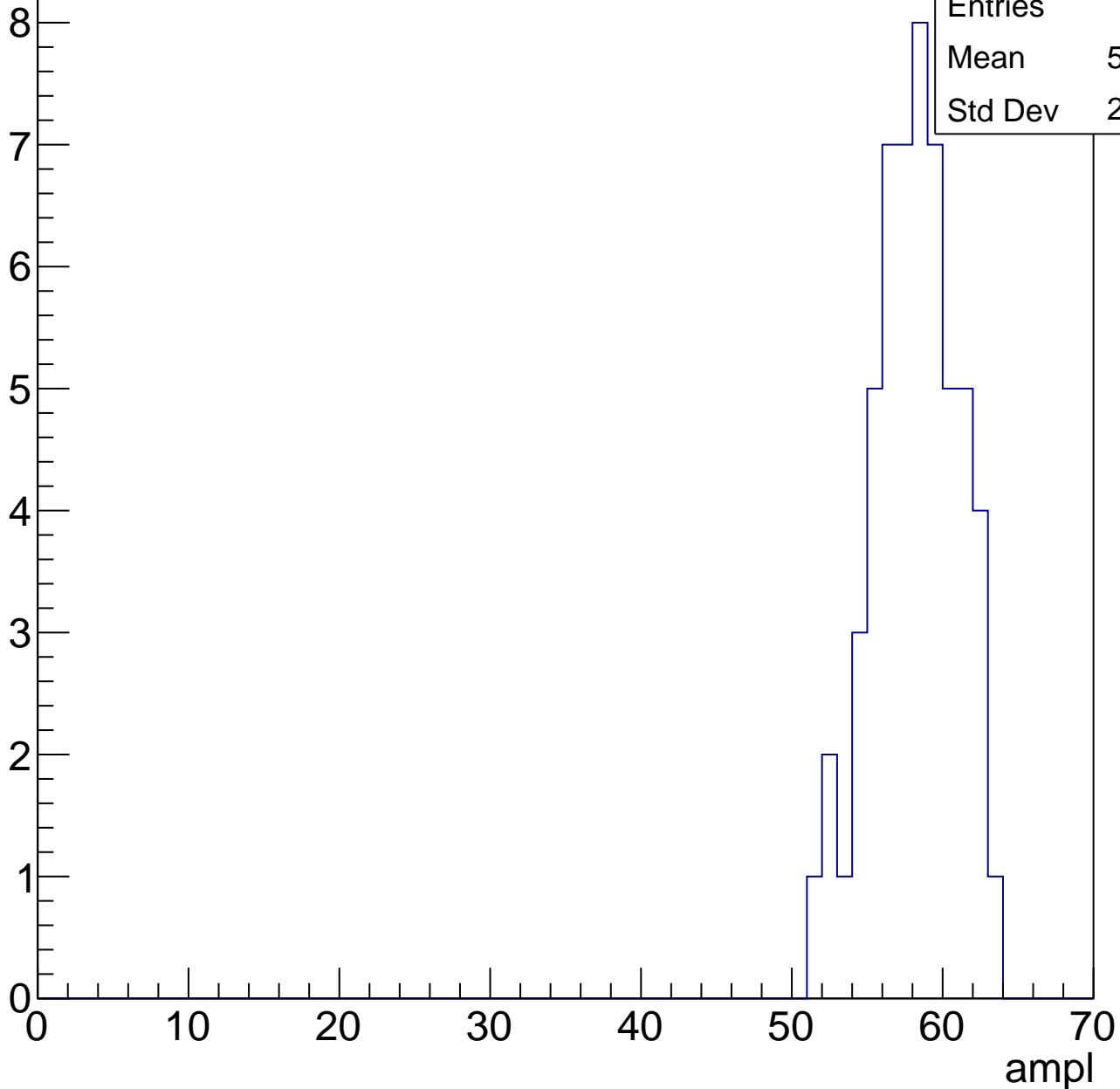


B1L100S, U12-ch19, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	57.66
Std Dev	2.766

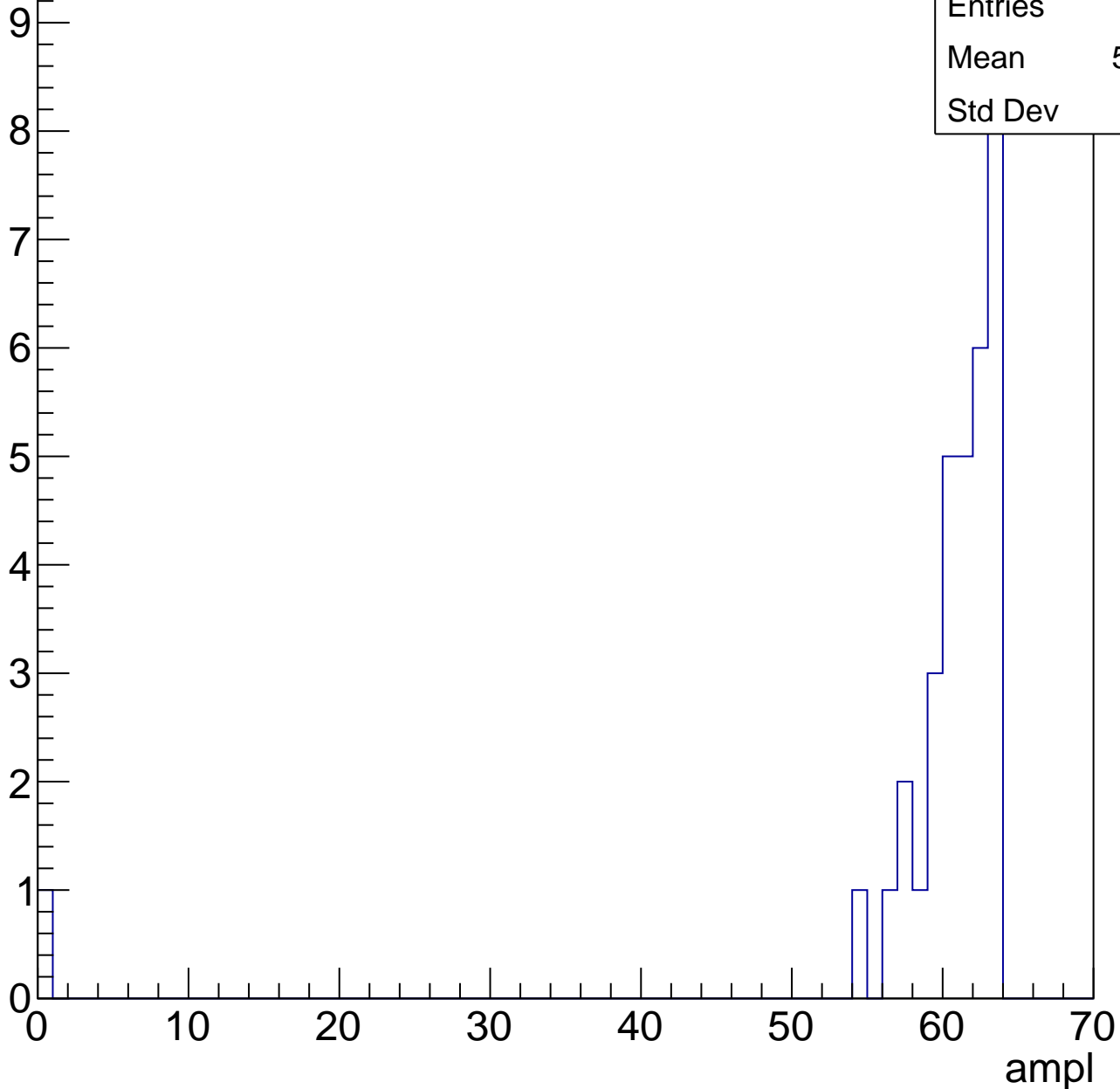


B1L100S, U12-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	58.91
Std Dev	10.5



B1L100S, U12-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch20, adc0

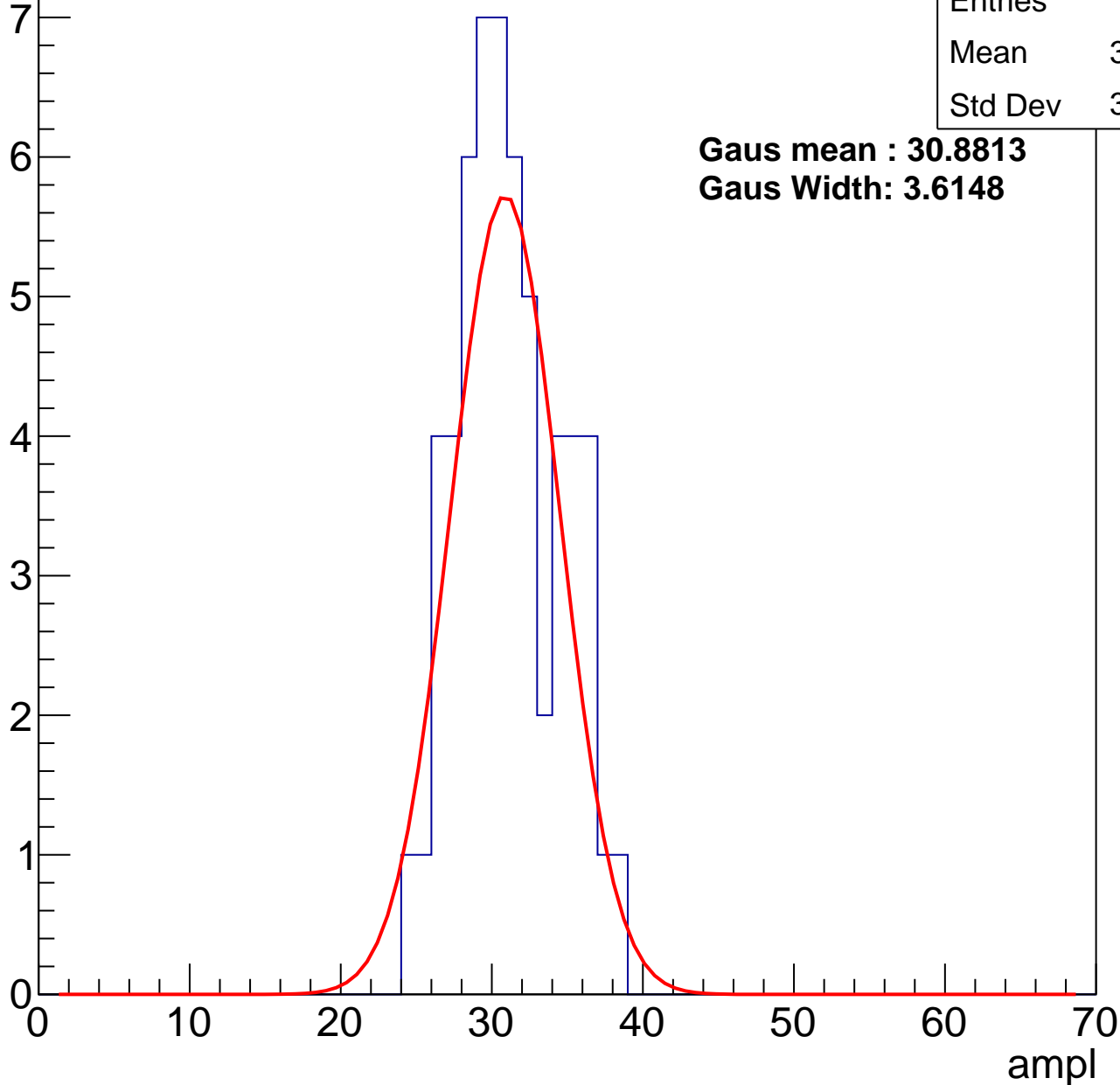
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	30.68
Std Dev	3.325

Gaus mean : 30.8813

Gaus Width: 3.6148



B1L100S, U12-ch20, adc1

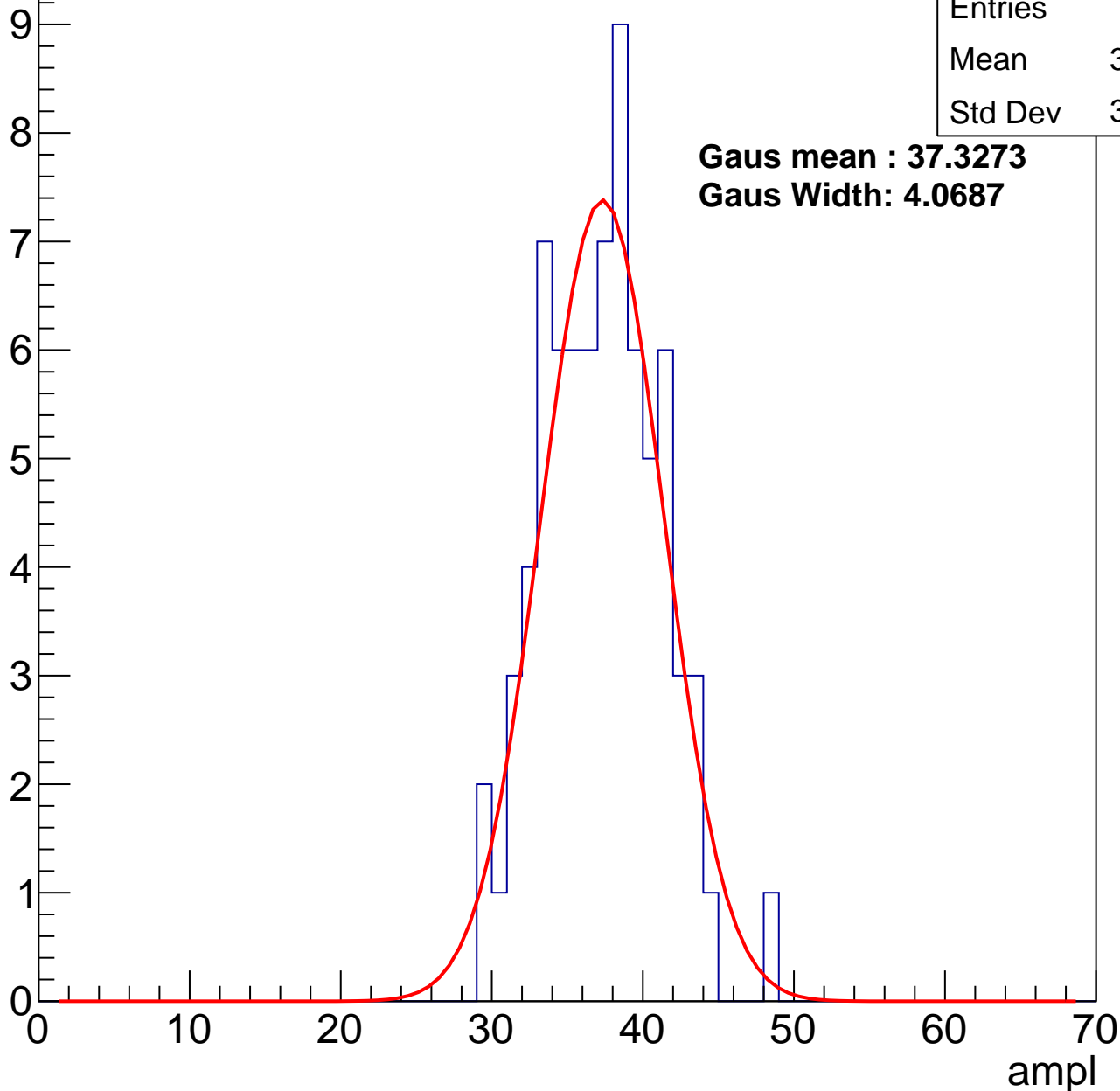
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.82
Std Dev	3.814

Gaus mean : 37.3273

Gaus Width: 4.0687



B1L100S, U12-ch20, adc2

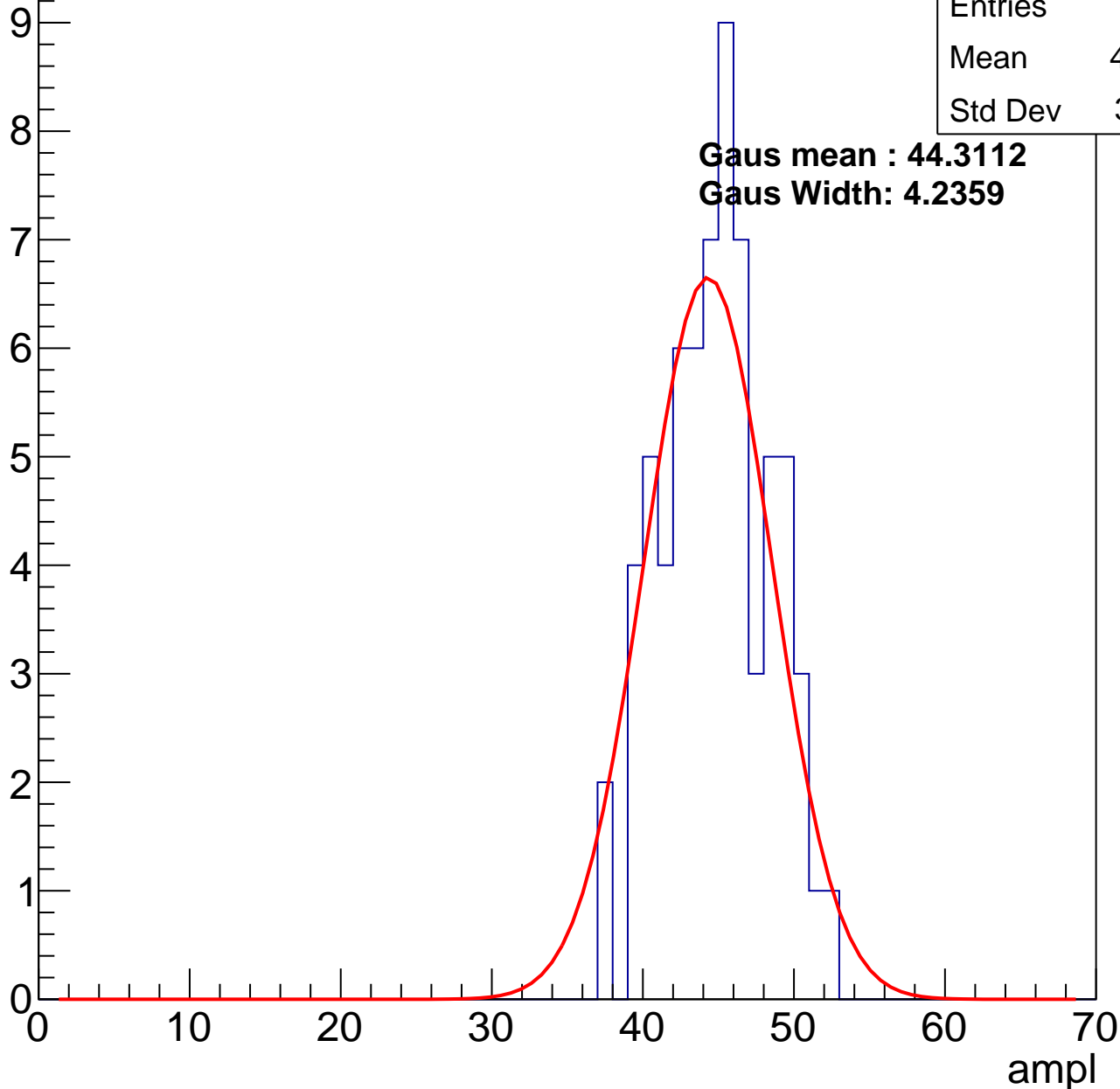
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.38
Std Dev	3.481

Gaus mean : 44.3112

Gaus Width: 4.2359

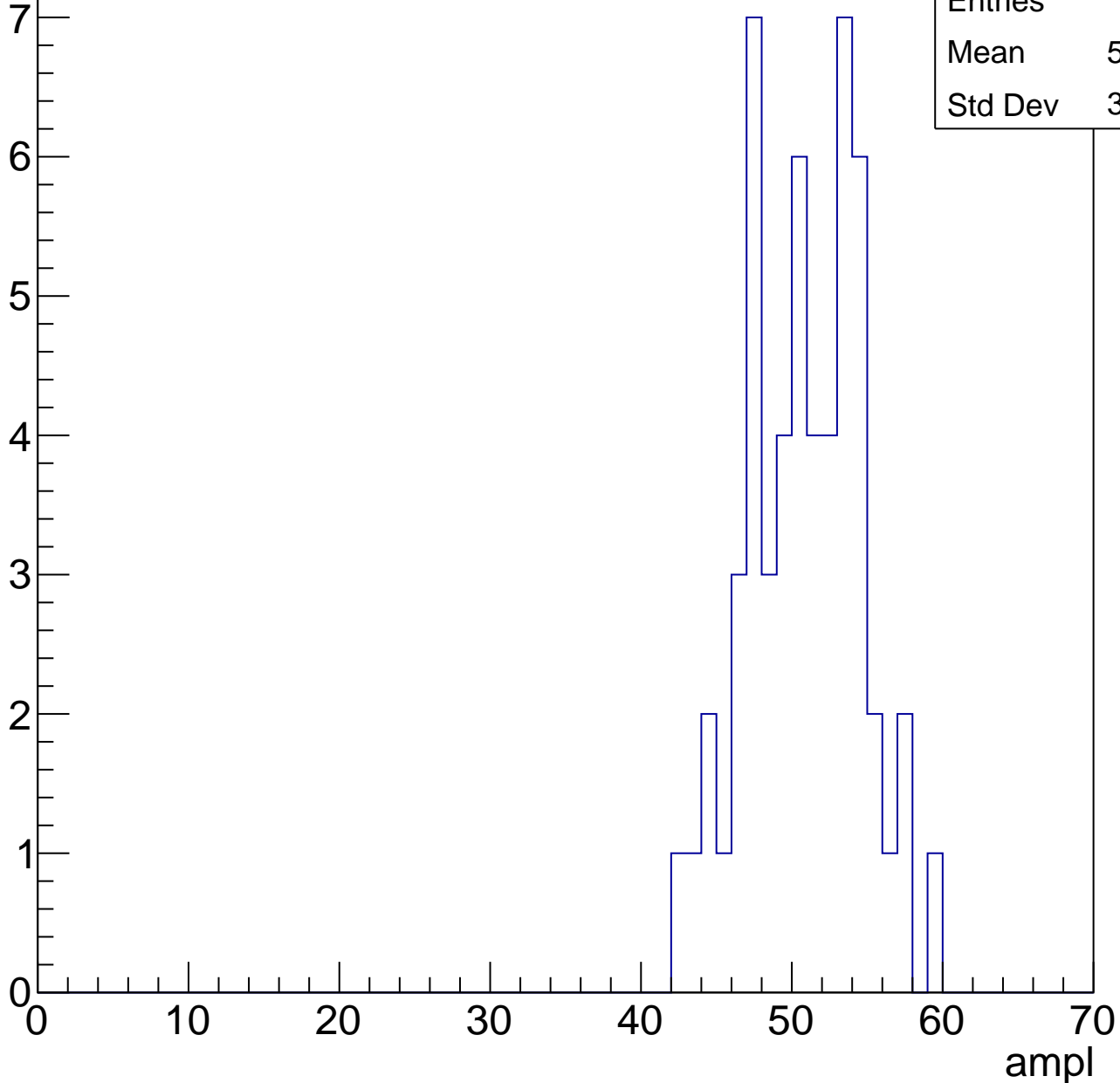


B1L100S, U12-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

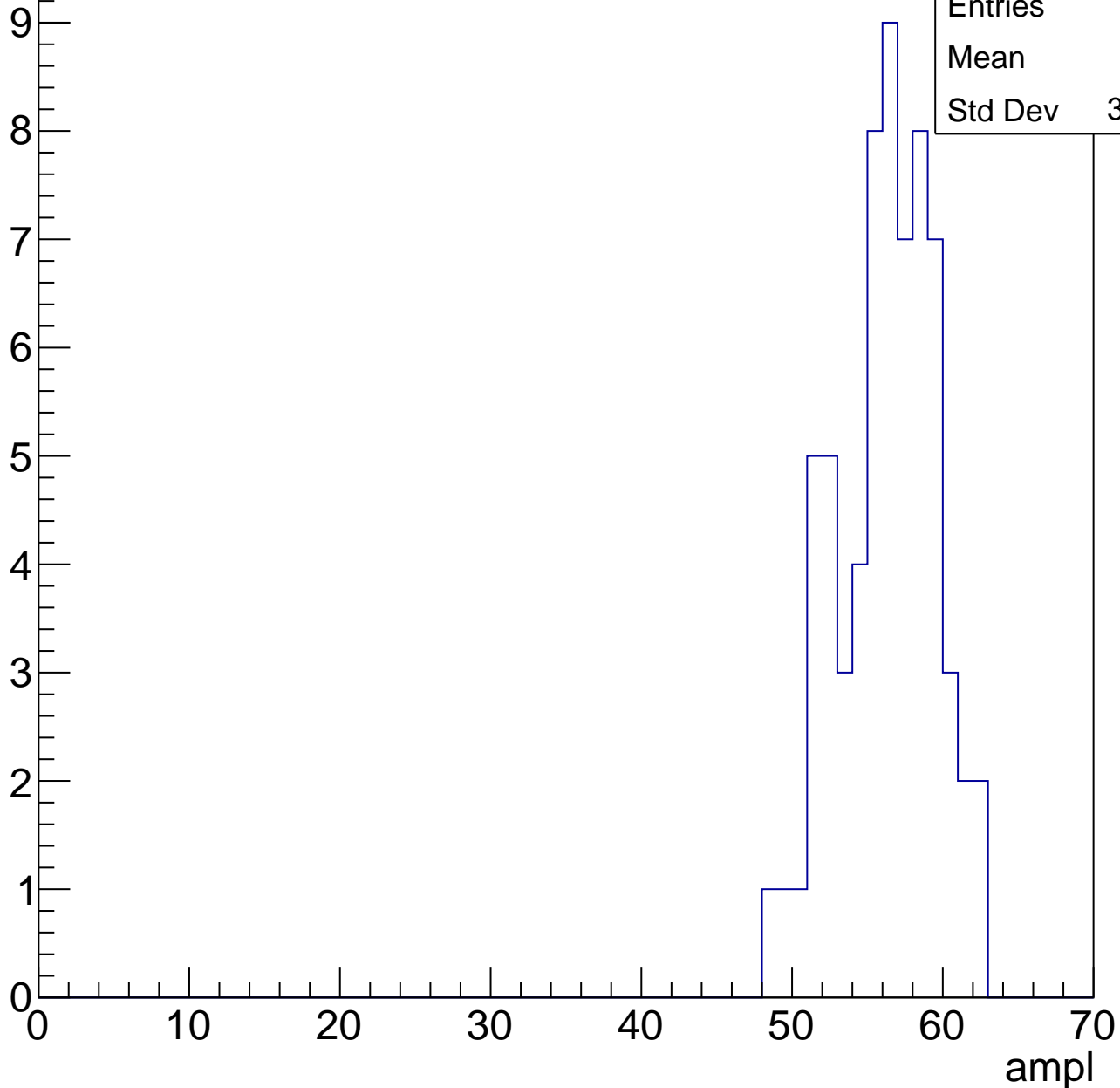
Entries	55
Mean	50.38
Std Dev	3.749



B1L100S, U12-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	66
Mean	55.8
Std Dev	3.197

B1L100S, U12-ch20, adc5

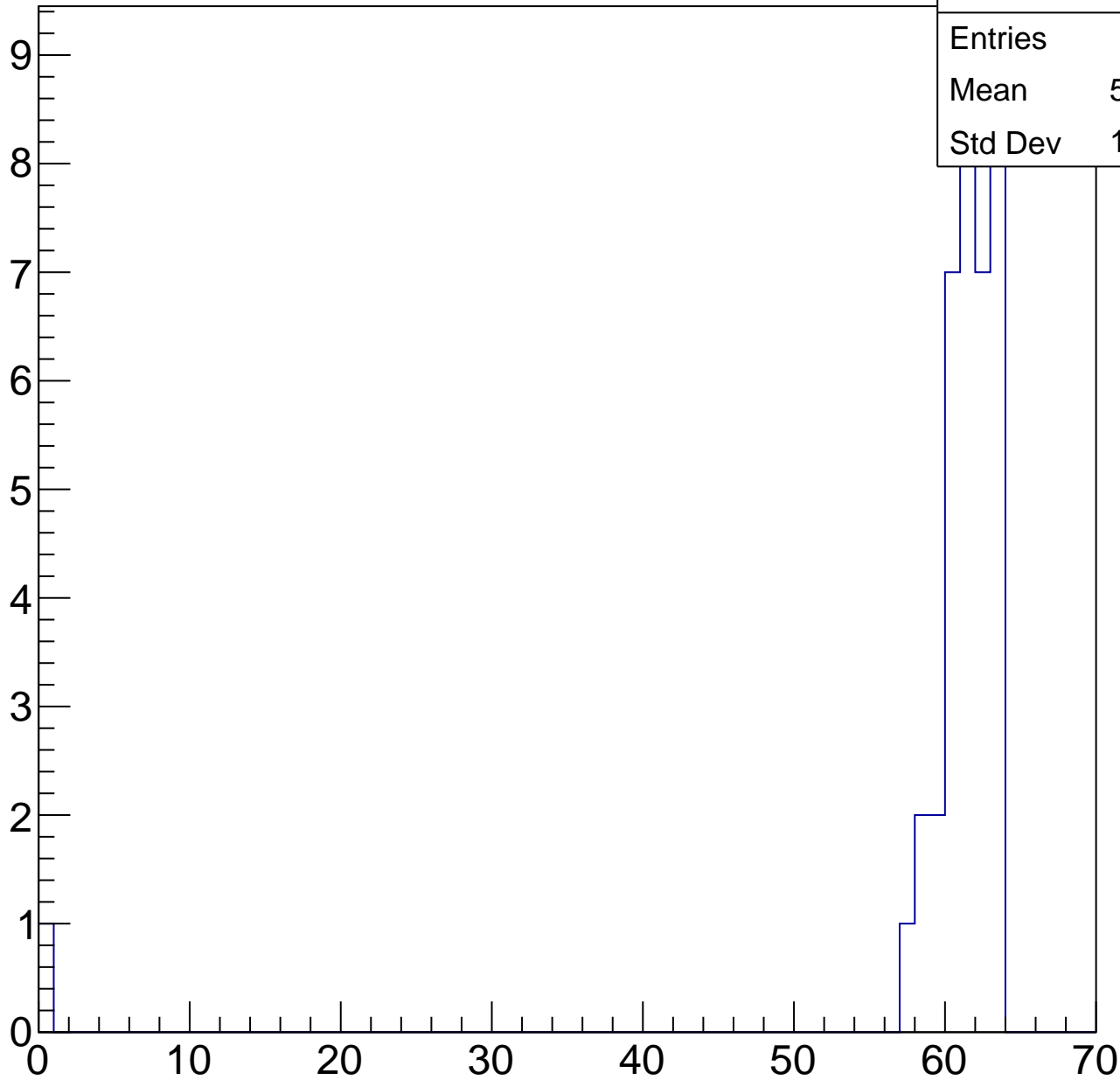
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	37
Mean	59.46
Std Dev	10.03

ampl



B1L100S, U12-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

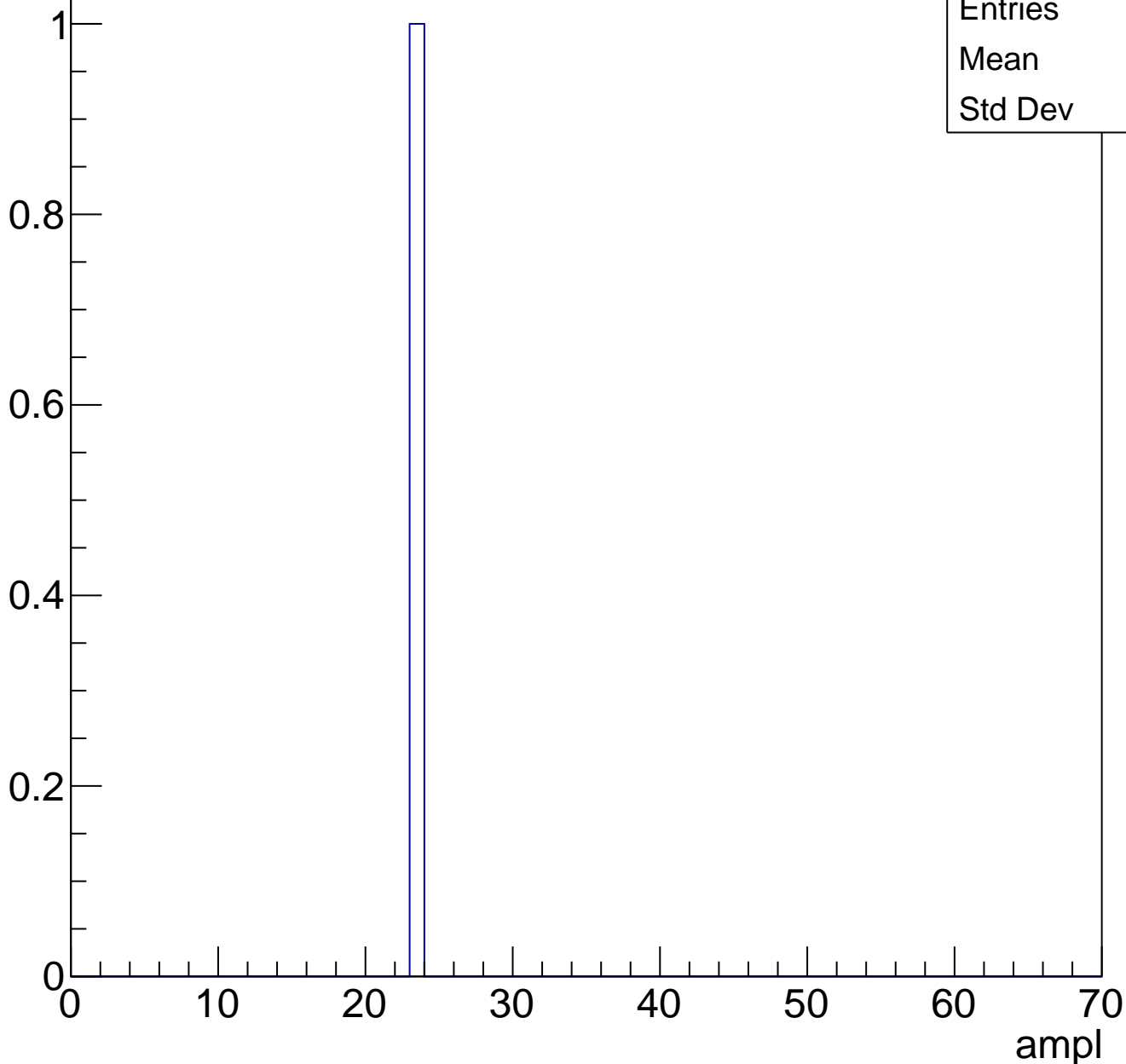
Entry



B1L100S, U12-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U12-ch21, adc0

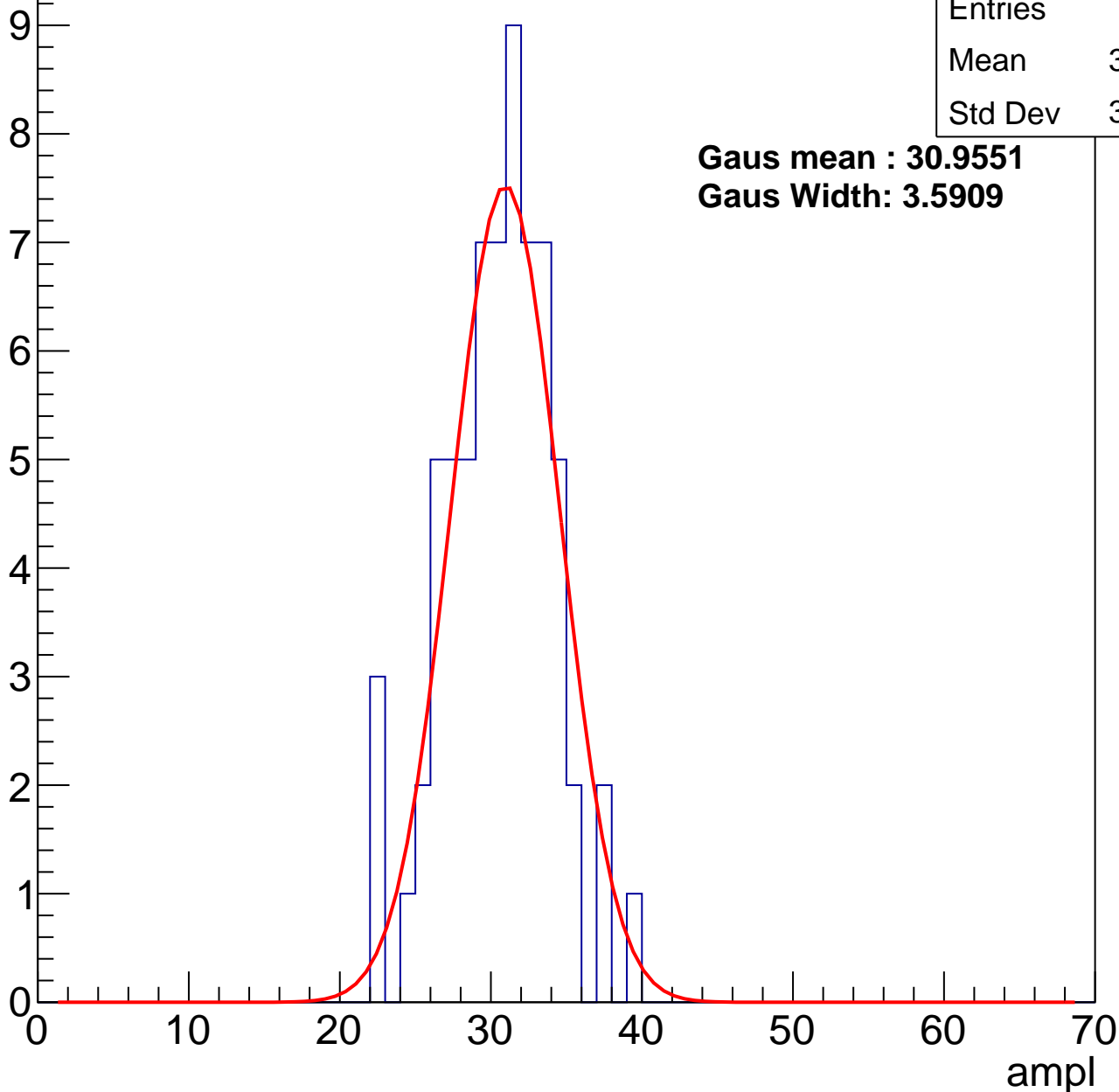
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.07
Std Dev	3.512

Gaus mean : 30.9551

Gaus Width: 3.5909



B1L100S, U12-ch21, adc1

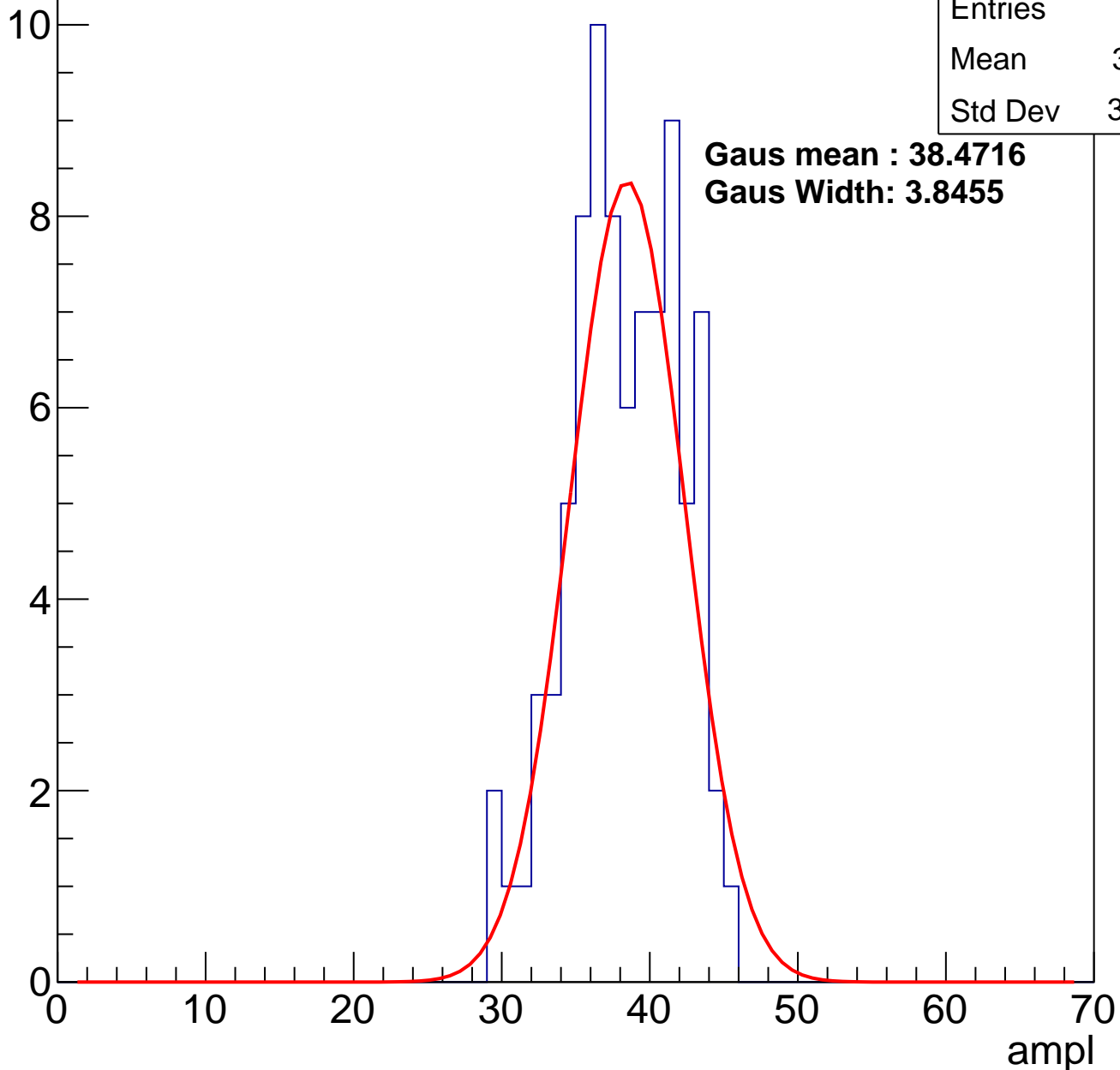
calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	37.81
Std Dev	3.673

Gaus mean : 38.4716

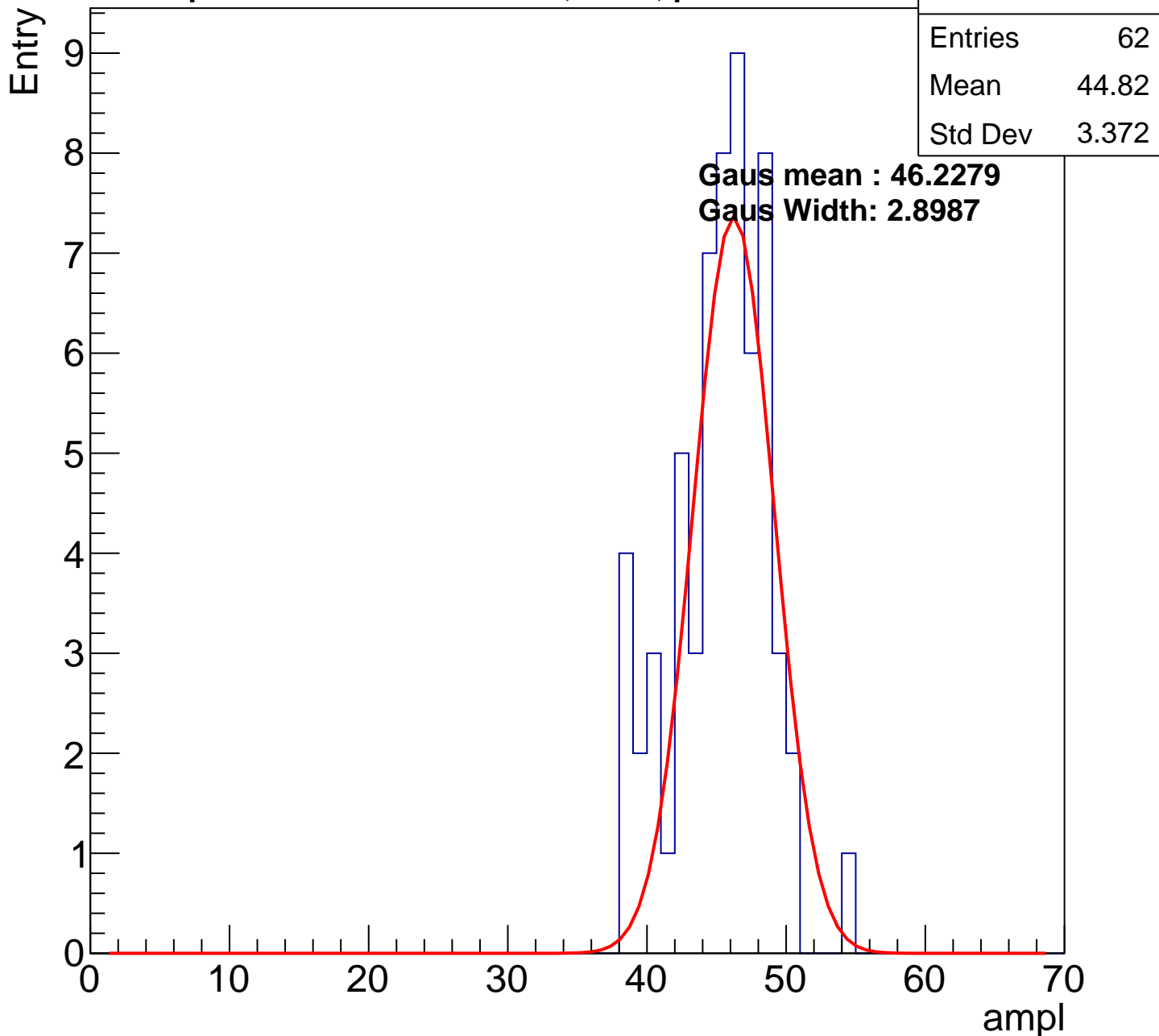
Gaus Width: 3.8455

Entry



B1L100S, U12-ch21, adc2

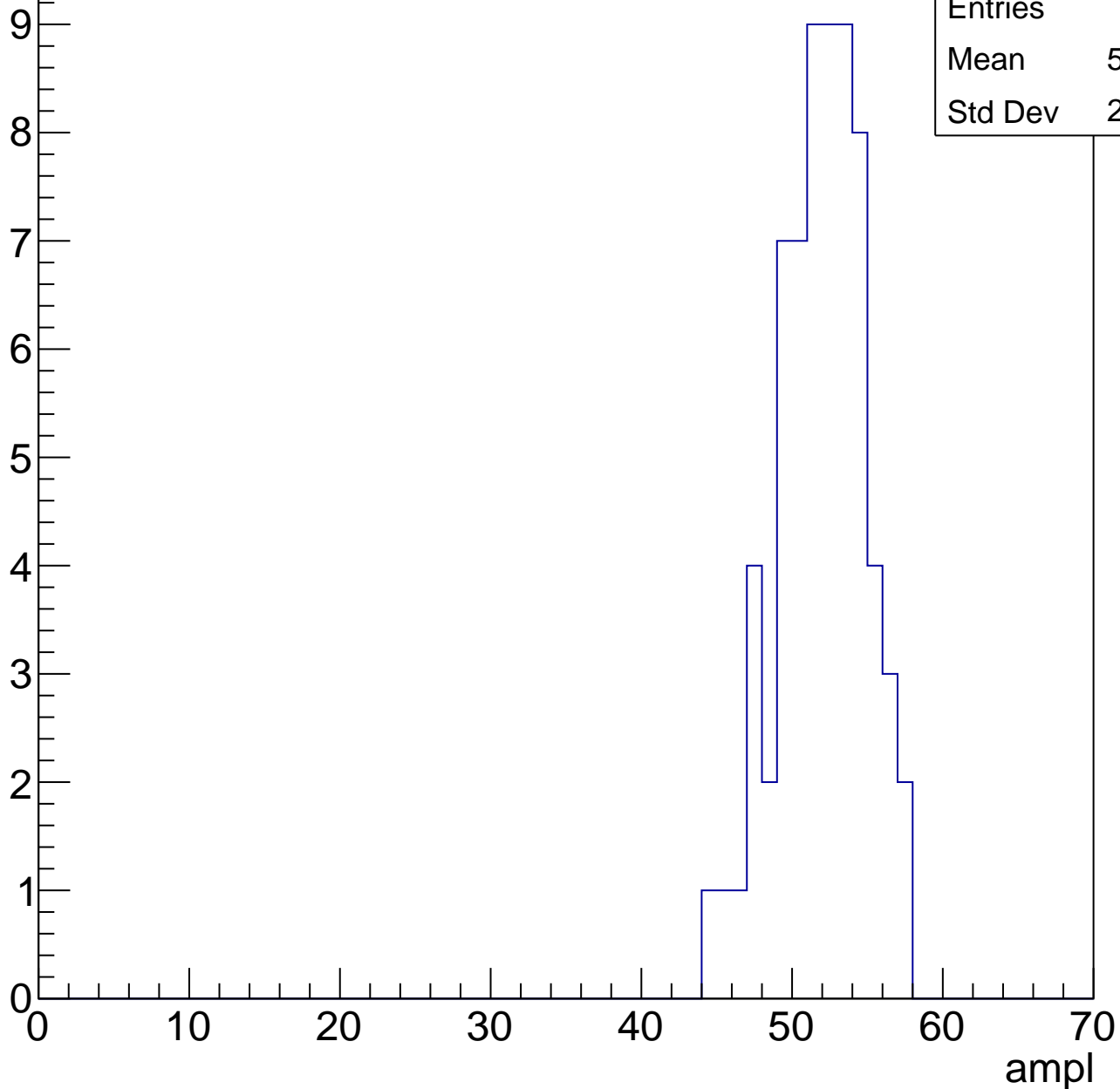
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



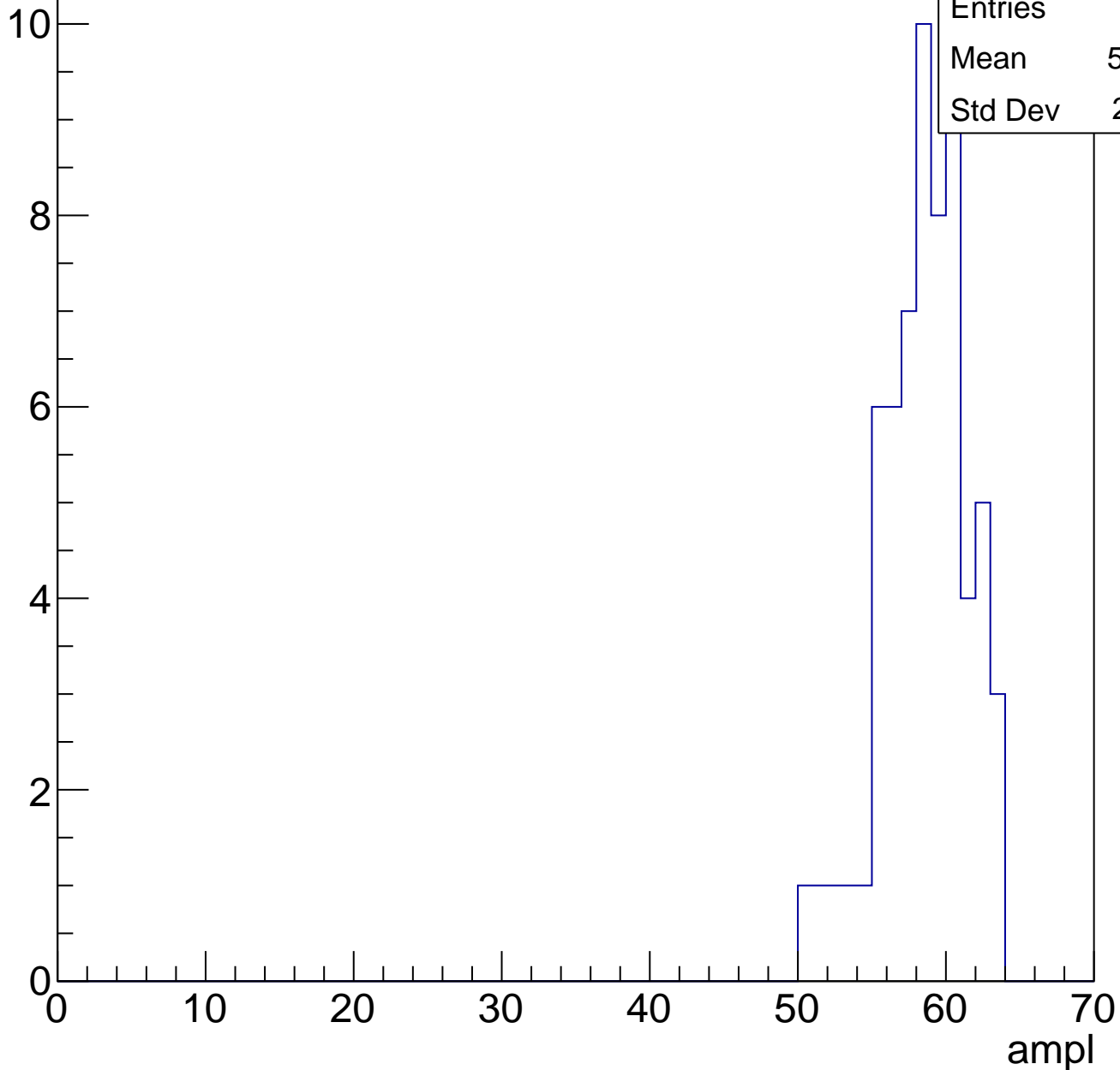
Entries	67
Mean	51.49
Std Dev	2.836

B1L100S, U12-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	58.12
Std Dev	2.831

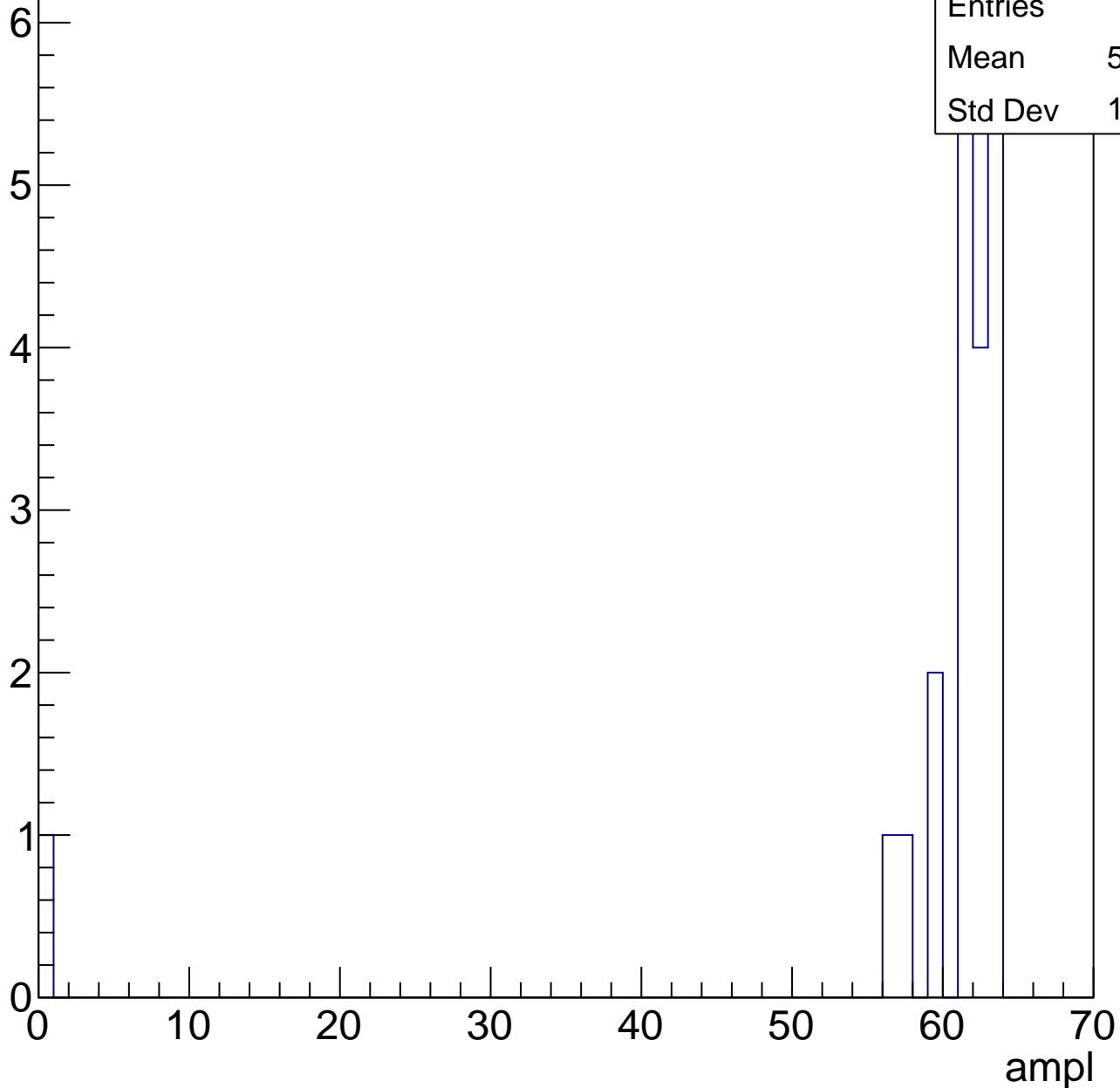
Entry



B1L100S, U12-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch22, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	28.55
Std Dev	3.276

Gaus mean : 28.7690

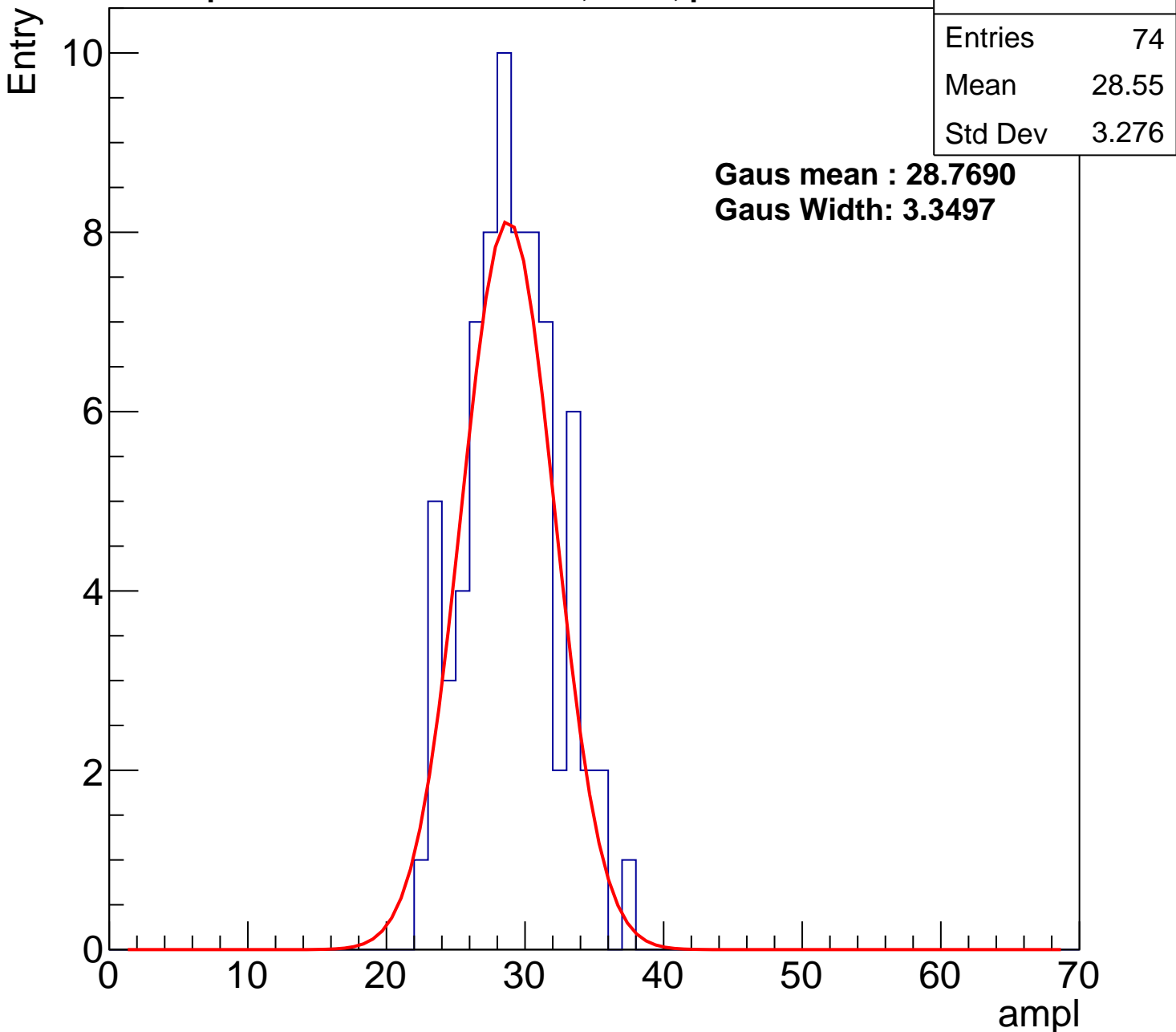
Gaus Width: 3.3497

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



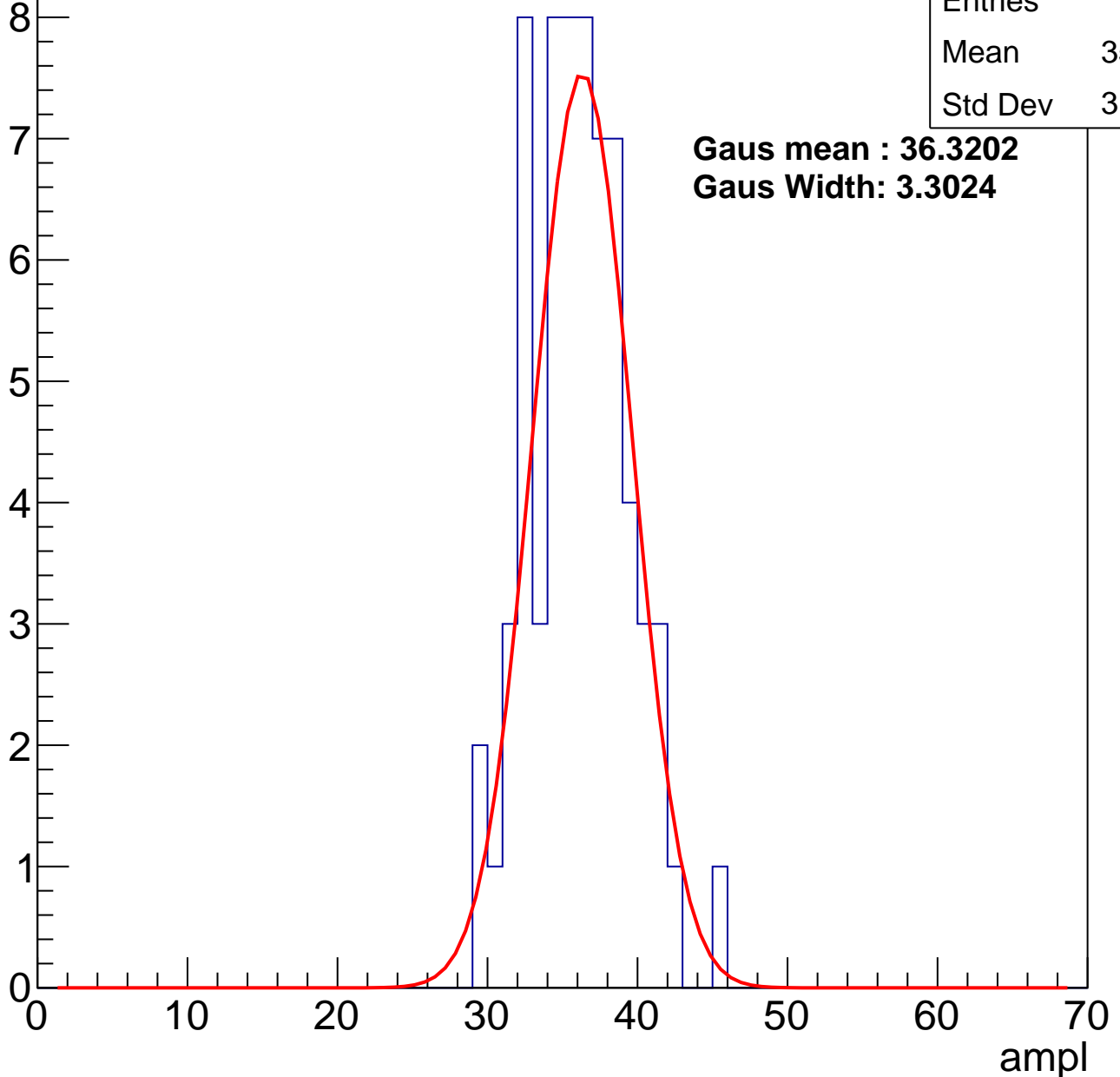
B1L100S, U12-ch22, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.63
Std Dev	3.245

Gaus mean : 36.3202
Gaus Width: 3.3024

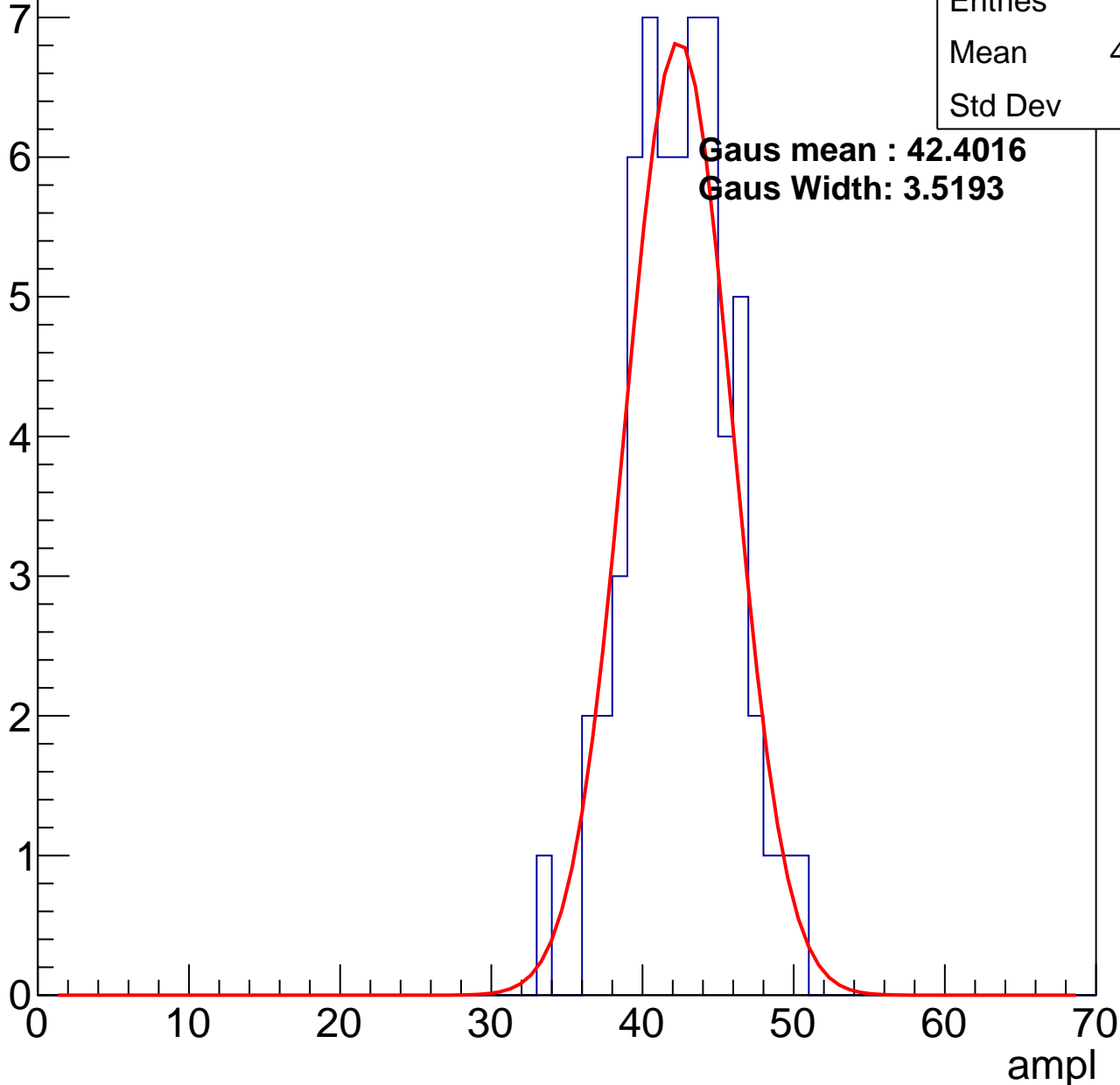


B1L100S, U12-ch22, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

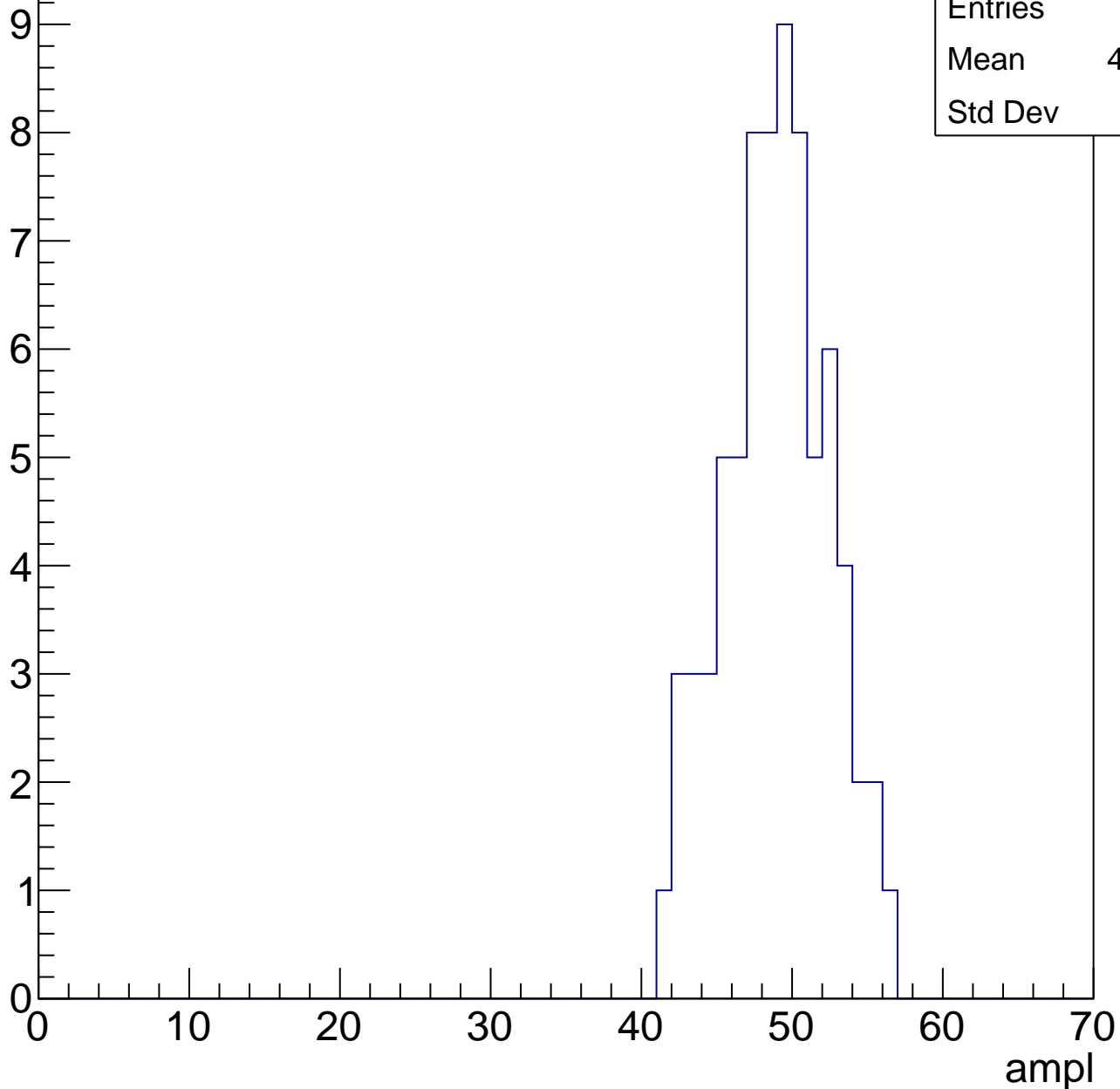
Entries	61
Mean	42.05
Std Dev	3.36



B1L100S, U12-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

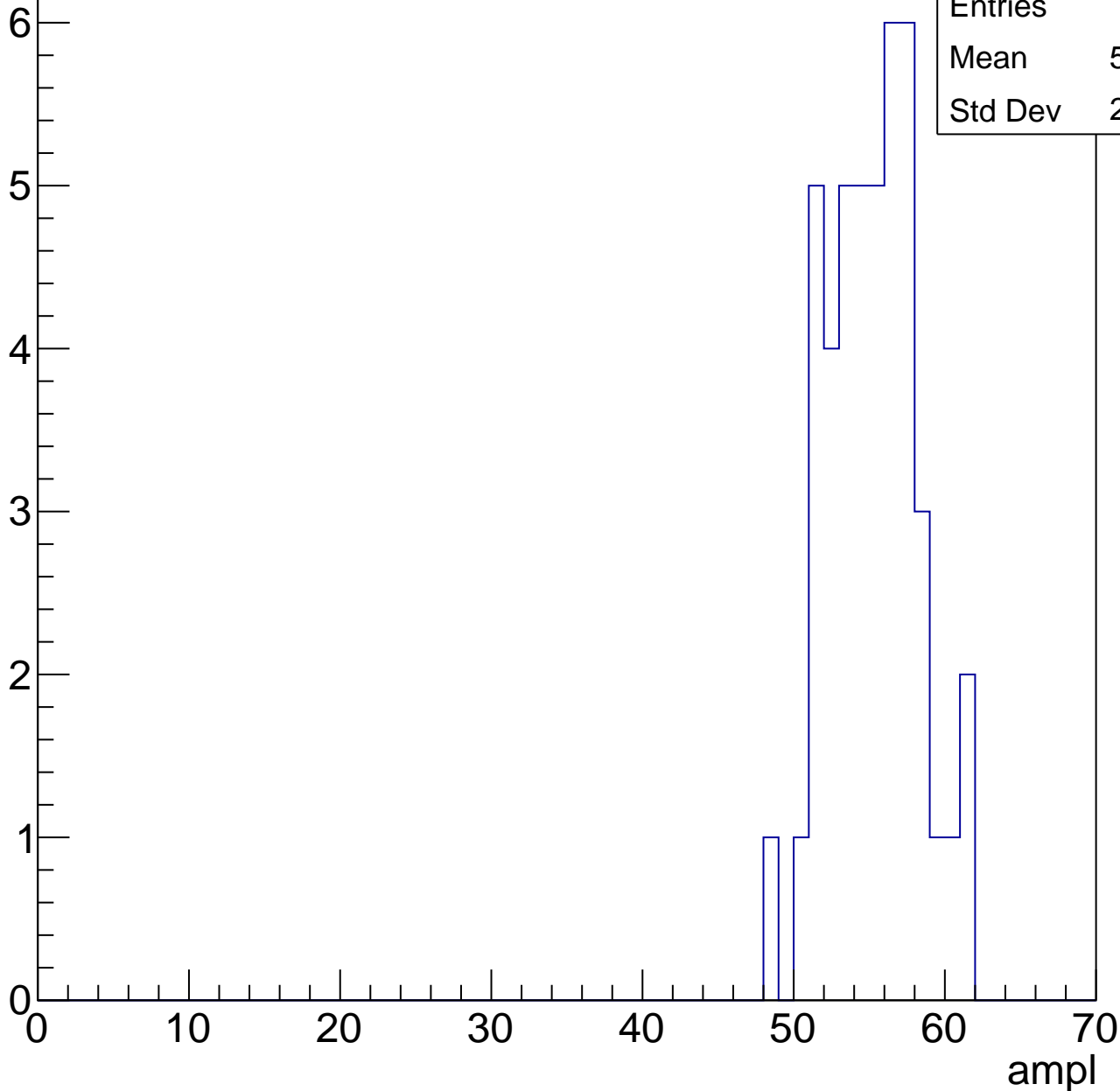


B1L100S, U12-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	54.76
Std Dev	2.915



B1L100S, U12-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

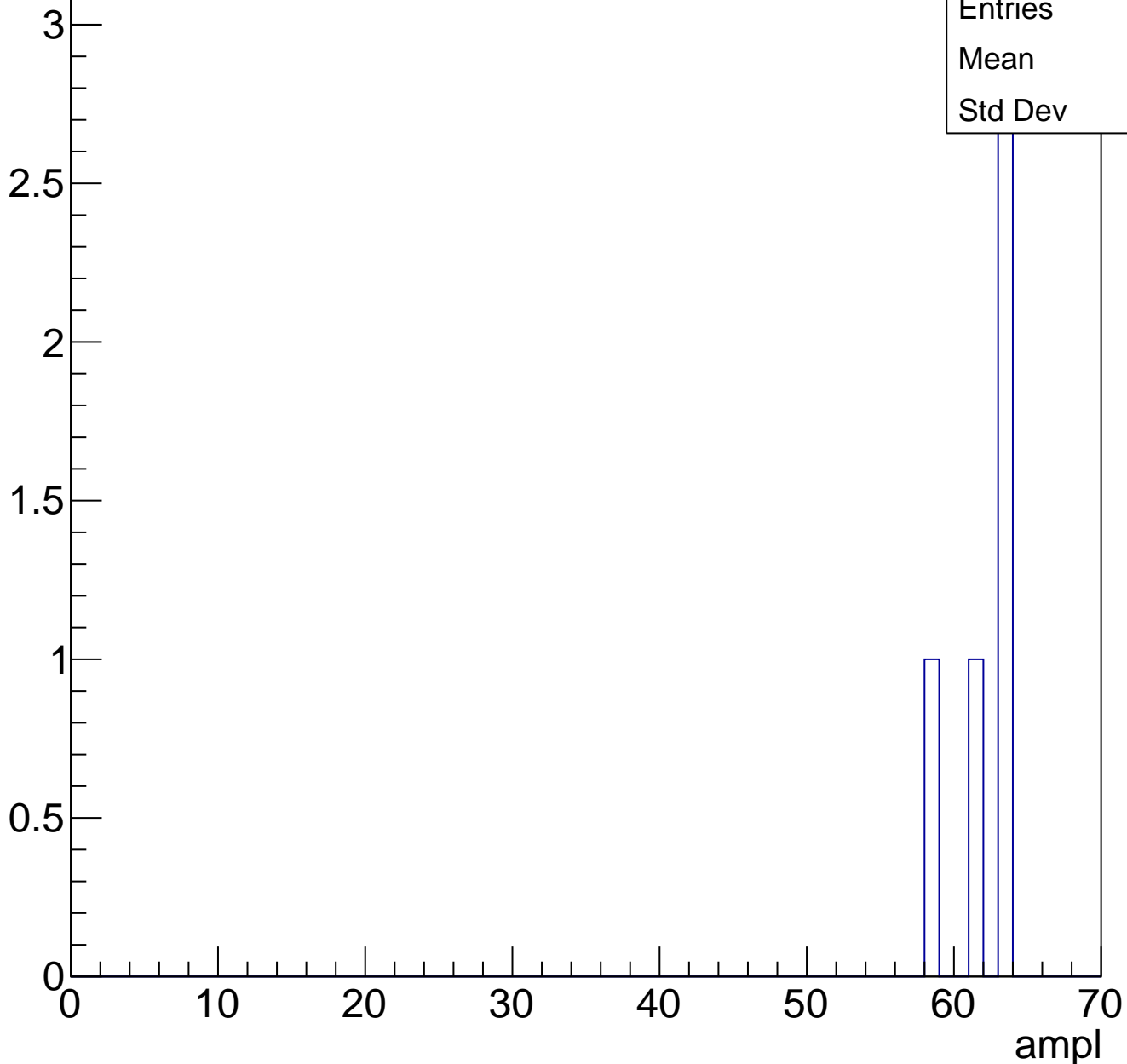
ampl

Entries	63
Mean	58.46
Std Dev	7.878

B1L100S, U12-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	61.6
Std Dev	1.96

B1L100S, U12-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch23, adc0

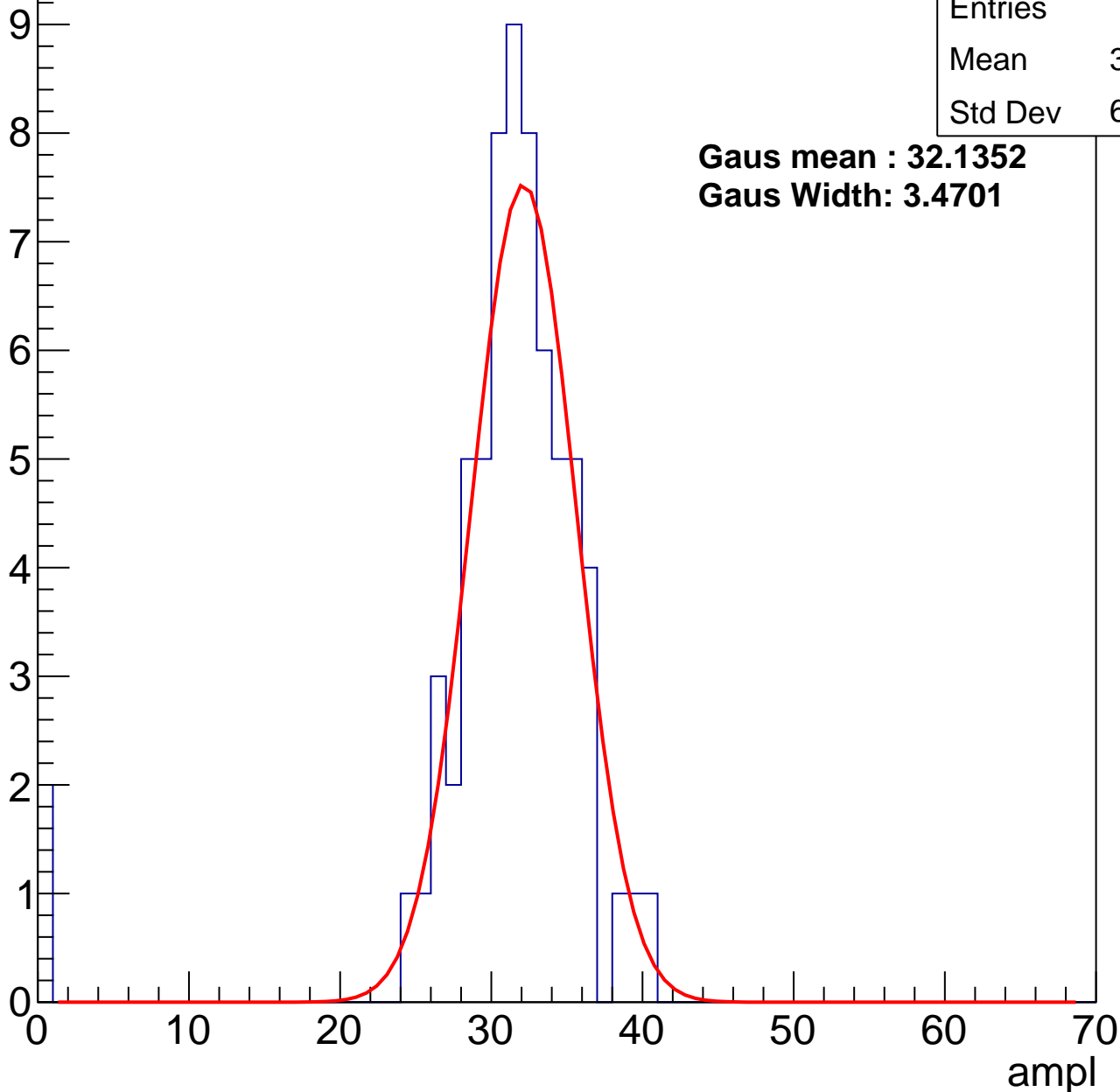
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.52
Std Dev	6.257

Gaus mean : 32.1352

Gaus Width: 3.4701



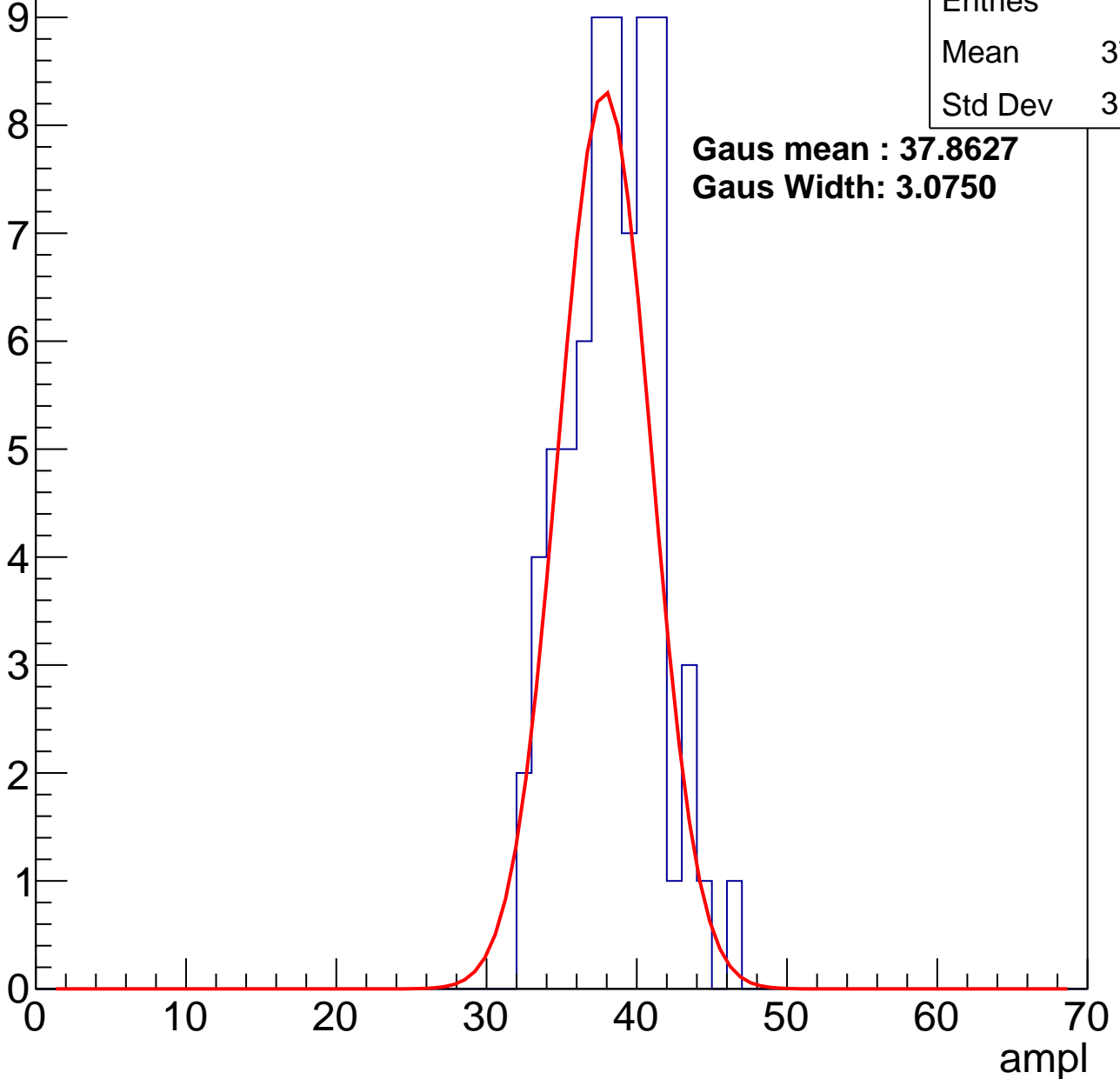
B1L100S, U12-ch23, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	37.96
Std Dev	3.014

Gaus mean : 37.8627
Gaus Width: 3.0750



B1L100S, U12-ch23, adc2

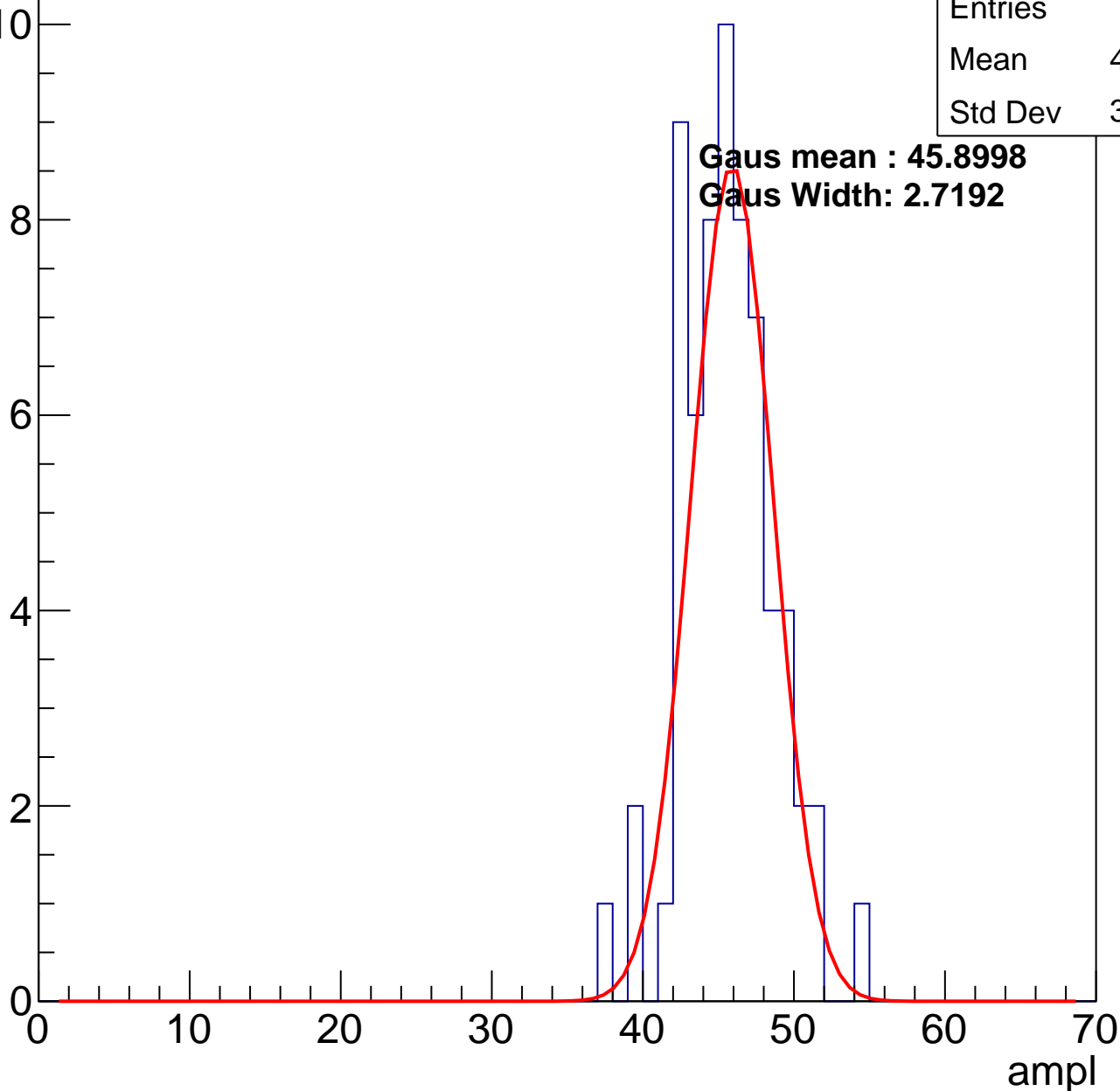
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	45.15
Std Dev	3.044

Gaus mean : 45.8998

Gaus Width: 2.7192



B1L100S, U12-ch23, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	51.88
Std Dev	3.426

Entry

10

8

6

4

2

0

0

10

20

30

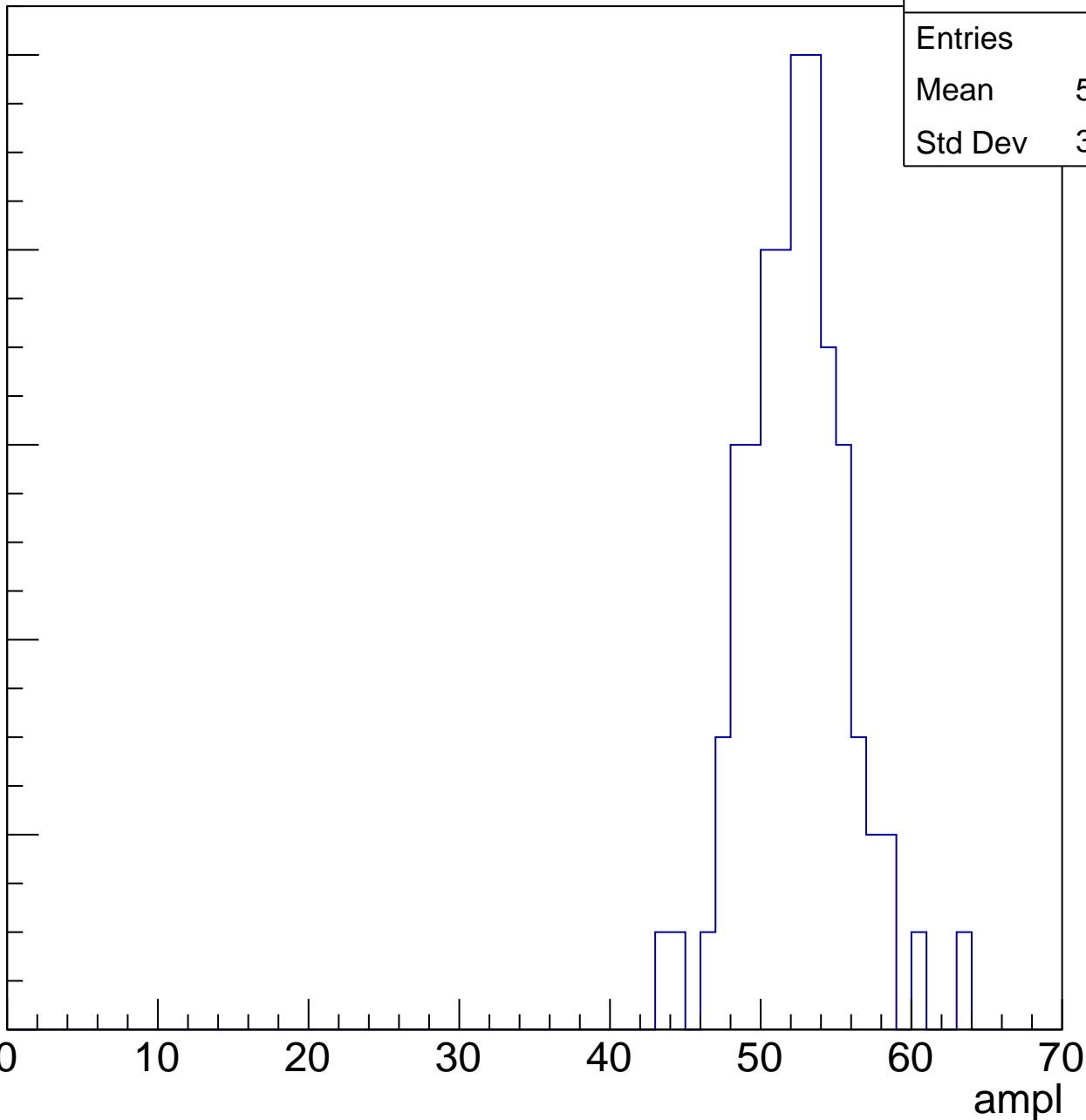
40

50

60

70

ampl



B1L100S, U12-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	59.02
Std Dev	2.577

Entry

10

8

6

4

2

0

0

10

20

30

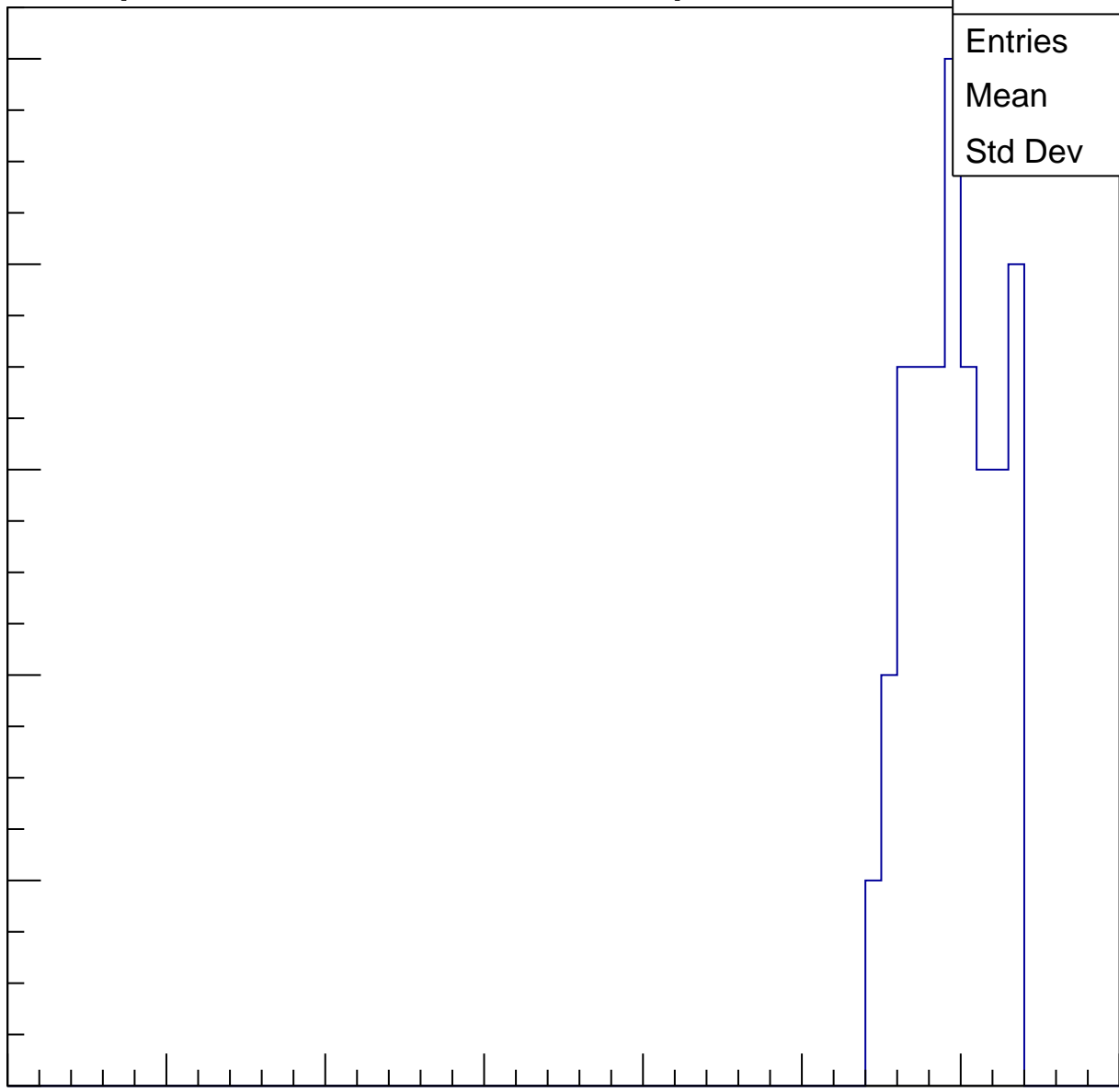
40

50

60

70

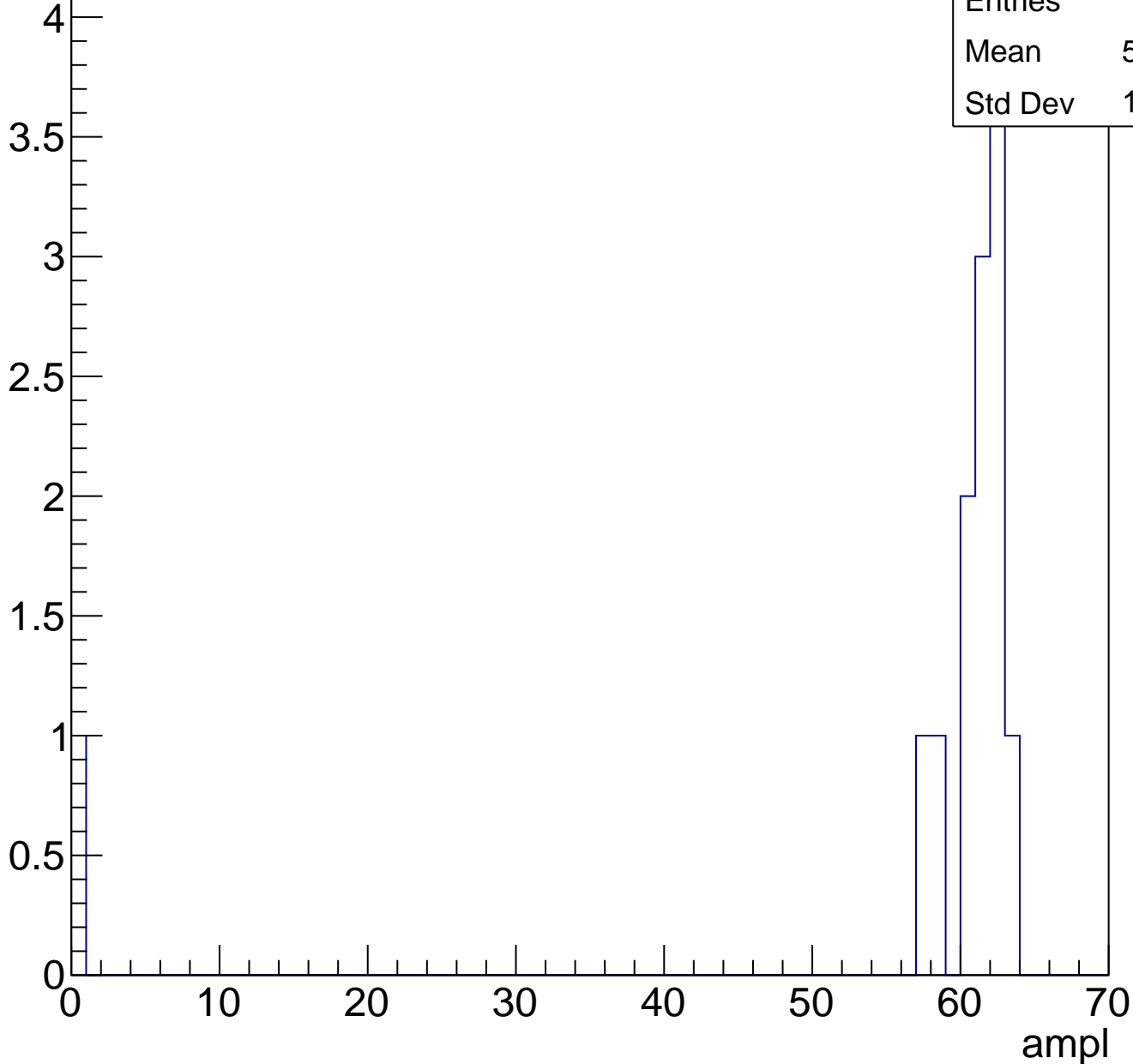
ampl



B1L100S, U12-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch24, adc0

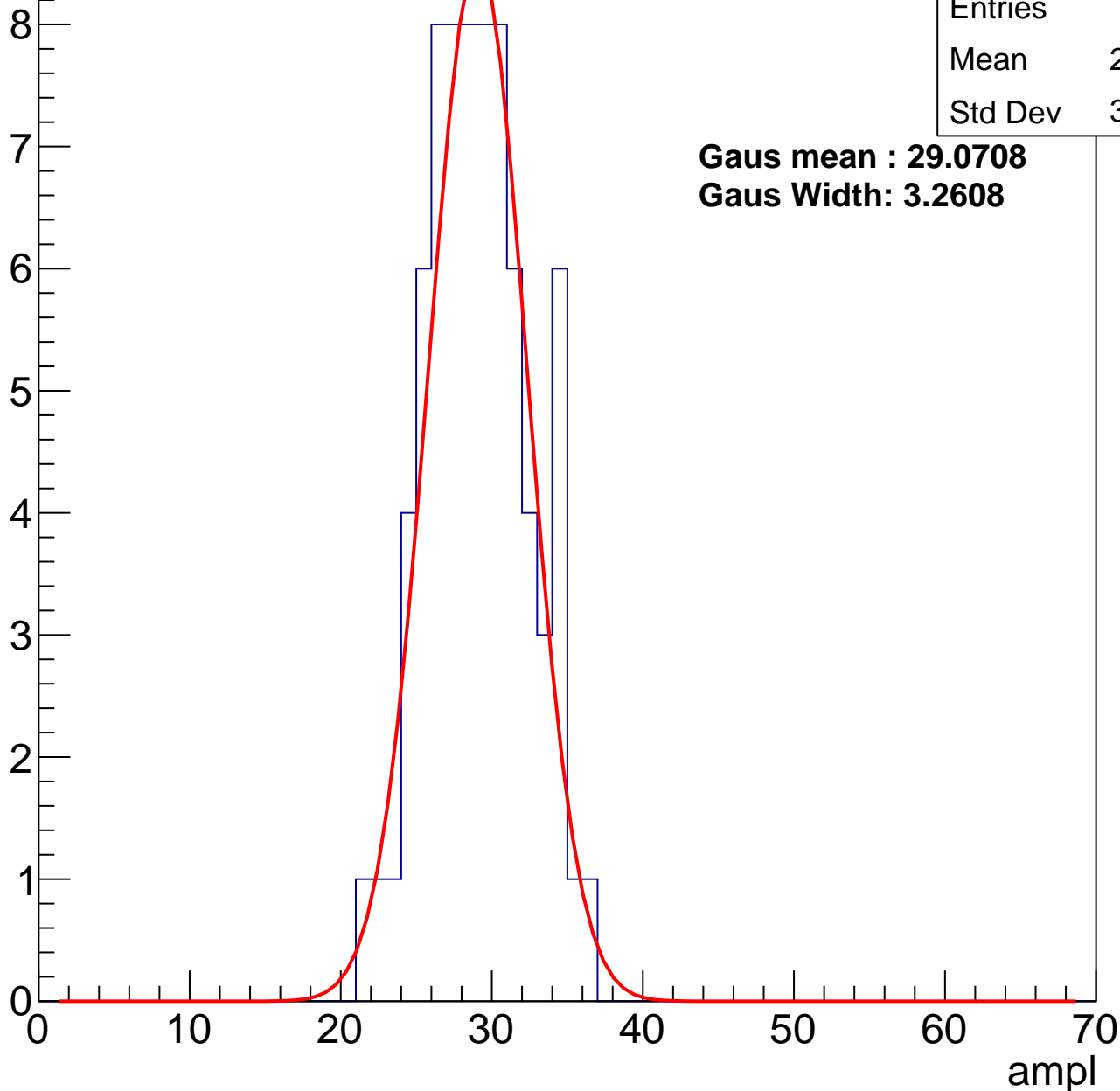
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.65
Std Dev	3.286

Gaus mean : 29.0708

Gaus Width: 3.2608



B1L100S, U12-ch24, adc1

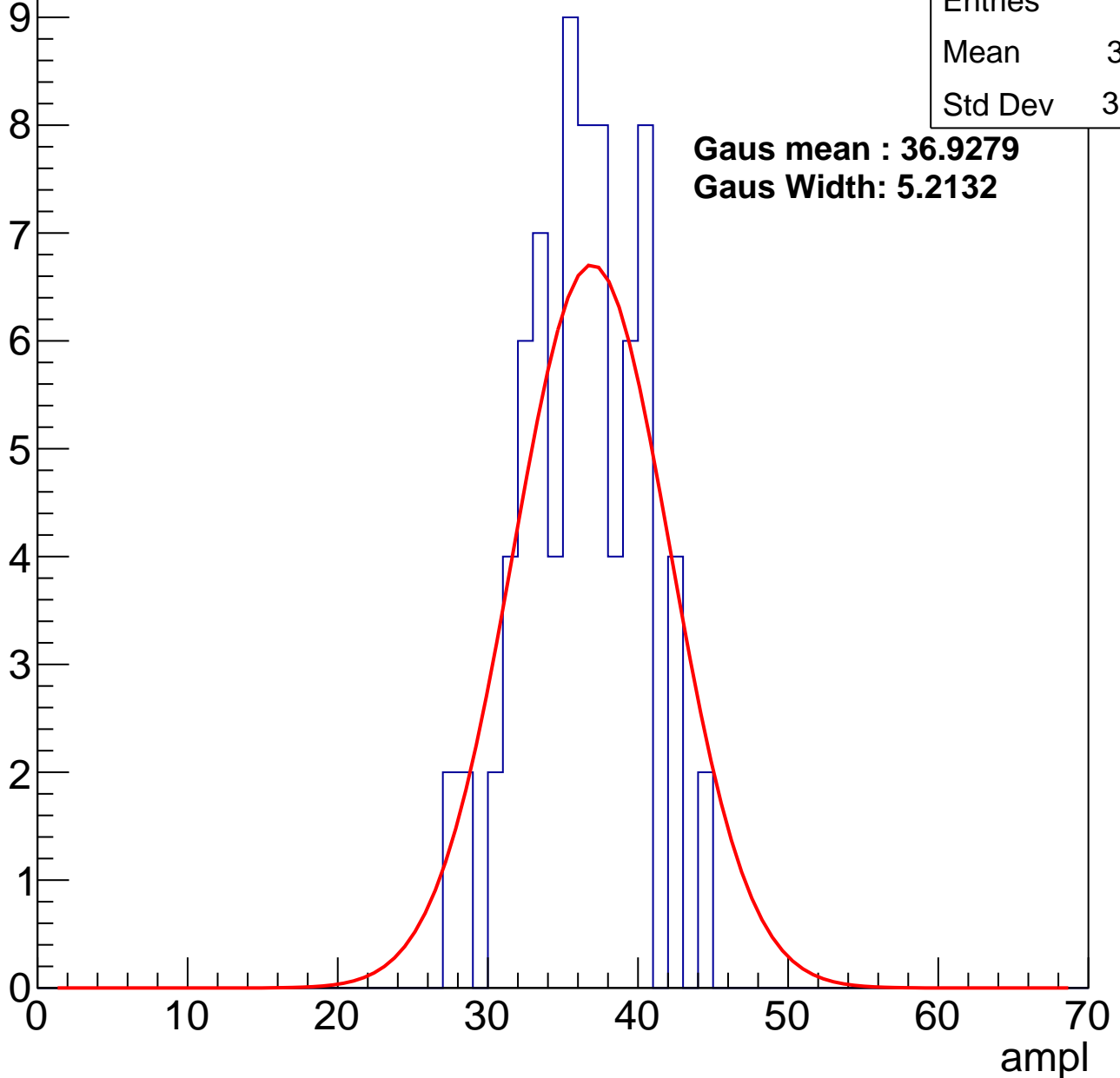
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	35.71
Std Dev	3.835

Gaus mean : 36.9279

Gaus Width: 5.2132



B1L100S, U12-ch24, adc2

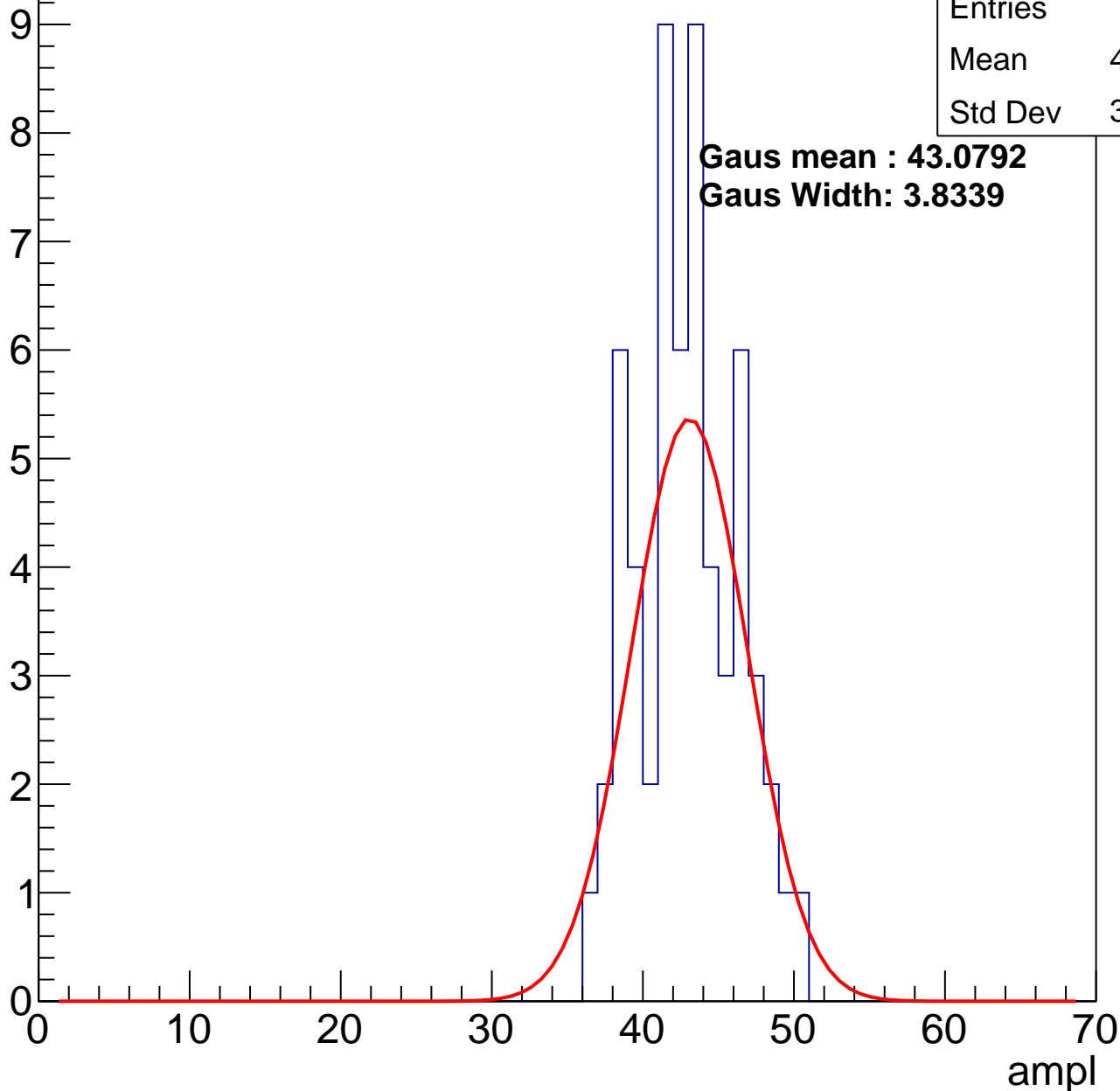
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.46
Std Dev	3.269

Gaus mean : 43.0792

Gaus Width: 3.8339

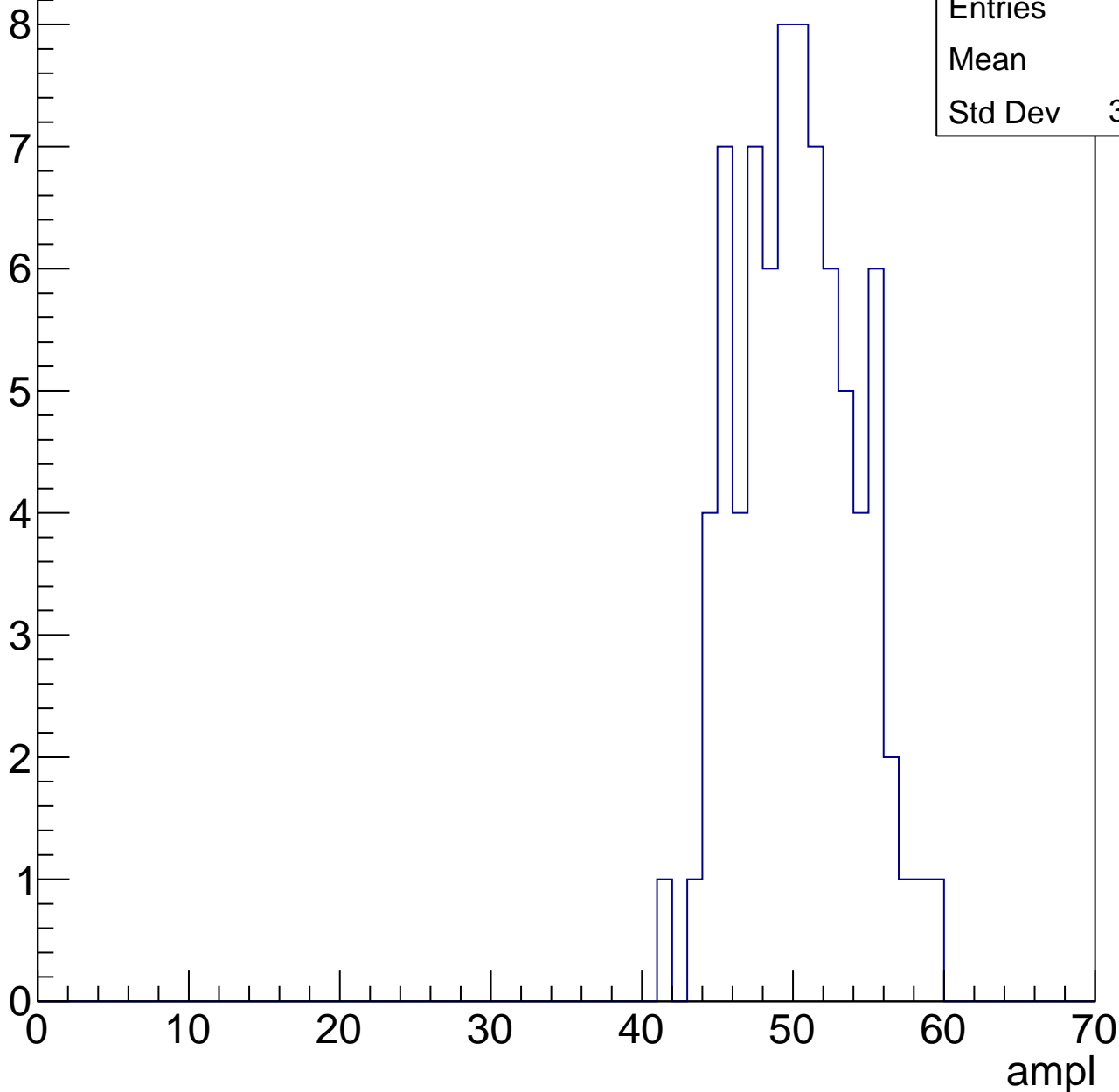


B1L100S, U12-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

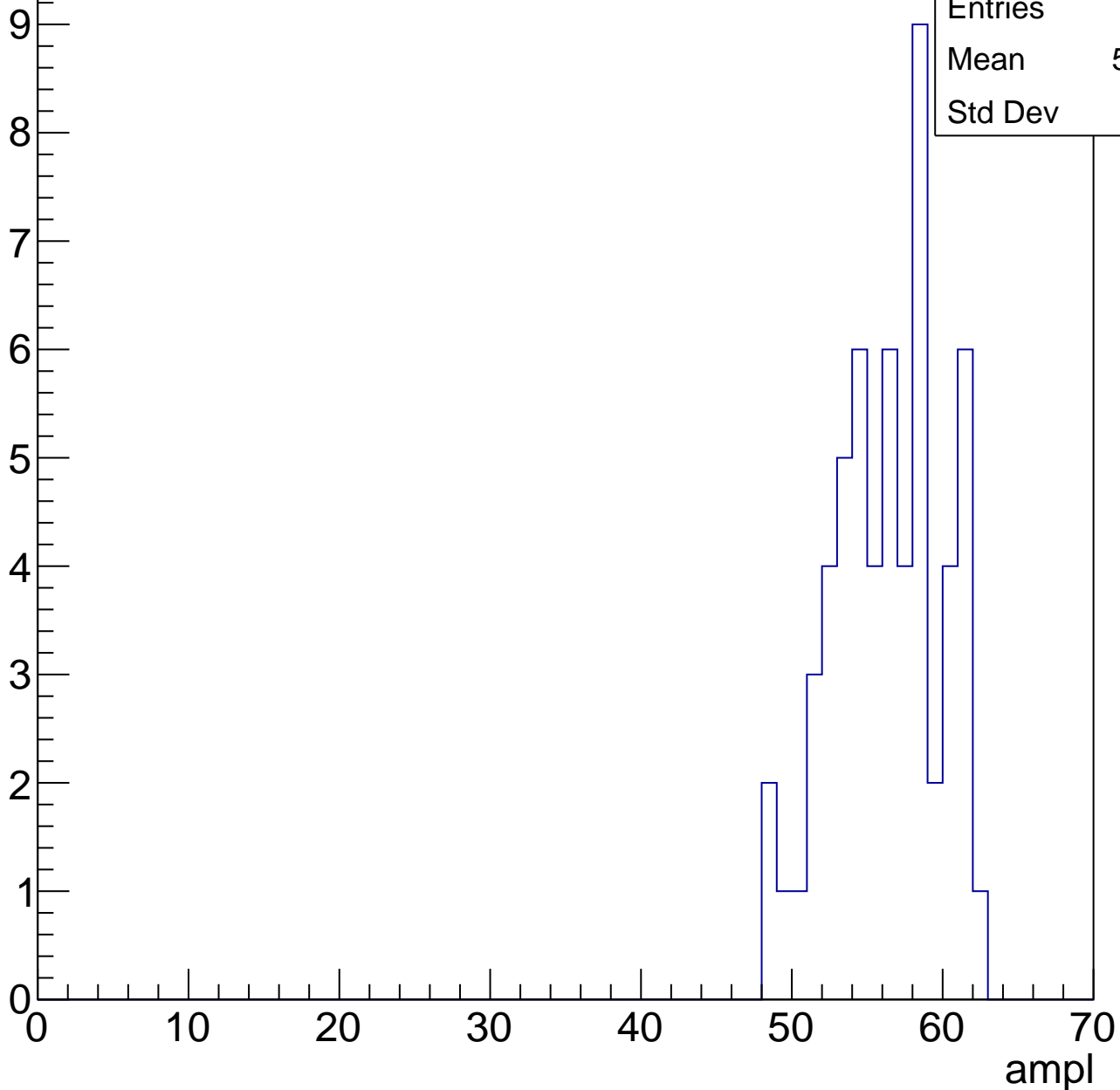
Entries	79
Mean	49.8
Std Dev	3.833



B1L100S, U12-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



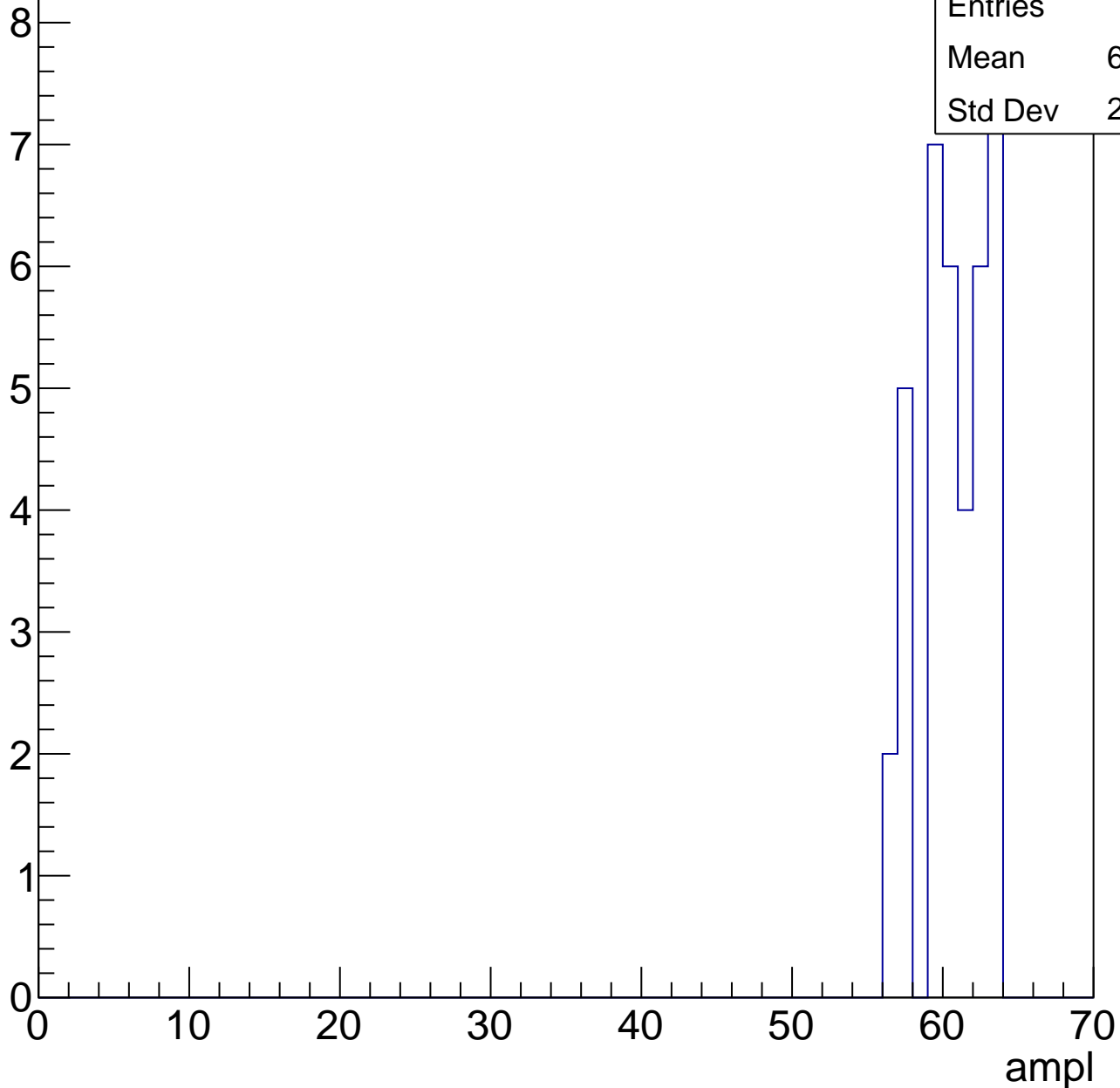
Entries	58
Mean	55.81
Std Dev	3.55

B1L100S, U12-ch24, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

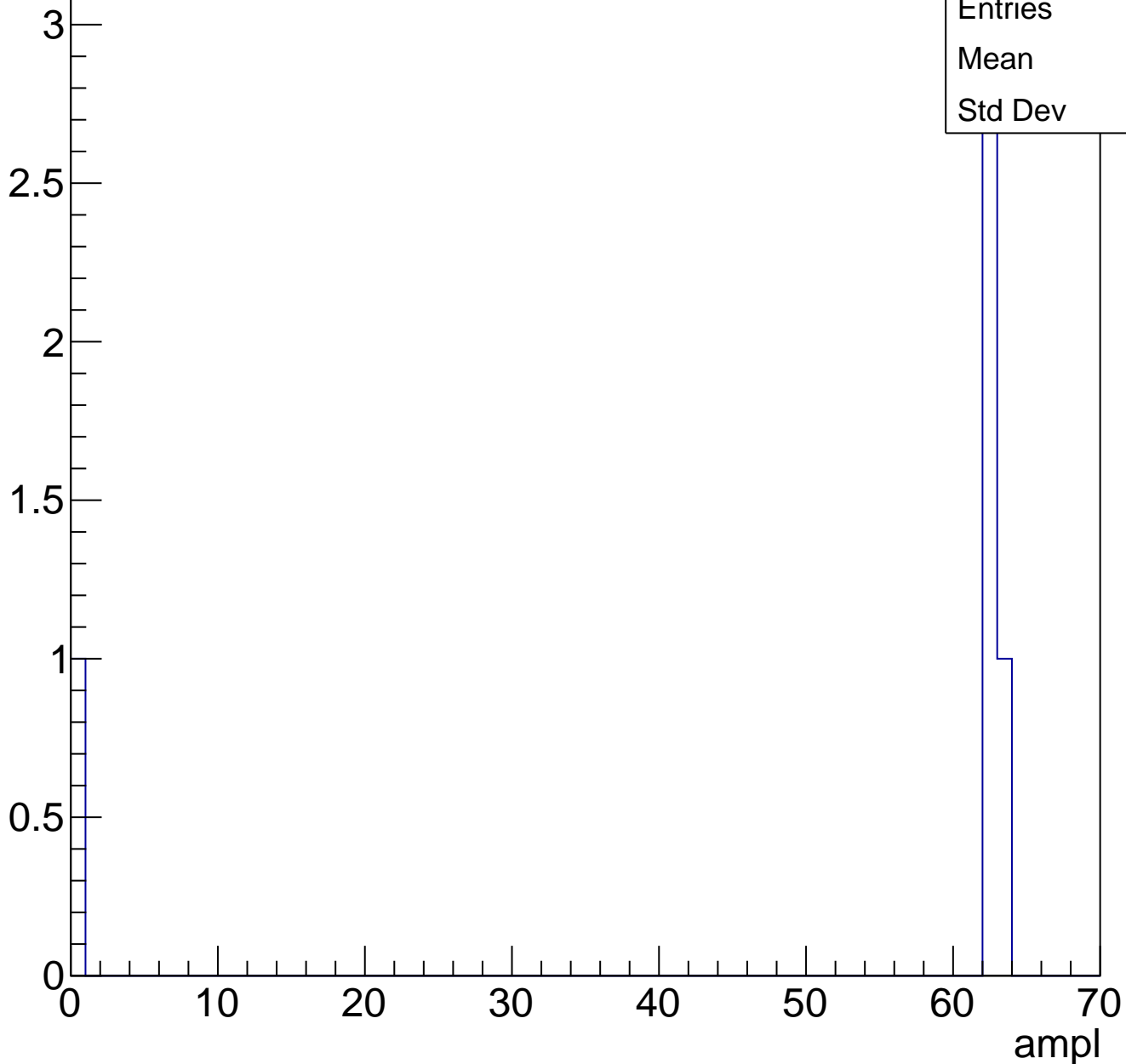
Entries	38
Mean	60.26
Std Dev	2.185



B1L100S, U12-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U12-ch25, adc0

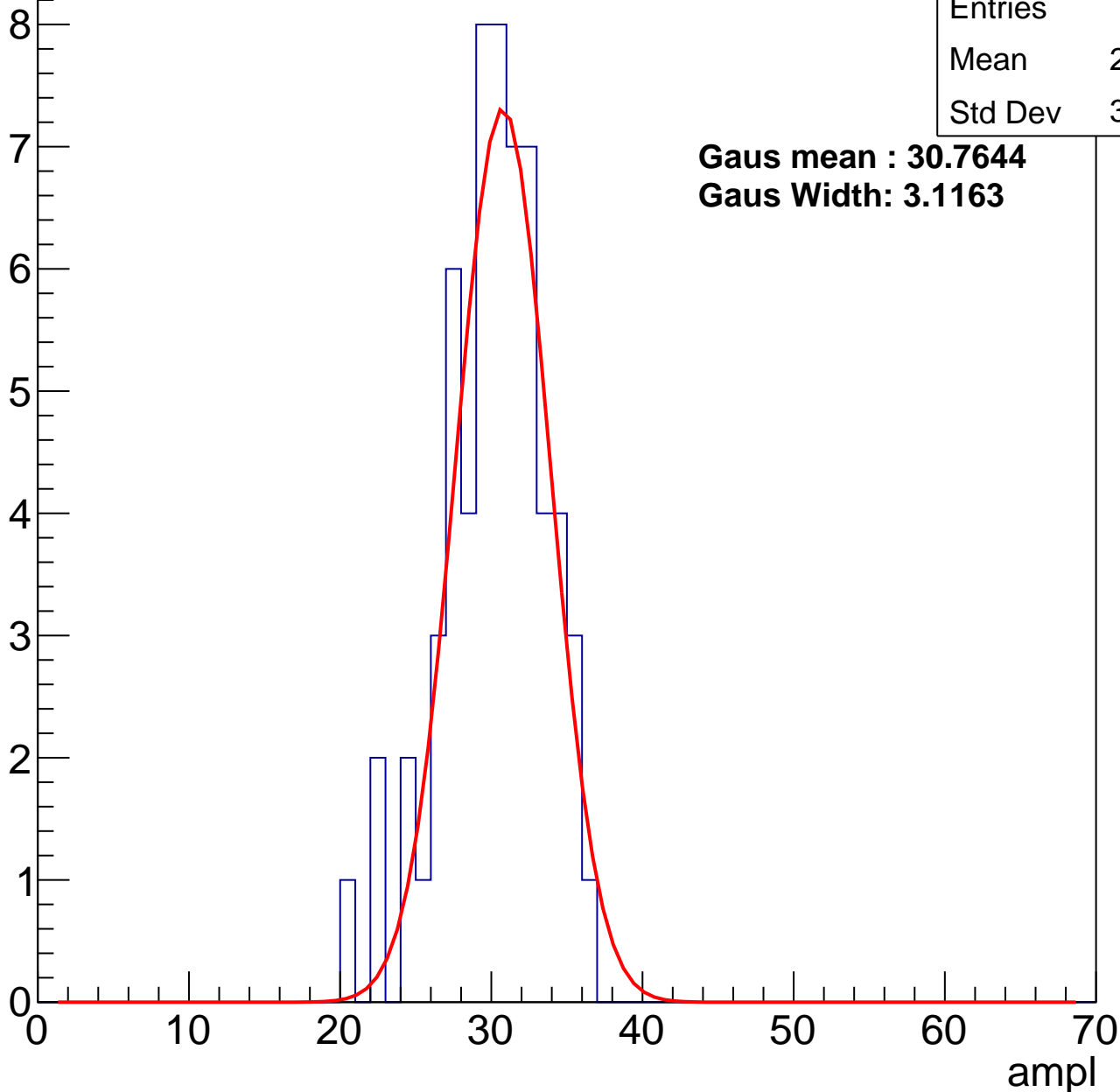
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.69
Std Dev	3.366

Gaus mean : 30.7644

Gaus Width: 3.1163



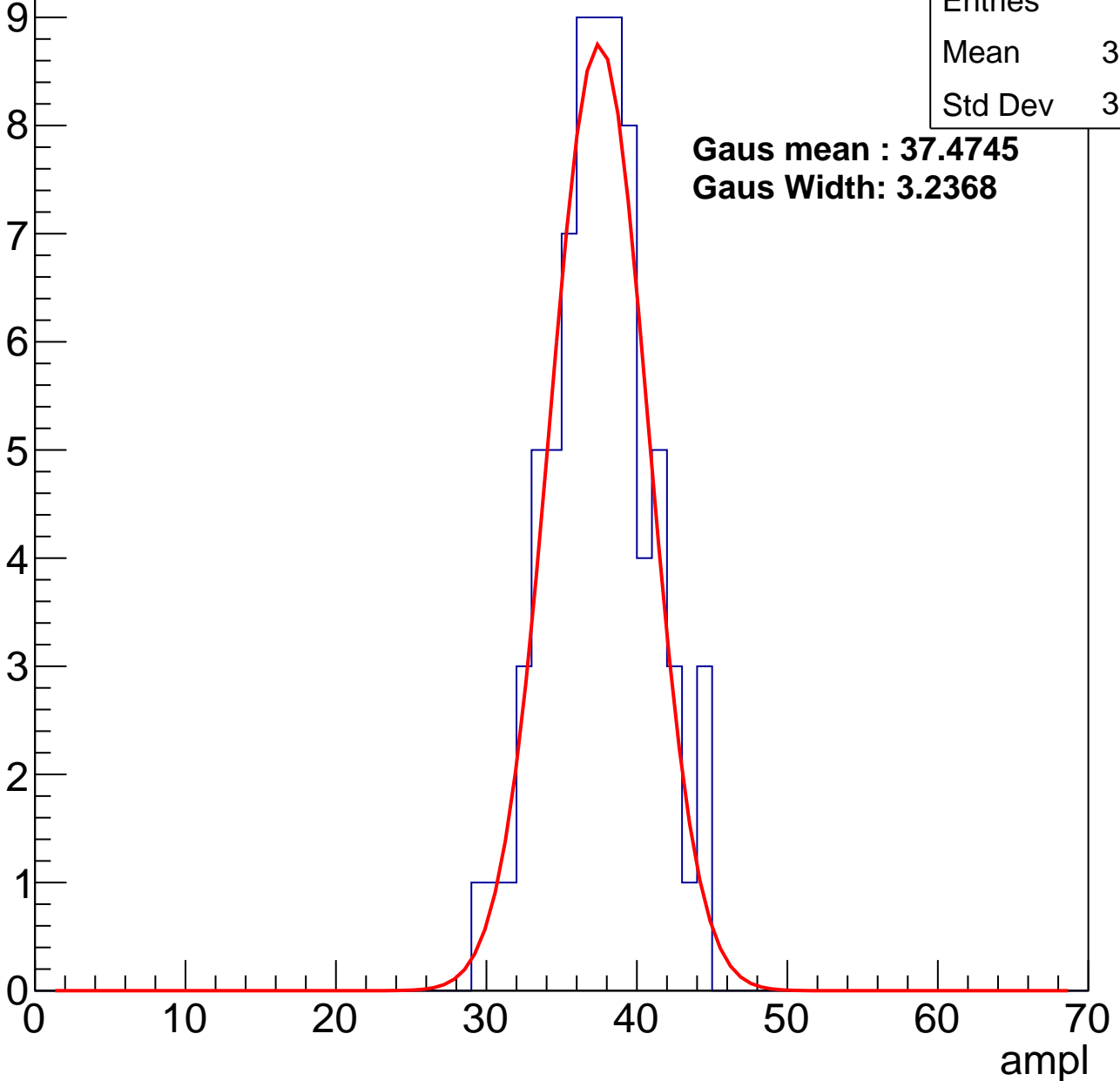
B1L100S, U12-ch25, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.07
Std Dev	3.289

Gaus mean : 37.4745
Gaus Width: 3.2368



B1L100S, U12-ch25, adc2

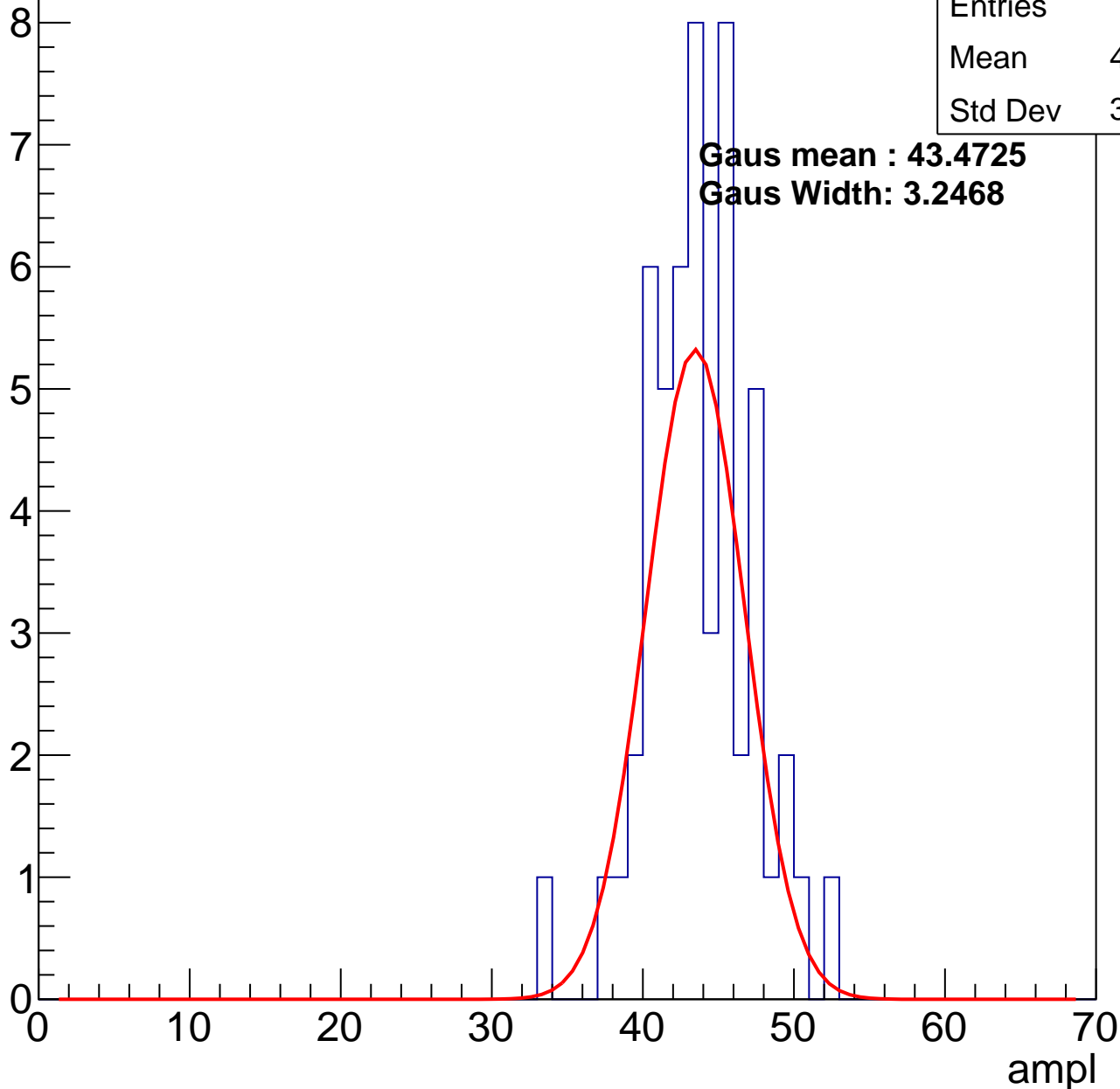
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	43.28
Std Dev	3.444

Gaus mean : 43.4725

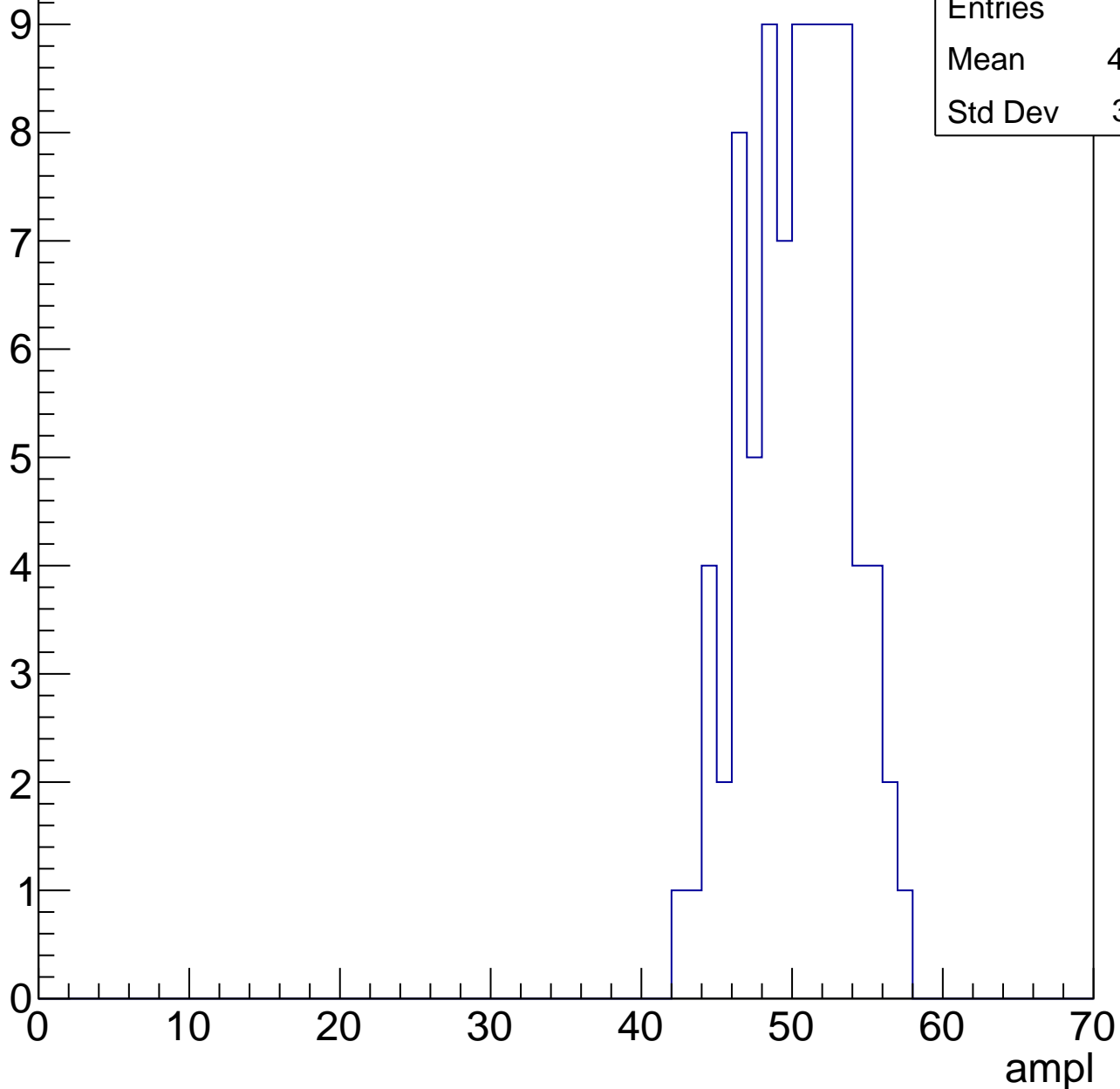
Gaus Width: 3.2468



B1L100S, U12-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

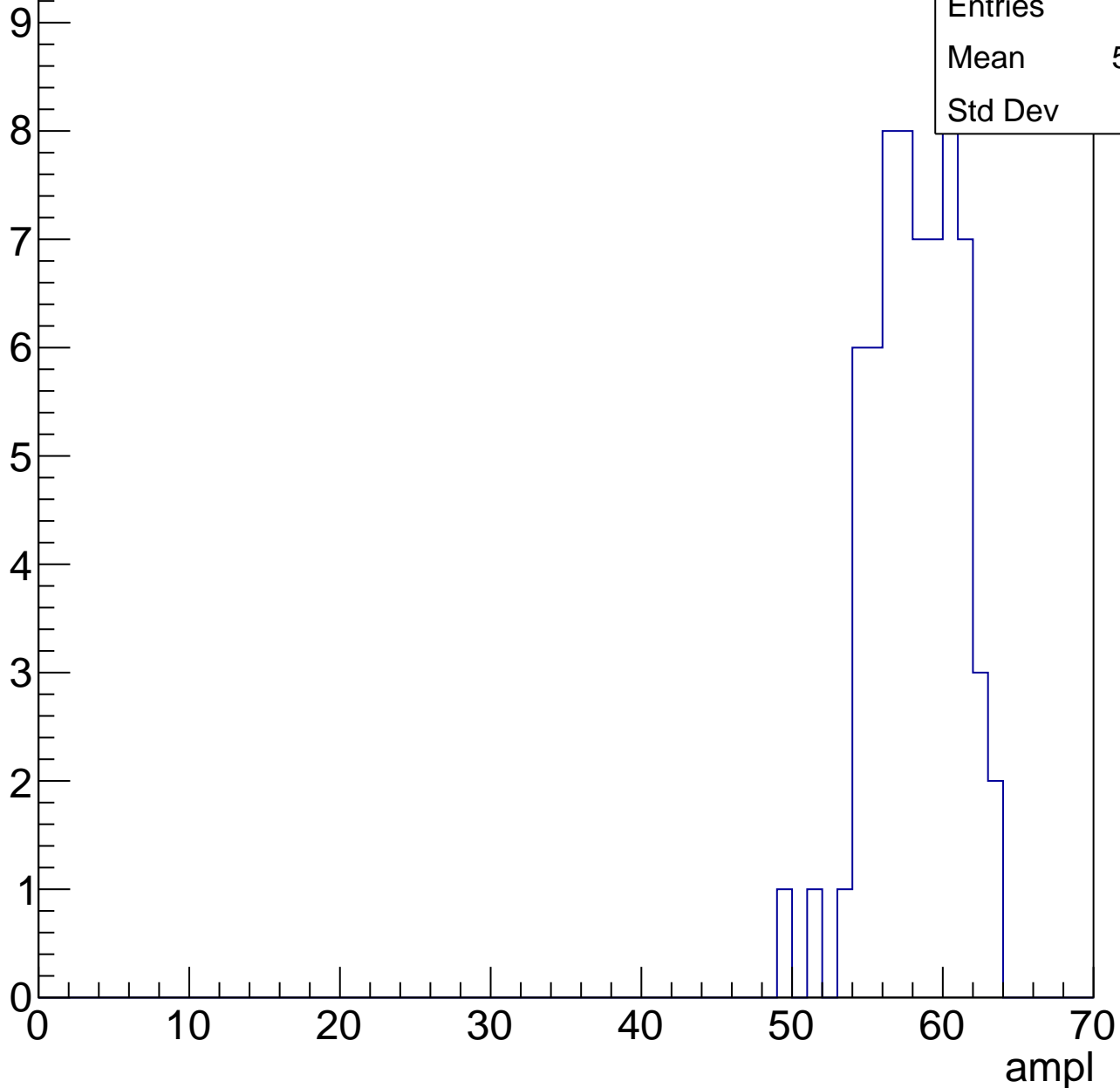
Entry



B1L100S, U12-ch25, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

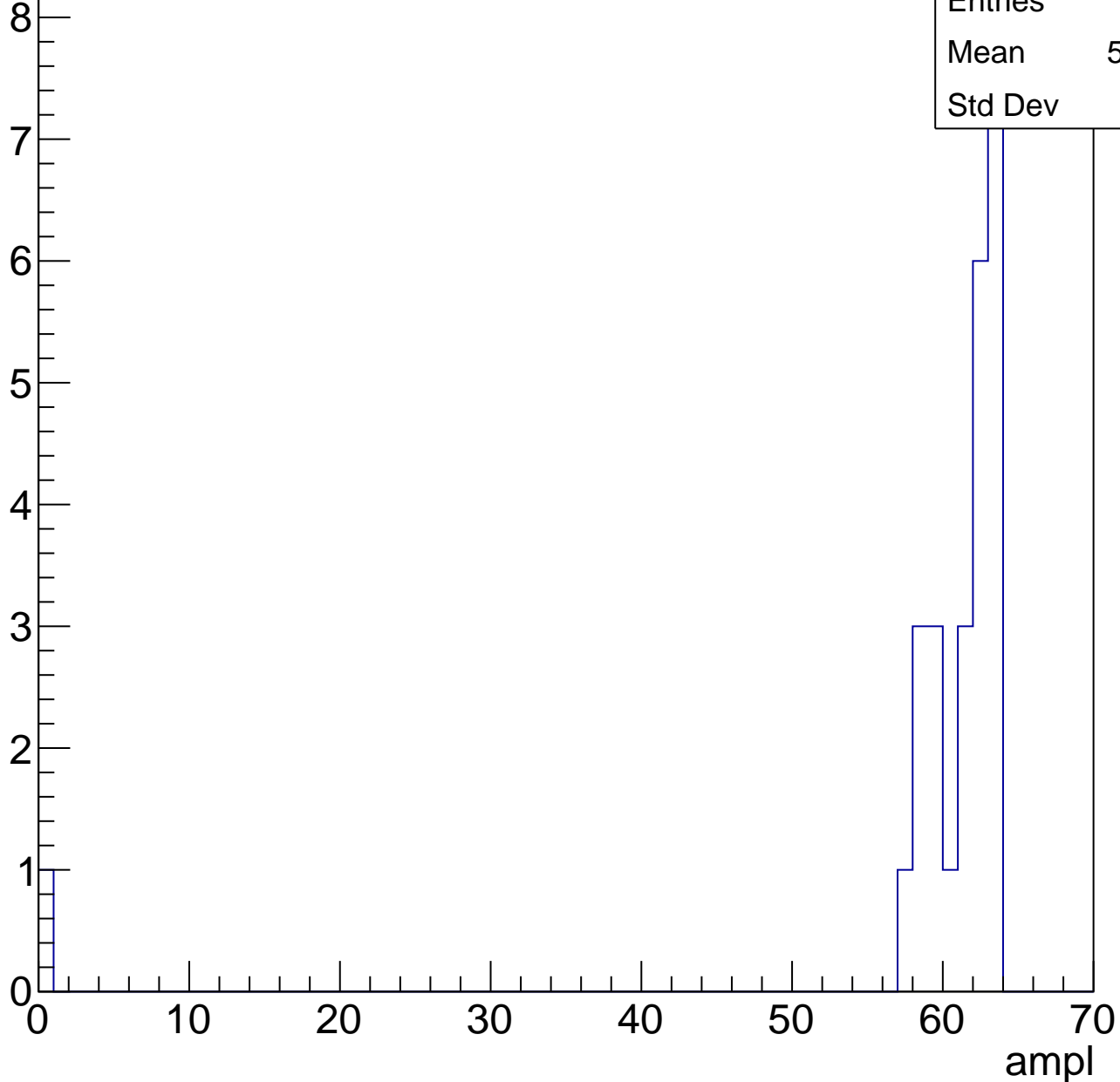


B1L100S, U12-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.73
Std Dev	11.9



B1L100S, U12-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U12-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch26, adc0

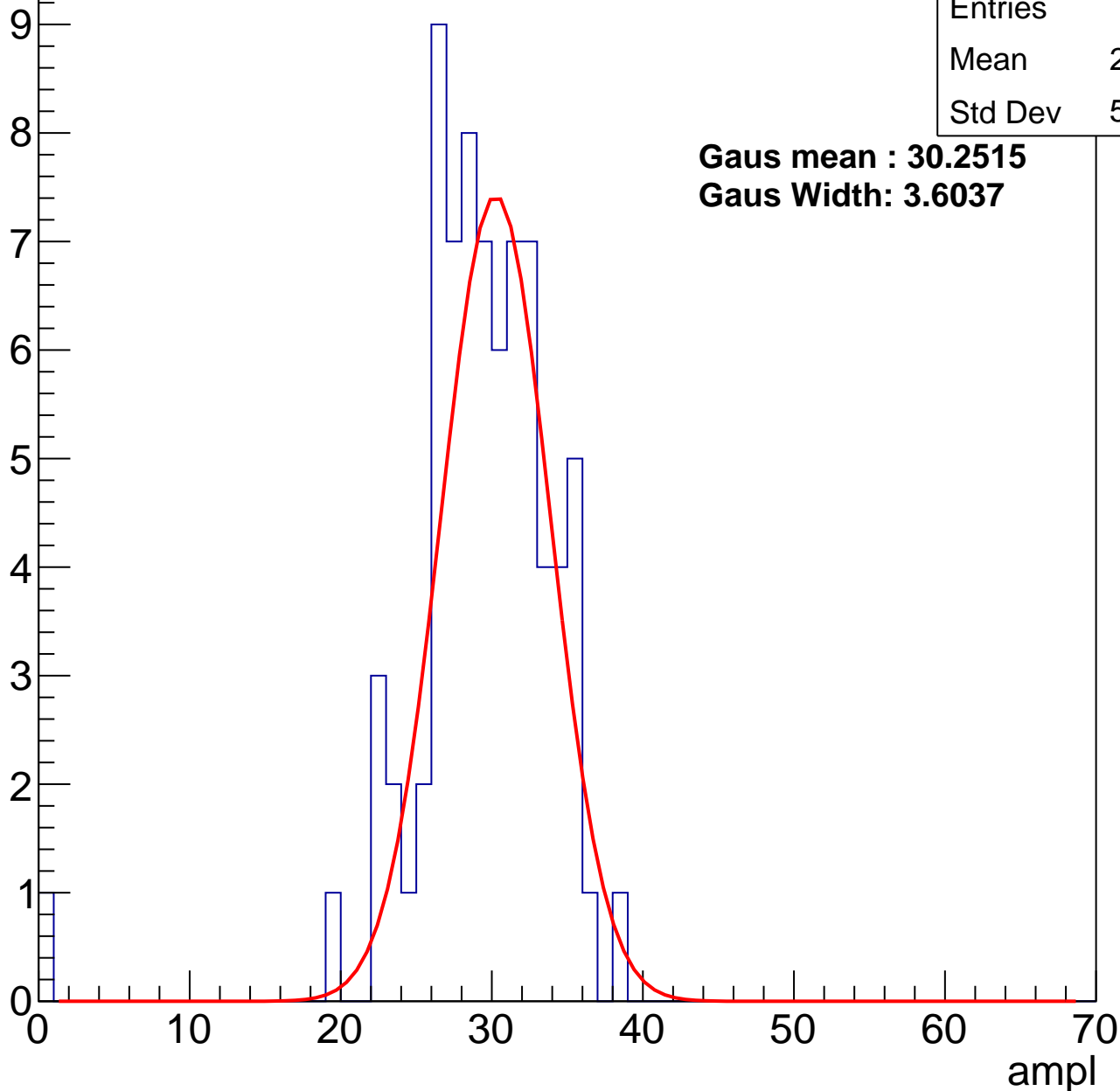
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	28.86
Std Dev	5.004

Gaus mean : 30.2515

Gaus Width: 3.6037



B1L100S, U12-ch26, adc1

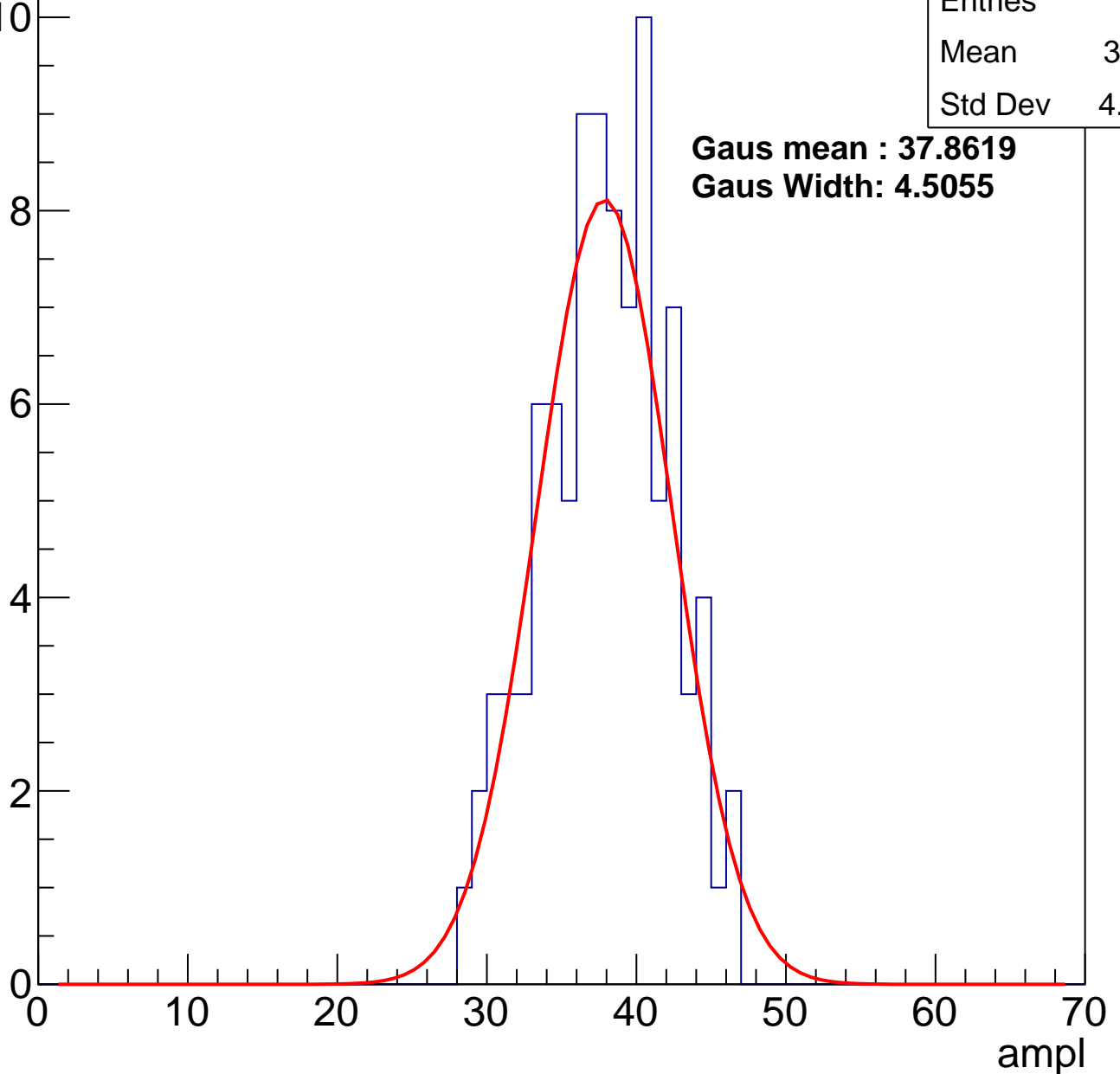
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	94
Mean	37.41
Std Dev	4.142

Gaus mean : 37.8619

Gaus Width: 4.5055



B1L100S, U12-ch26, adc2

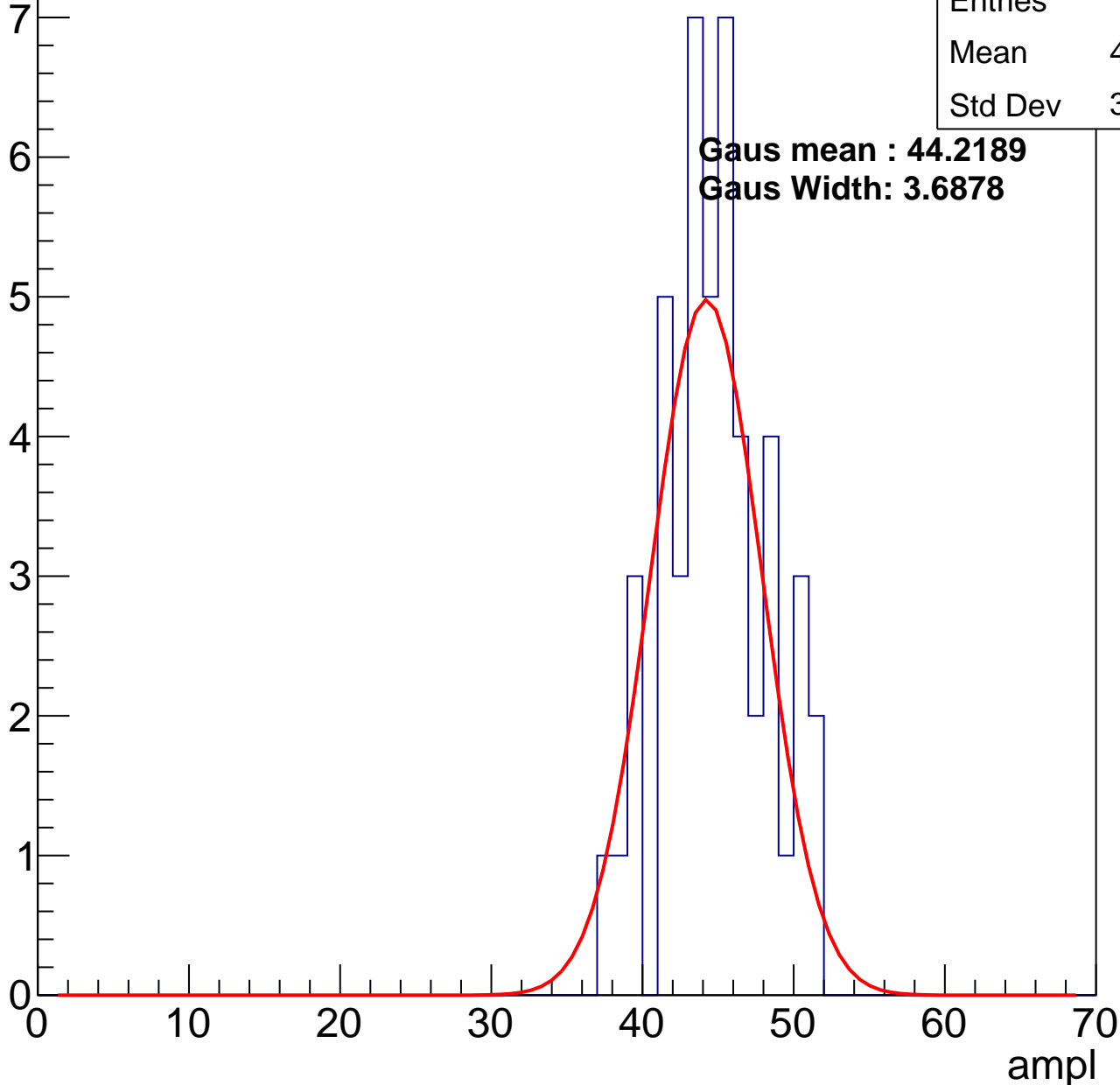
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	44.38
Std Dev	3.395

Gaus mean : 44.2189

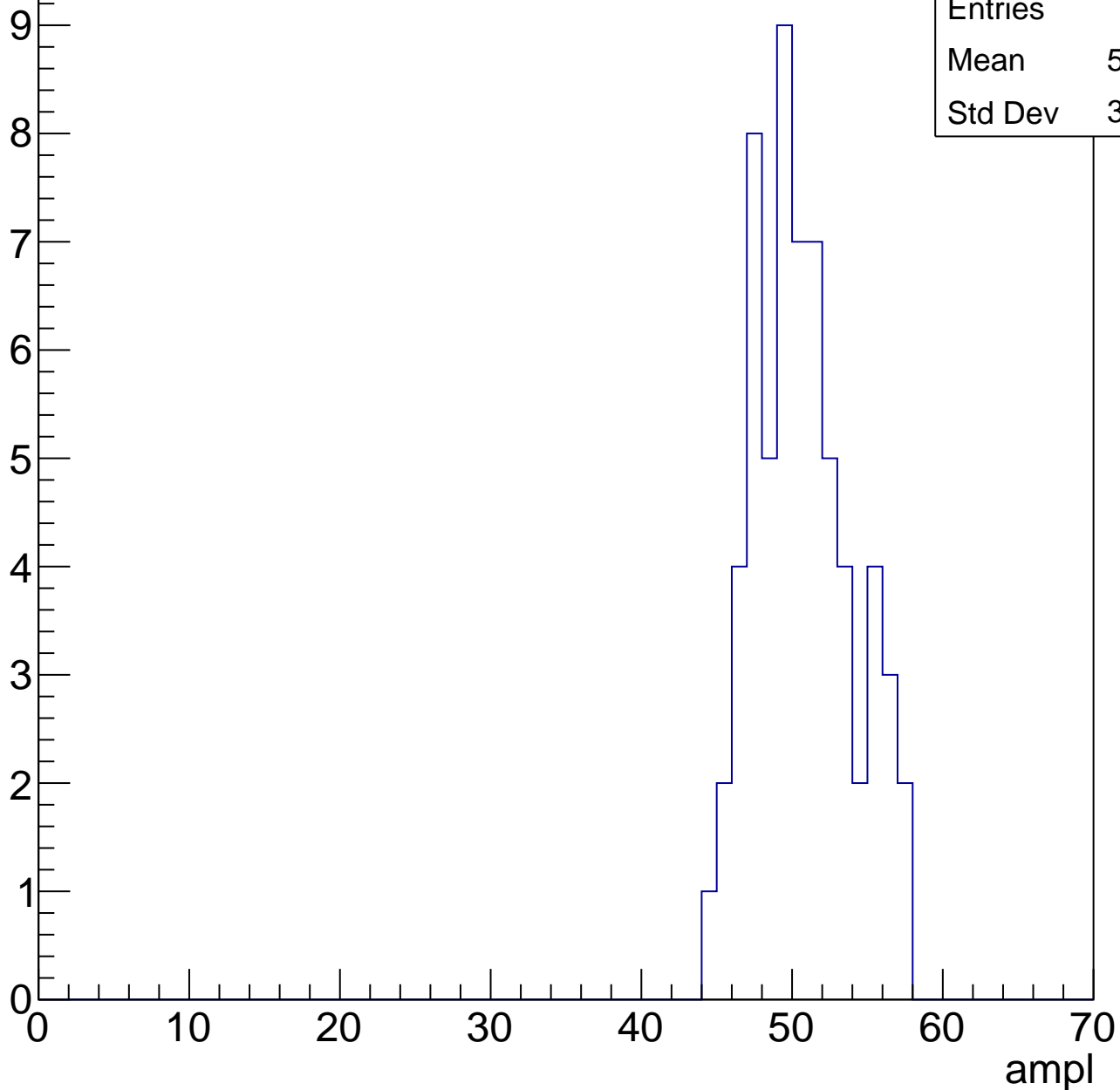
Gaus Width: 3.6878



B1L100S, U12-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

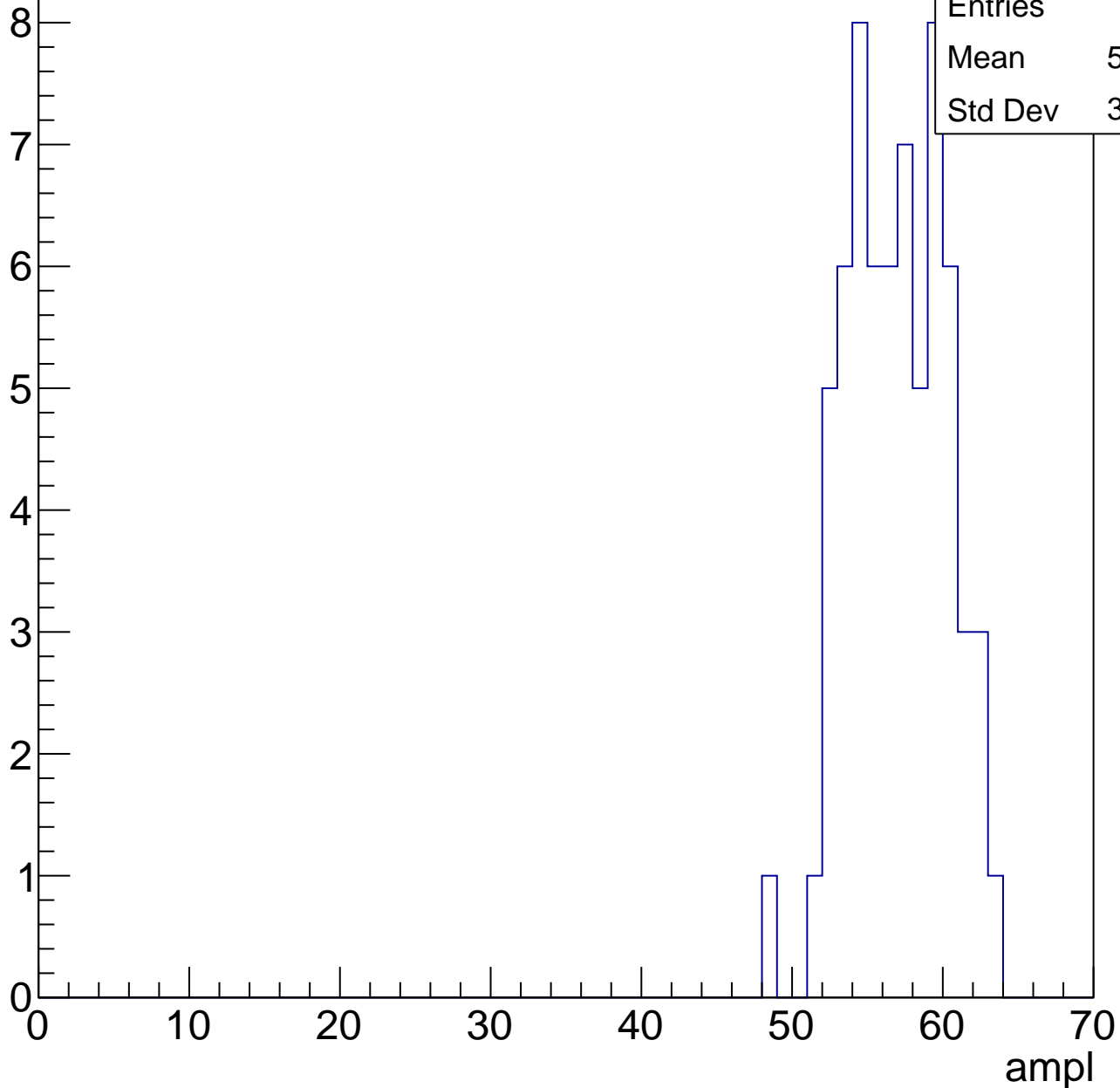


Entries	63
Mean	50.22
Std Dev	3.209

B1L100S, U12-ch26, adc4

calib_packv5_042523_0143.root, FC#4, port A2

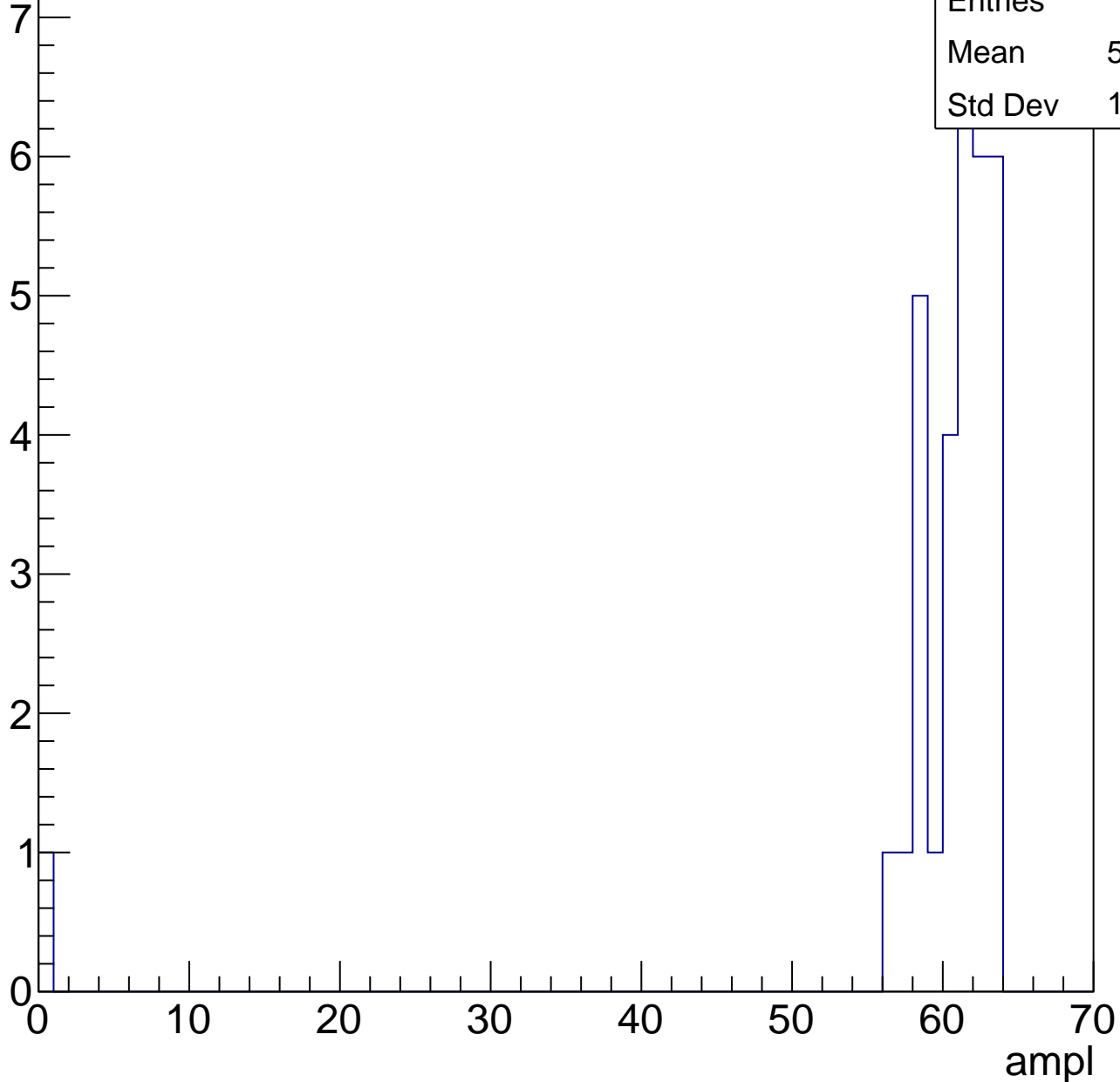
Entry



B1L100S, U12-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

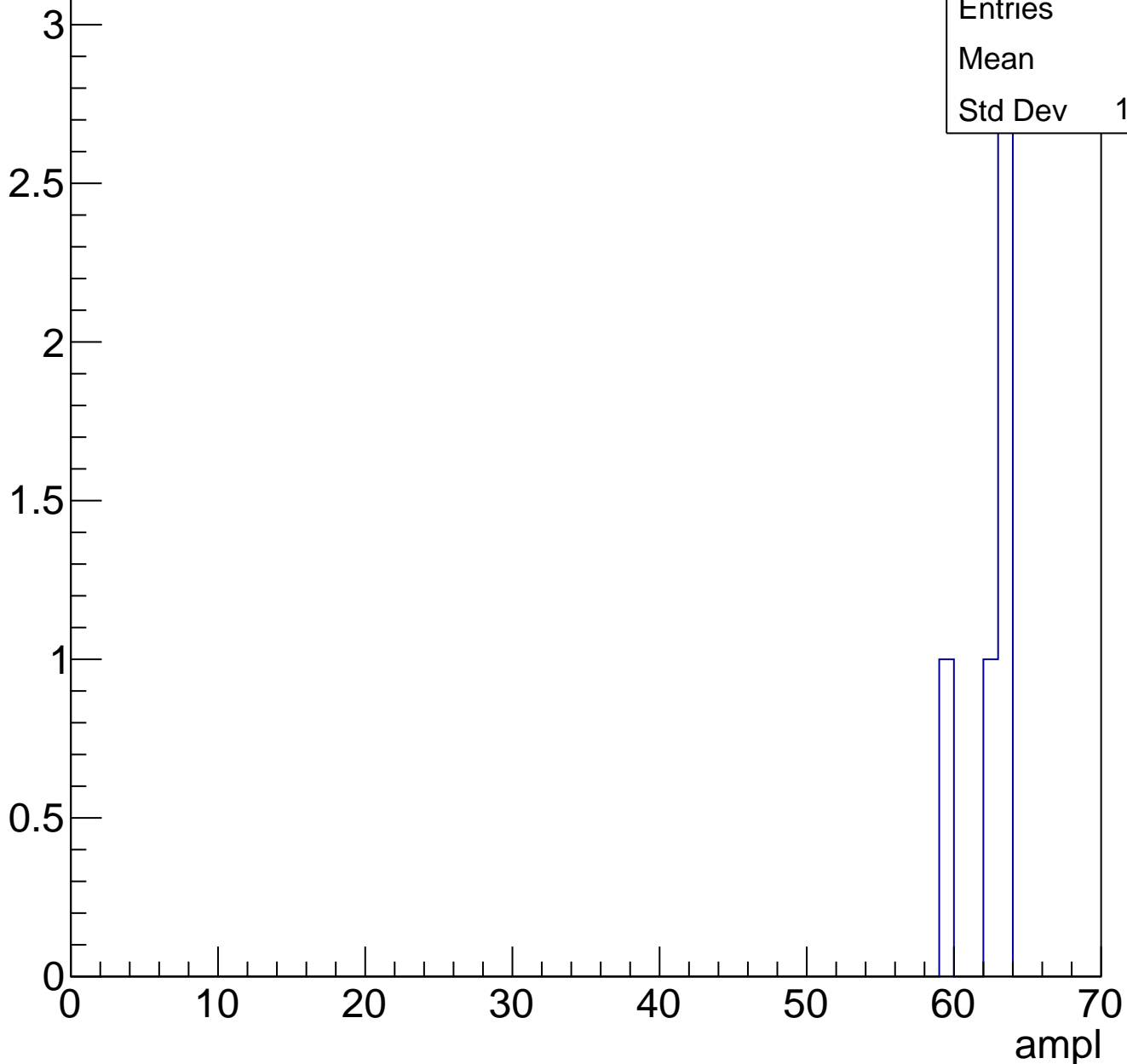
Entry



B1L100S, U12-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch27, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	29.37
Std Dev	3.745

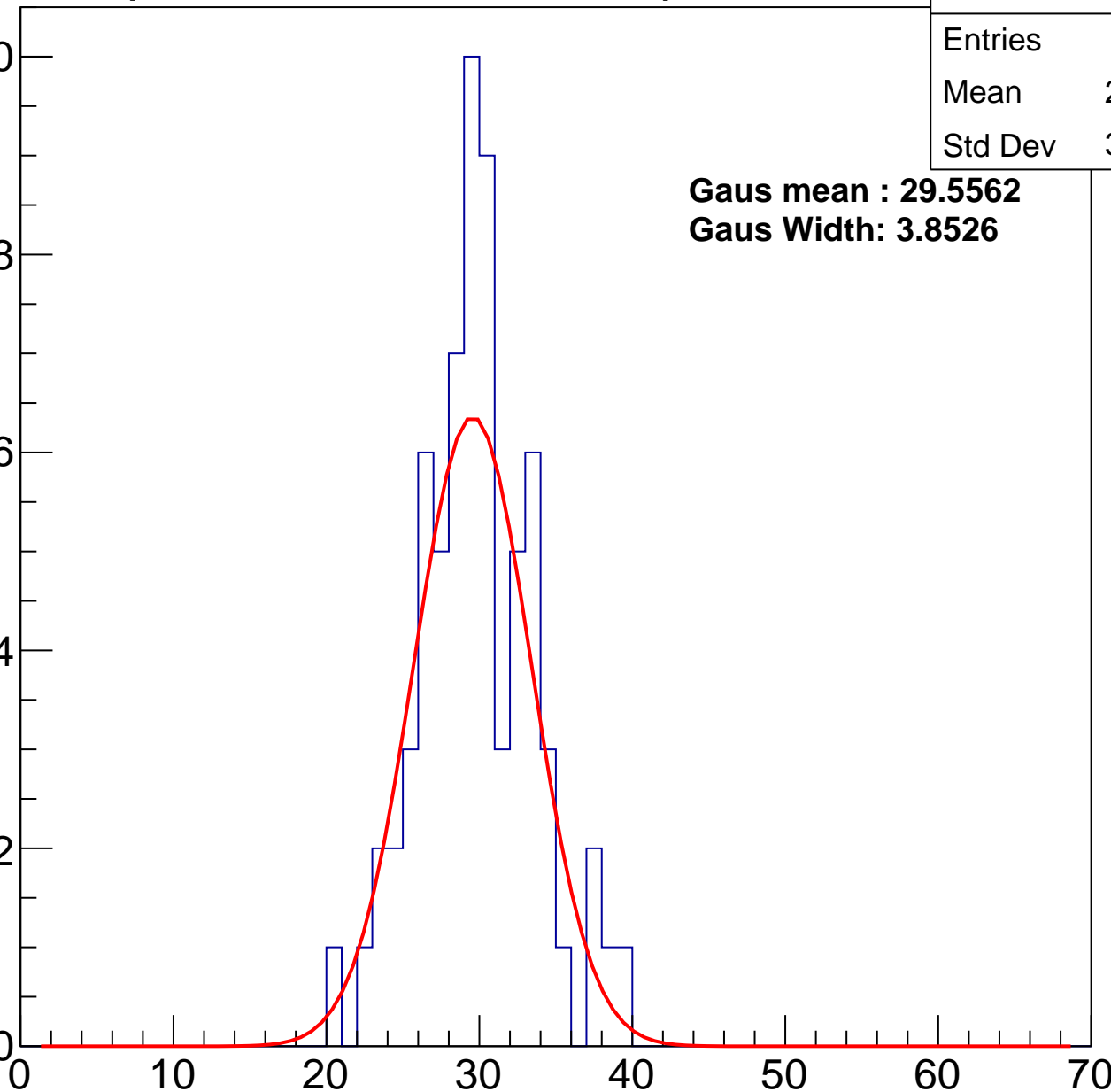
Gaus mean : 29.5562

Gaus Width: 3.8526

Entry

10
8
6
4
2
0

ampl



B1L100S, U12-ch27, adc1

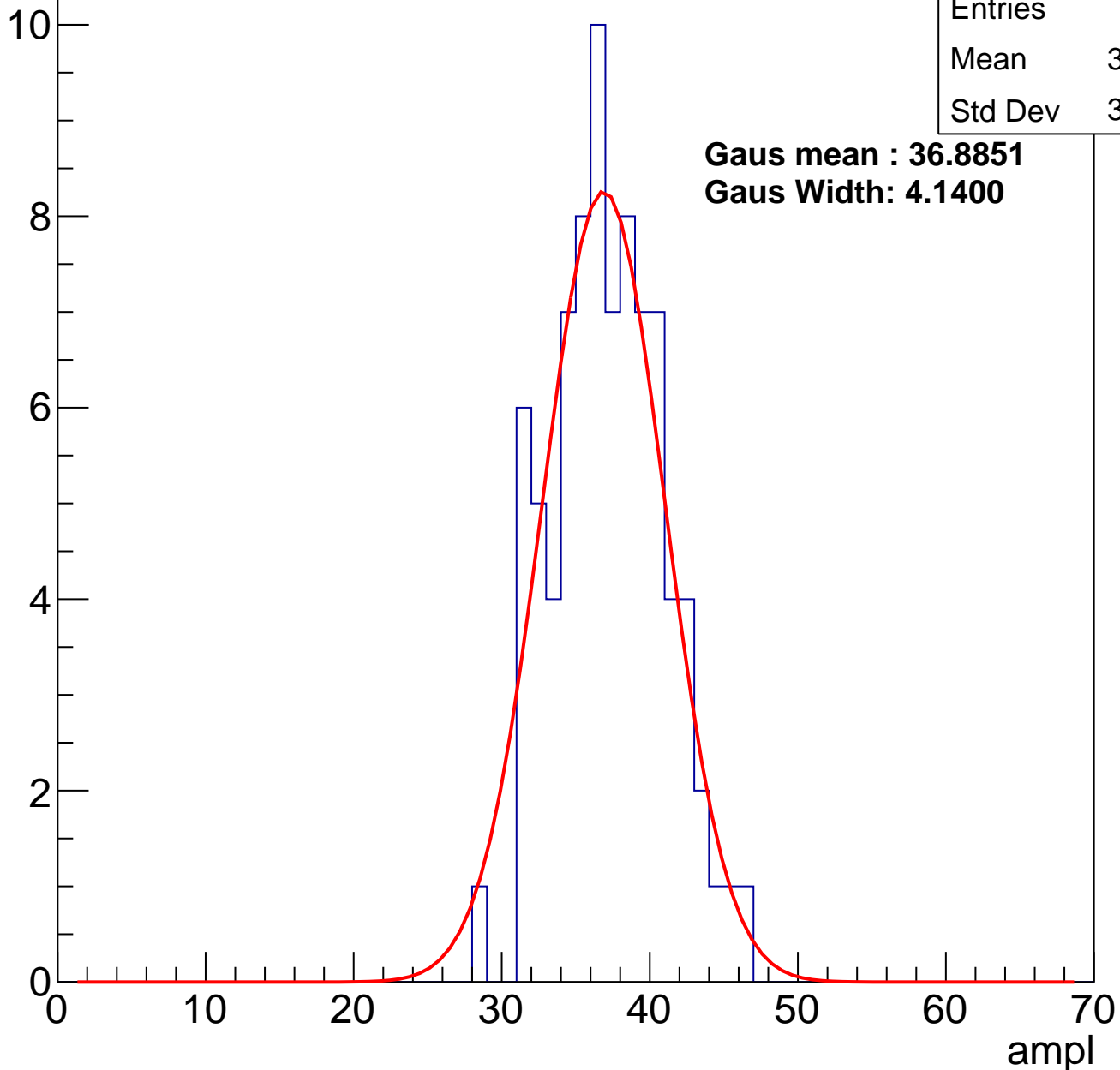
calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	36.78
Std Dev	3.667

Gaus mean : 36.8851

Gaus Width: 4.1400

Entry



B1L100S, U12-ch27, adc2

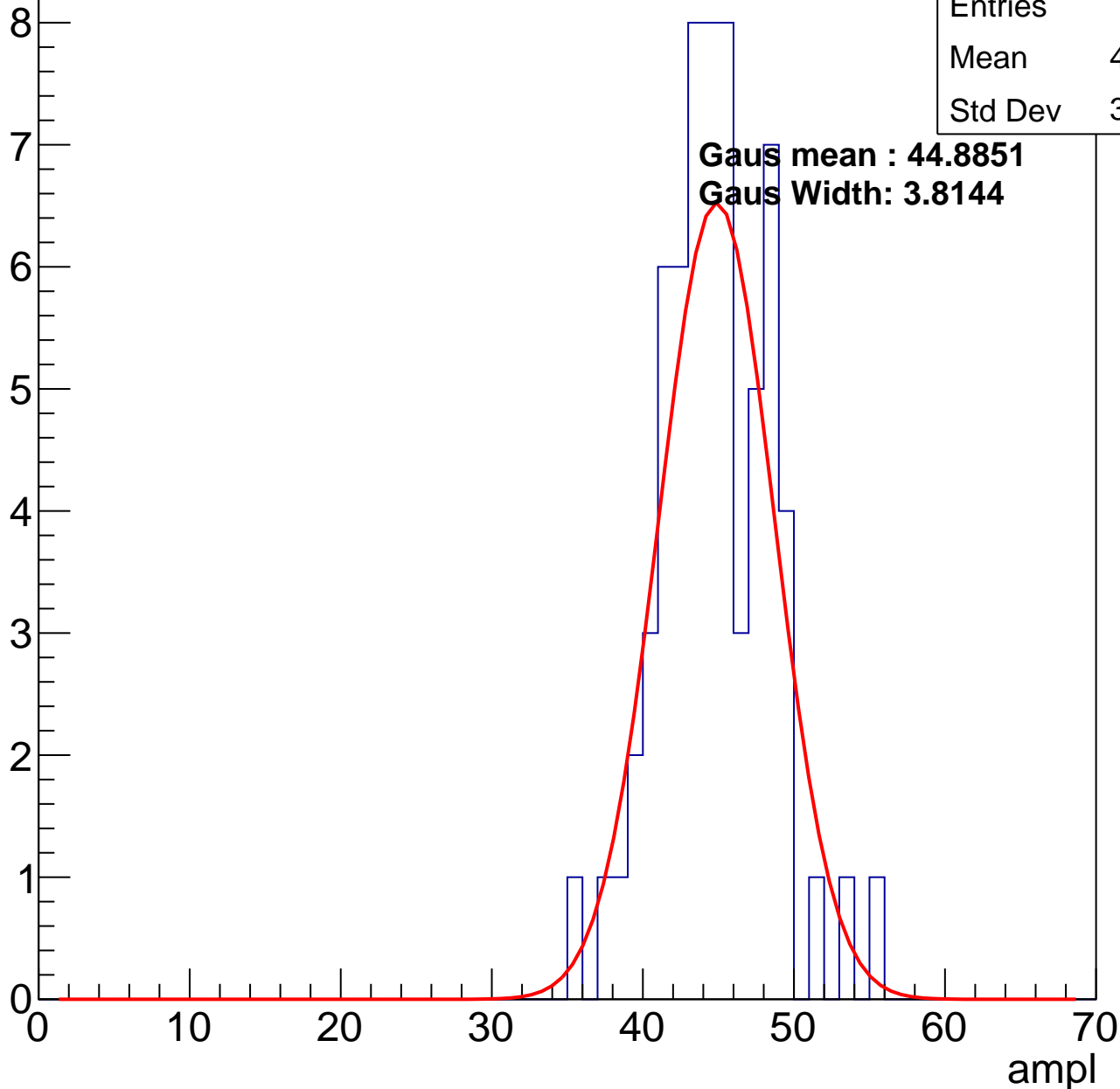
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.33
Std Dev	3.632

Gaus mean : 44.8851

Gaus Width: 3.8144

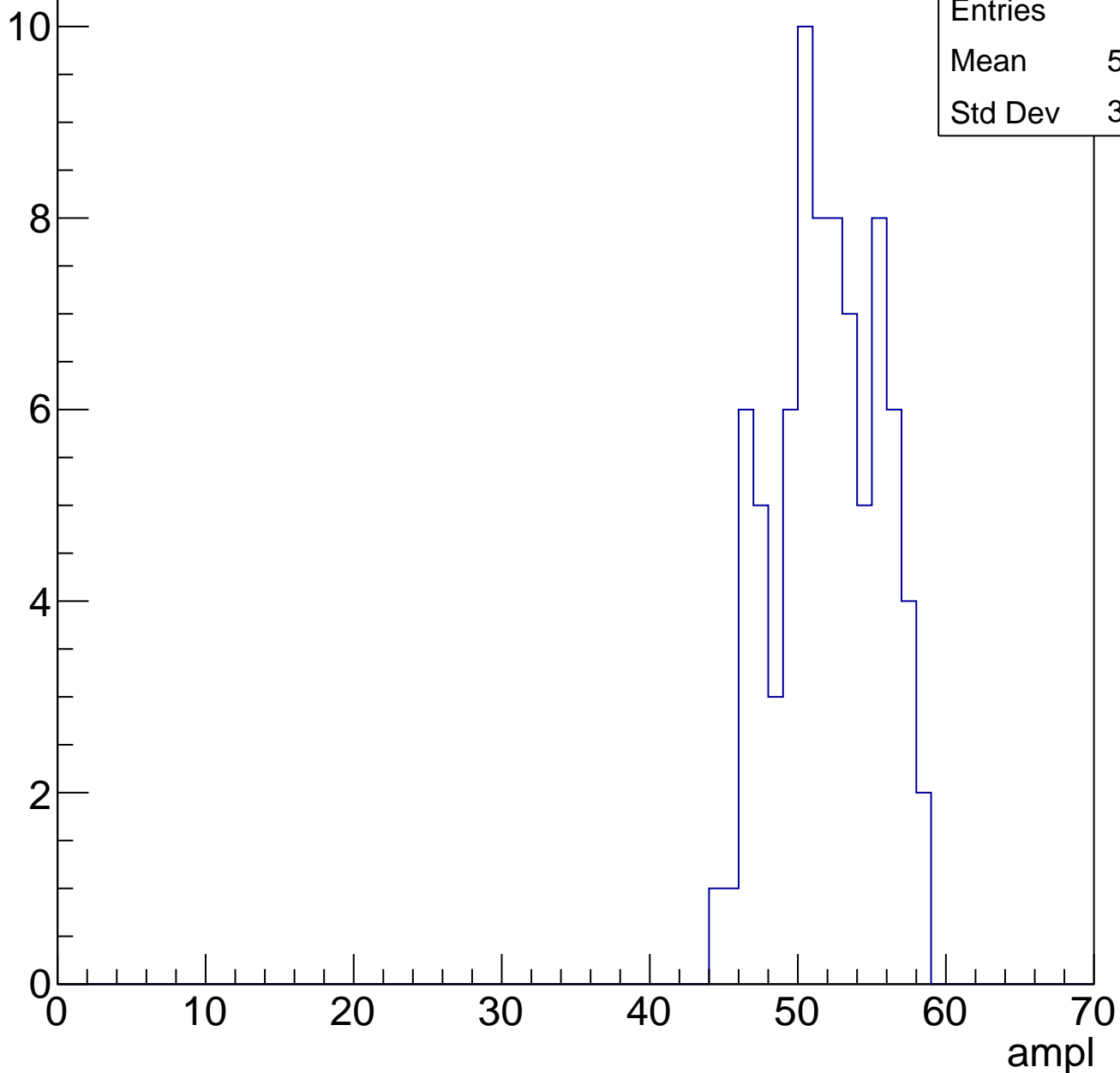


B1L100S, U12-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	51.54
Std Dev	3.453

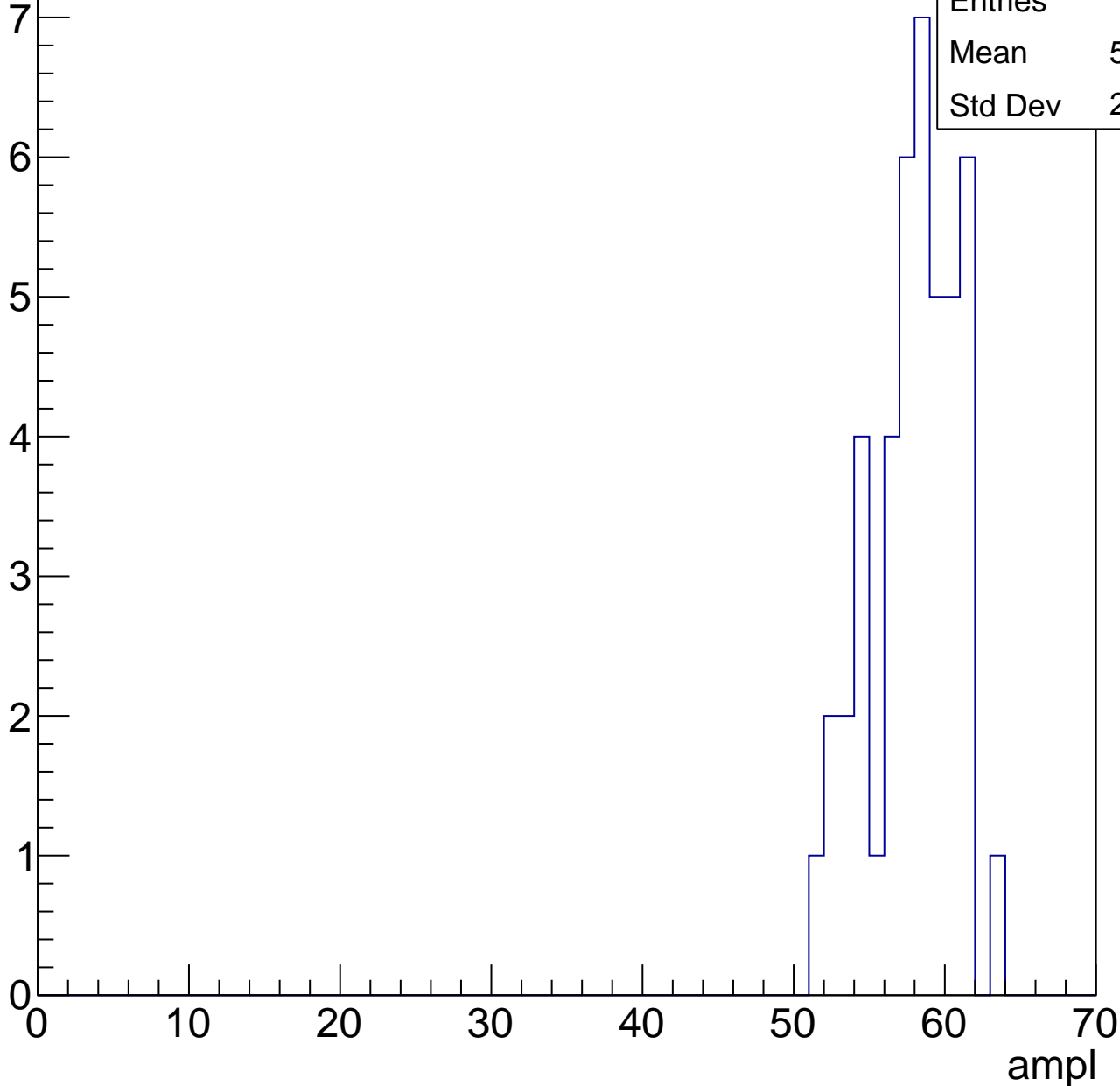


B1L100S, U12-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	57.45
Std Dev	2.848



B1L100S, U12-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	36
Mean	59.39
Std Dev	10.22

Entry

10

8

6

4

2

0

0

10

20

30

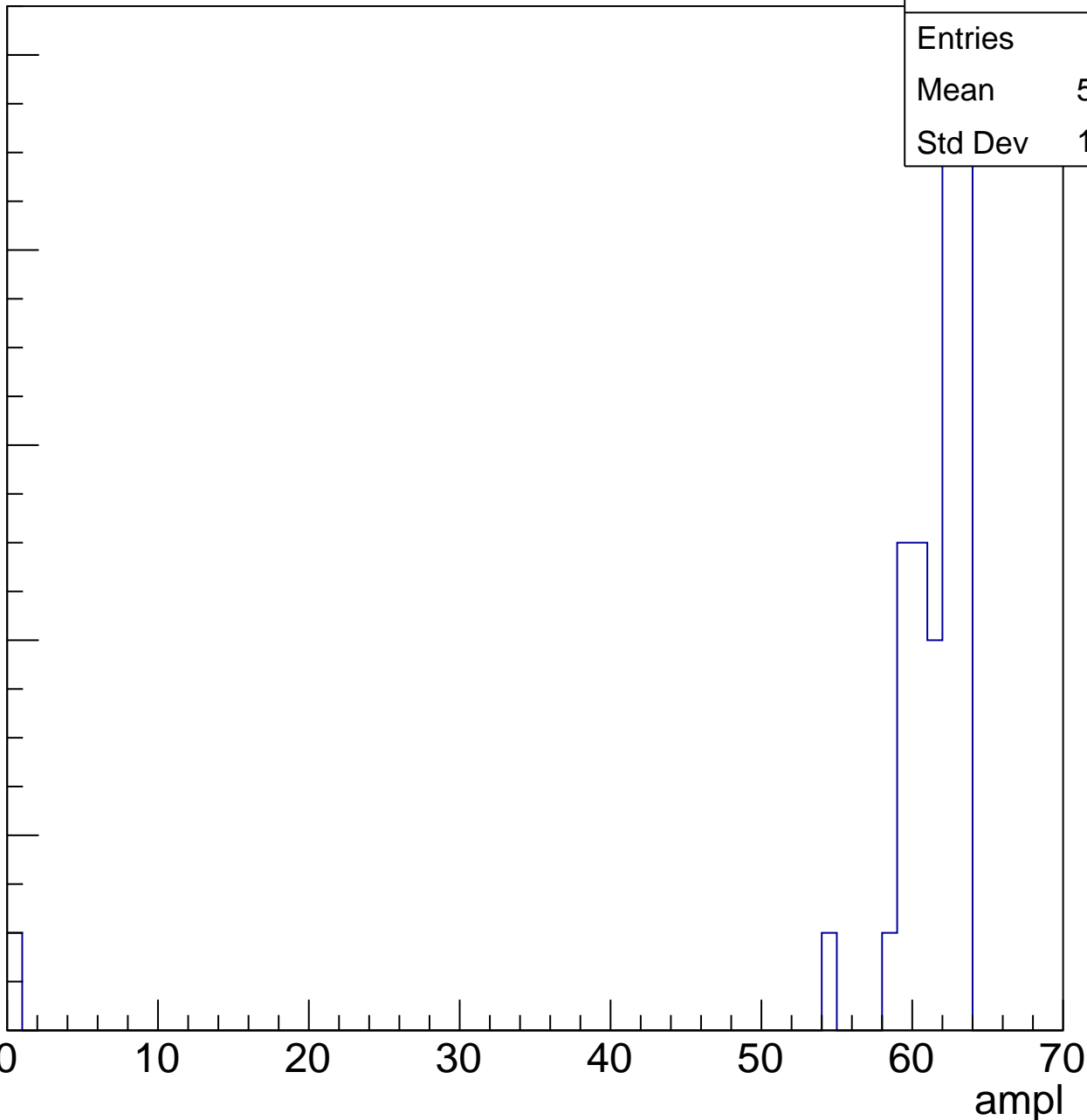
40

50

60

70

ampl



B1L100S, U12-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch28, adc0

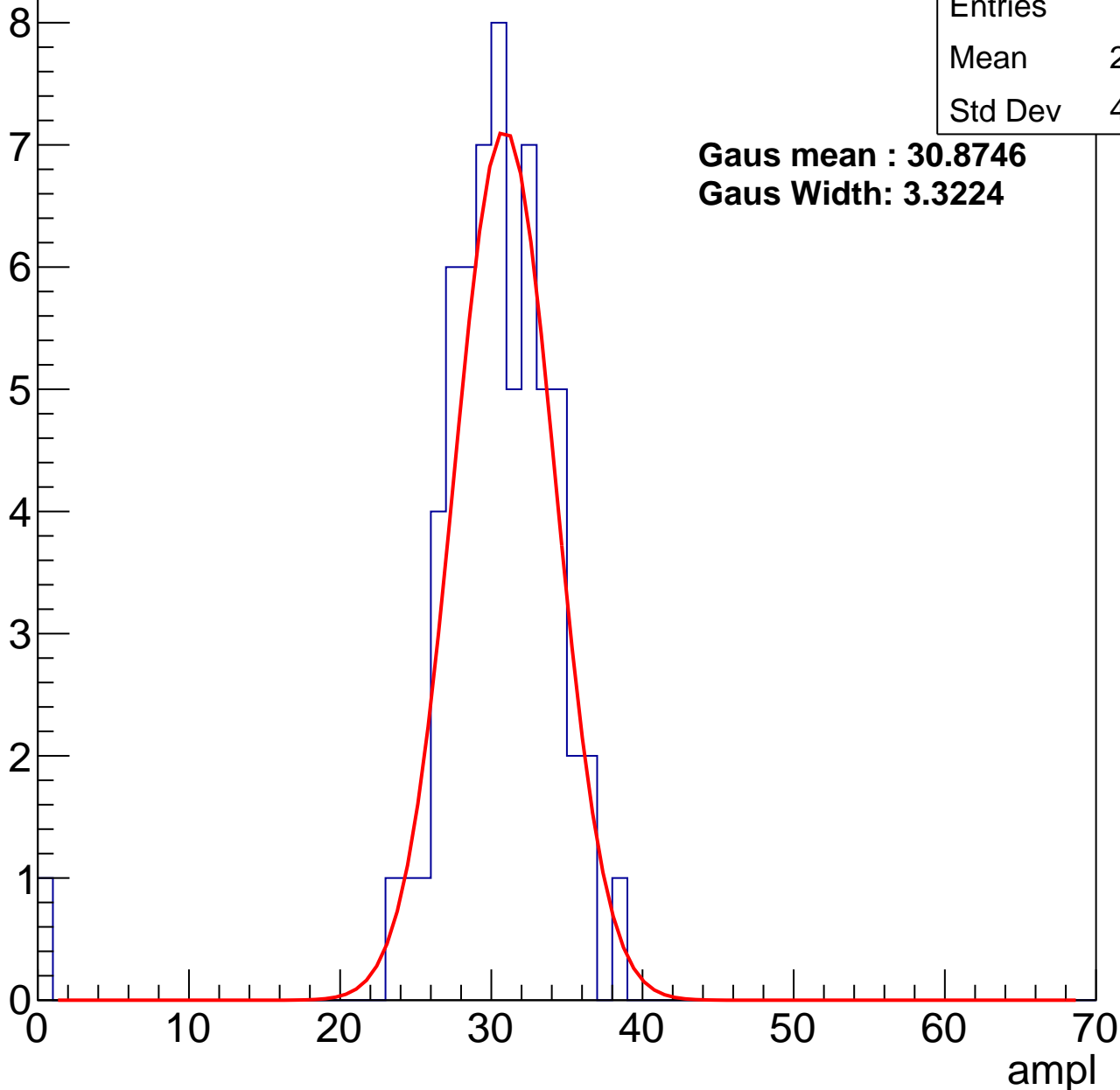
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	29.73
Std Dev	4.916

Gaus mean : 30.8746

Gaus Width: 3.3224



B1L100S, U12-ch28, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	37.01
Std Dev	3.485

Gaus mean : 37.9634

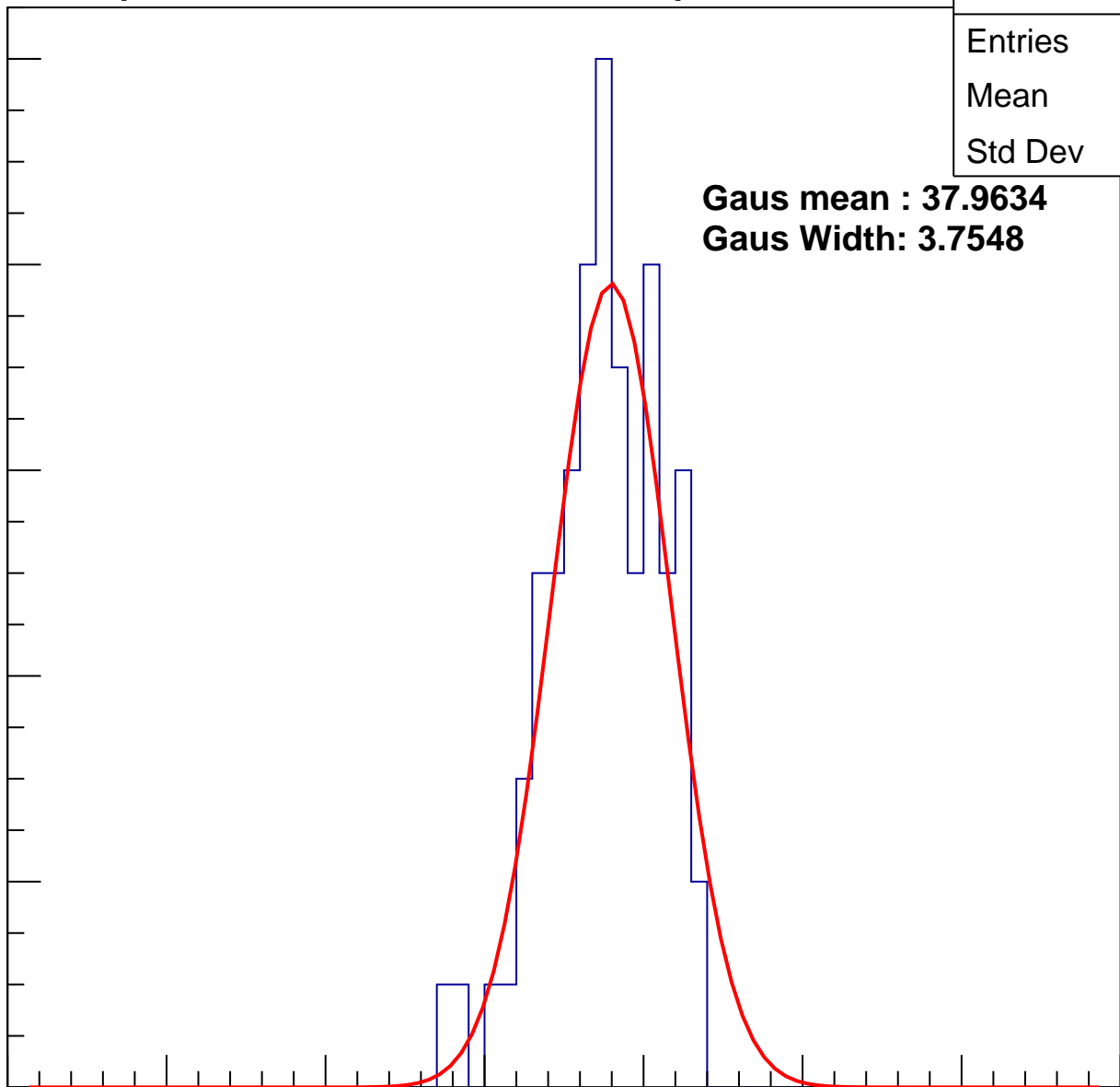
Gaus Width: 3.7548

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch28, adc2

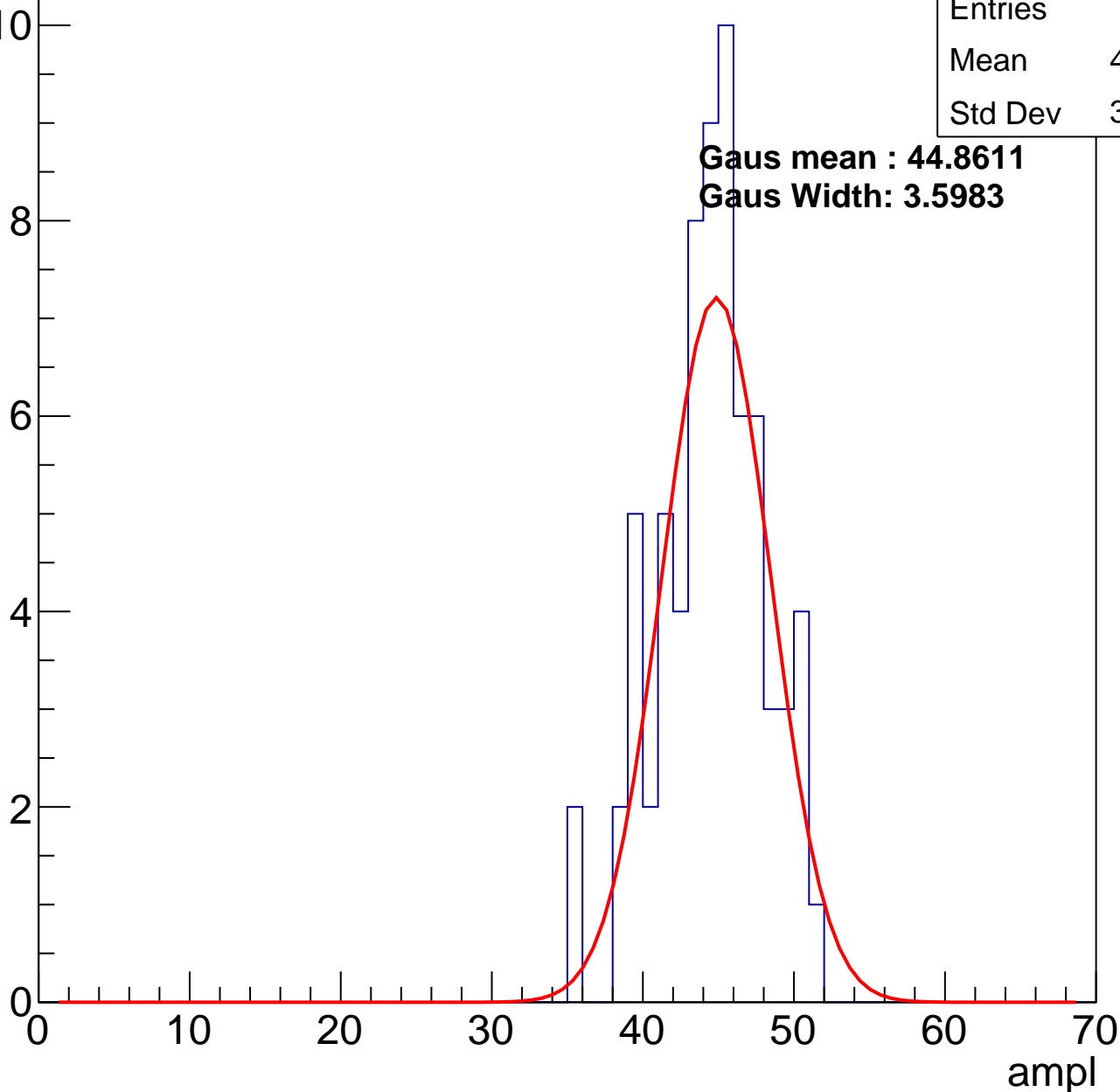
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.06
Std Dev	3.513

Gaus mean : 44.8611

Gaus Width: 3.5983

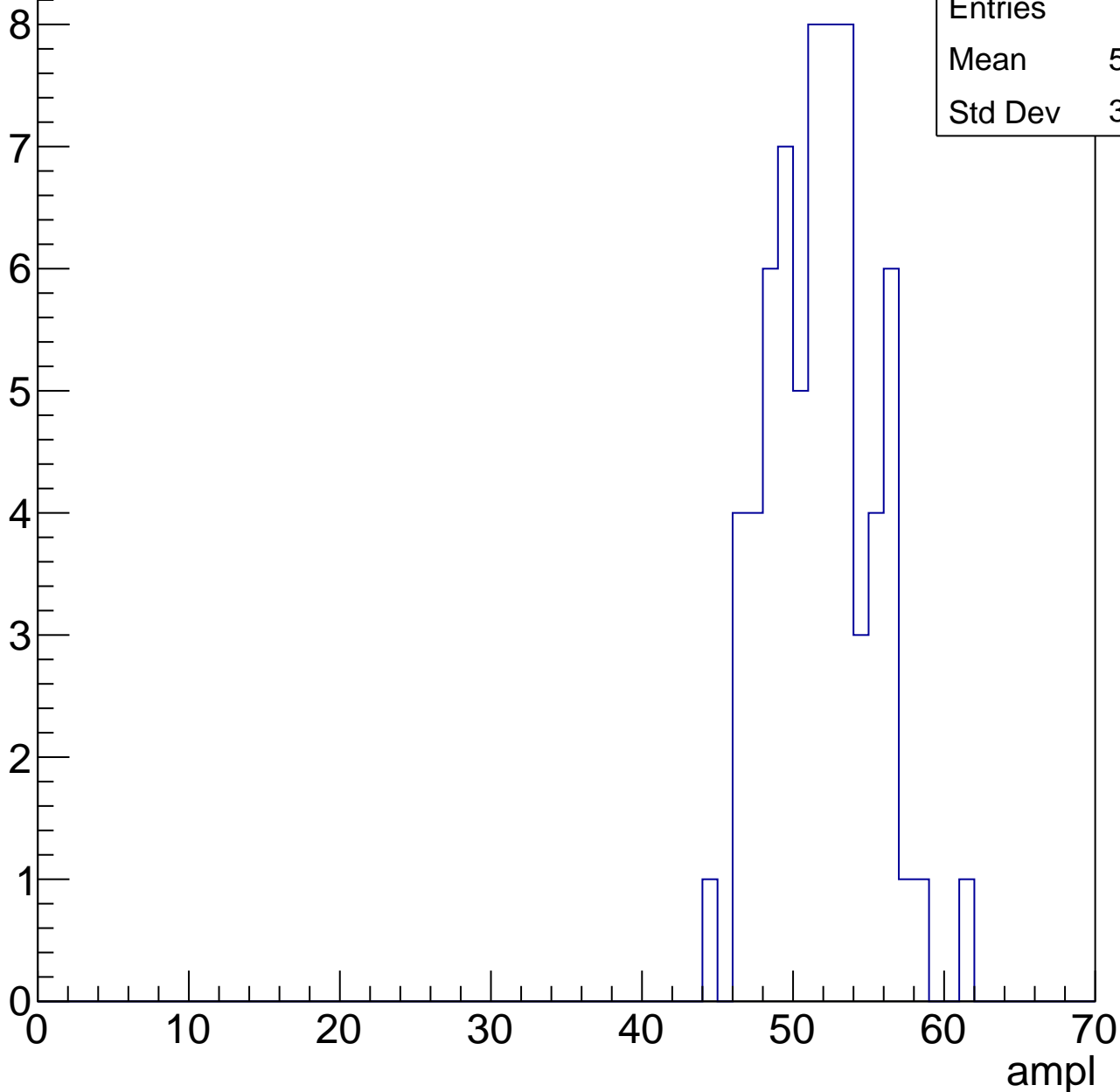


B1L100S, U12-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	51.33
Std Dev	3.365

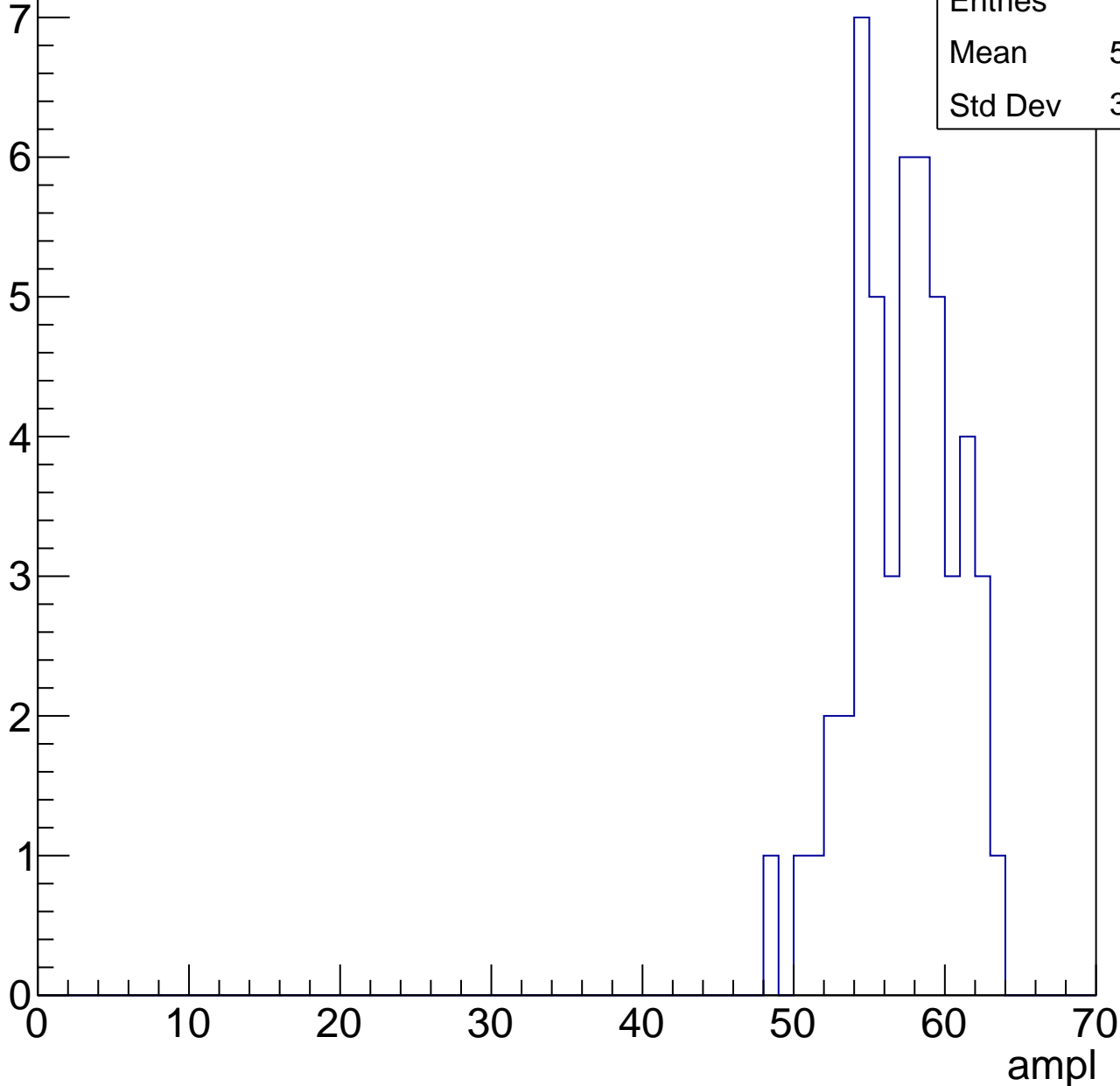


B1L100S, U12-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	56.76
Std Dev	3.344

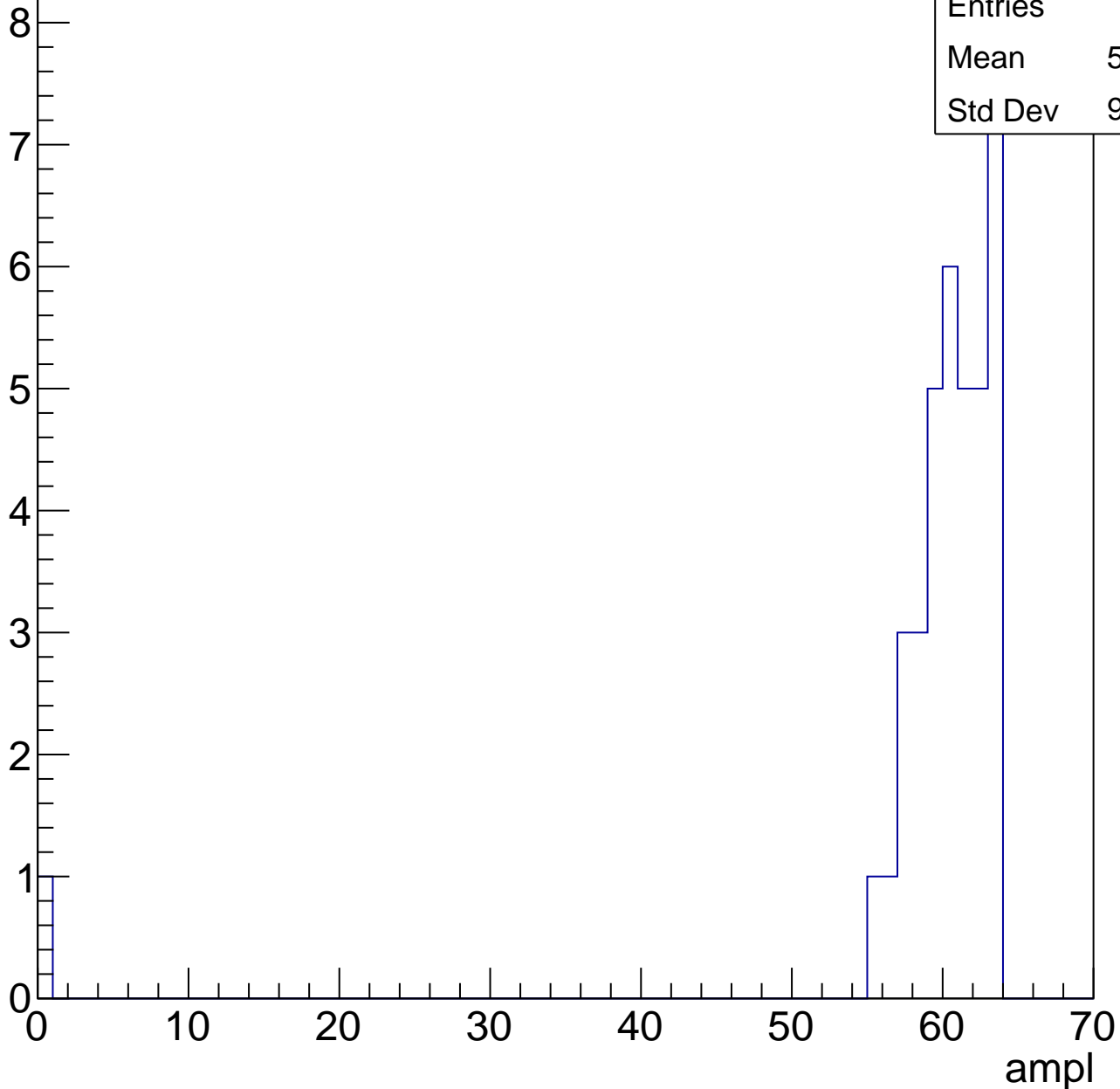


B1L100S, U12-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

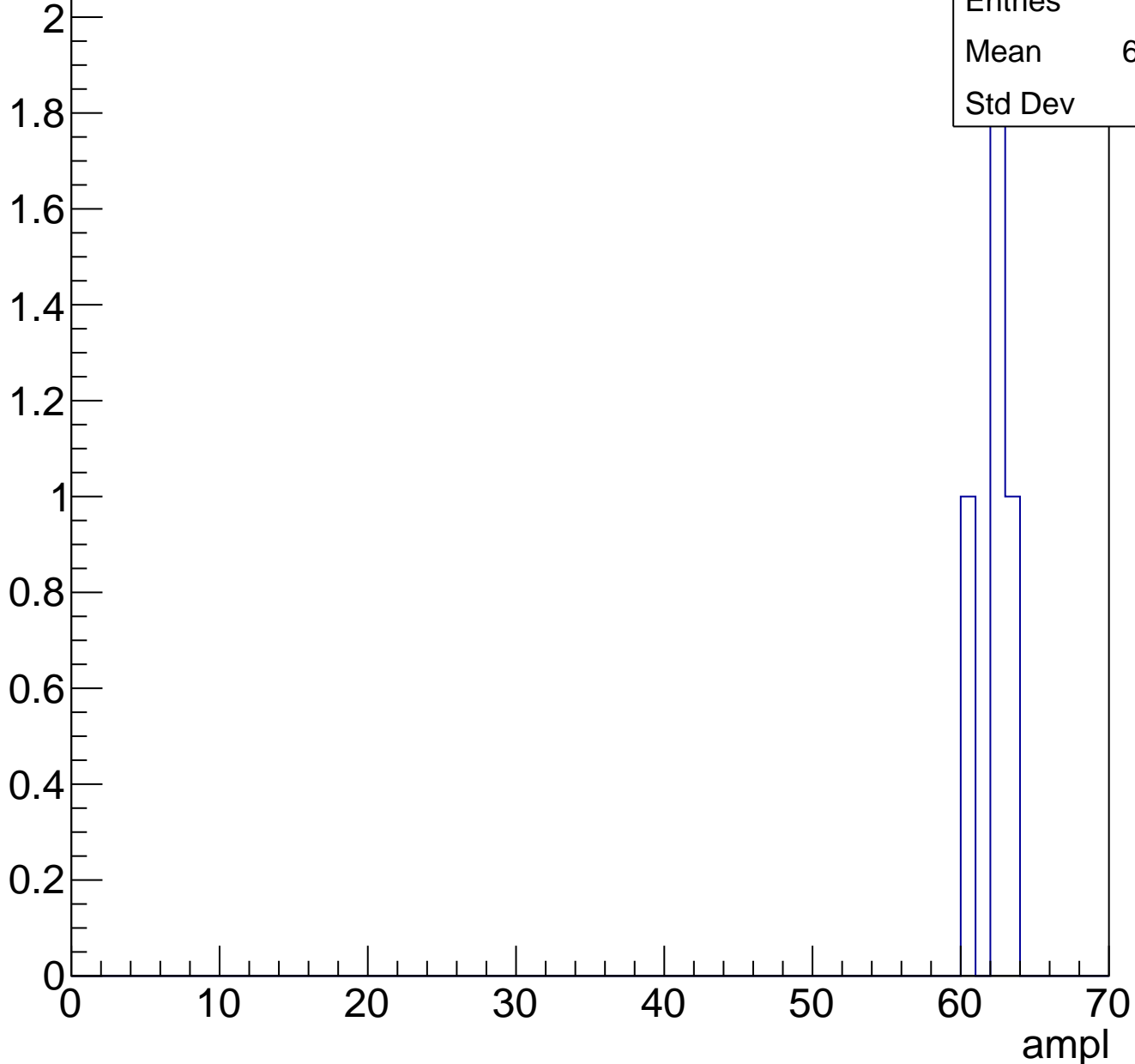
Entries	38
Mean	58.68
Std Dev	9.889



B1L100S, U12-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

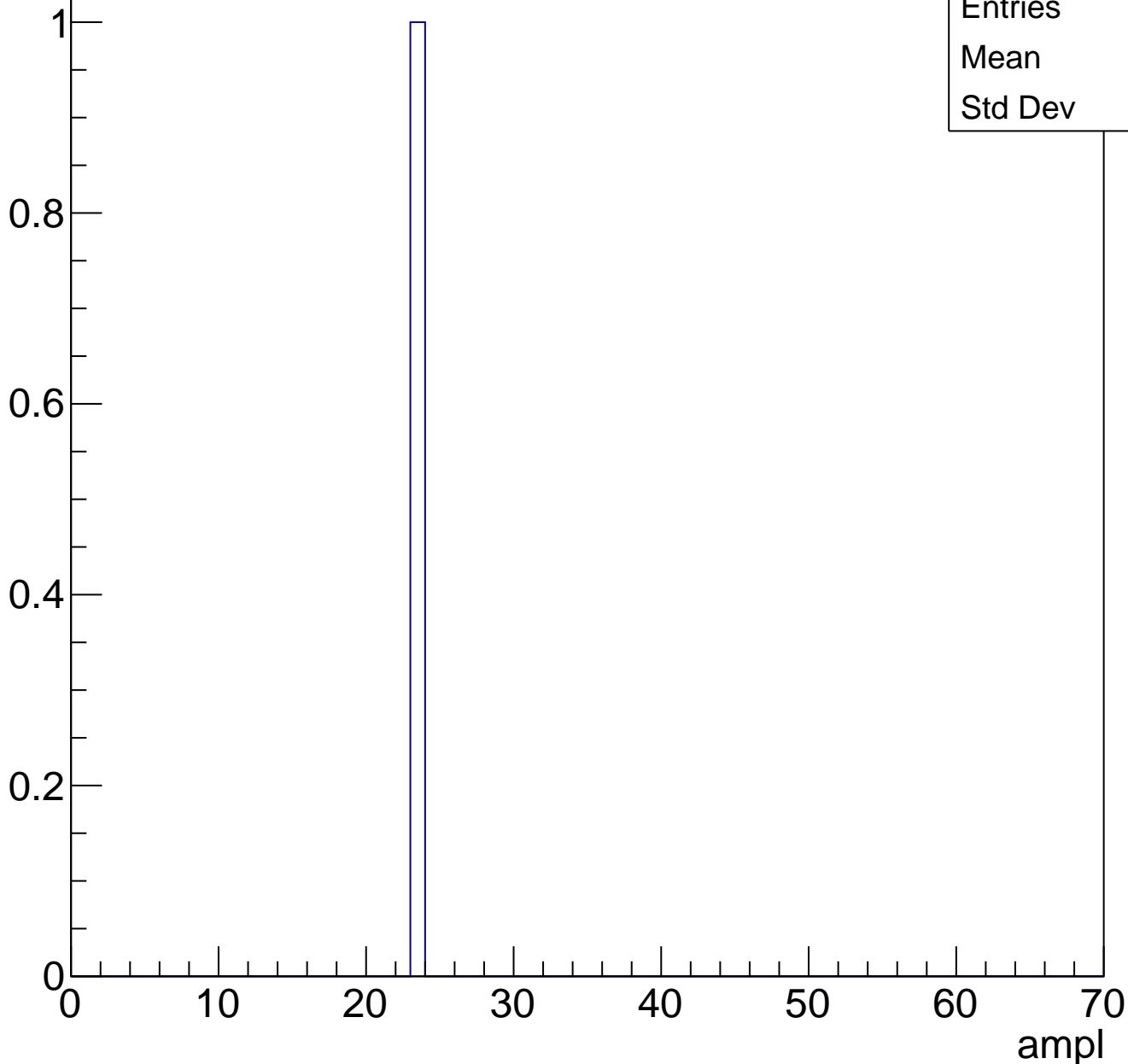
Entry



B1L100S, U12-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U12-ch29, adc0

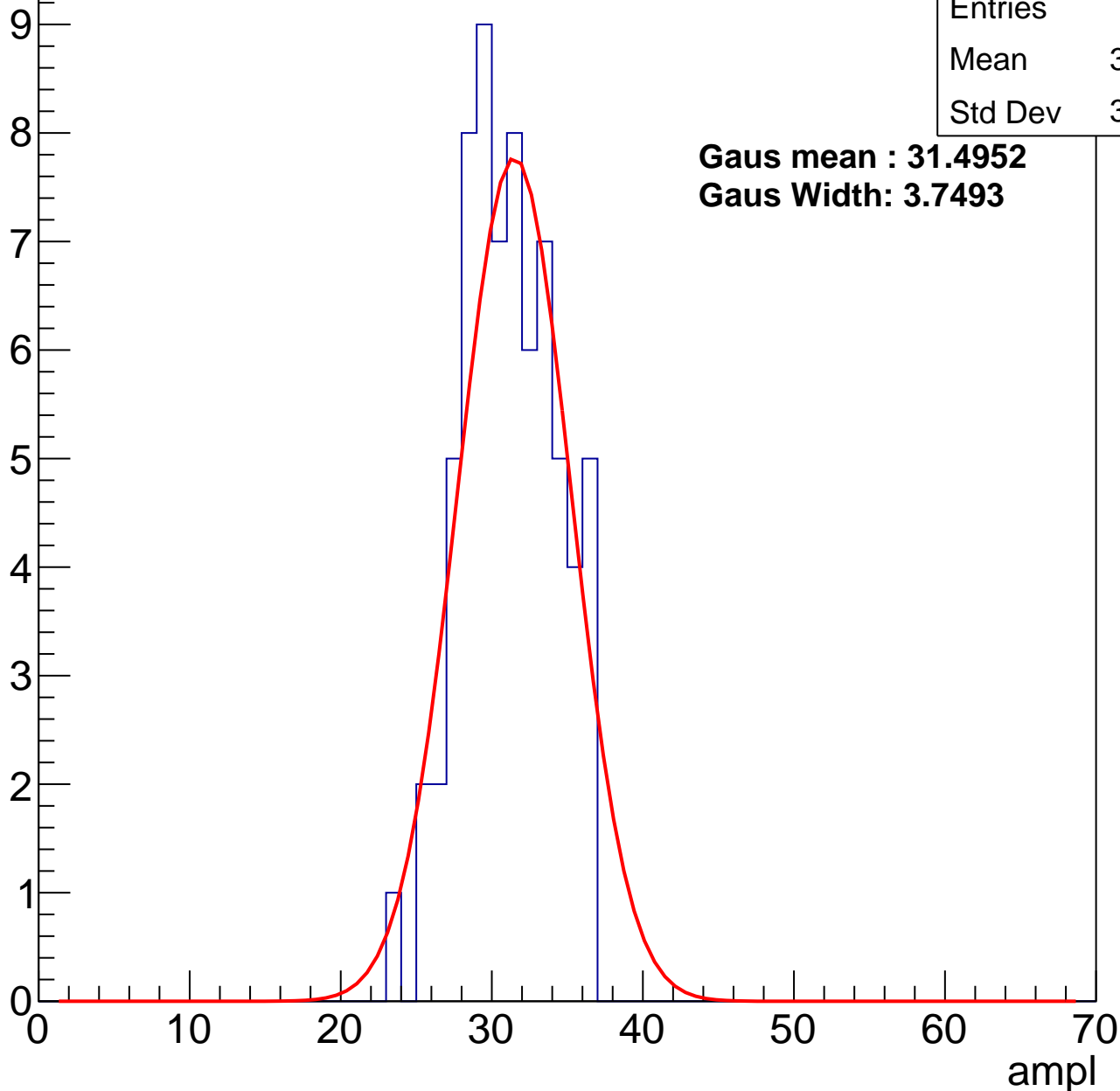
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.67
Std Dev	3.049

Gaus mean : 31.4952

Gaus Width: 3.7493



B1L100S, U12-ch29, adc1

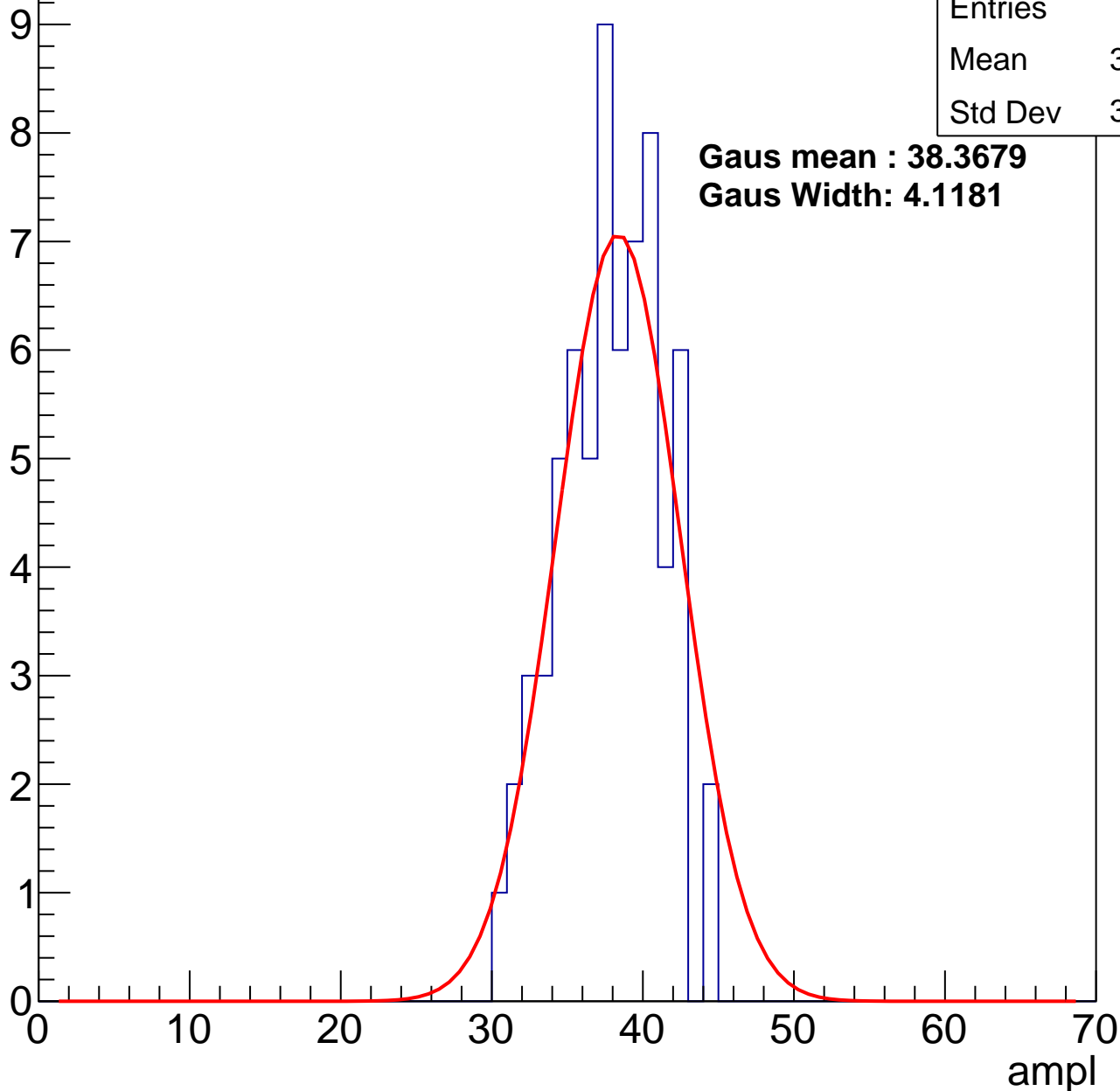
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.39
Std Dev	3.292

Gaus mean : 38.3679

Gaus Width: 4.1181



B1L100S, U12-ch29, adc2

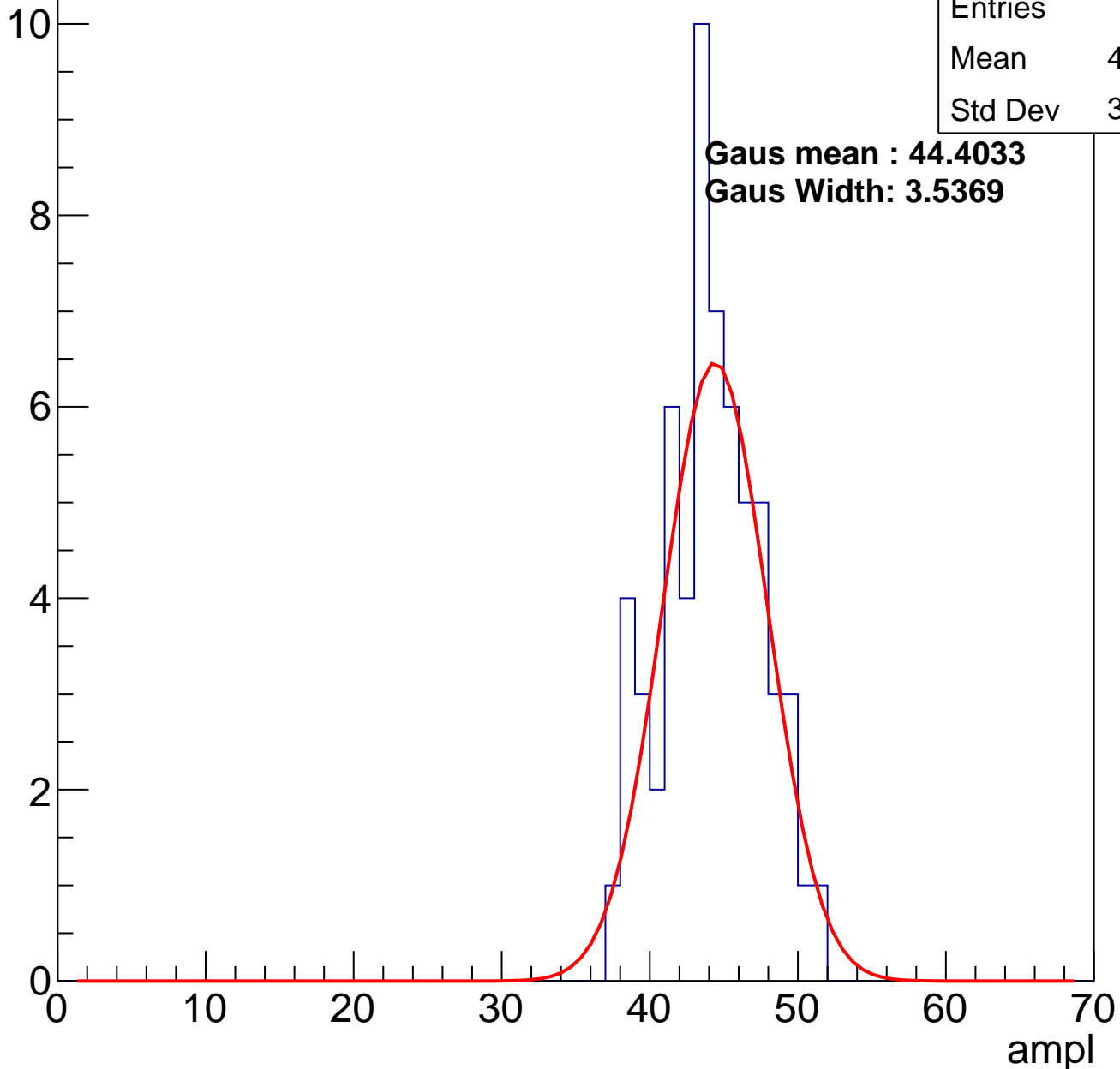
calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	43.69
Std Dev	3.272

Gaus mean : 44.4033

Gaus Width: 3.5369

Entry

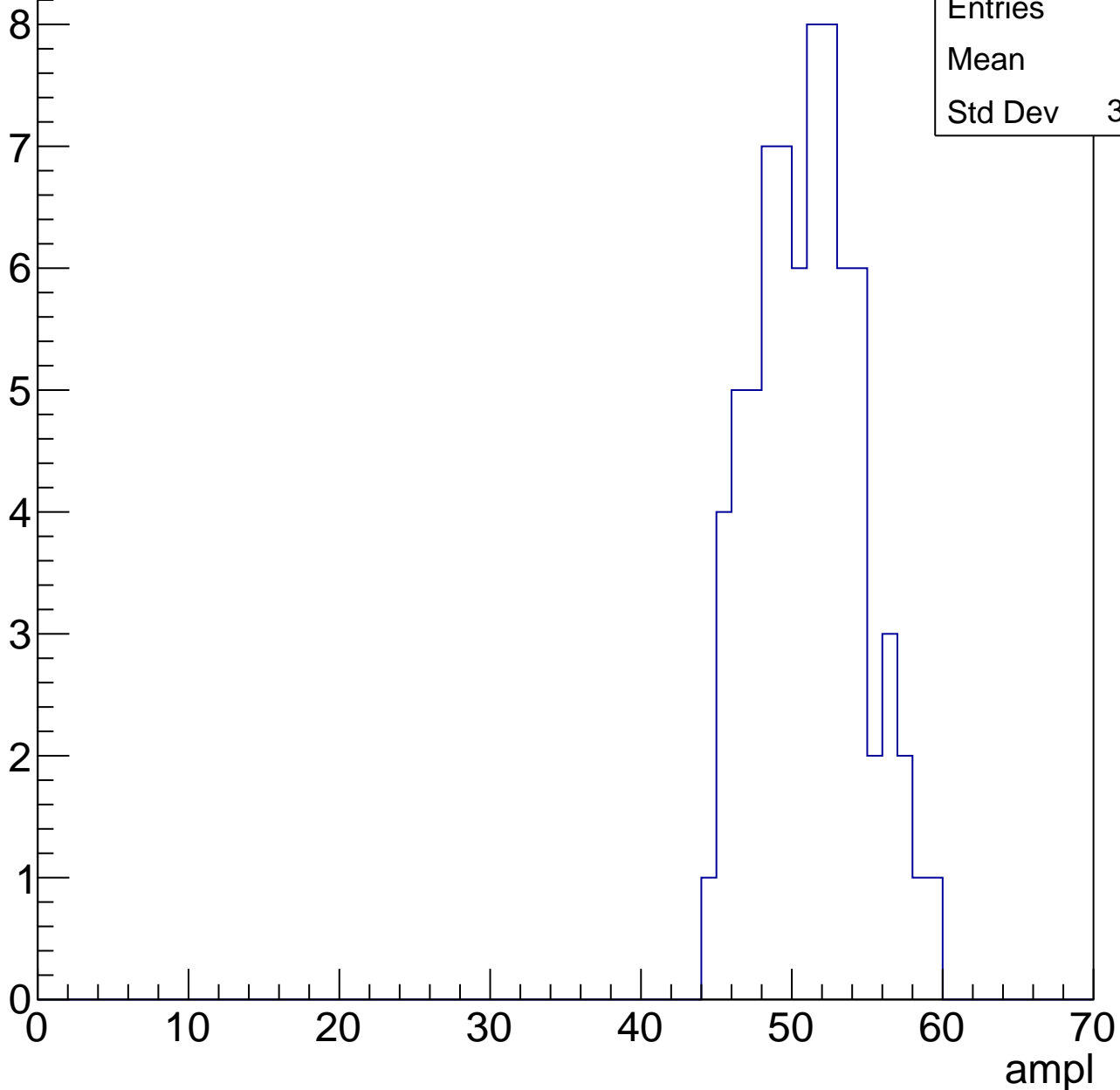


B1L100S, U12-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

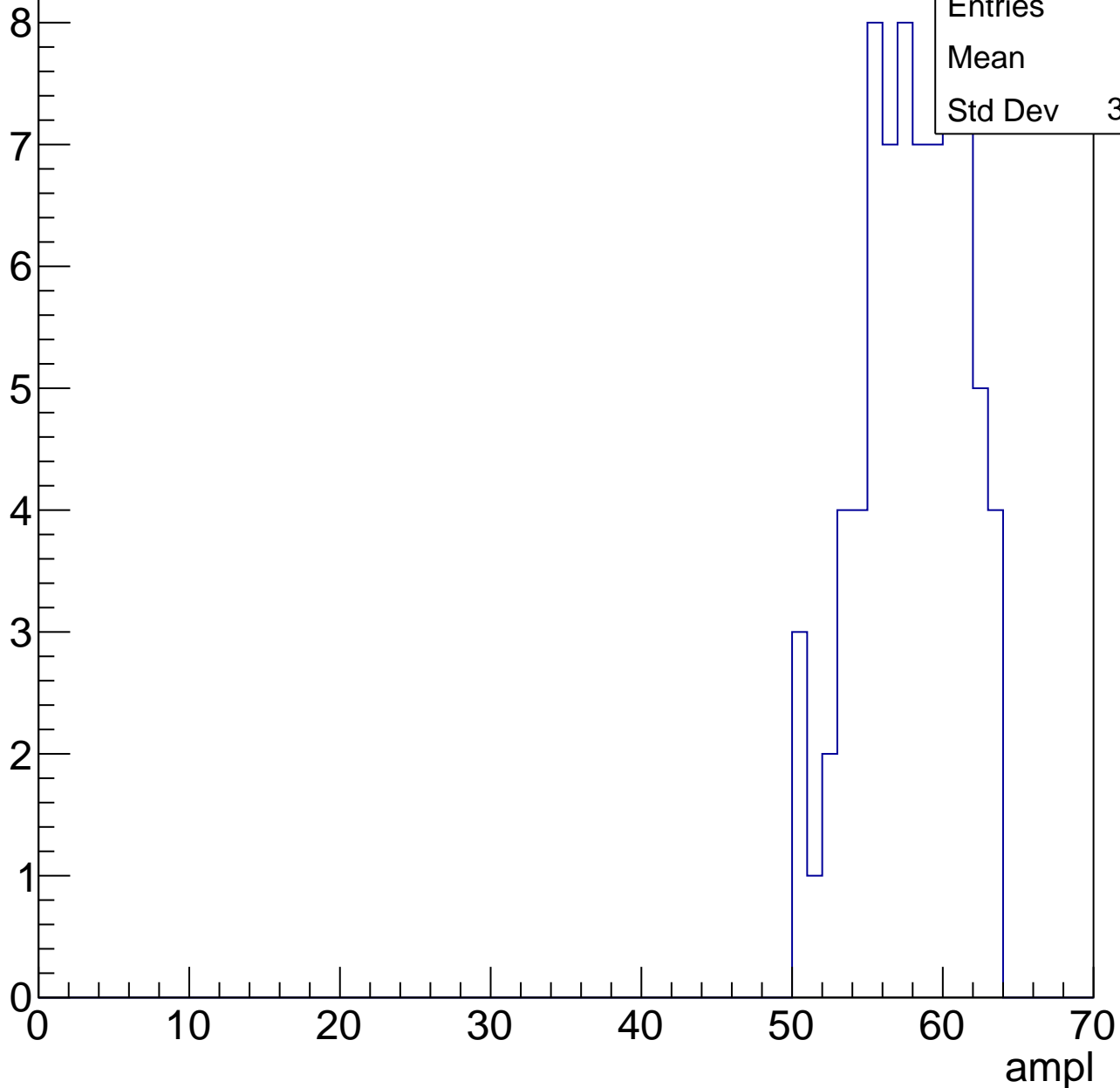
Entries	72
Mean	50.6
Std Dev	3.459



B1L100S, U12-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



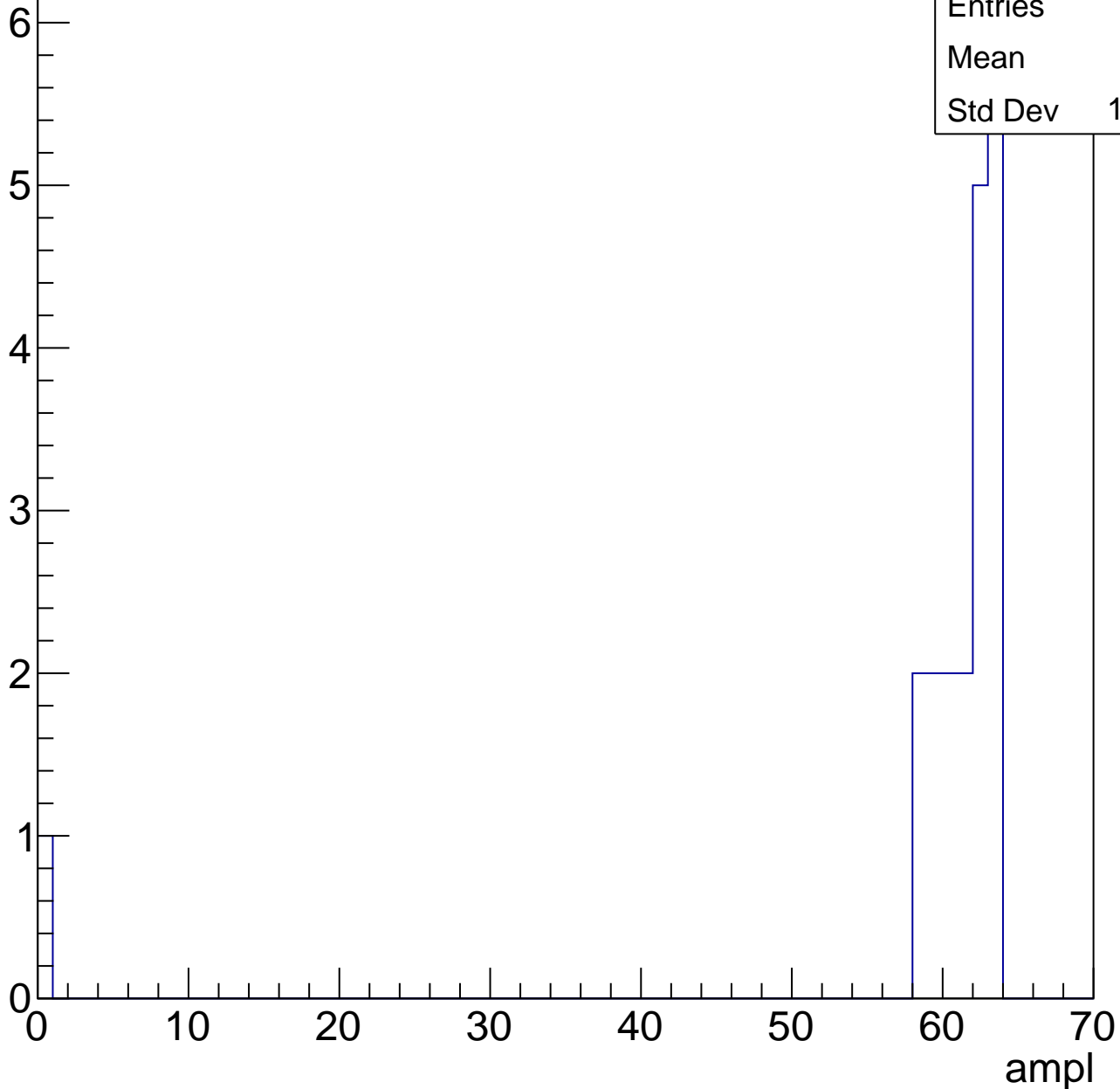
Entries	76
Mean	57.5
Std Dev	3.358

B1L100S, U12-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	58.2
Std Dev	13.46



B1L100S, U12-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch30, adc0

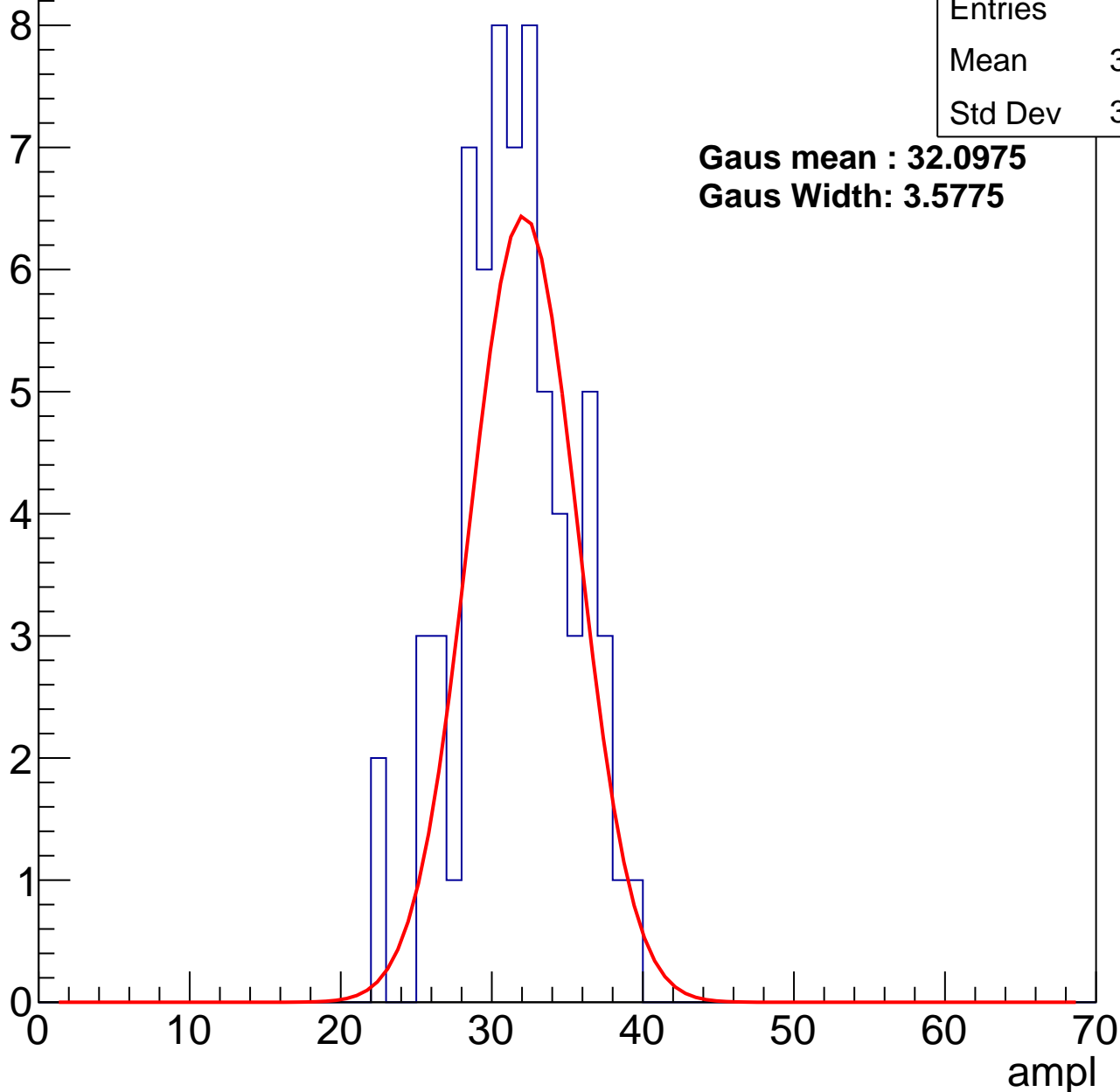
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	31.06
Std Dev	3.693

Gaus mean : 32.0975

Gaus Width: 3.5775



B1L100S, U12-ch30, adc1

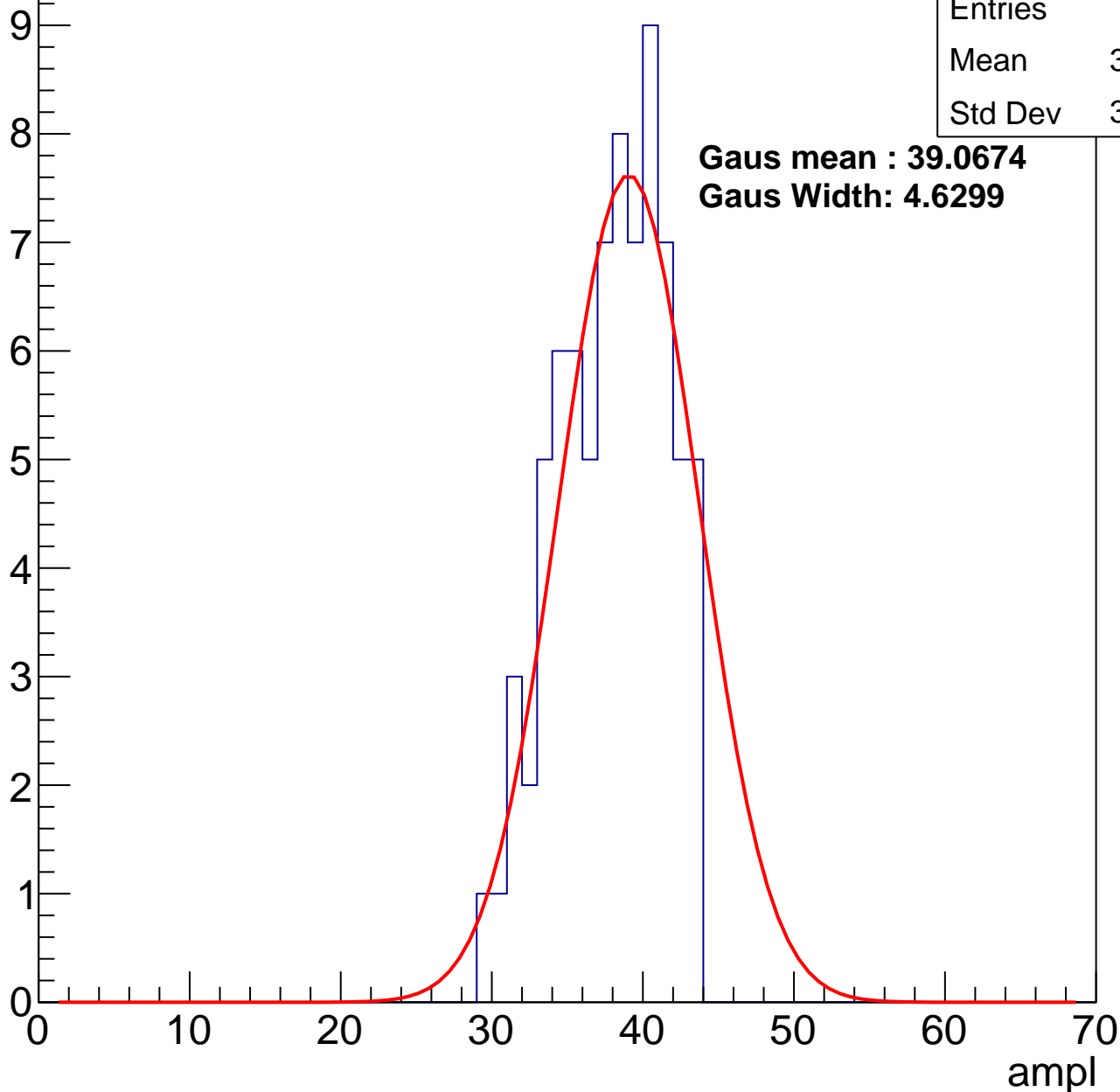
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	37.44
Std Dev	3.518

Gaus mean : 39.0674

Gaus Width: 4.6299



B1L100S, U12-ch30, adc2

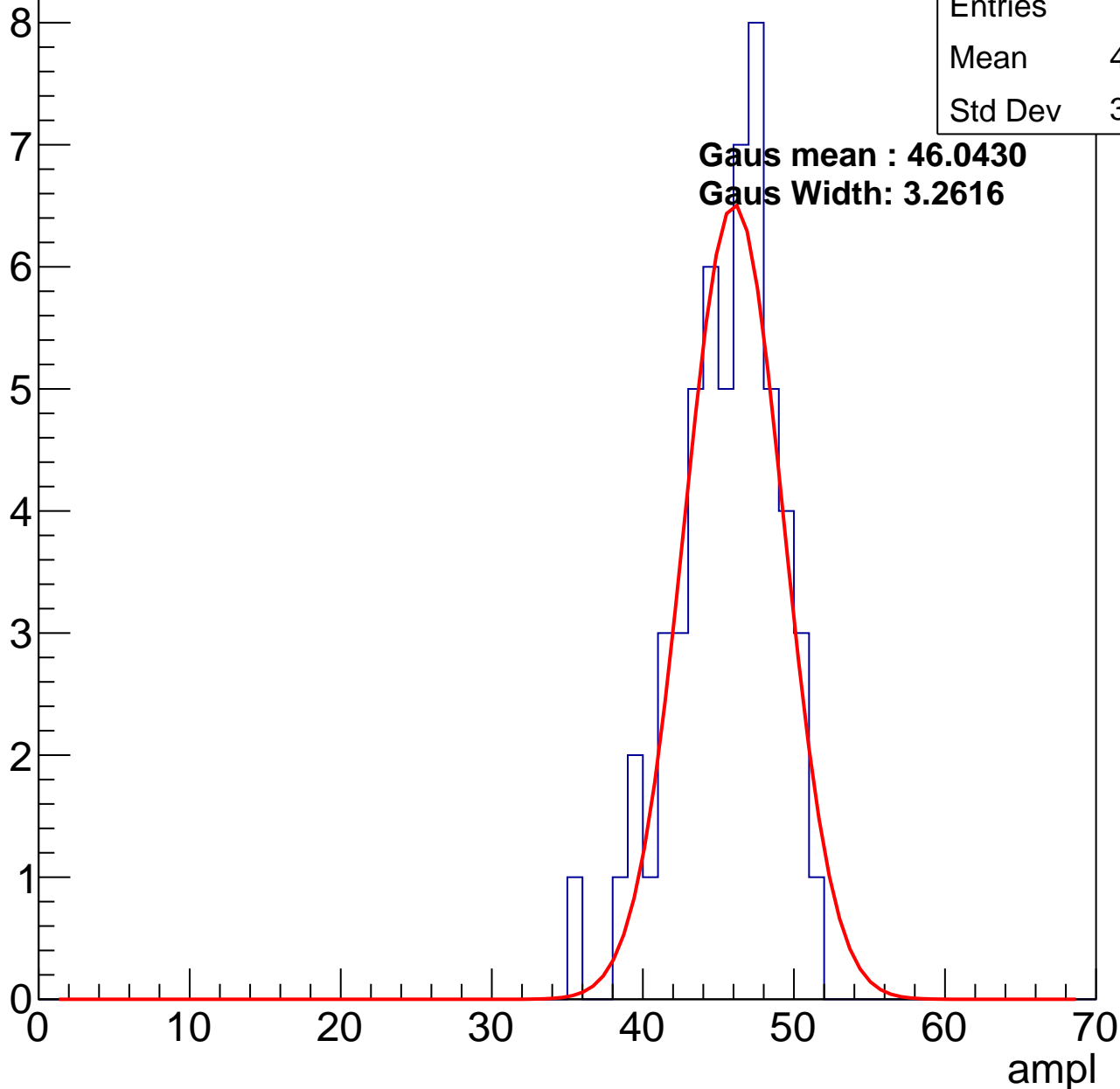
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	45.07
Std Dev	3.319

Gaus mean : 46.0430

Gaus Width: 3.2616

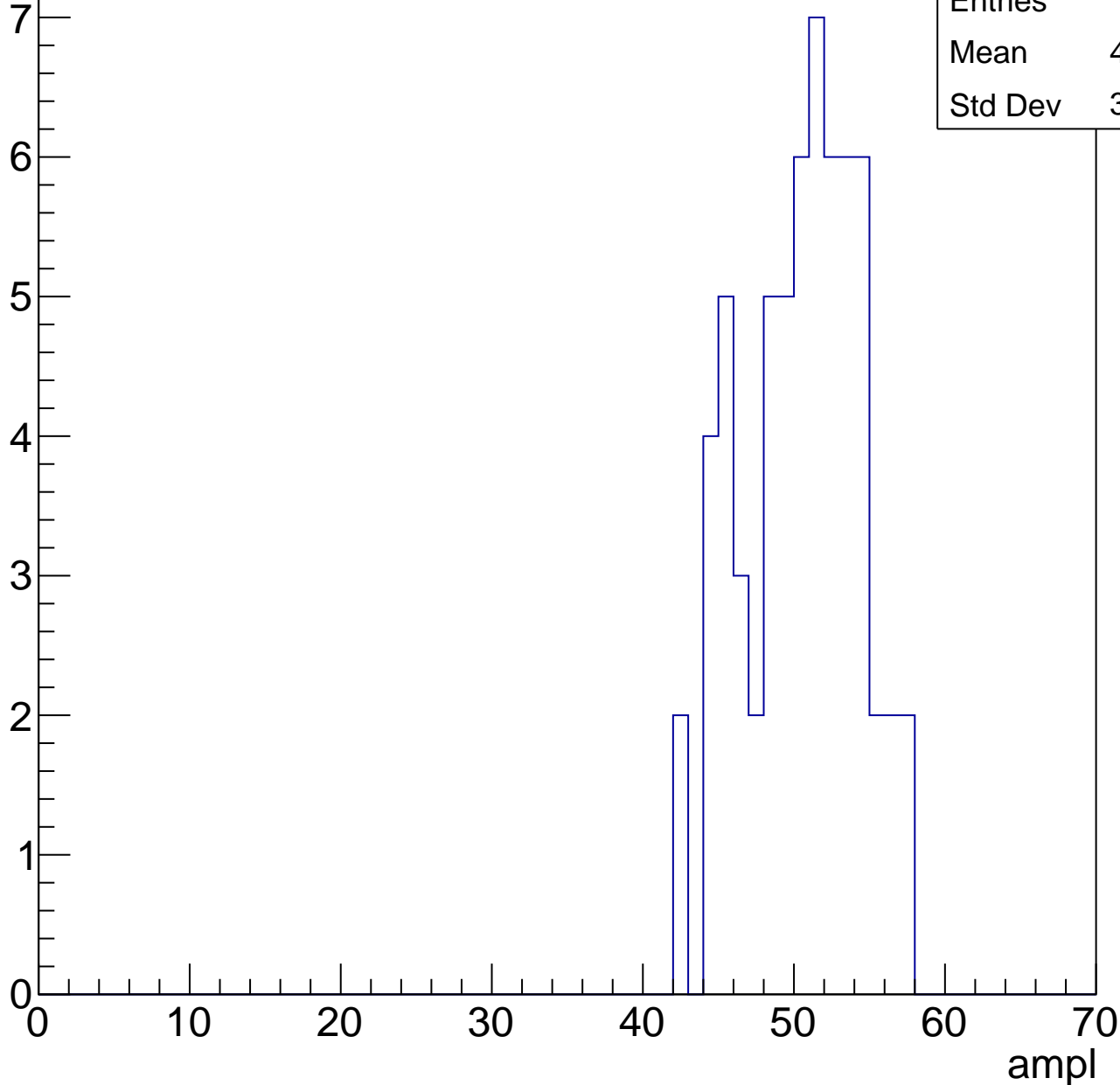


B1L100S, U12-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

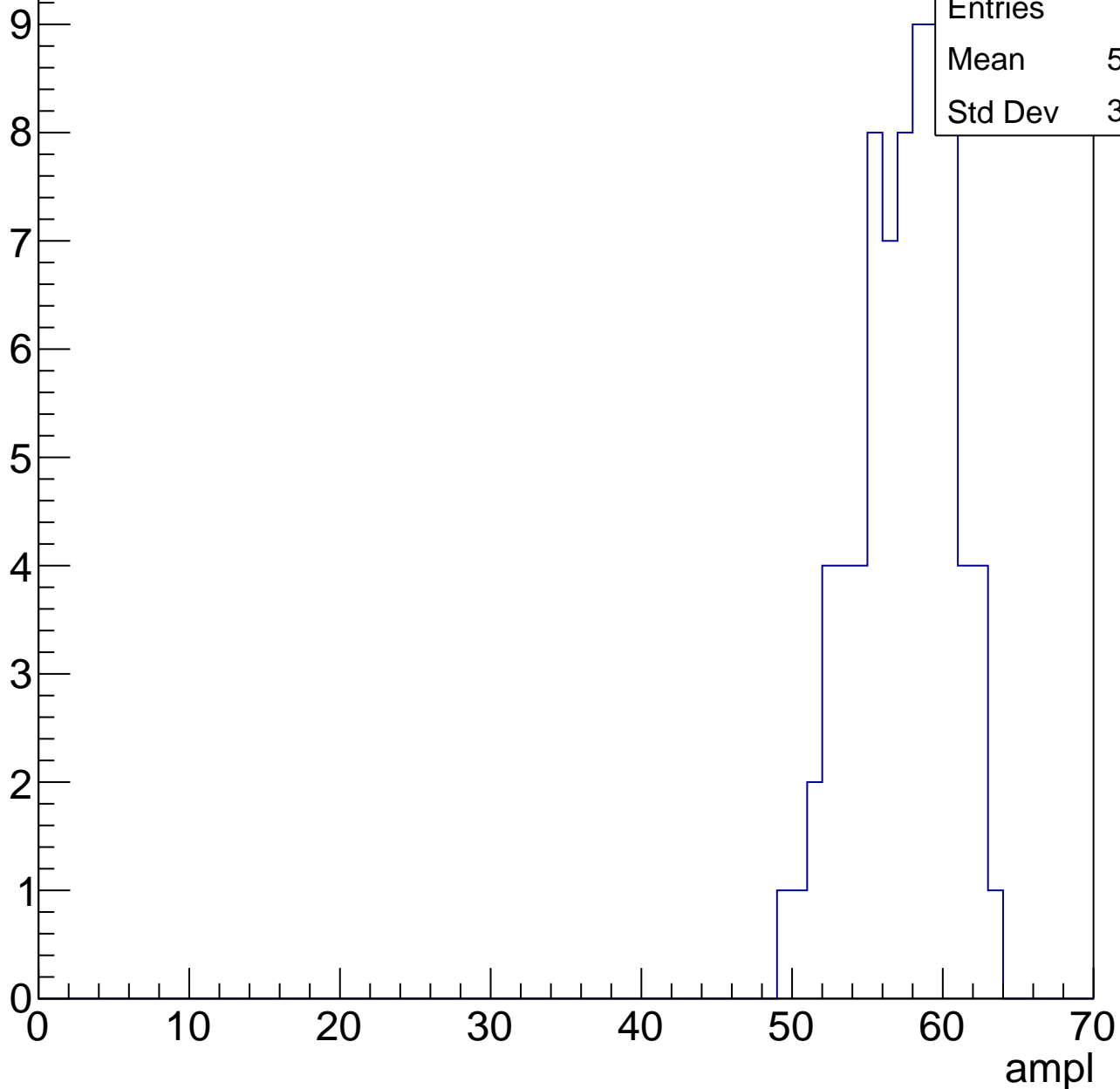
Entries	63
Mean	49.98
Std Dev	3.756



B1L100S, U12-ch30, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



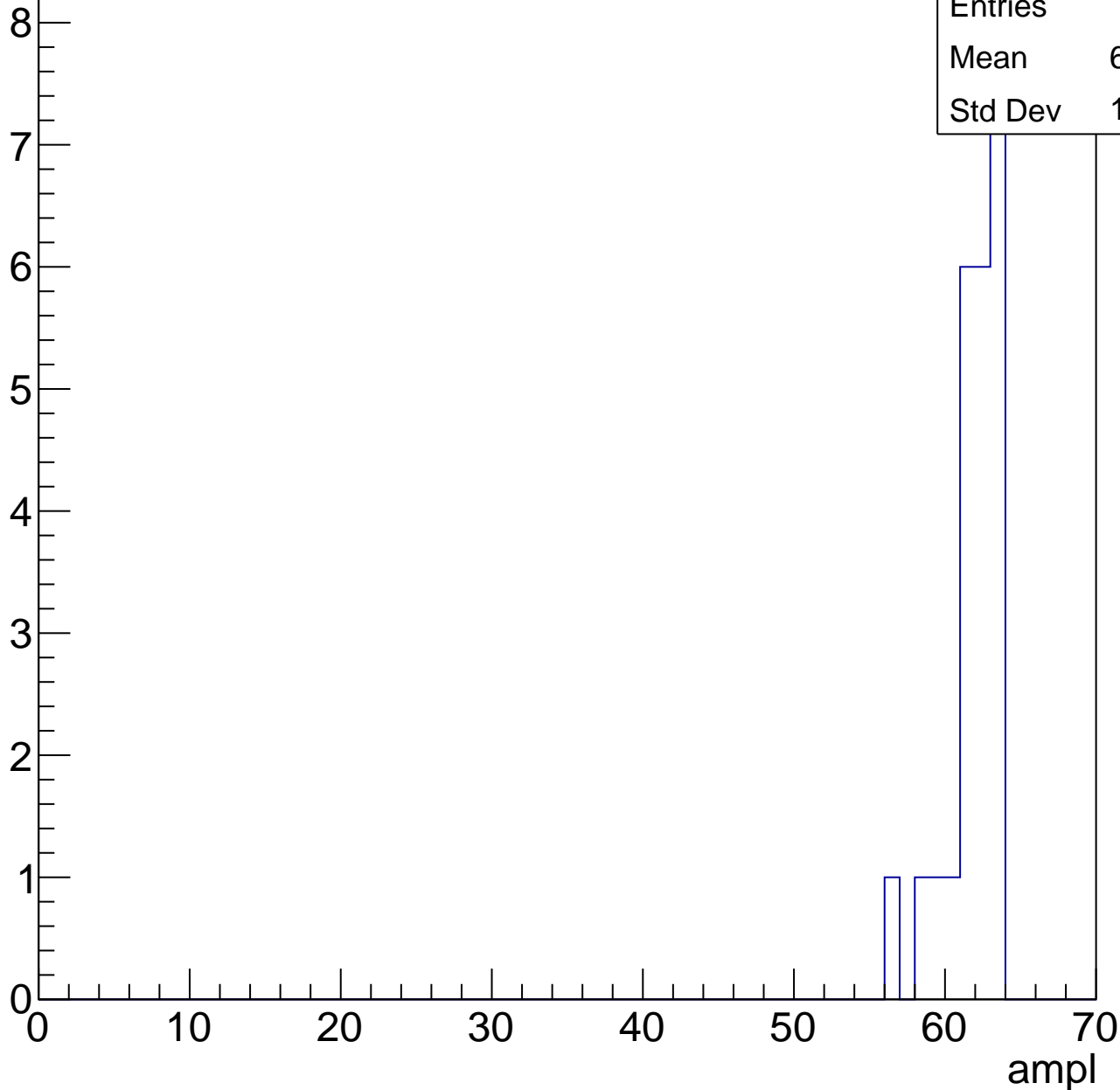
Entries	75
Mean	56.97
Std Dev	3.162

B1L100S, U12-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

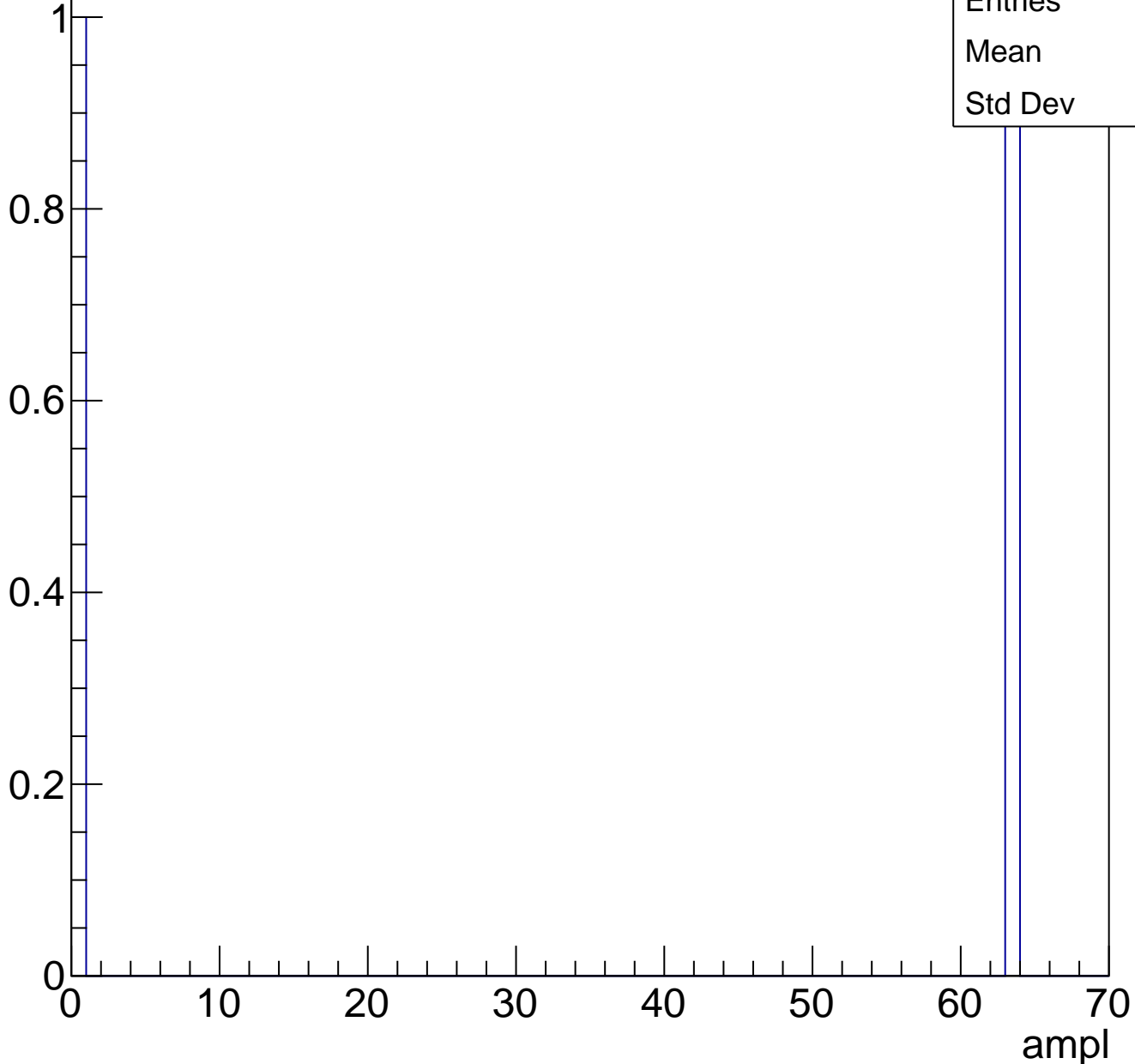
Entries	24
Mean	61.46
Std Dev	1.732



B1L100S, U12-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch31, adc0

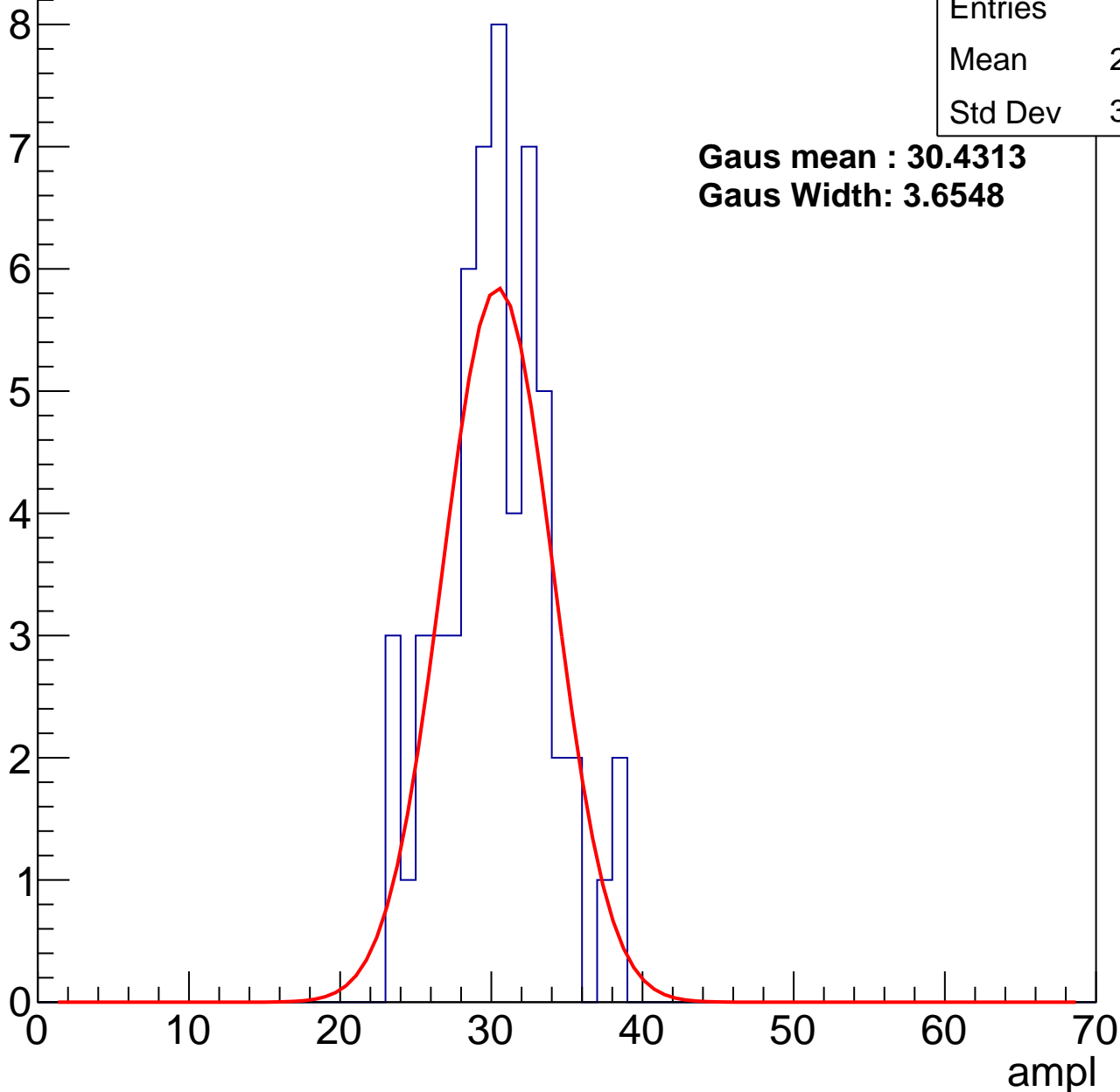
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	29.86
Std Dev	3.502

Gaus mean : 30.4313

Gaus Width: 3.6548



B1L100S, U12-ch31, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	36.84
Std Dev	3.322

Gaus mean : 37.4538

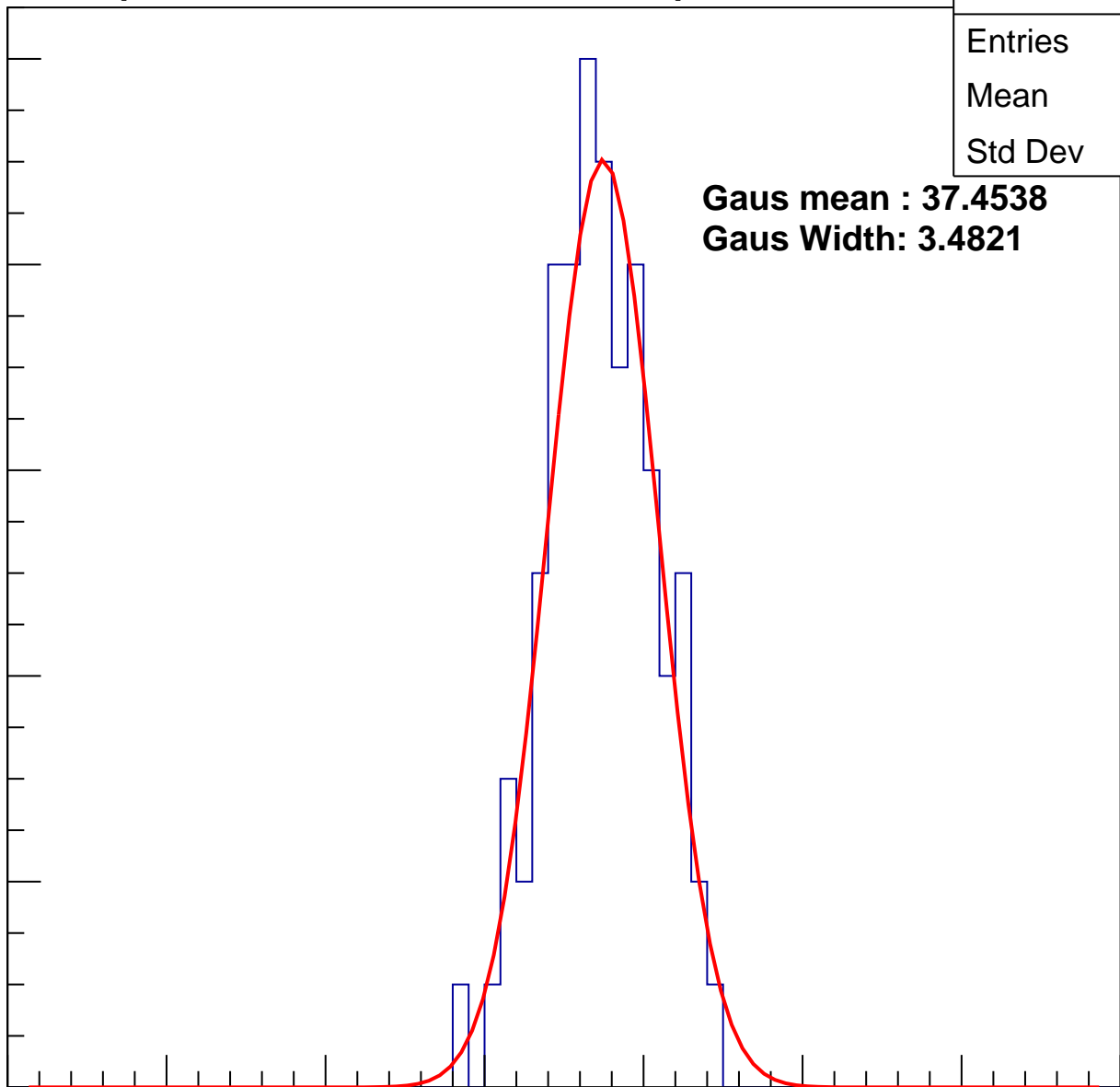
Gaus Width: 3.4821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch31, adc2

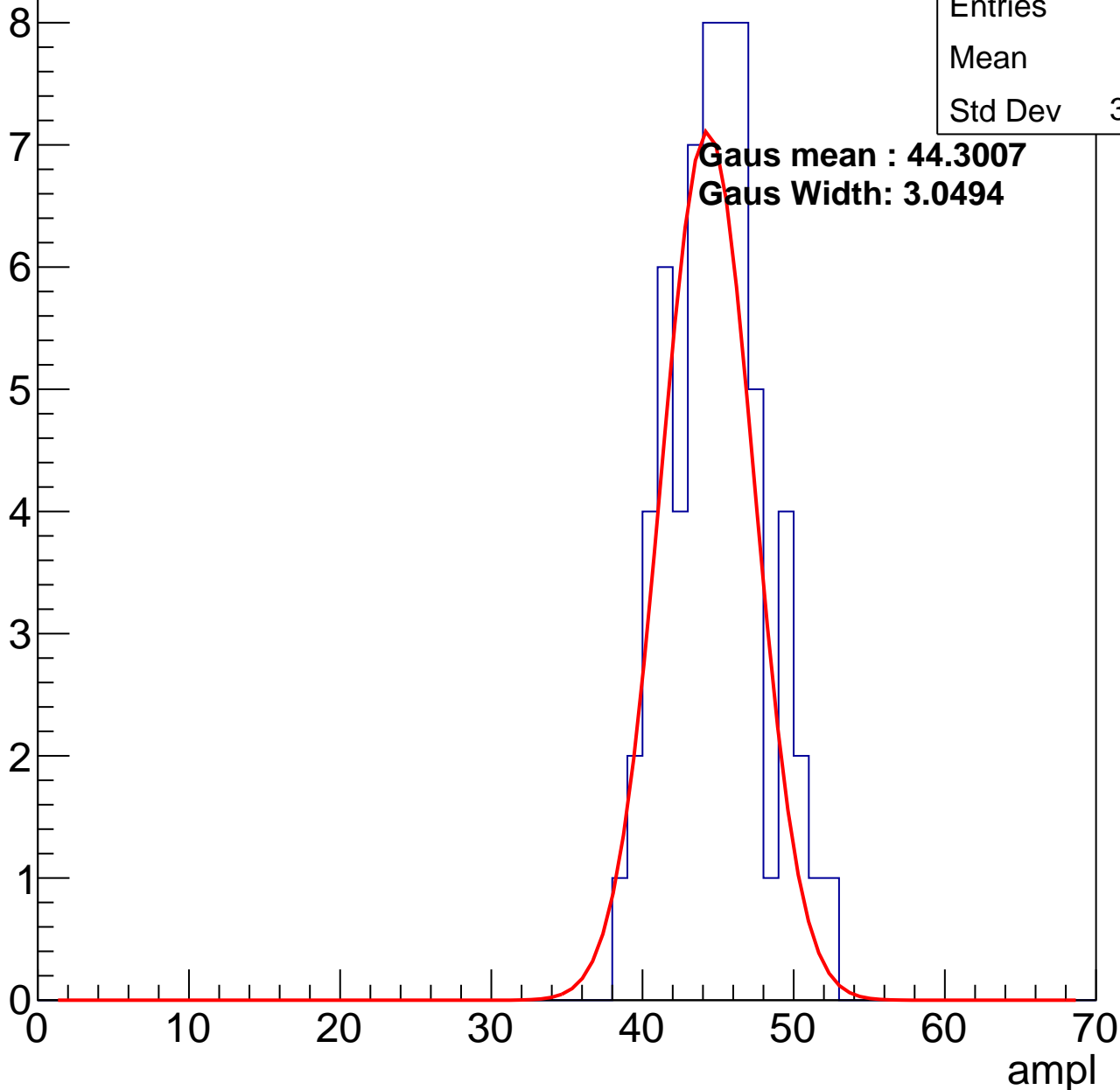
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.4
Std Dev	3.118

Gaus mean : 44.3007

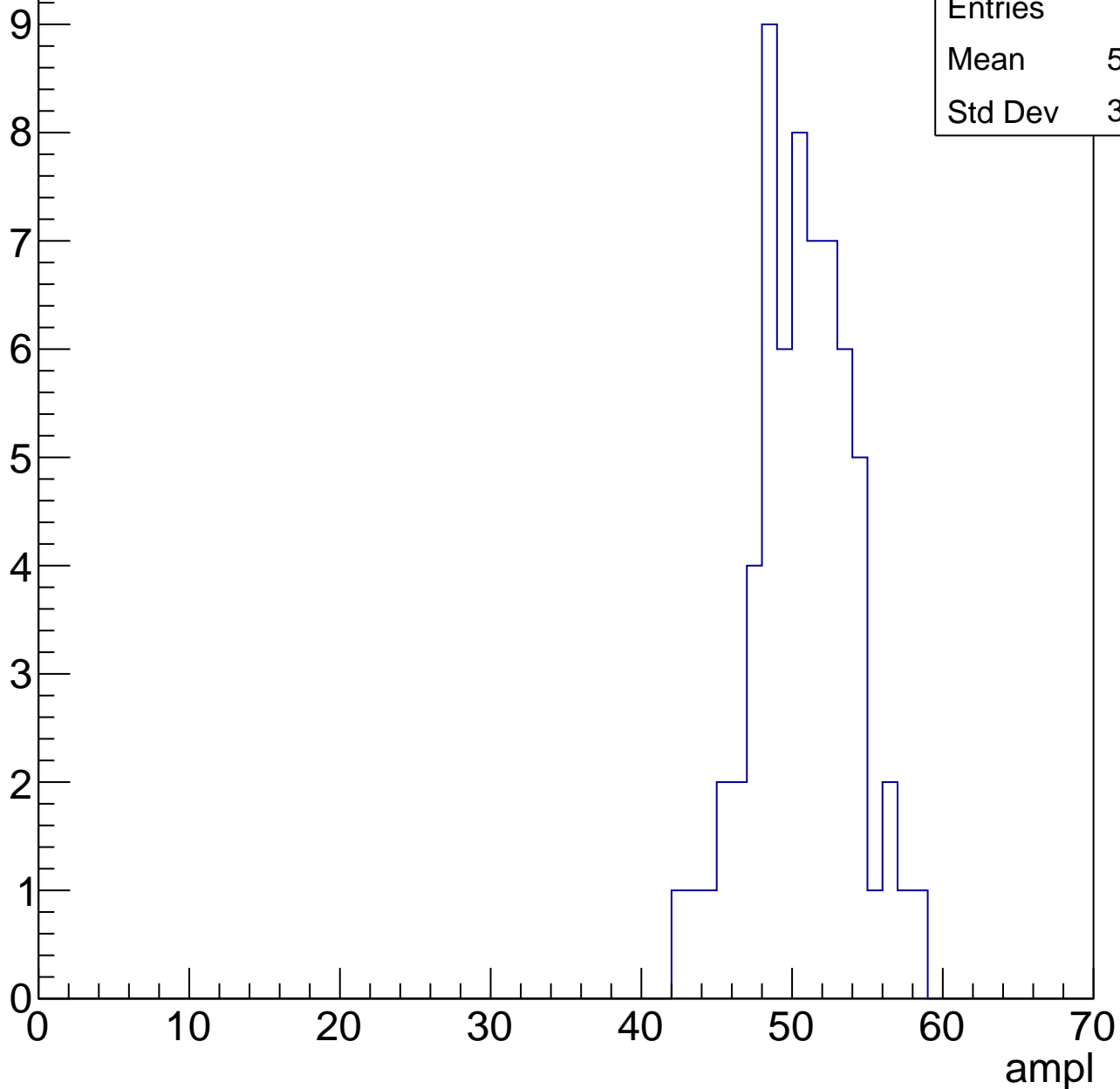
Gaus Width: 3.0494



B1L100S, U12-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

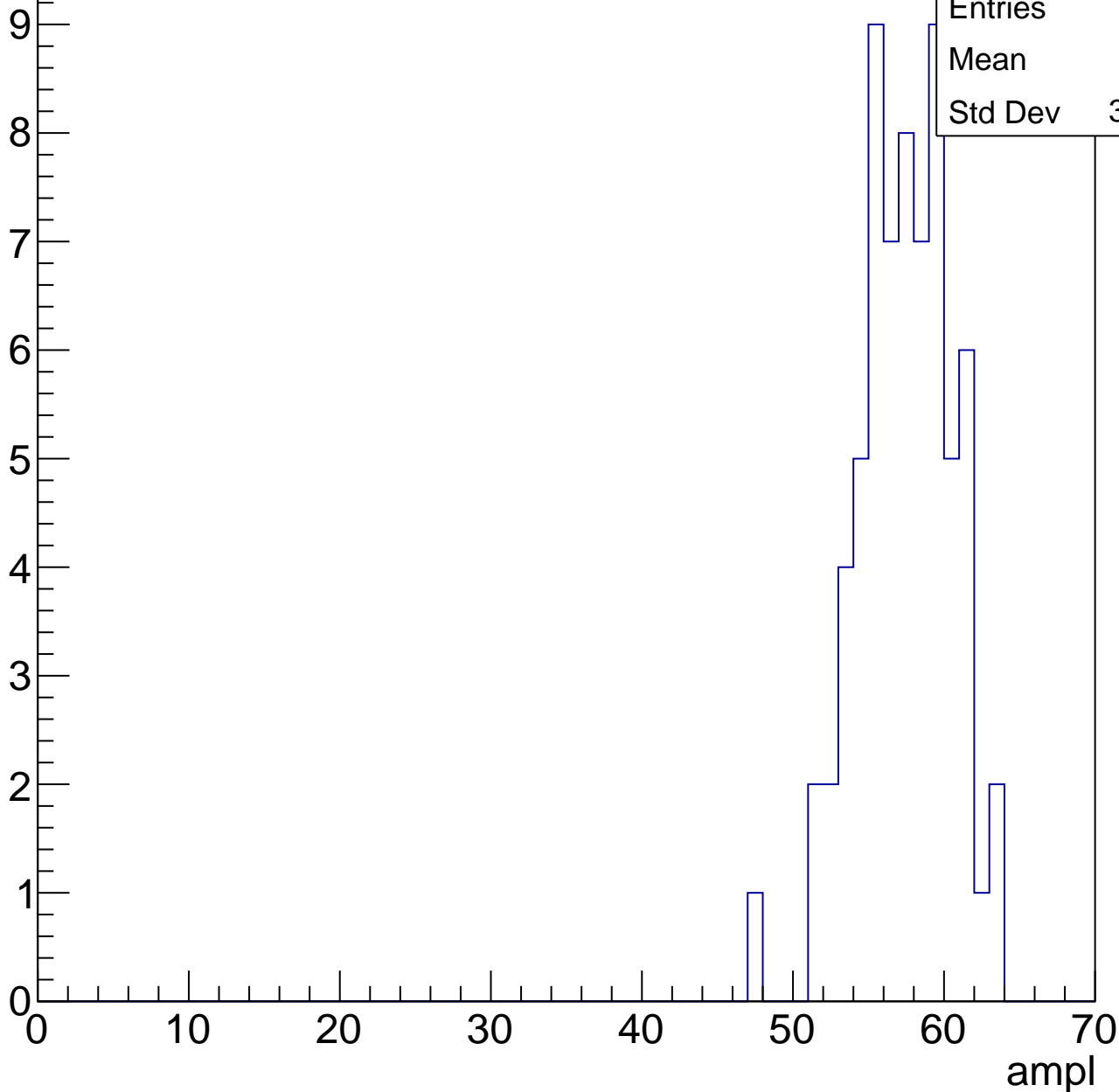


B1L100S, U12-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	56.9
Std Dev	3.102

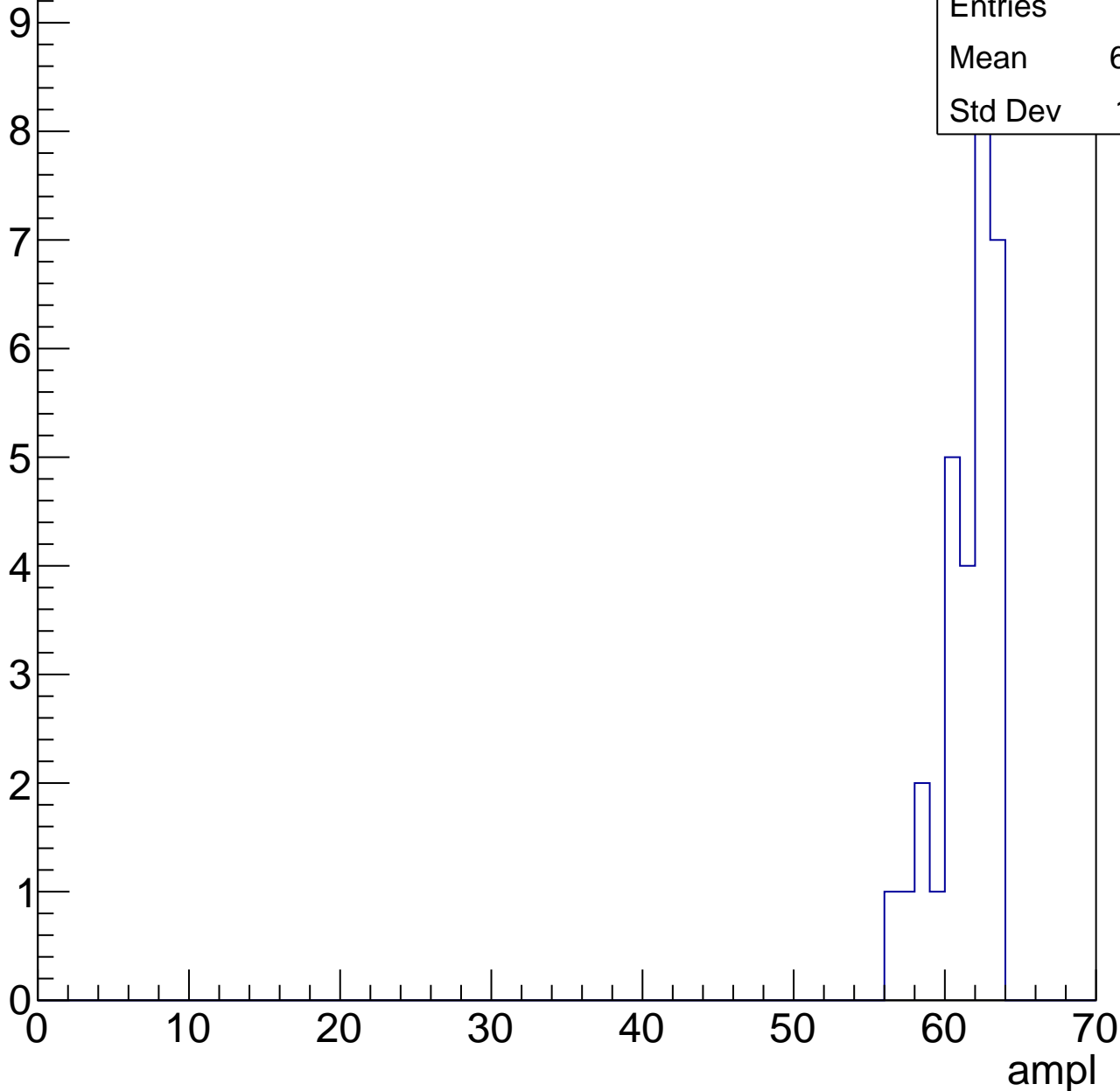


B1L100S, U12-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	61.03
Std Dev	1.871



B1L100S, U12-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch32, adc0

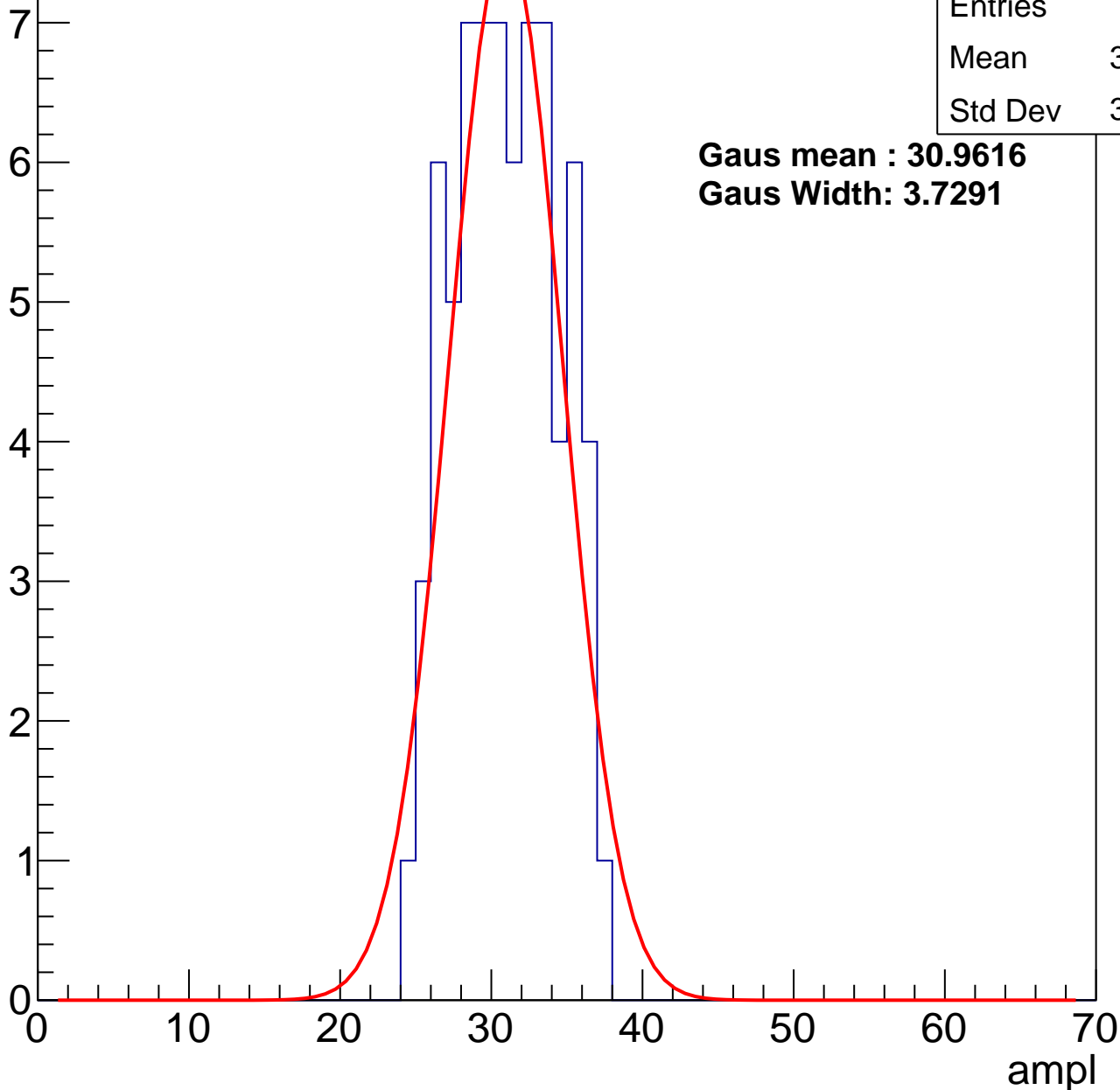
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.52
Std Dev	3.297

Gaus mean : 30.9616

Gaus Width: 3.7291



B1L100S, U12-ch32, adc1

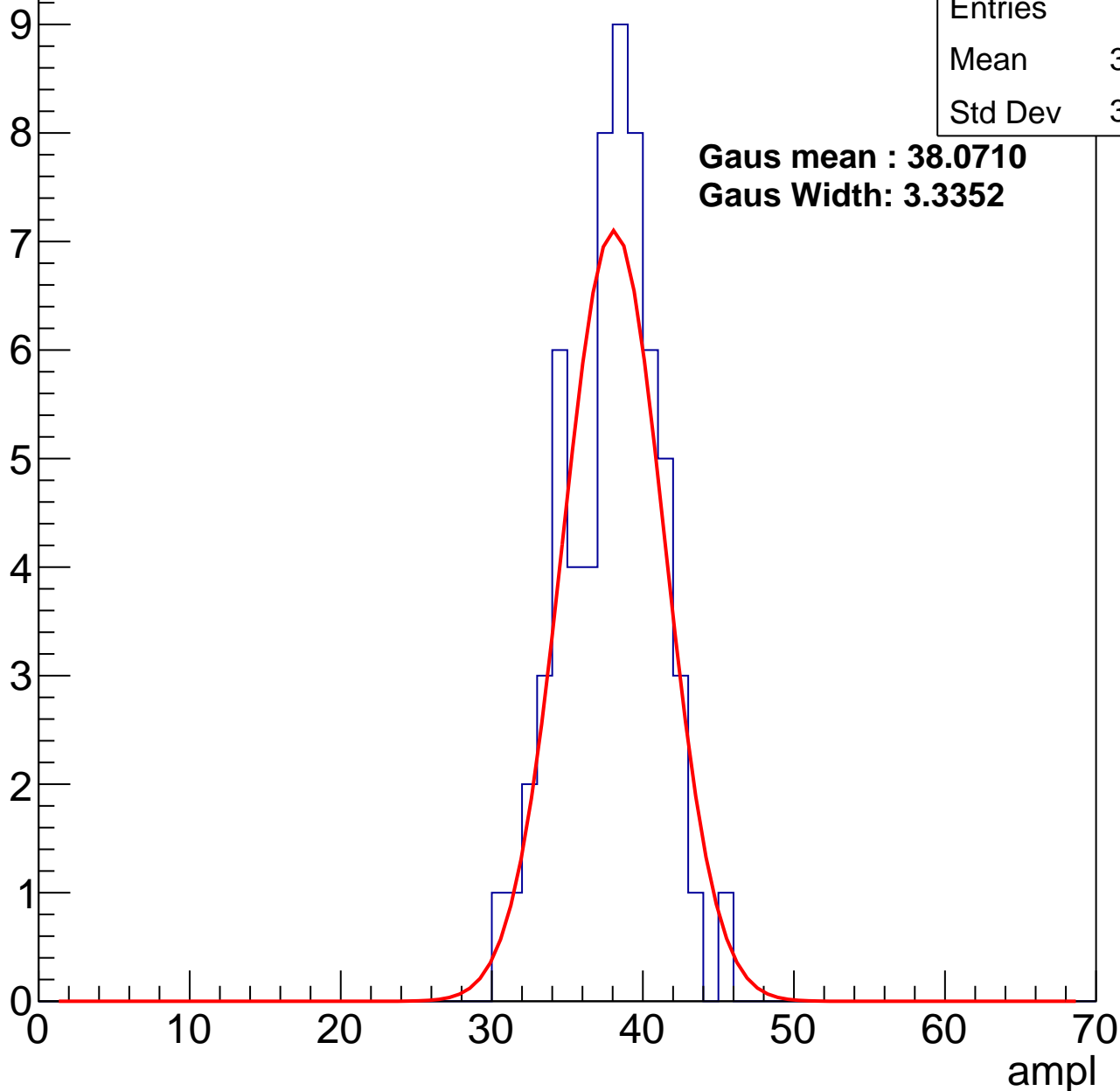
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	37.44
Std Dev	3.098

Gaus mean : 38.0710

Gaus Width: 3.3352



B1L100S, U12-ch32, adc2

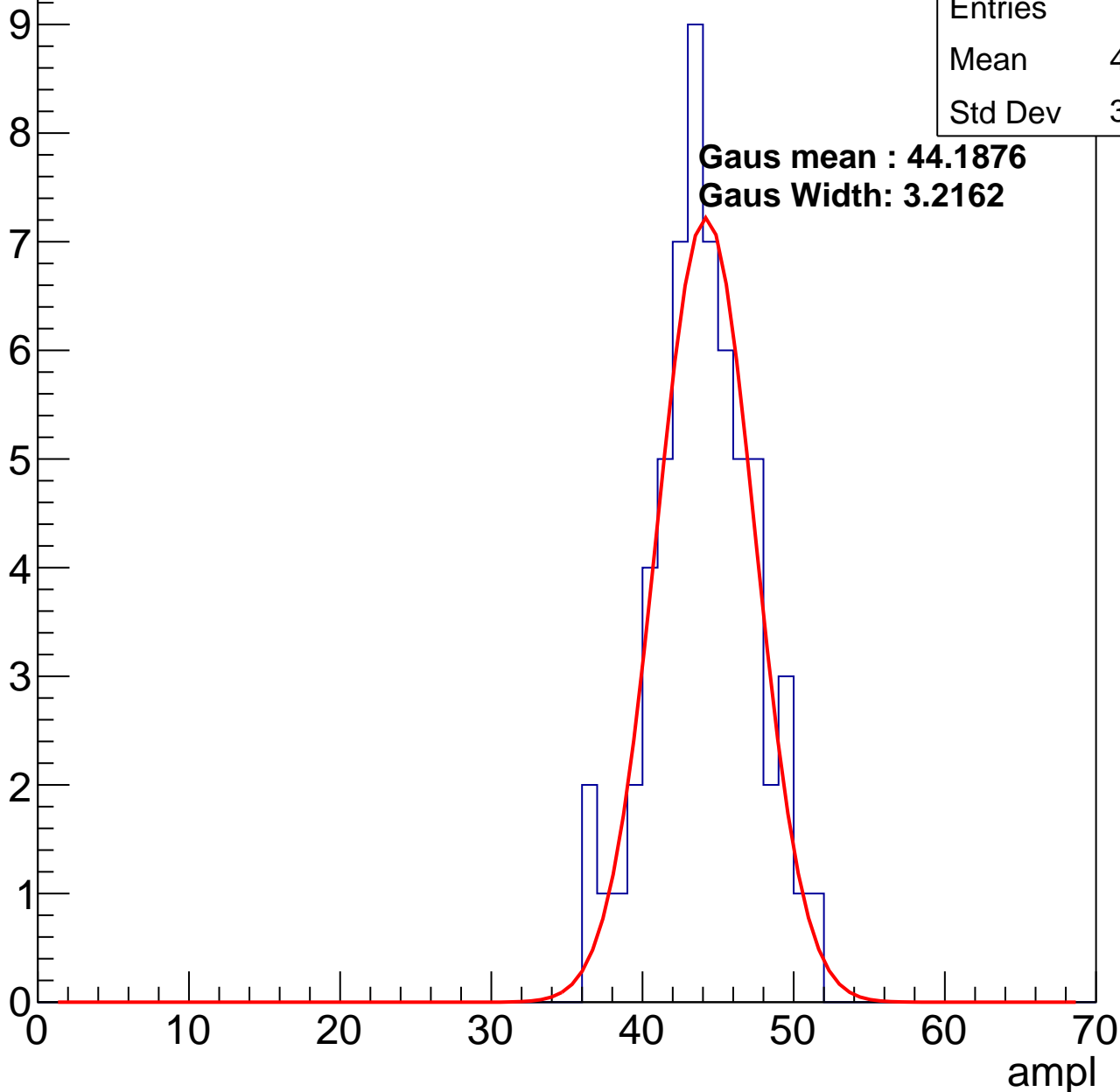
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	43.57
Std Dev	3.292

Gaus mean : 44.1876

Gaus Width: 3.2162

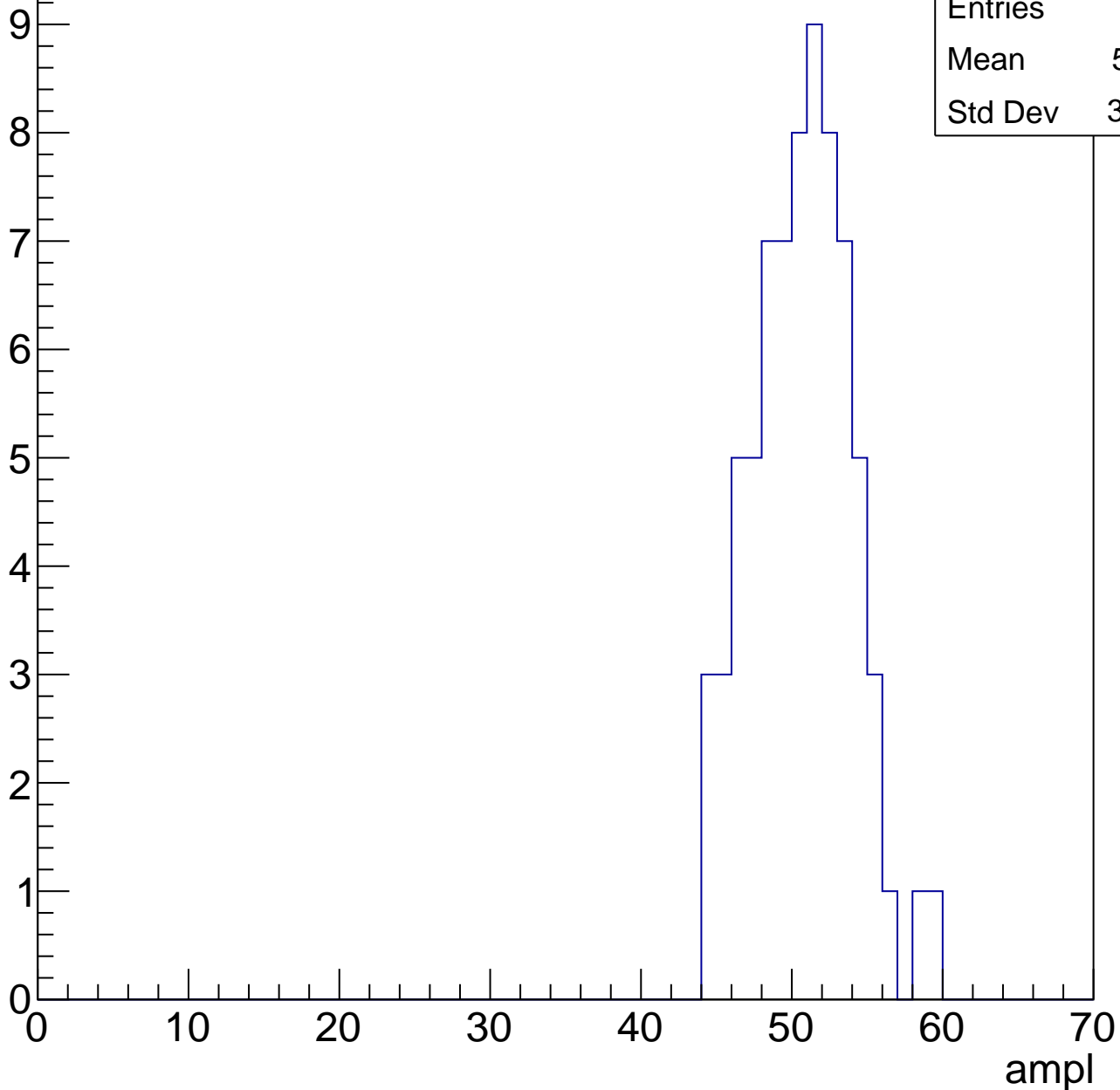


B1L100S, U12-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

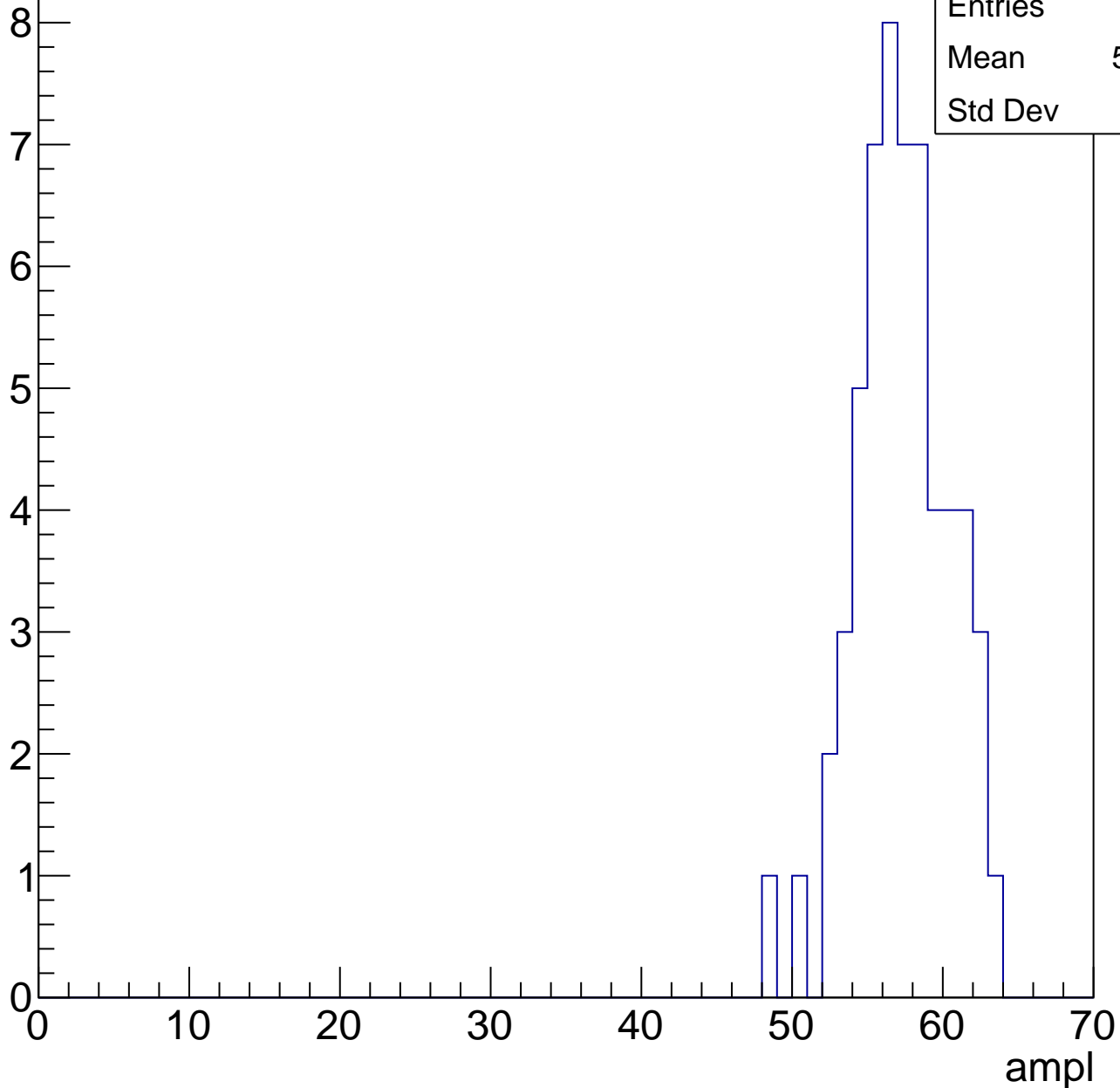
Entries	73
Mean	50.21
Std Dev	3.269



B1L100S, U12-ch32, adc4

calib_packv5_042523_0143.root, FC#4, port A2

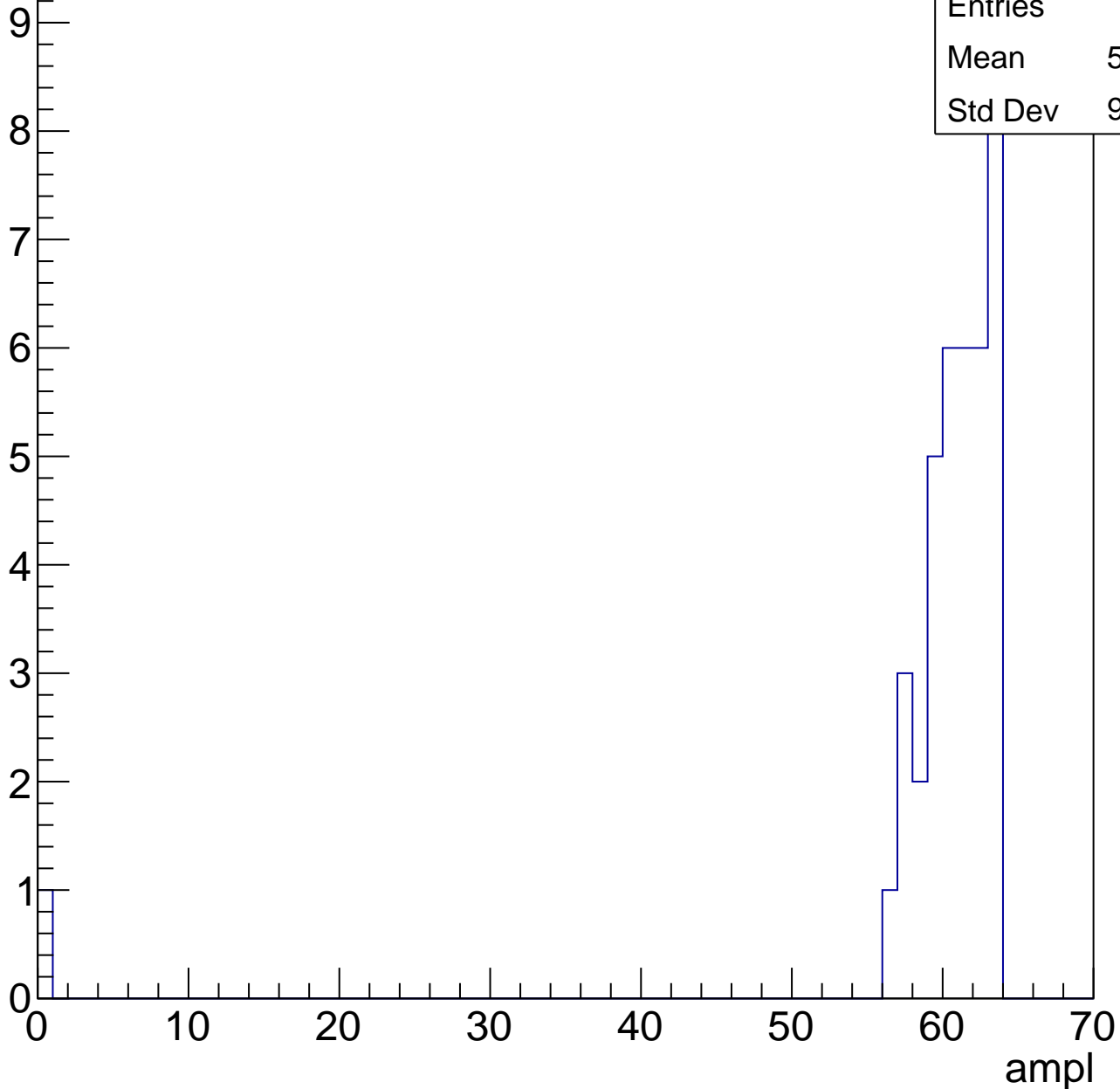
Entry



B1L100S, U12-ch32, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U12-ch33, adc0

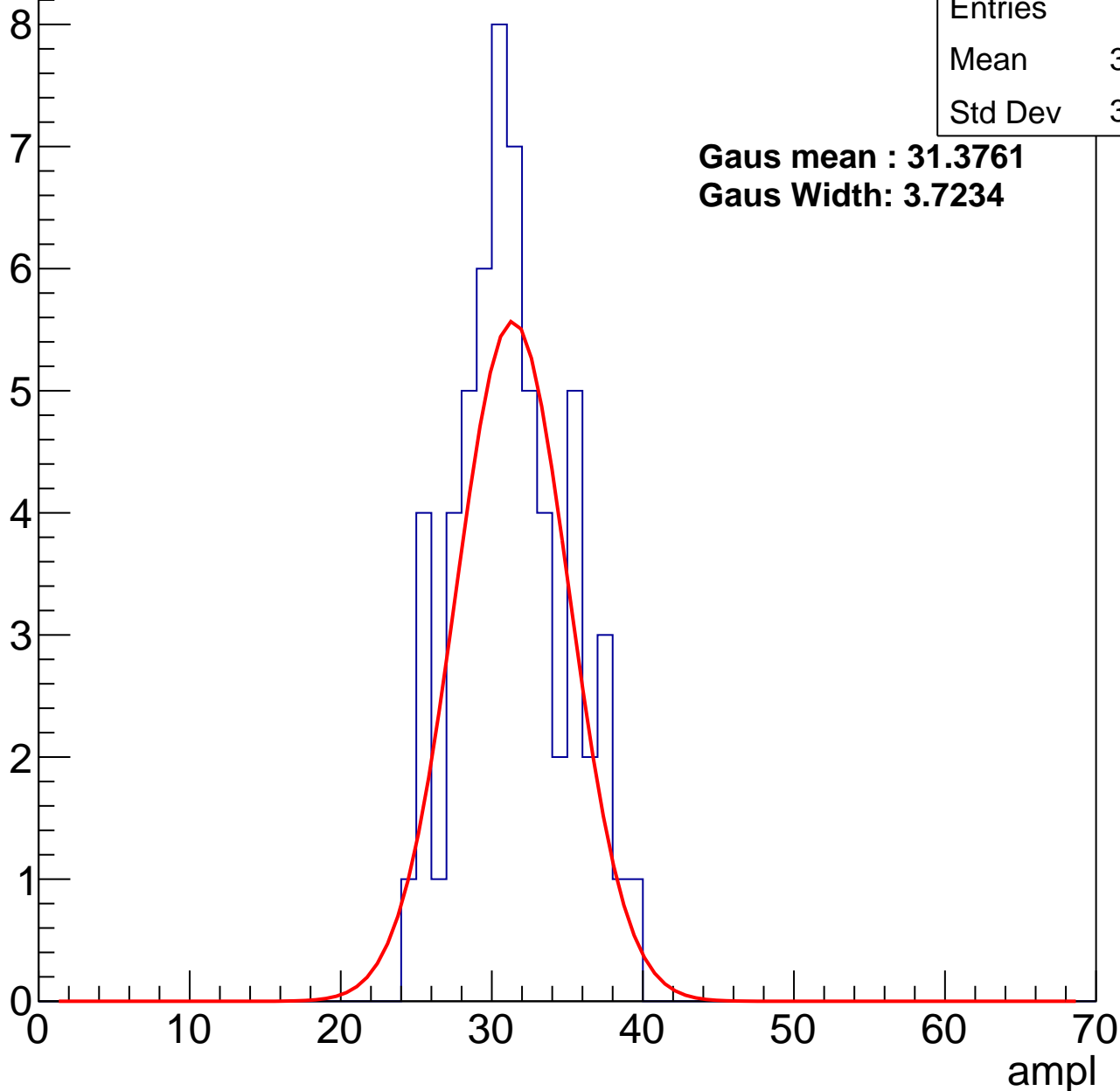
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	30.92
Std Dev	3.562

Gaus mean : 31.3761

Gaus Width: 3.7234



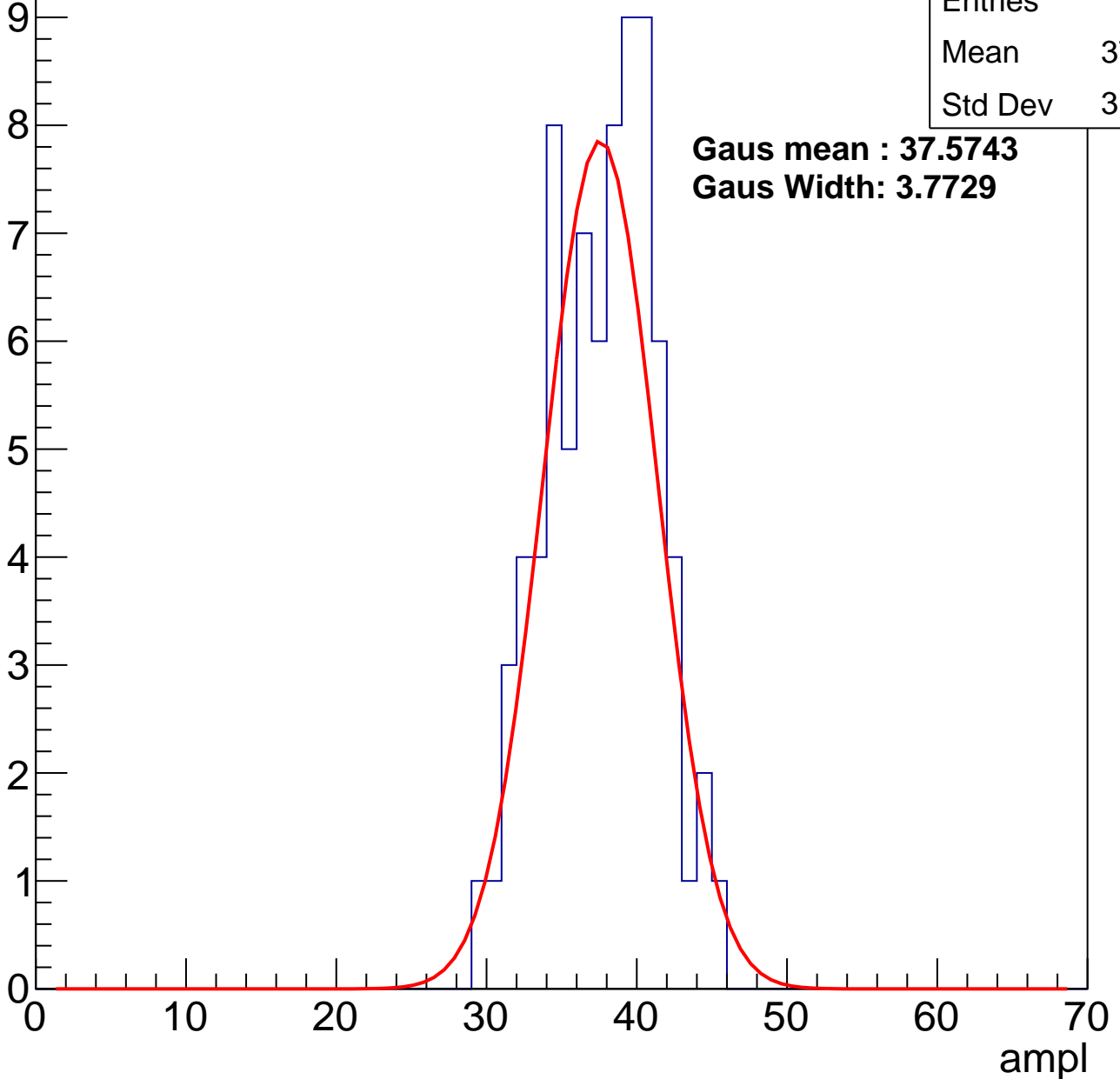
B1L100S, U12-ch33, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37.19
Std Dev	3.558

Gaus mean : 37.5743
Gaus Width: 3.7729



B1L100S, U12-ch33, adc2

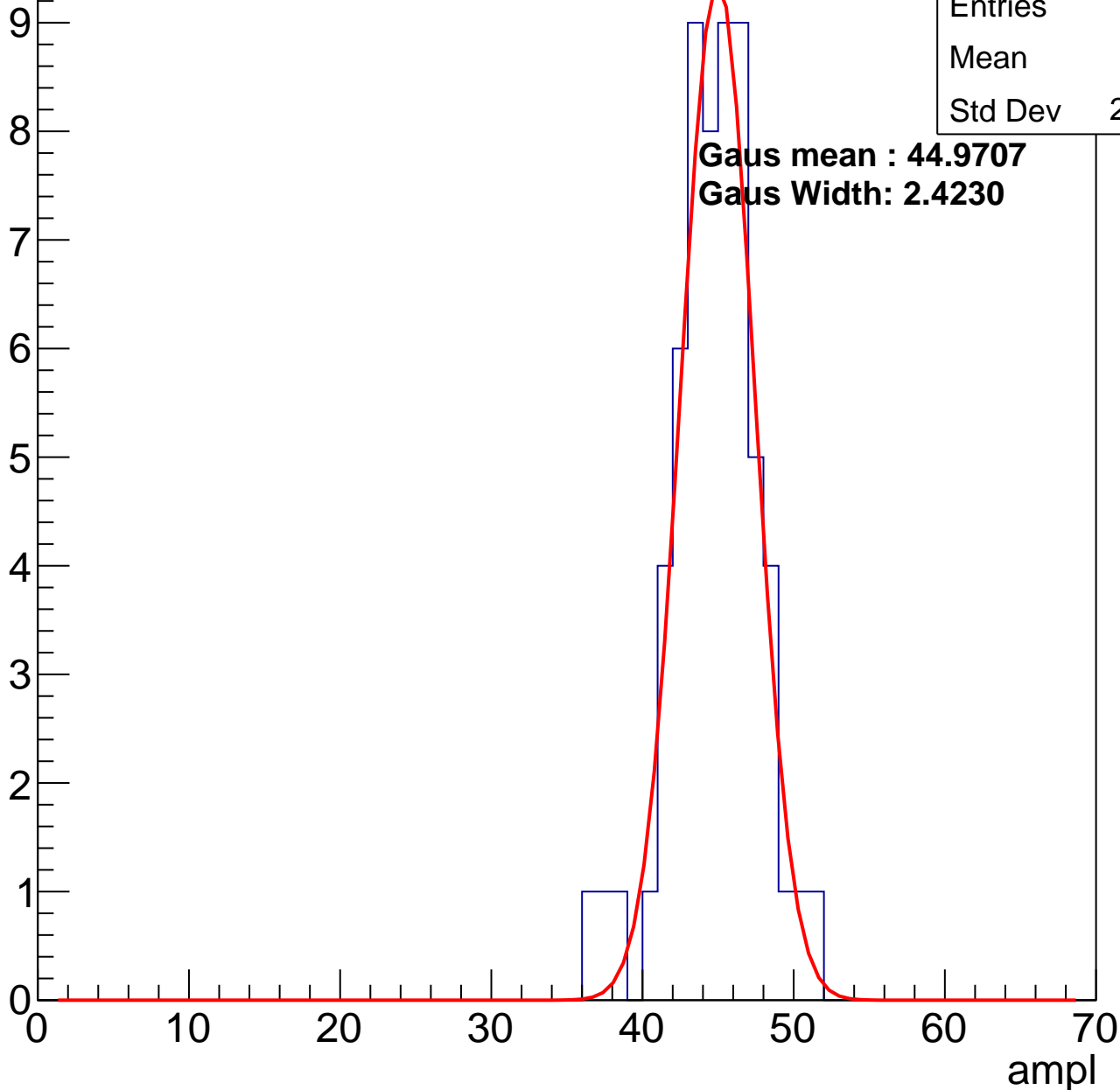
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.3
Std Dev	2.842

Gaus mean : 44.9707

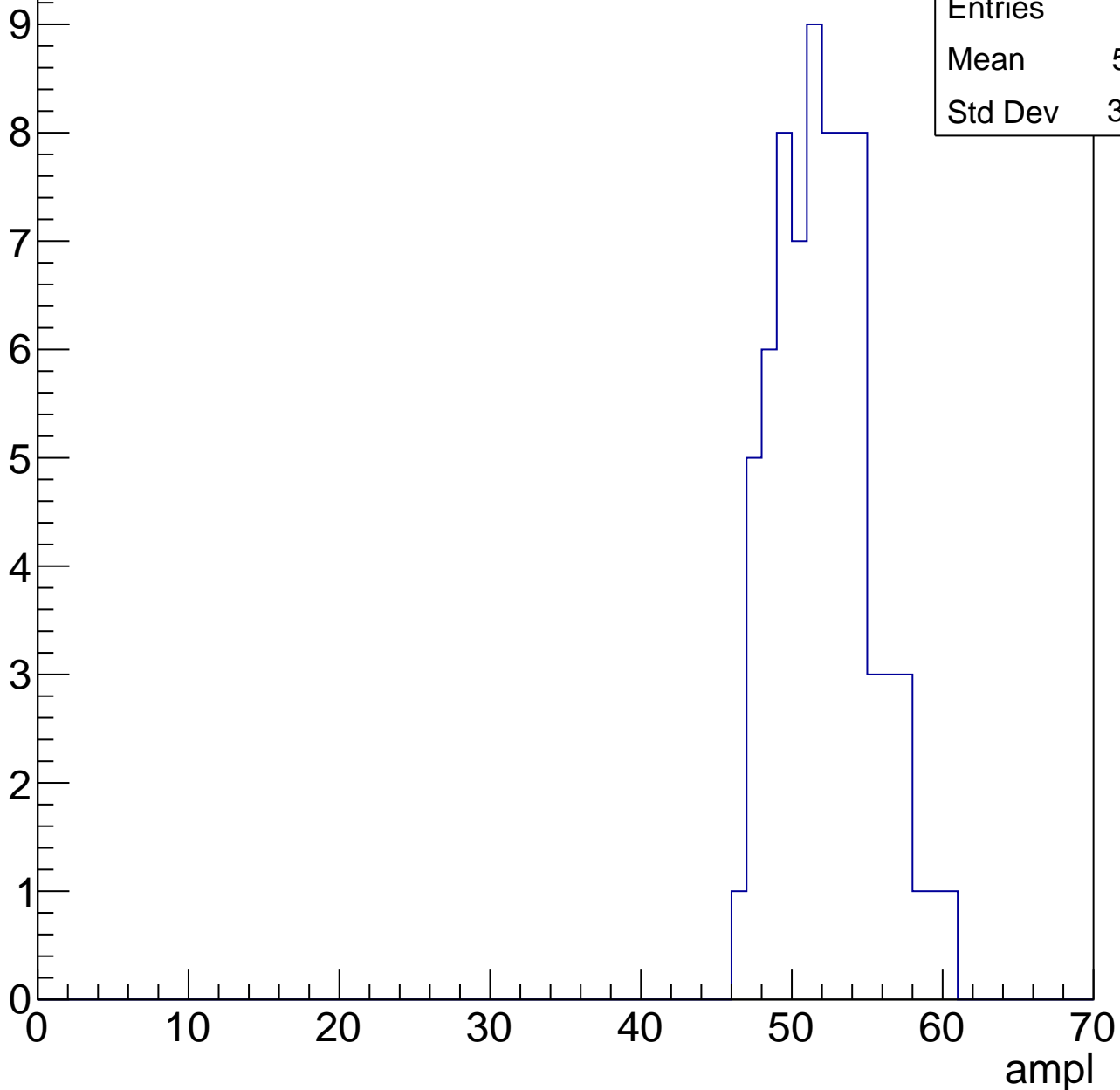
Gaus Width: 2.4230



B1L100S, U12-ch33, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	72
Mean	51.71
Std Dev	3.116

B1L100S, U12-ch33, adc4

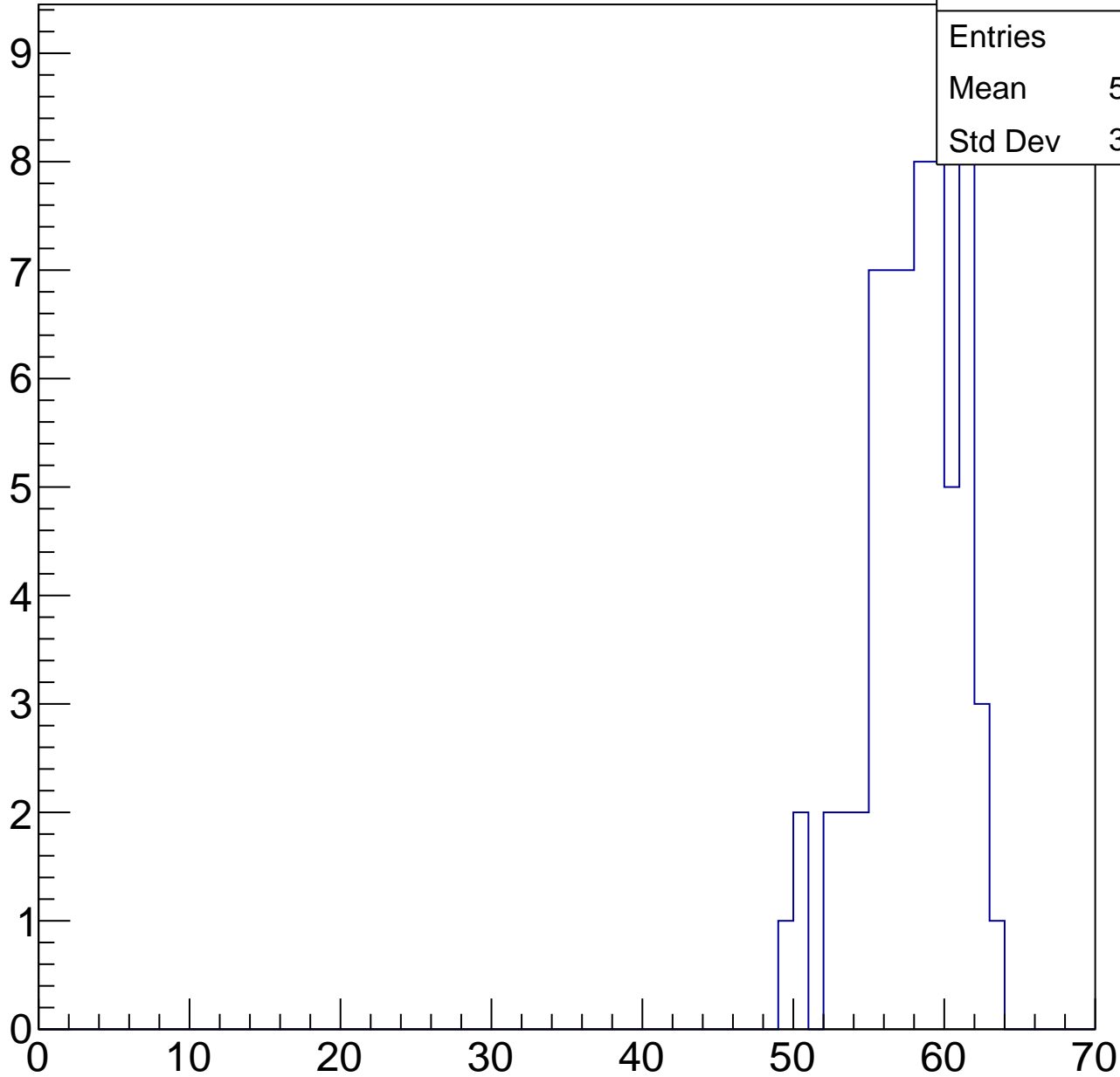
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	57.45
Std Dev	3.122

ampl

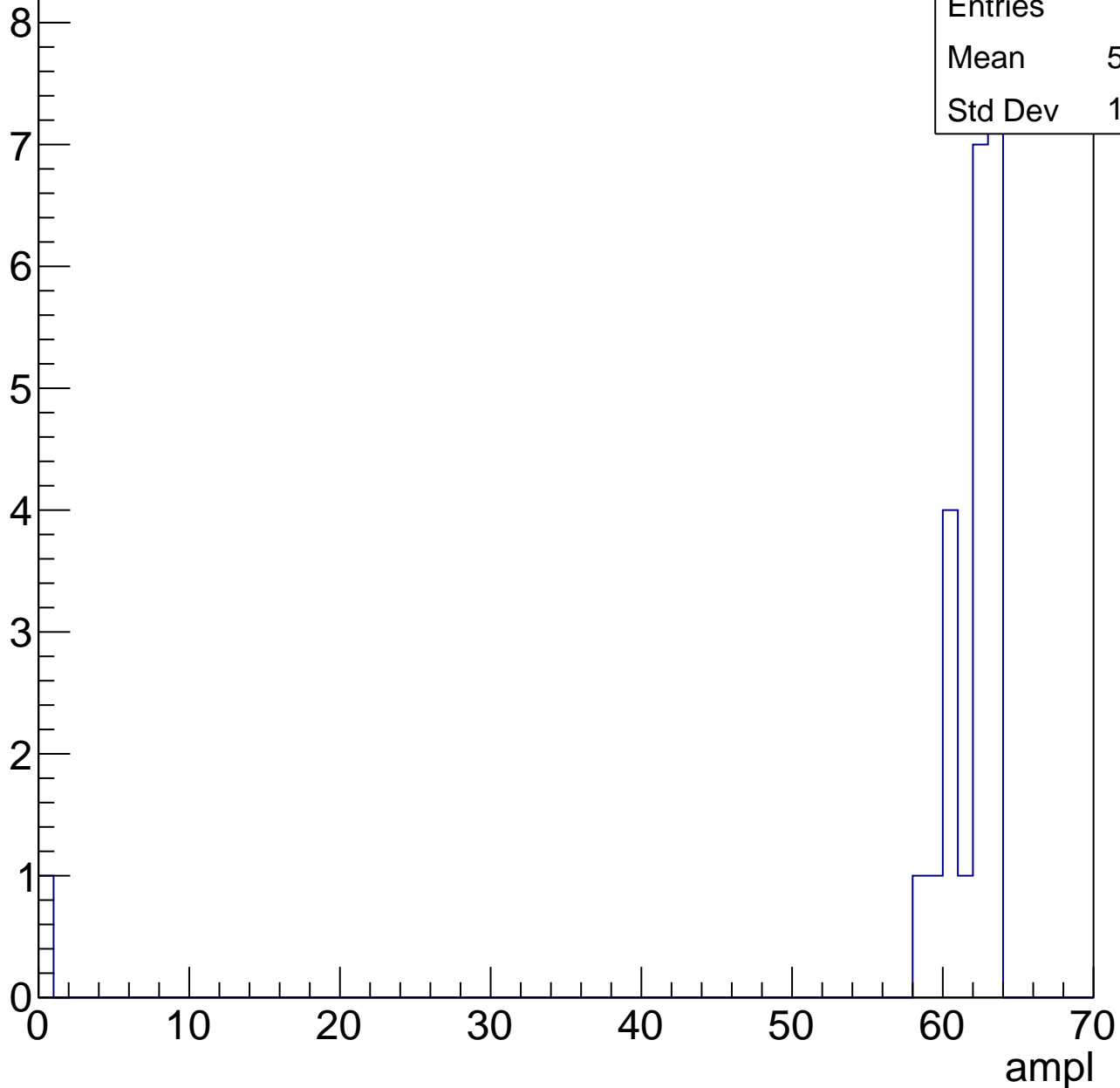


B1L100S, U12-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	58.96
Std Dev	12.65



B1L100S, U12-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch34, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	28.99
Std Dev	3.665

Gaus mean : 29.2974

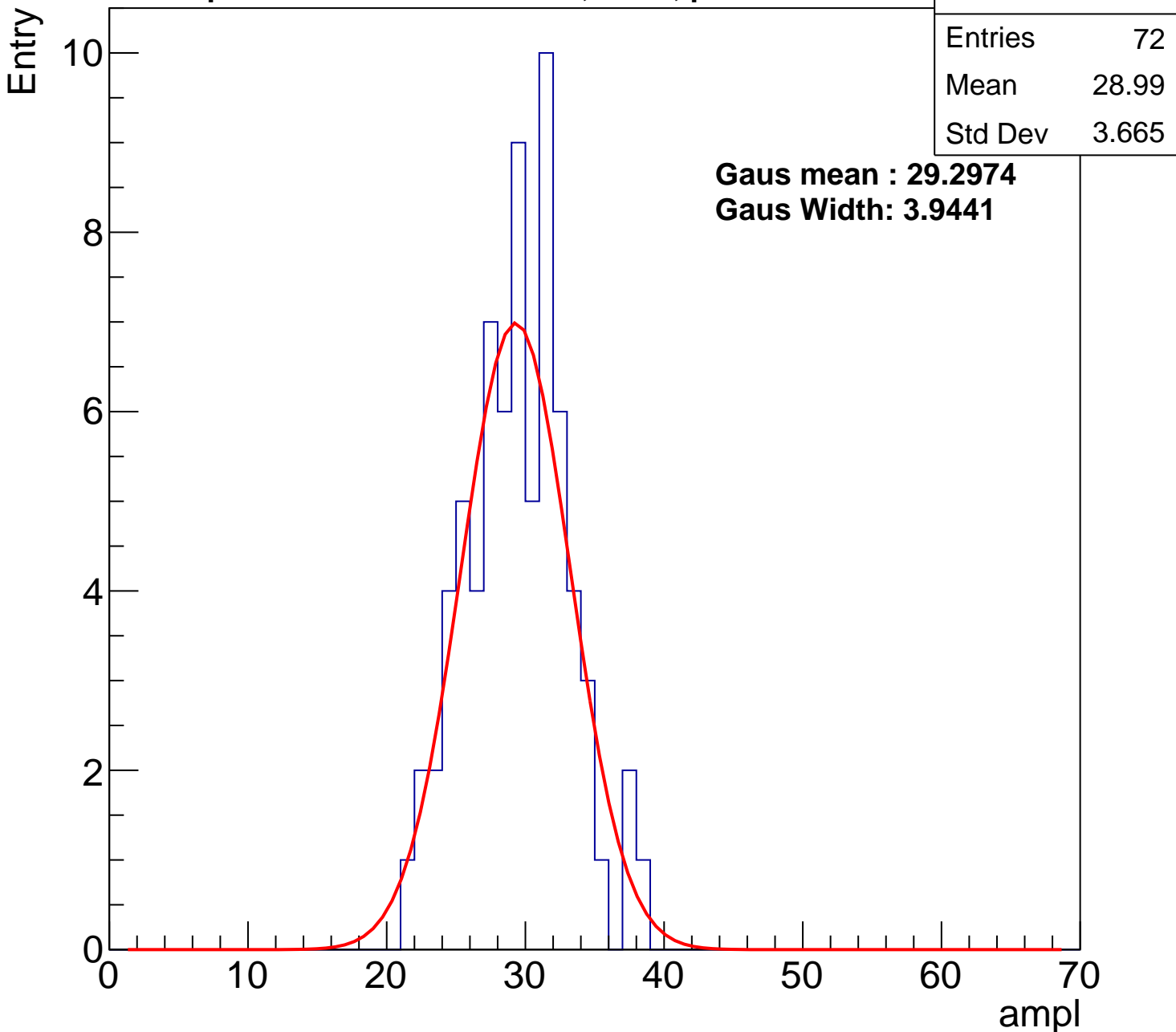
Gaus Width: 3.9441

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	36.42
Std Dev	3.684

Gaus mean : 36.3315

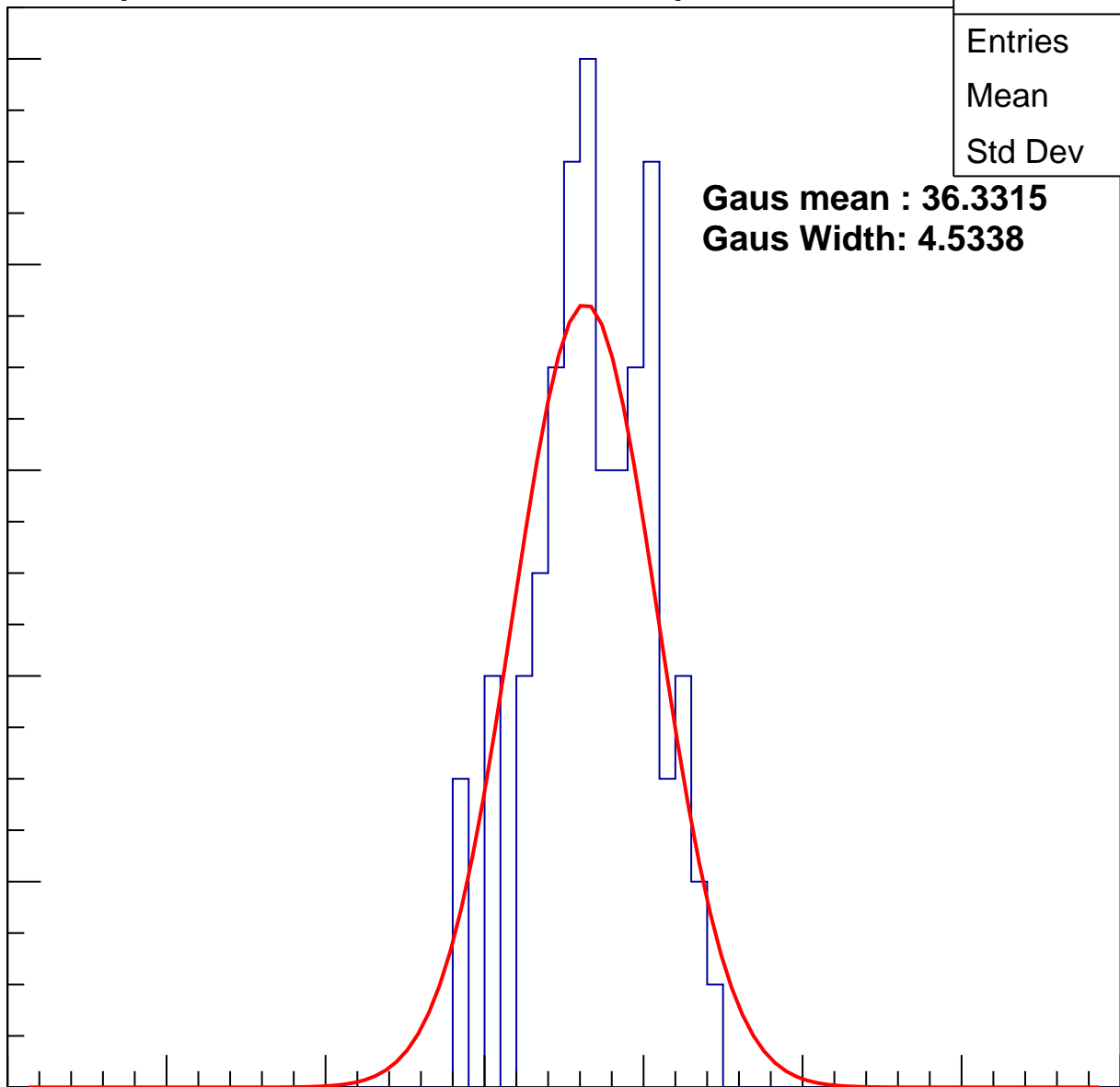
Gaus Width: 4.5338

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch34, adc2

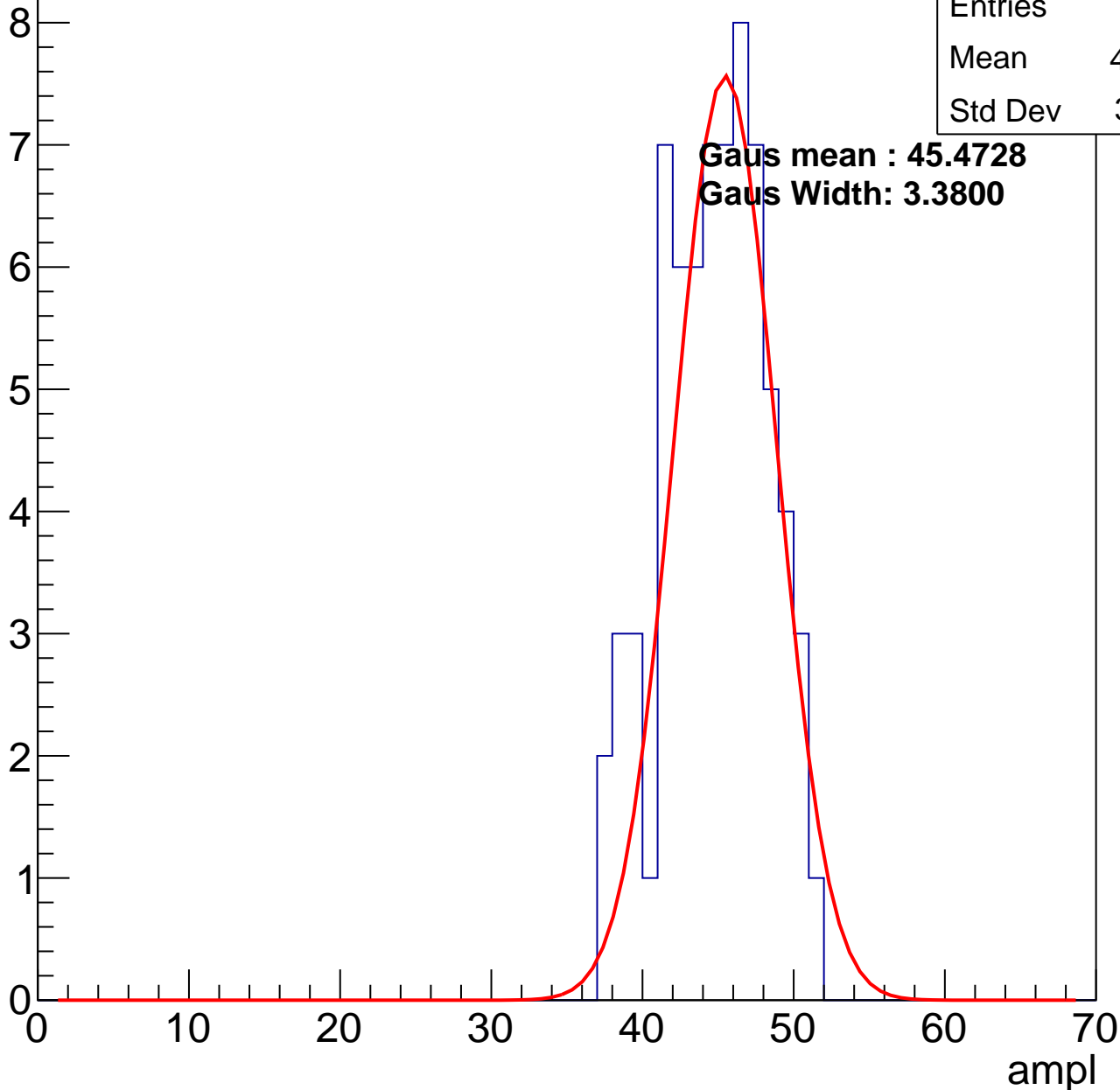
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.27
Std Dev	3.431

Gaus mean : 45.4728

Gaus Width: 3.3800

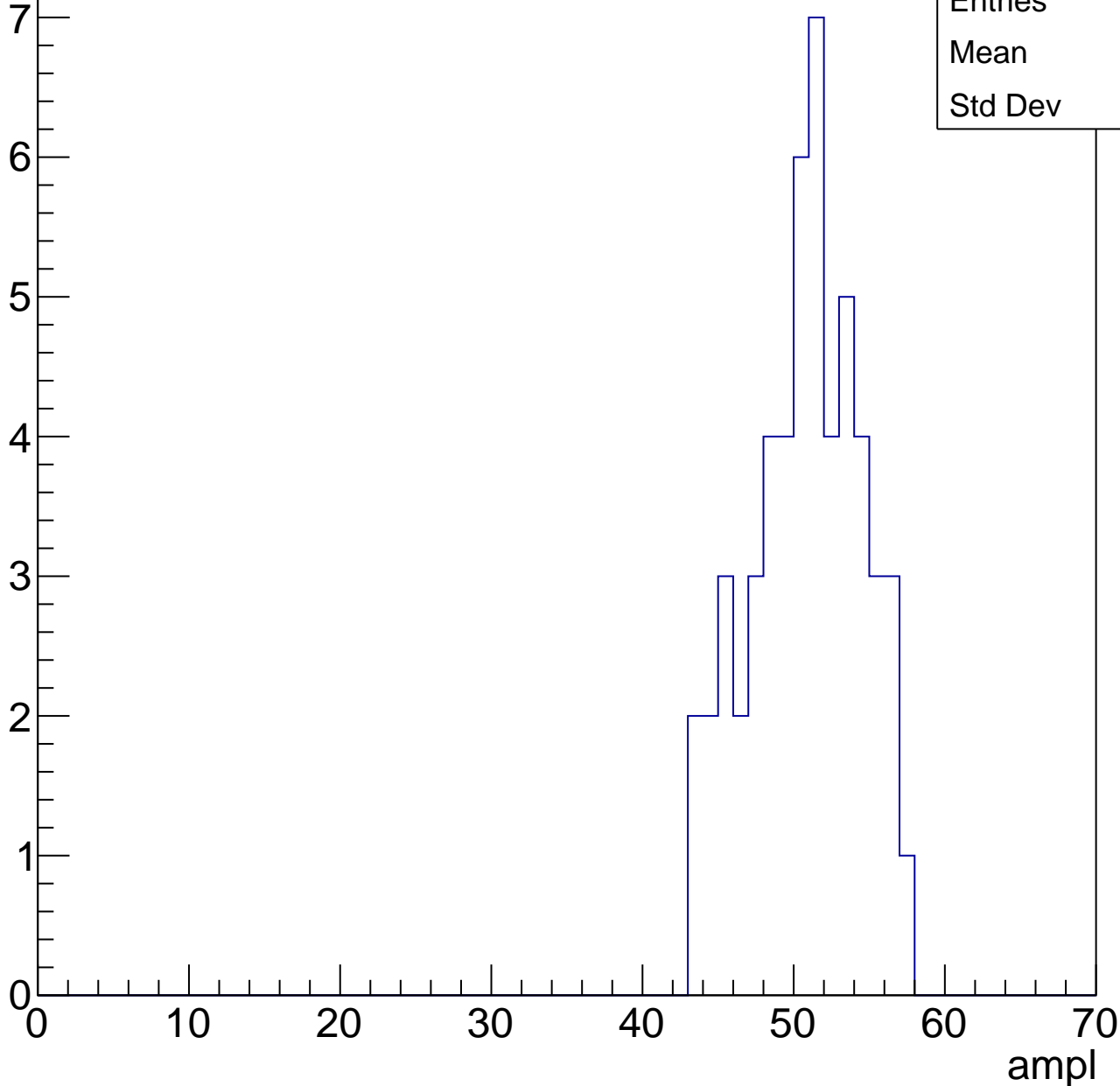


B1L100S, U12-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

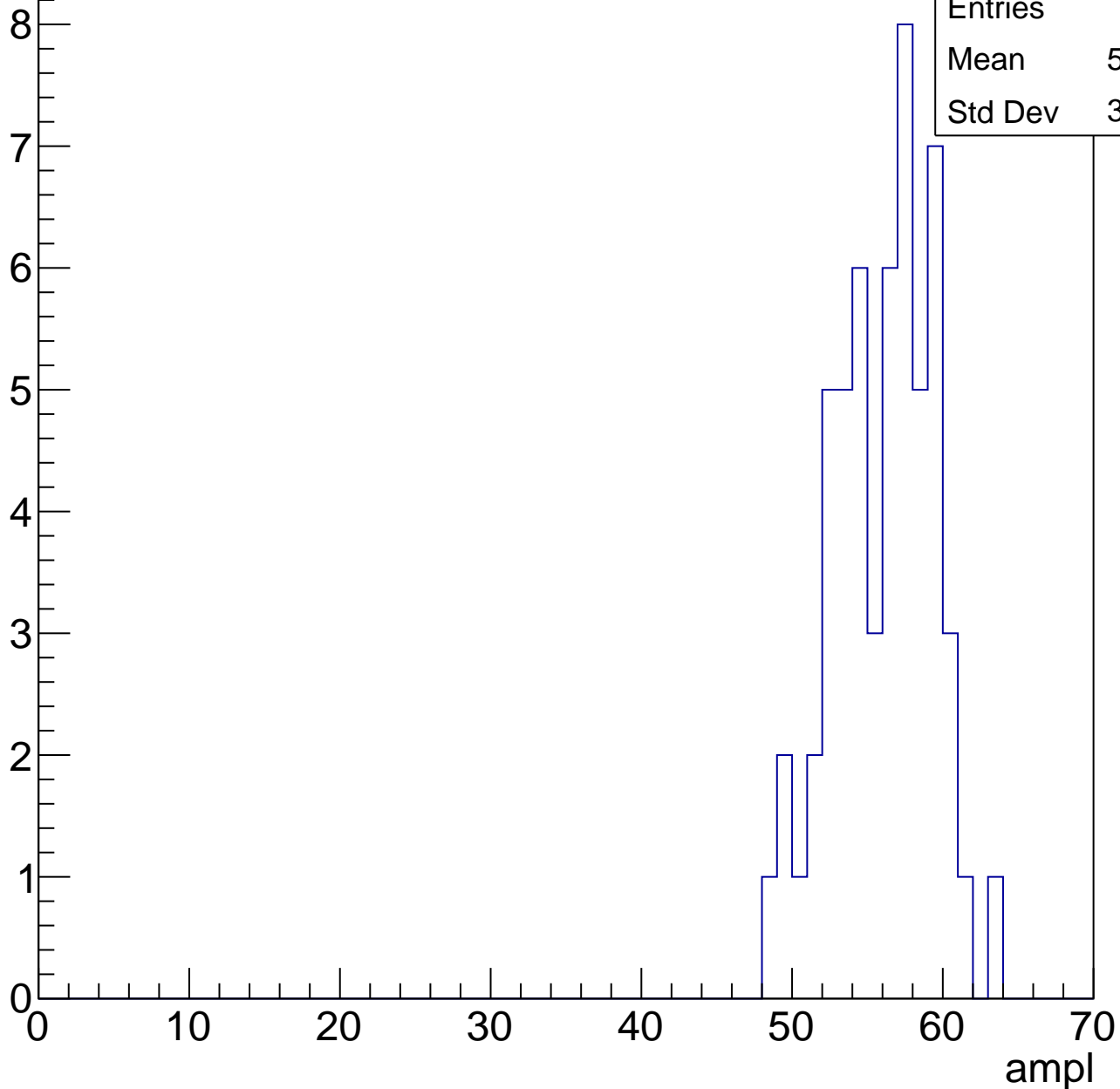
Entries	53
Mean	50.3
Std Dev	3.59



B1L100S, U12-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



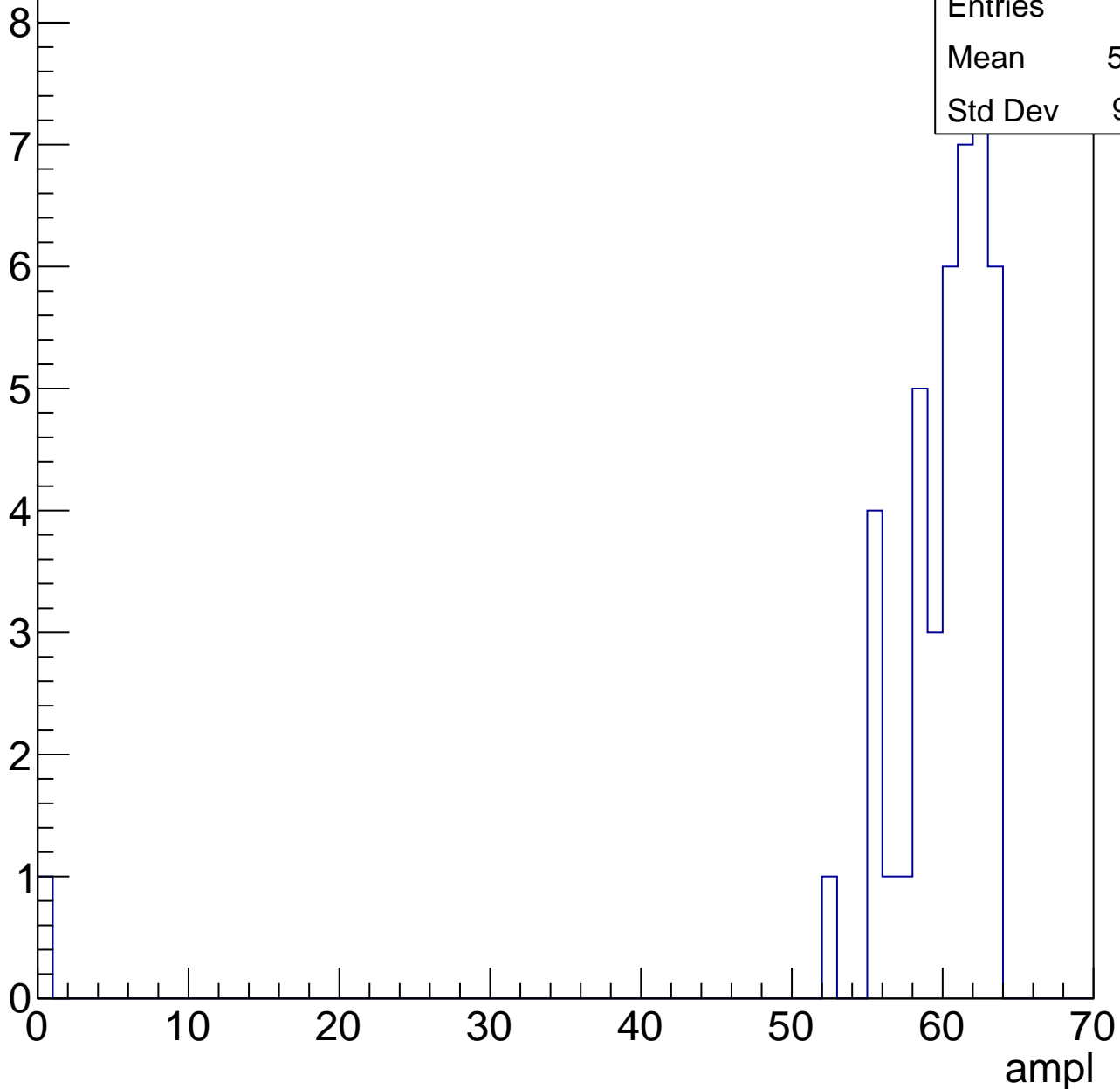
Entries	56
Mean	55.55
Std Dev	3.278

B1L100S, U12-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

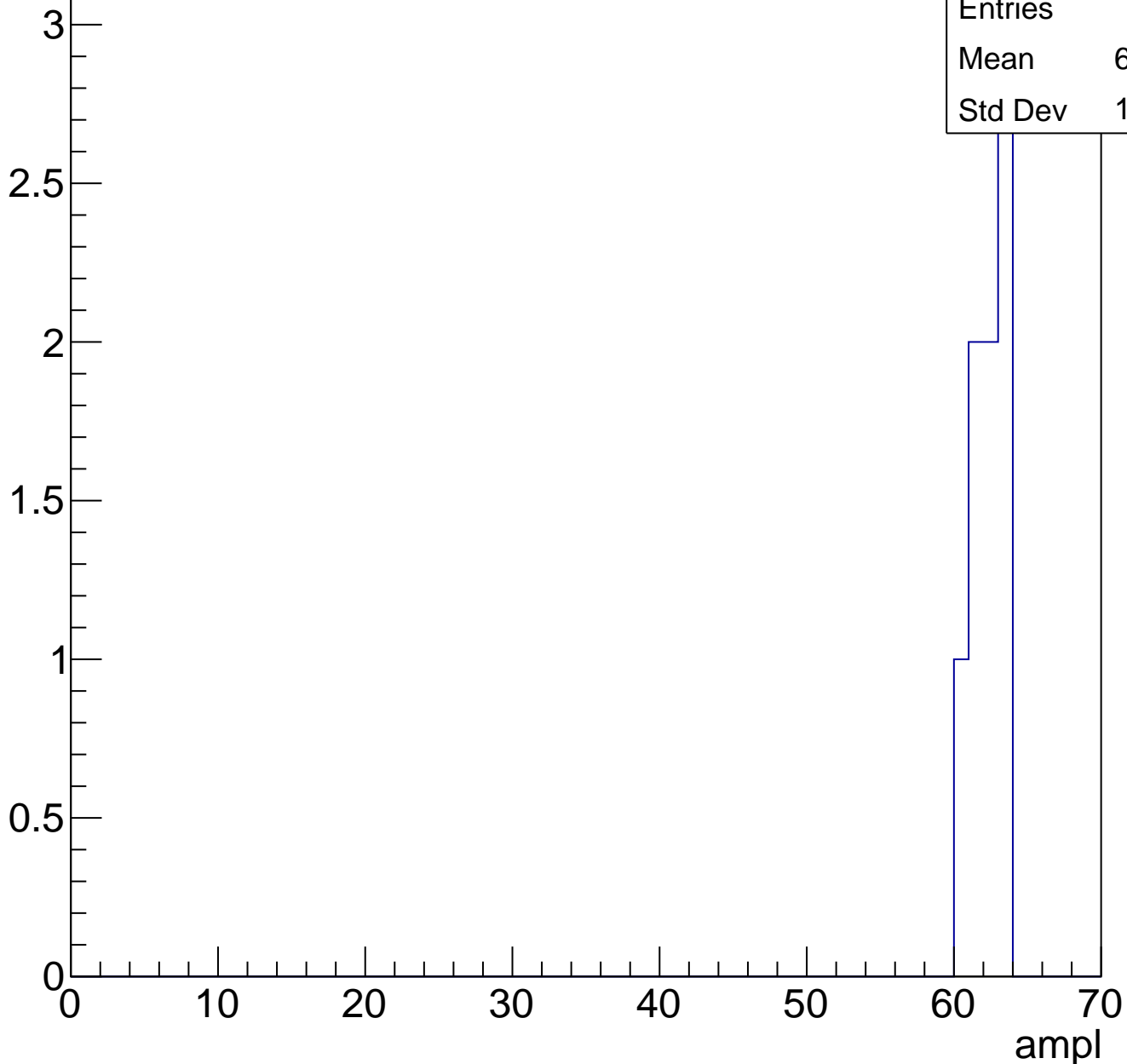
Entries	43
Mean	58.44
Std Dev	9.401



B1L100S, U12-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch35, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

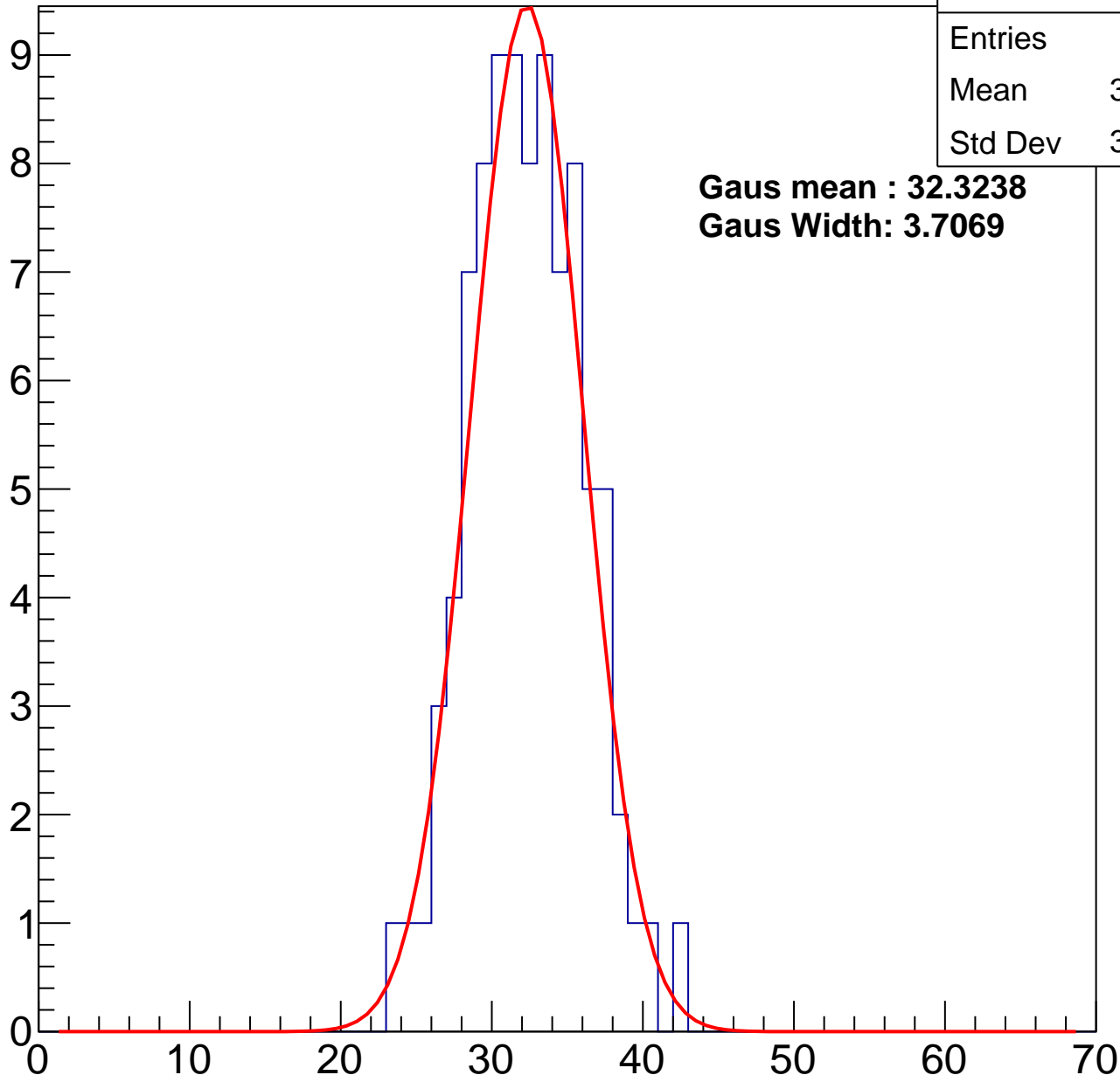
9
8
7
6
5
4
3
2
1
0

Entries	90
Mean	31.87
Std Dev	3.694

Gaus mean : 32.3238

Gaus Width: 3.7069

ampl



B1L100S, U12-ch35, adc1

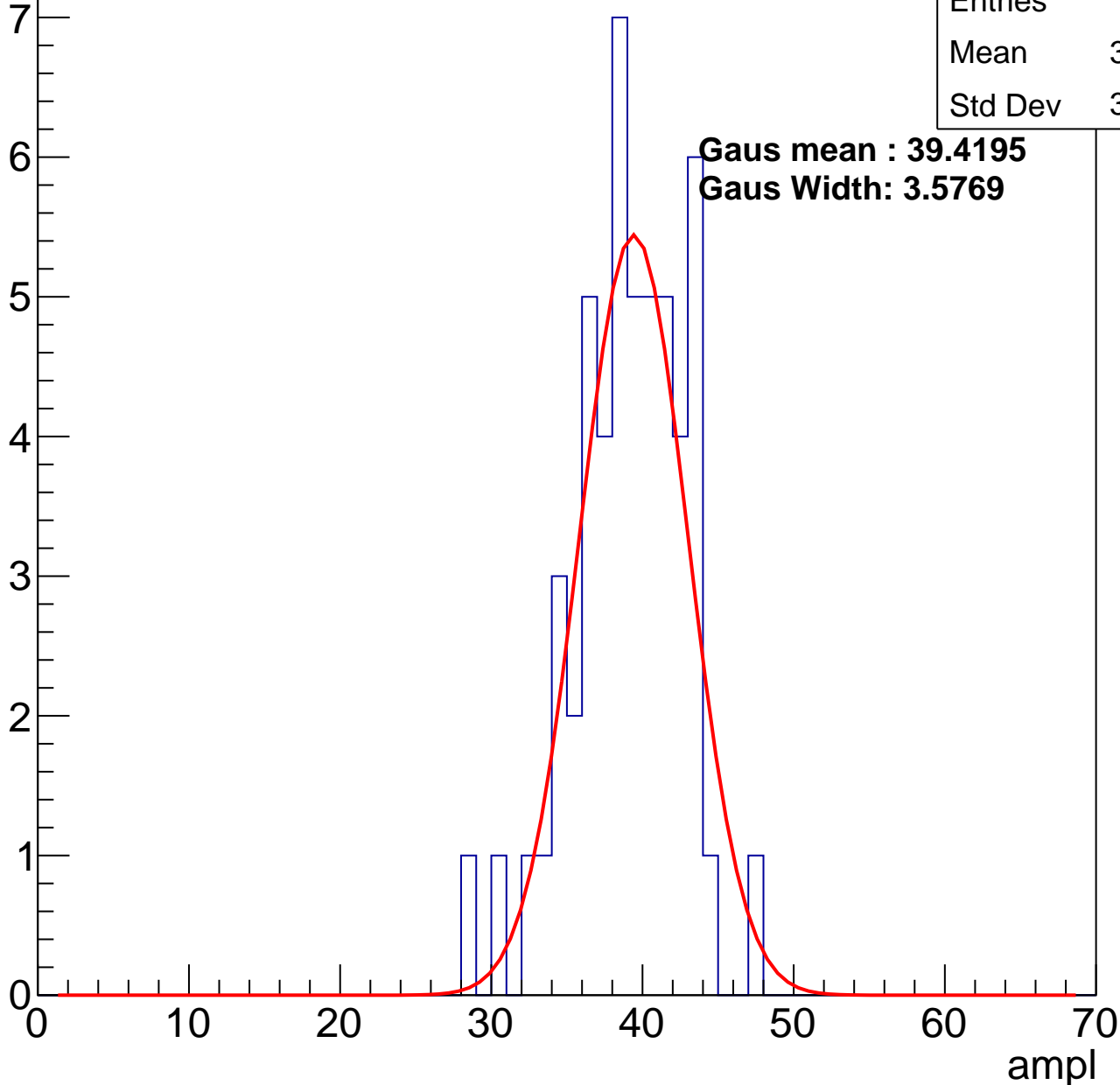
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	38.58
Std Dev	3.666

Gaus mean : 39.4195

Gaus Width: 3.5769



B1L100S, U12-ch35, adc2

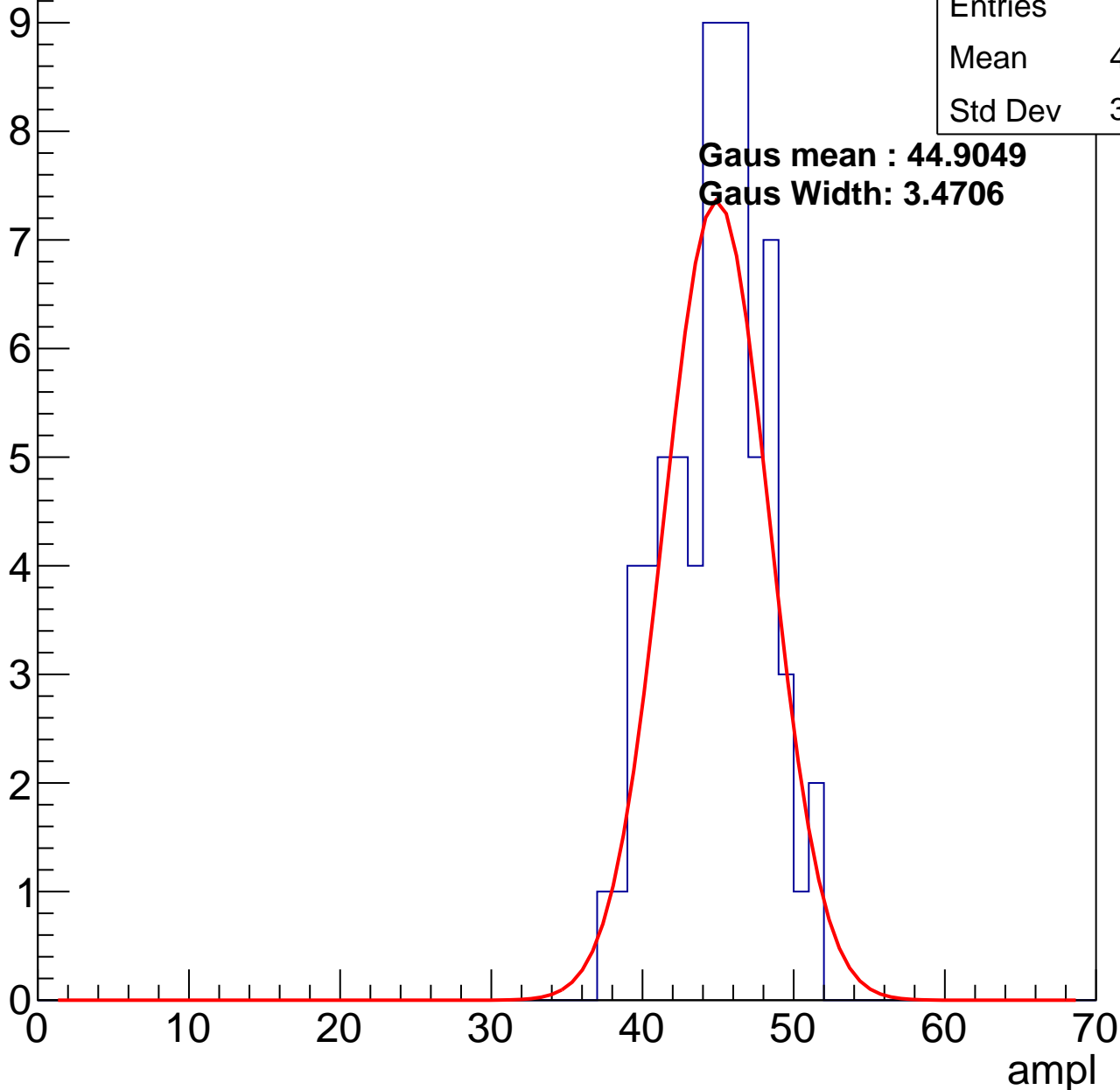
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	44.39
Std Dev	3.227

Gaus mean : 44.9049

Gaus Width: 3.4706

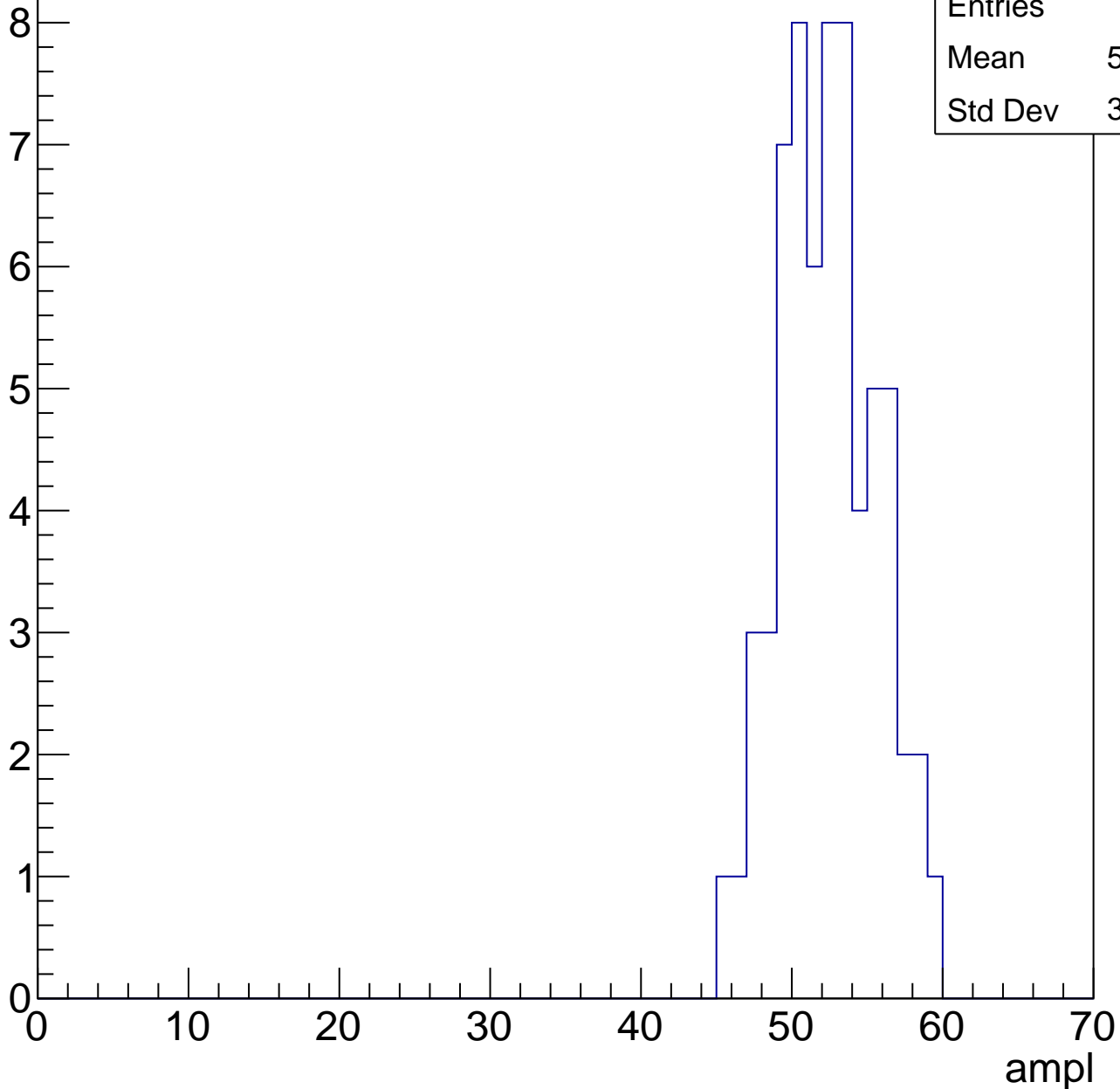


B1L100S, U12-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	51.95
Std Dev	3.135

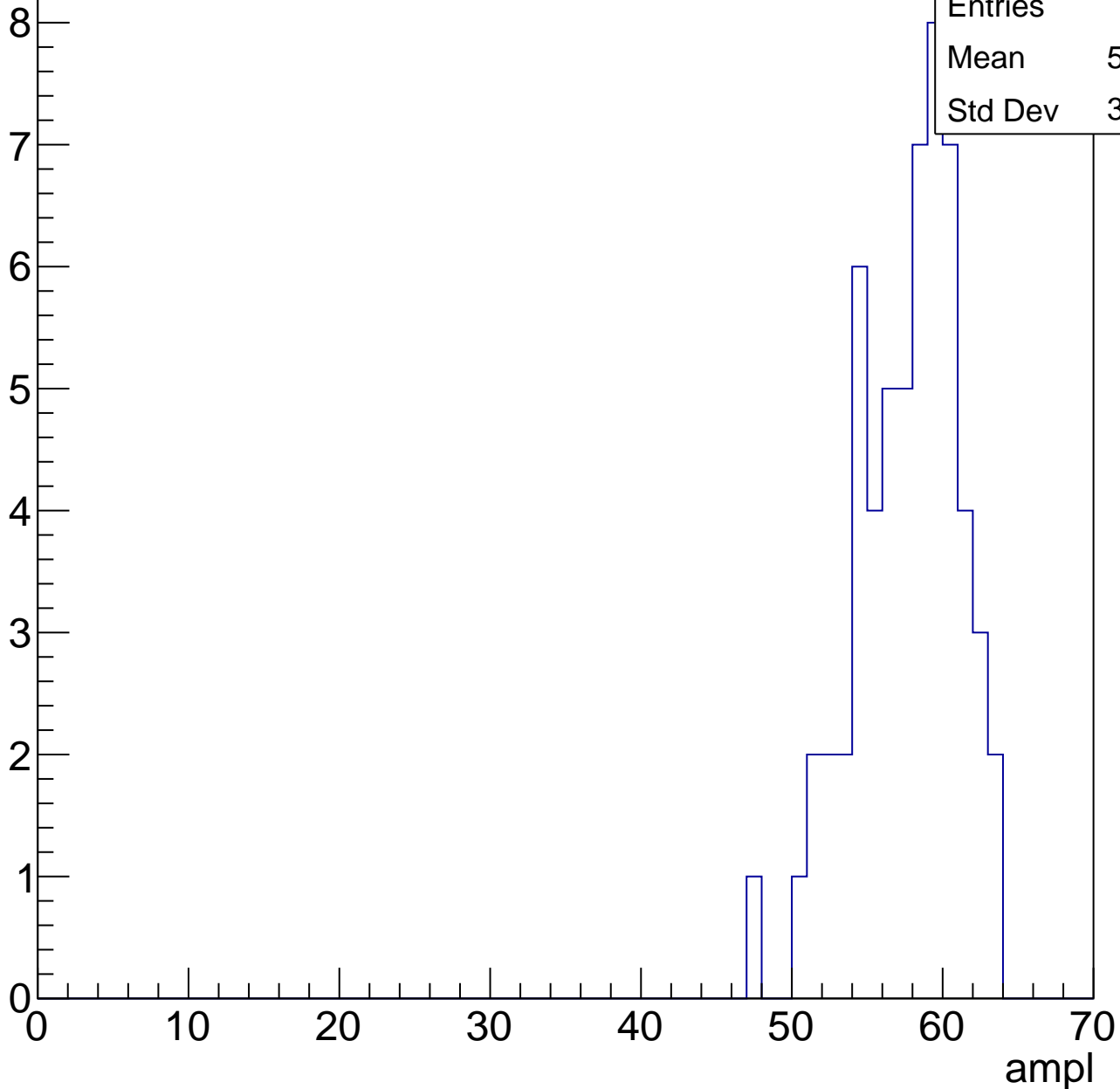


B1L100S, U12-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	57.15
Std Dev	3.419

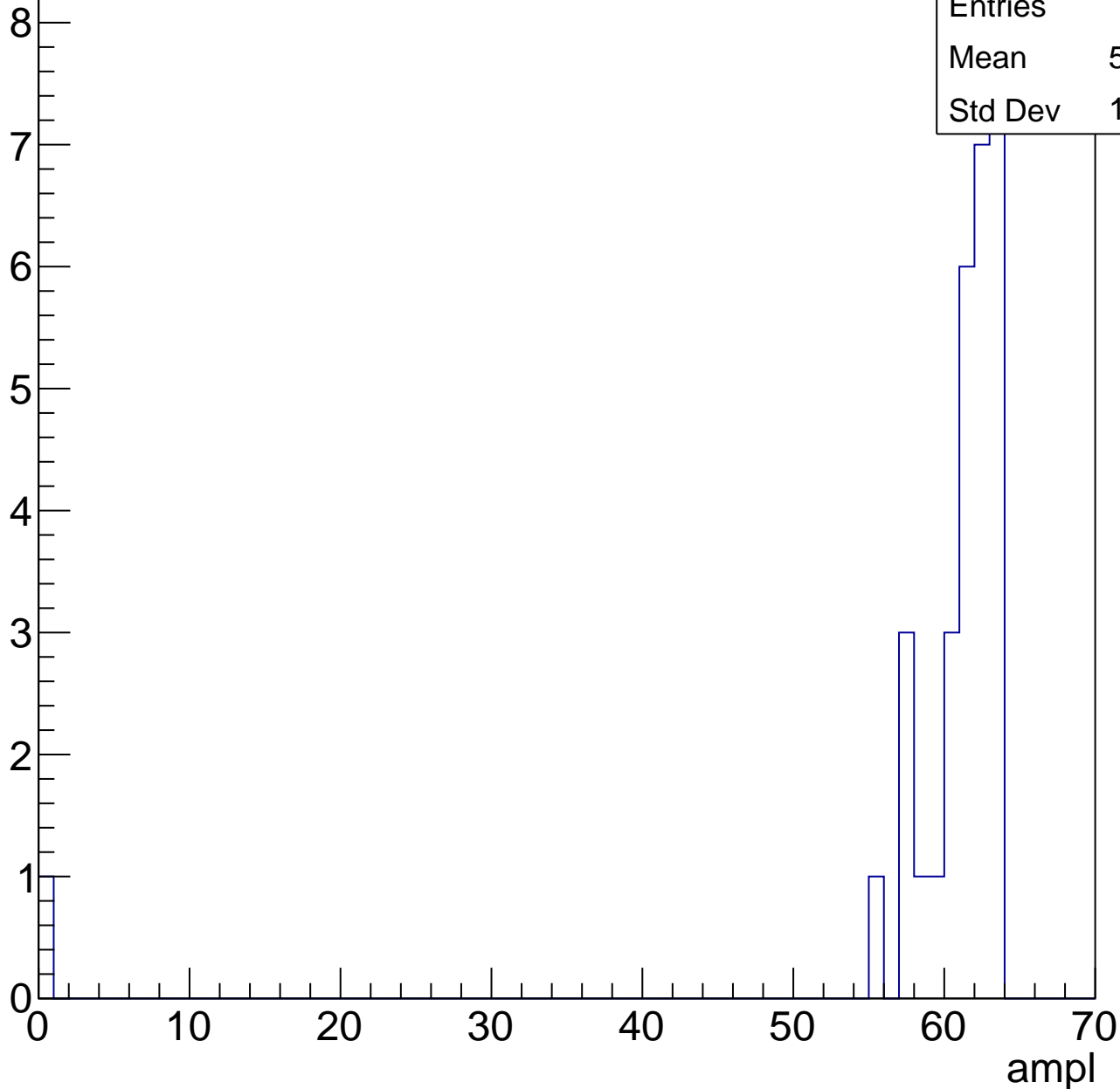


B1L100S, U12-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.94
Std Dev	10.97



B1L100S, U12-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch36, adc0

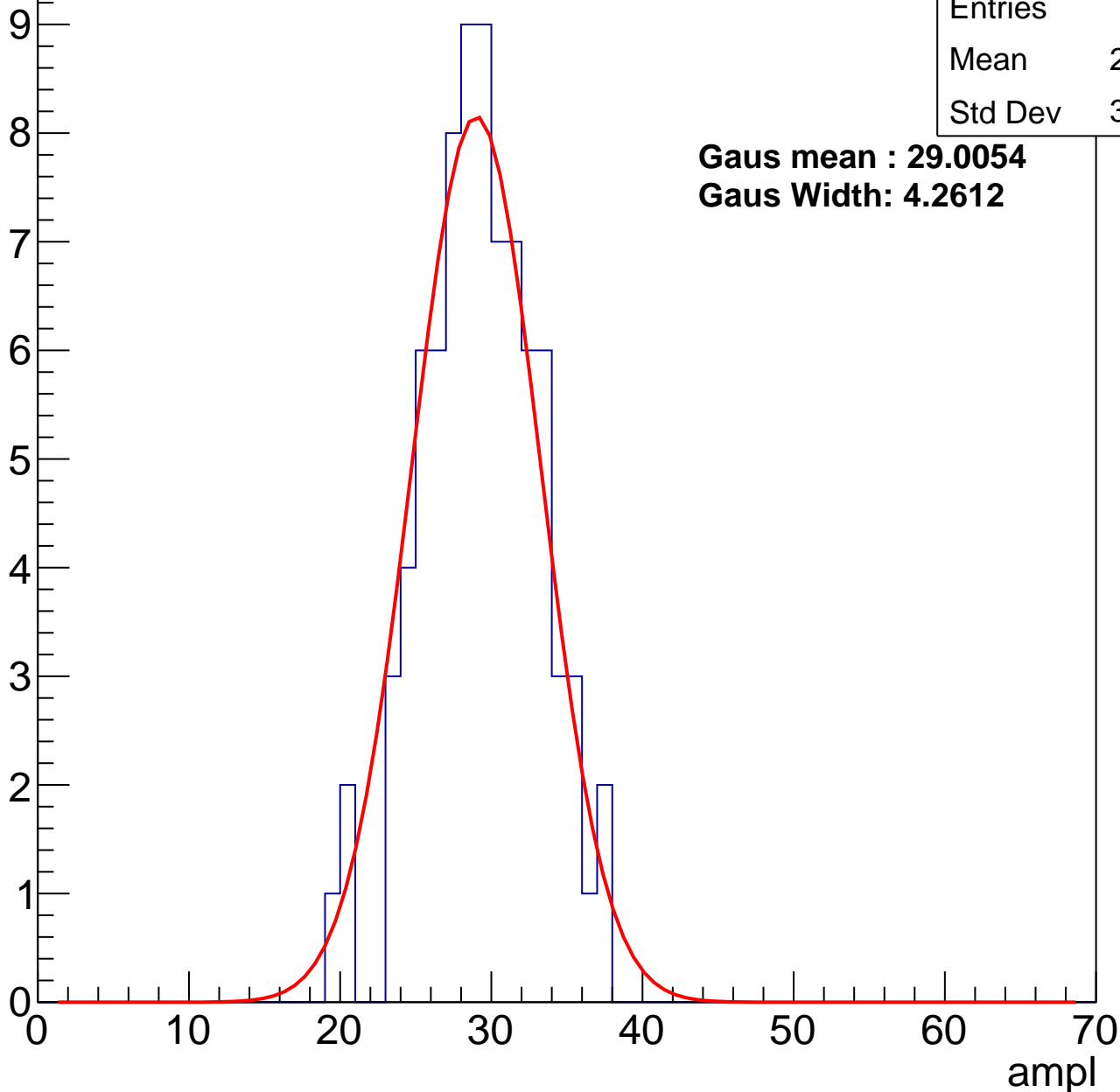
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	28.83
Std Dev	3.808

Gaus mean : 29.0054

Gaus Width: 4.2612



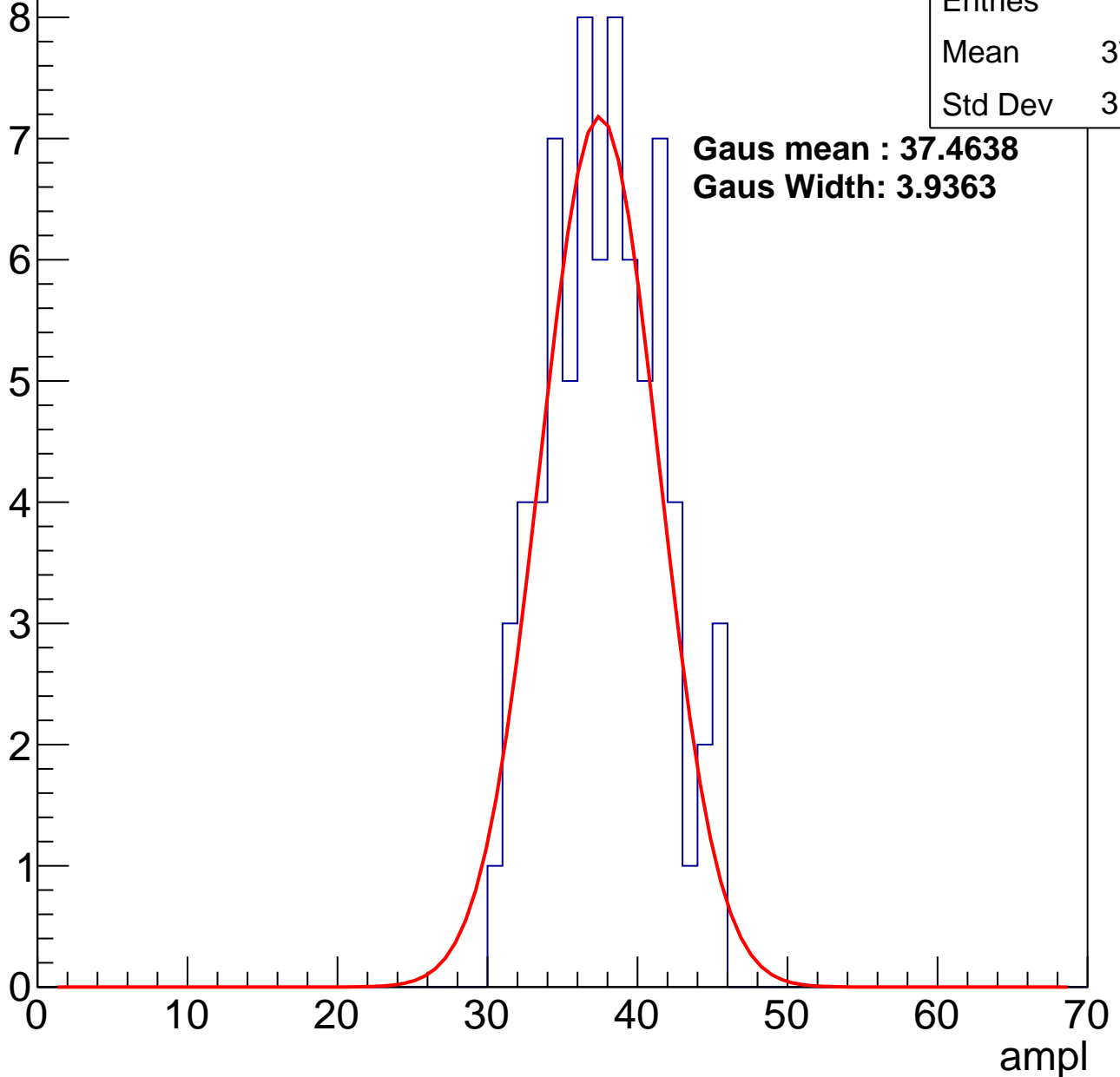
B1L100S, U12-ch36, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.36
Std Dev	3.707

Gaus mean : 37.4638
Gaus Width: 3.9363



B1L100S, U12-ch36, adc2

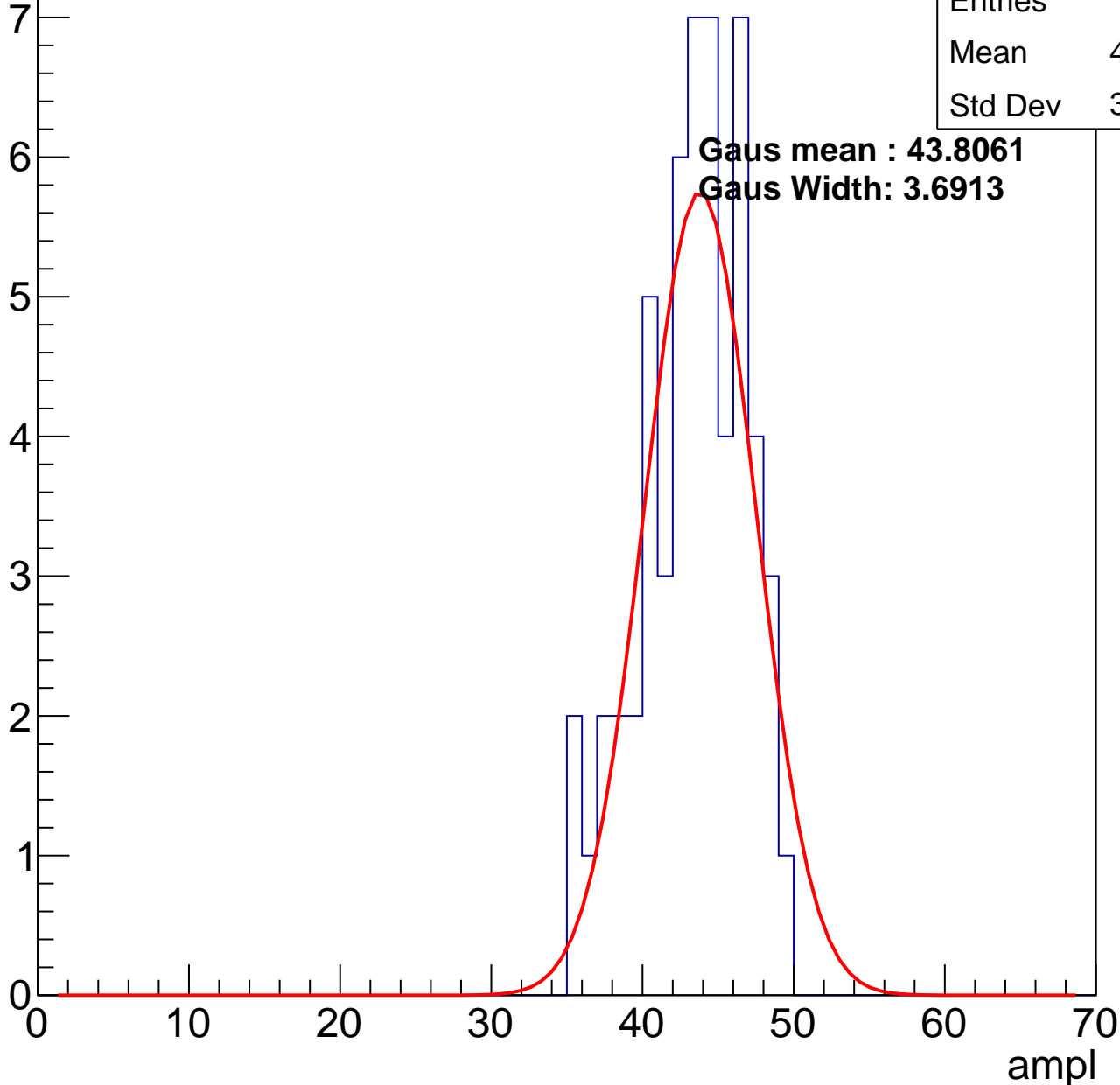
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.88
Std Dev	3.418

Gaus mean : 43.8061

Gaus Width: 3.6913



B1L100S, U12-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	50.27
Std Dev	3.752

Entry

10

8

6

4

2

0

0

10

20

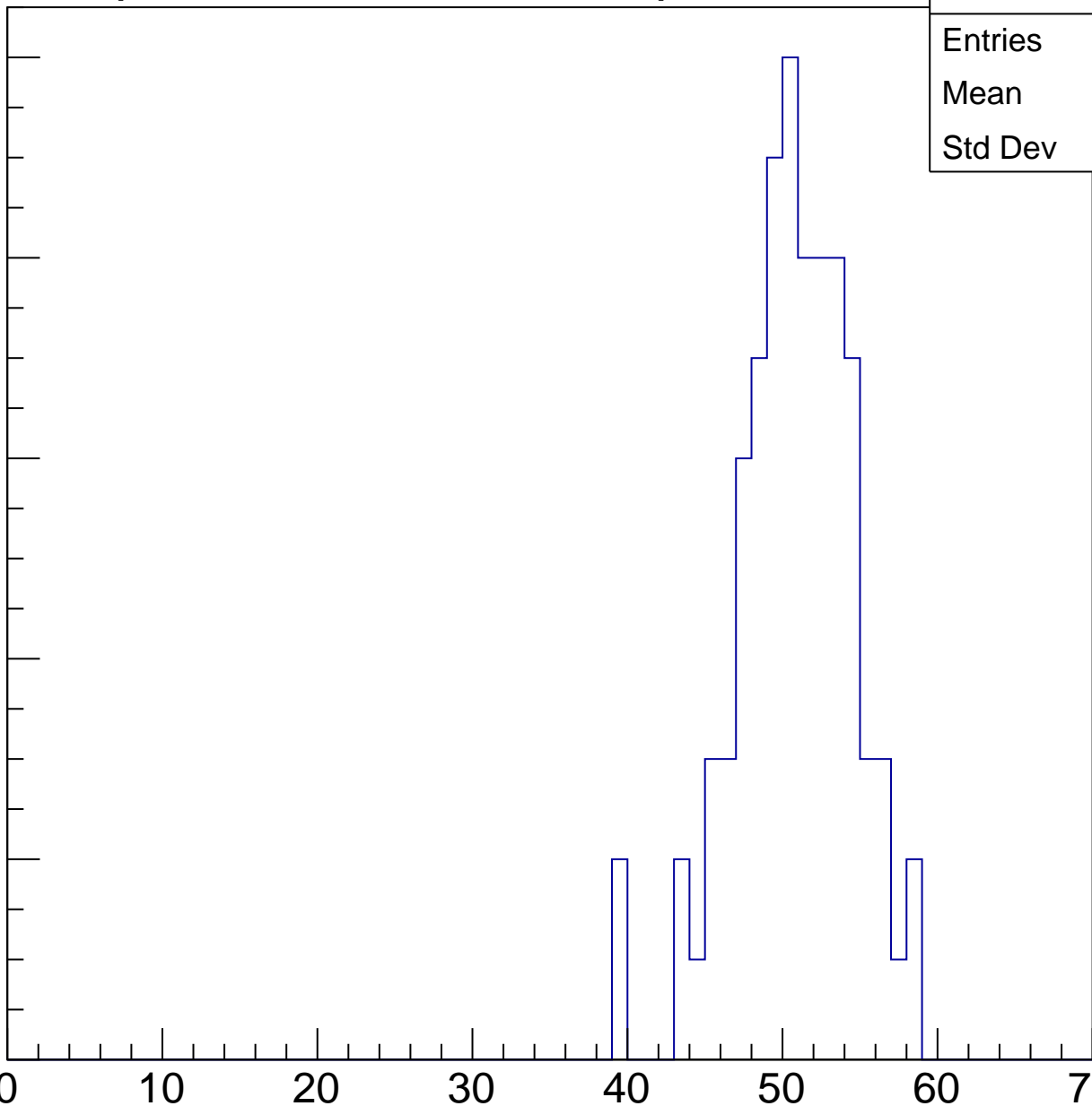
30

40

50

60

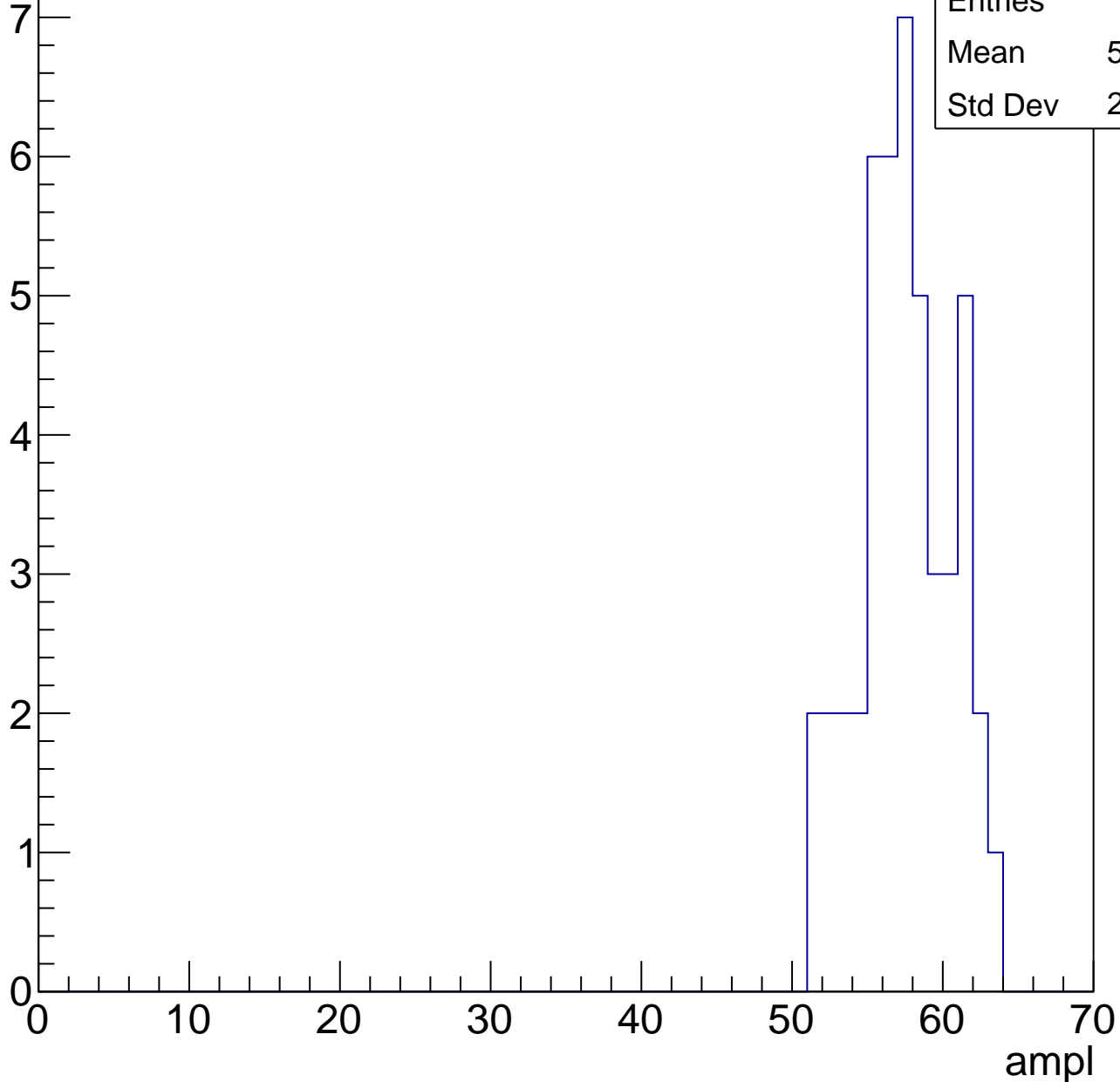
ampl



B1L100S, U12-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

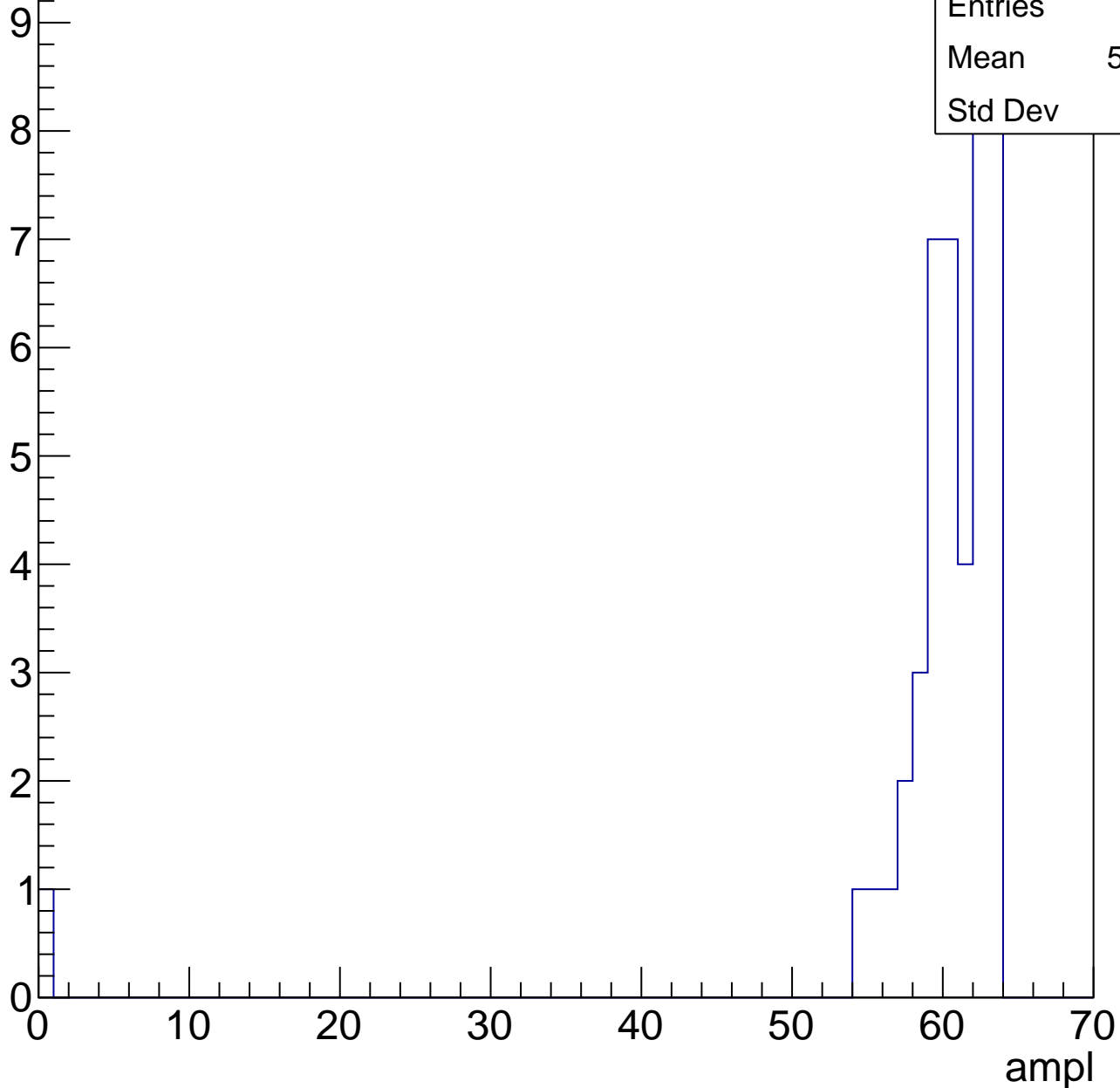


Entries	46
Mean	57.04
Std Dev	2.992

B1L100S, U12-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch37, adc0

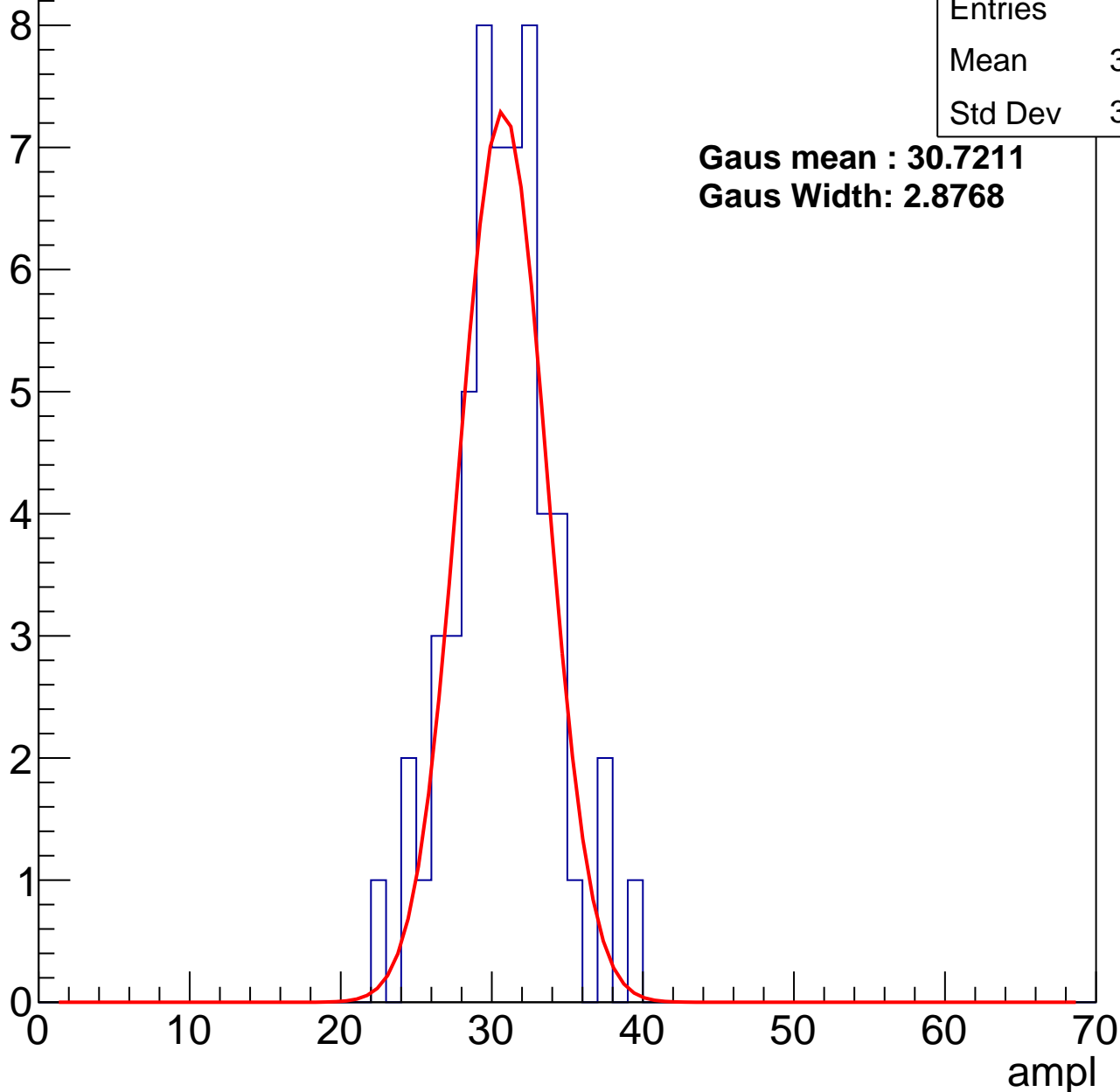
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	30.26
Std Dev	3.253

Gaus mean : 30.7211

Gaus Width: 2.8768



B1L100S, U12-ch37, adc1

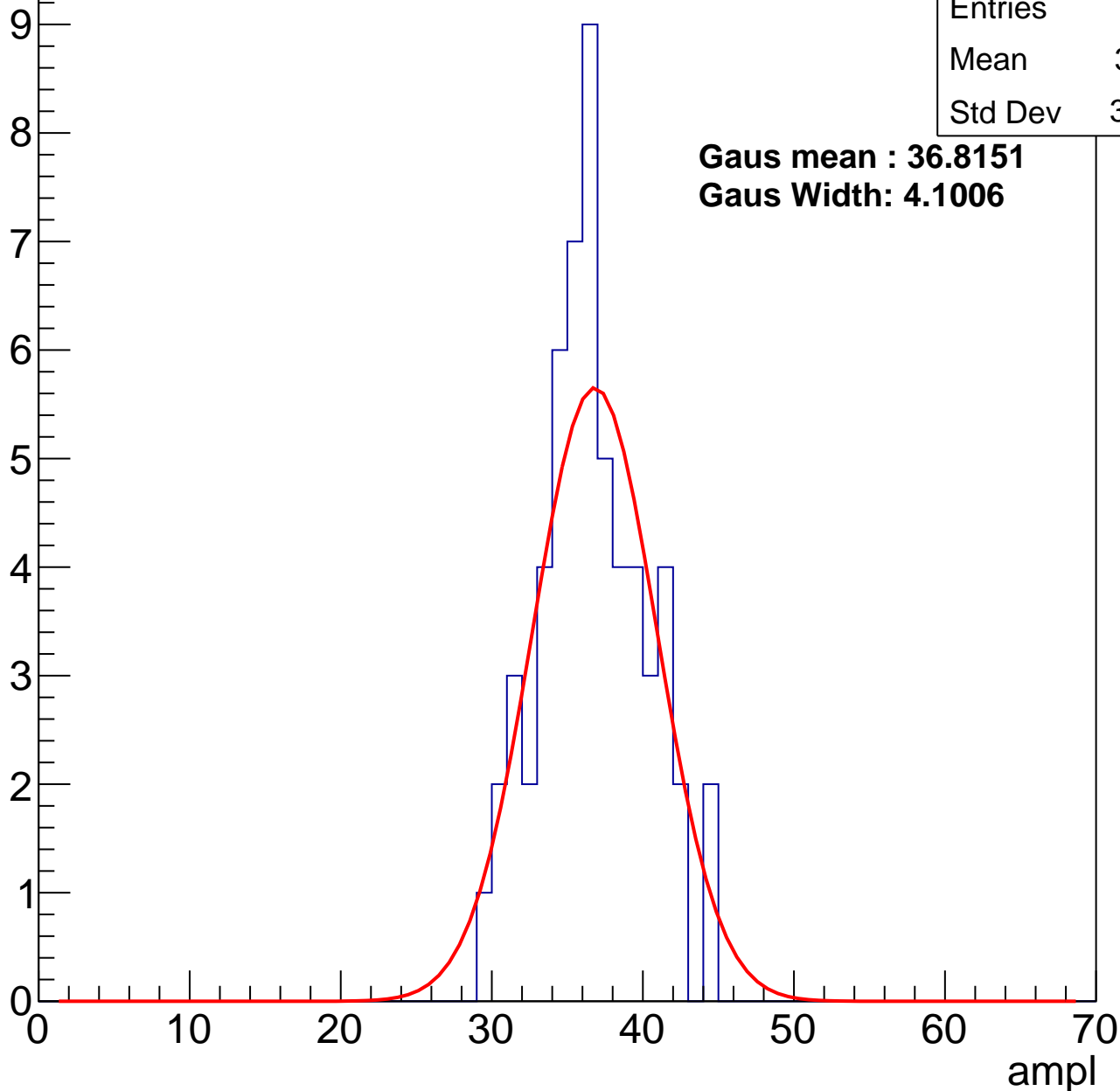
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	36.21
Std Dev	3.468

Gaus mean : 36.8151

Gaus Width: 4.1006



B1L100S, U12-ch37, adc2

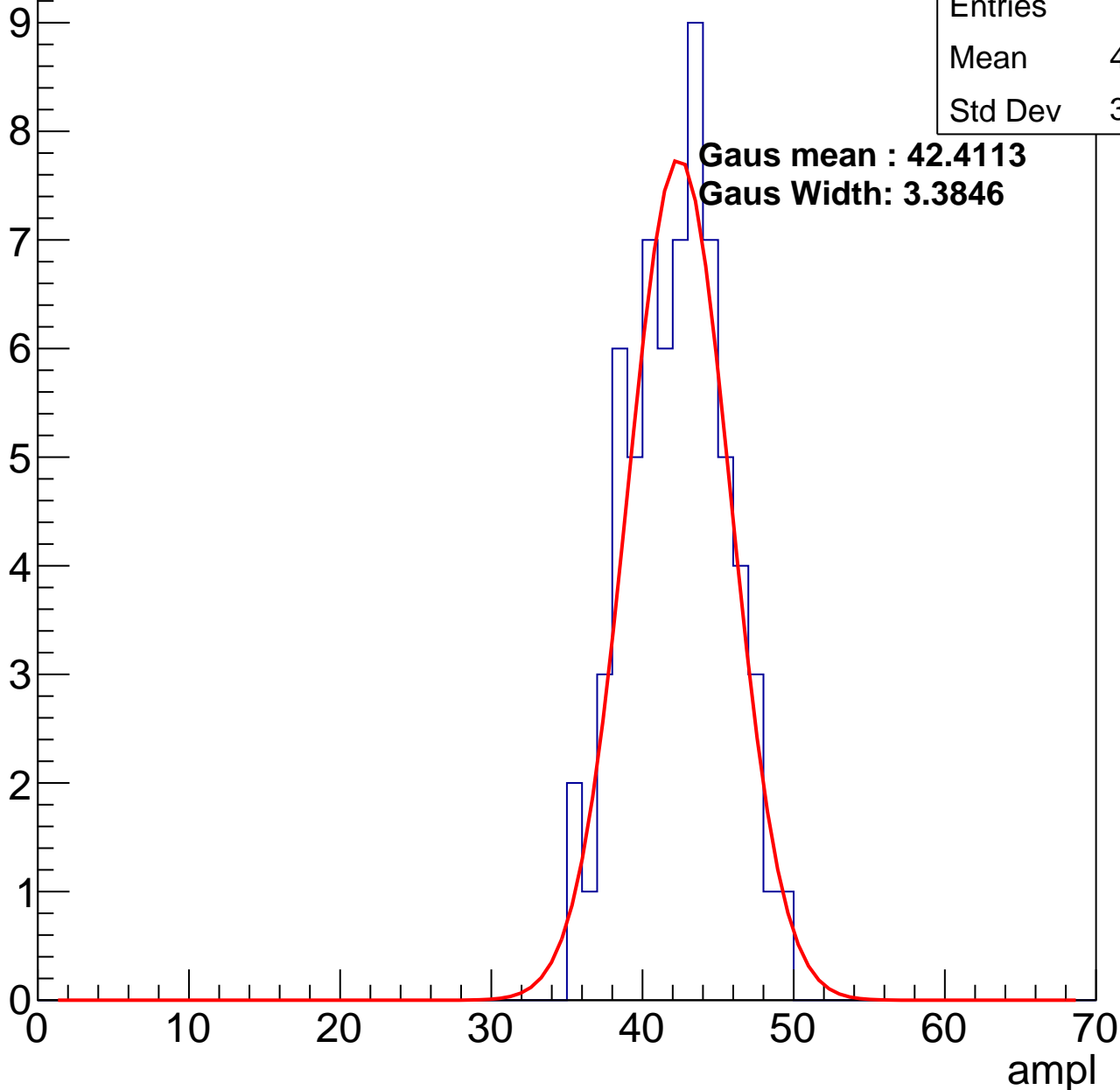
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	41.82
Std Dev	3.204

Gaus mean : 42.4113

Gaus Width: 3.3846

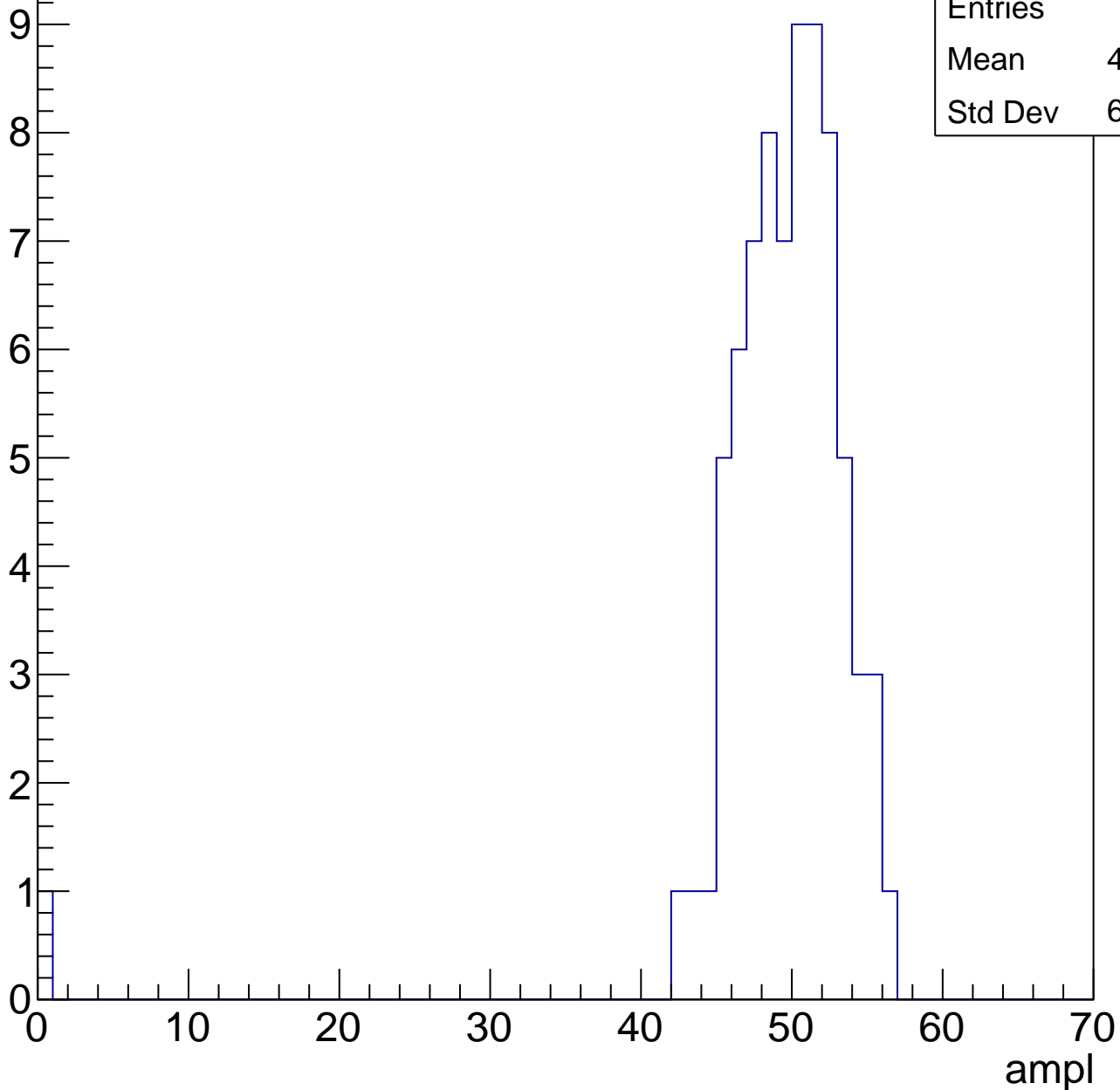


B1L100S, U12-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	48.79
Std Dev	6.436

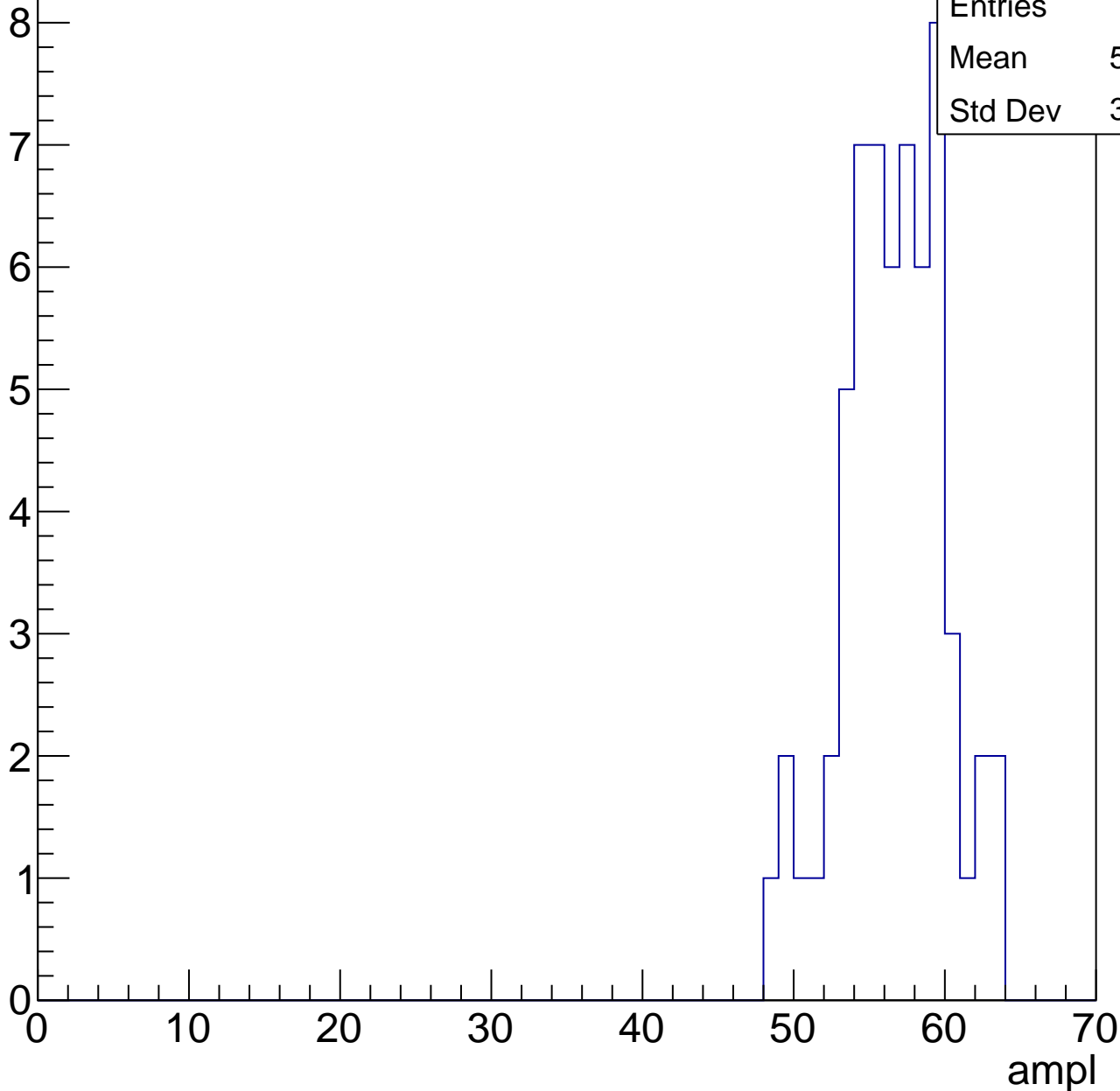


B1L100S, U12-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

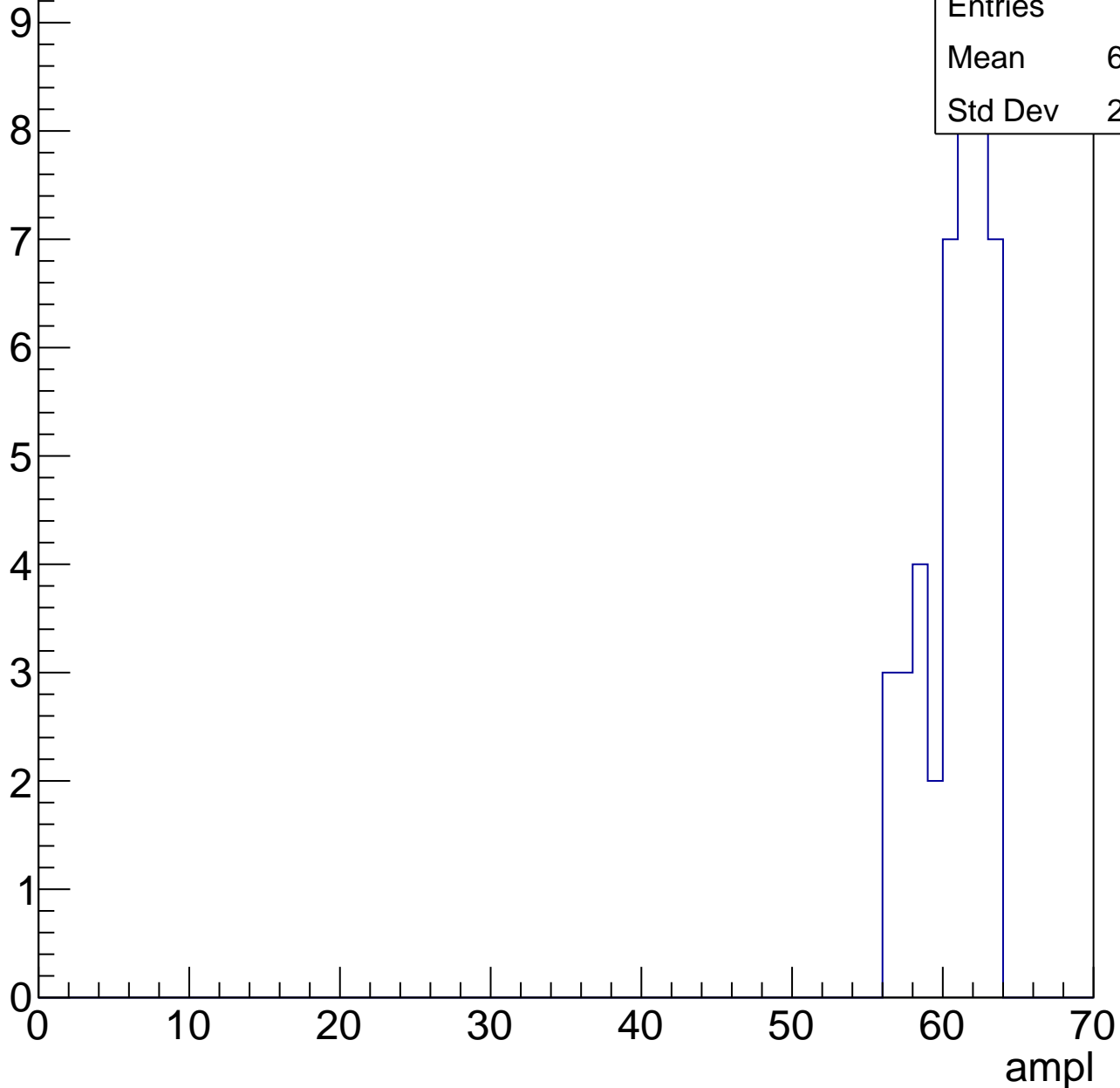
Entries	61
Mean	56.15
Std Dev	3.338



B1L100S, U12-ch37, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U12-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch38, adc0

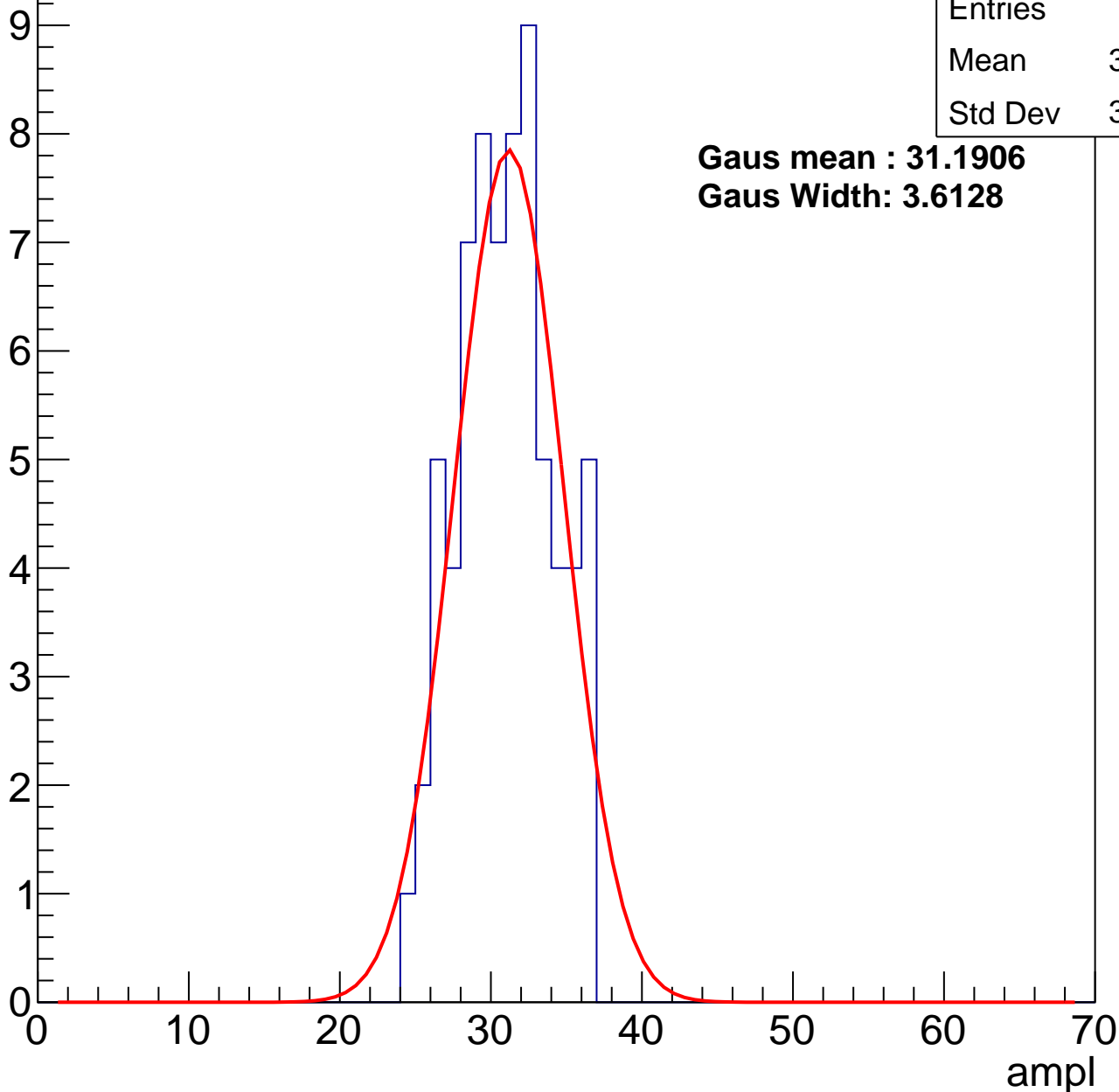
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.54
Std Dev	3.072

Gaus mean : 31.1906

Gaus Width: 3.6128



B1L100S, U12-ch38, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	38.7
Std Dev	3.249

Gaus mean : 38.7958

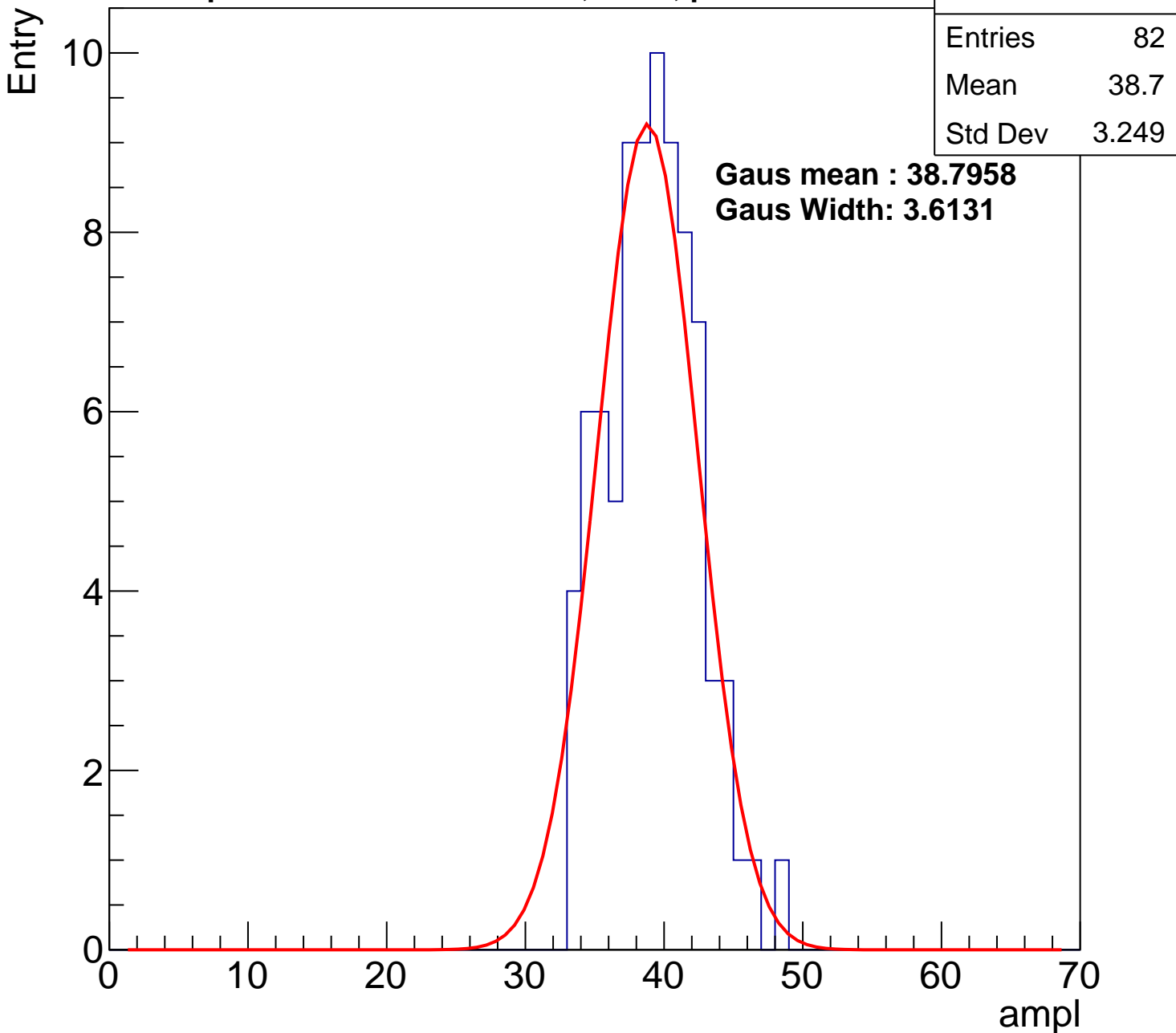
Gaus Width: 3.6131

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch38, adc2

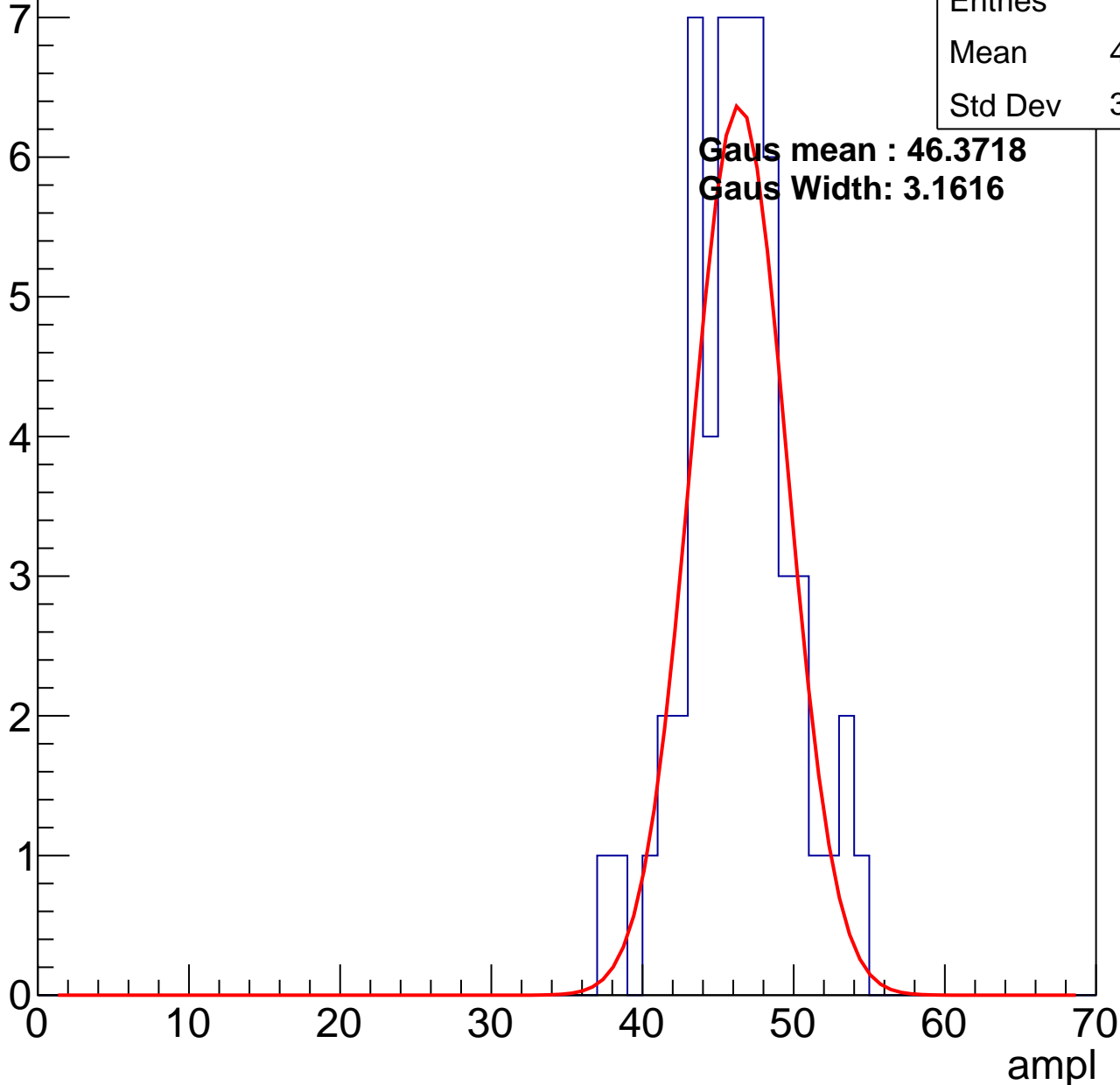
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	45.93
Std Dev	3.479

Gaus mean : 46.3718

Gaus Width: 3.1616

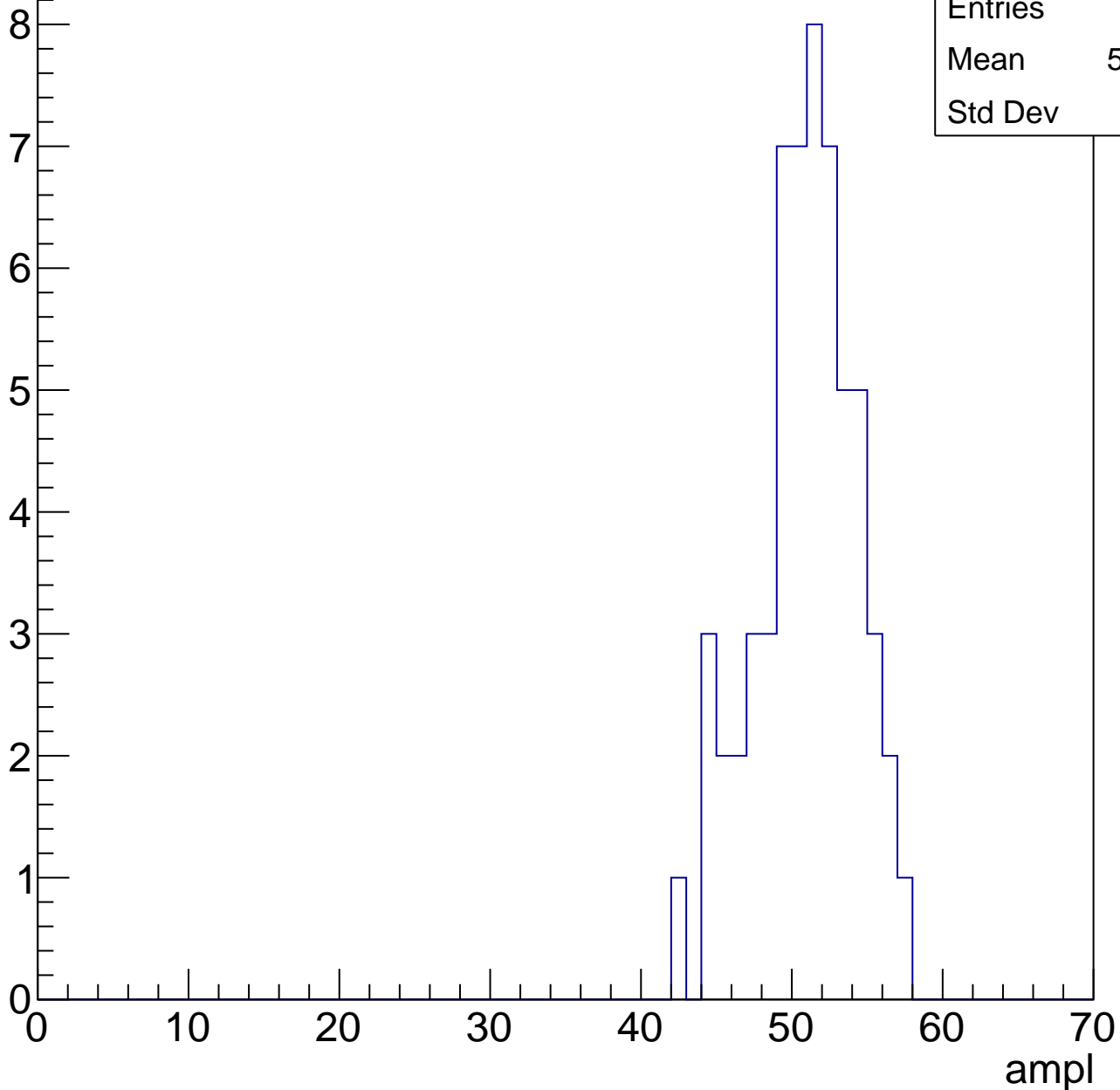


B1L100S, U12-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	50.42
Std Dev	3.31

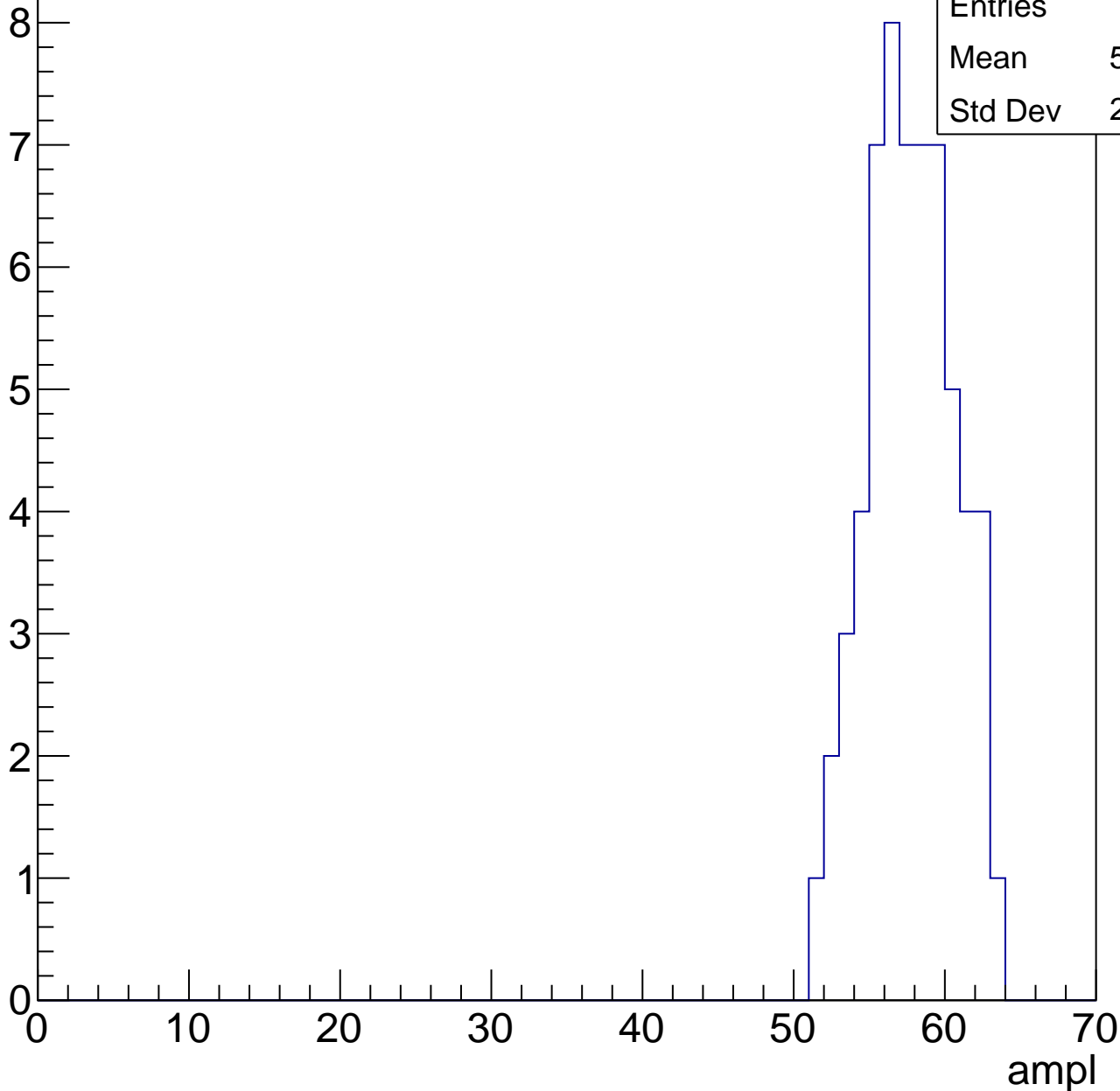


B1L100S, U12-ch38, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	57.27
Std Dev	2.834

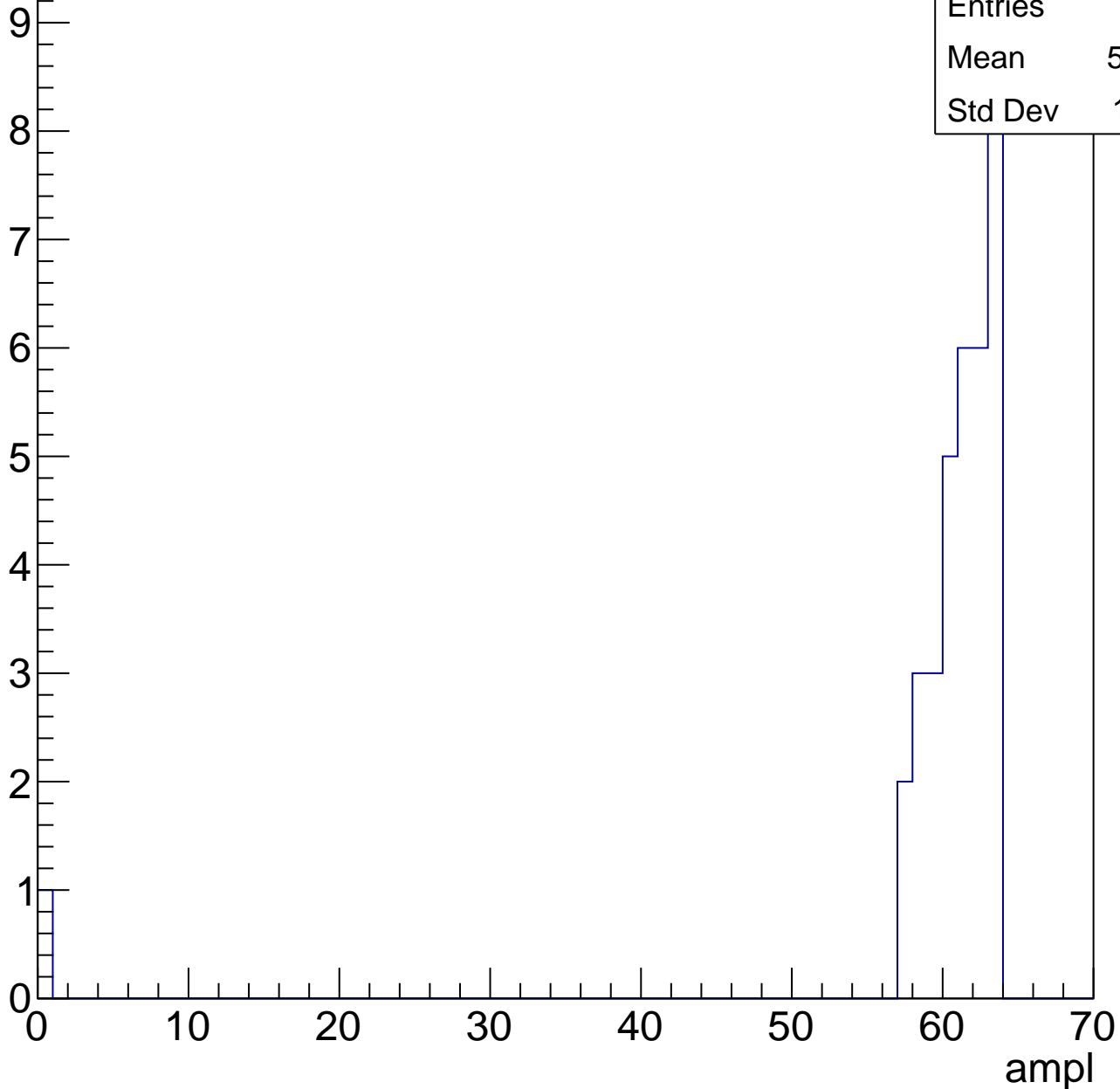


B1L100S, U12-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	59.14
Std Dev	10.31



B1L100S, U12-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch39, adc0

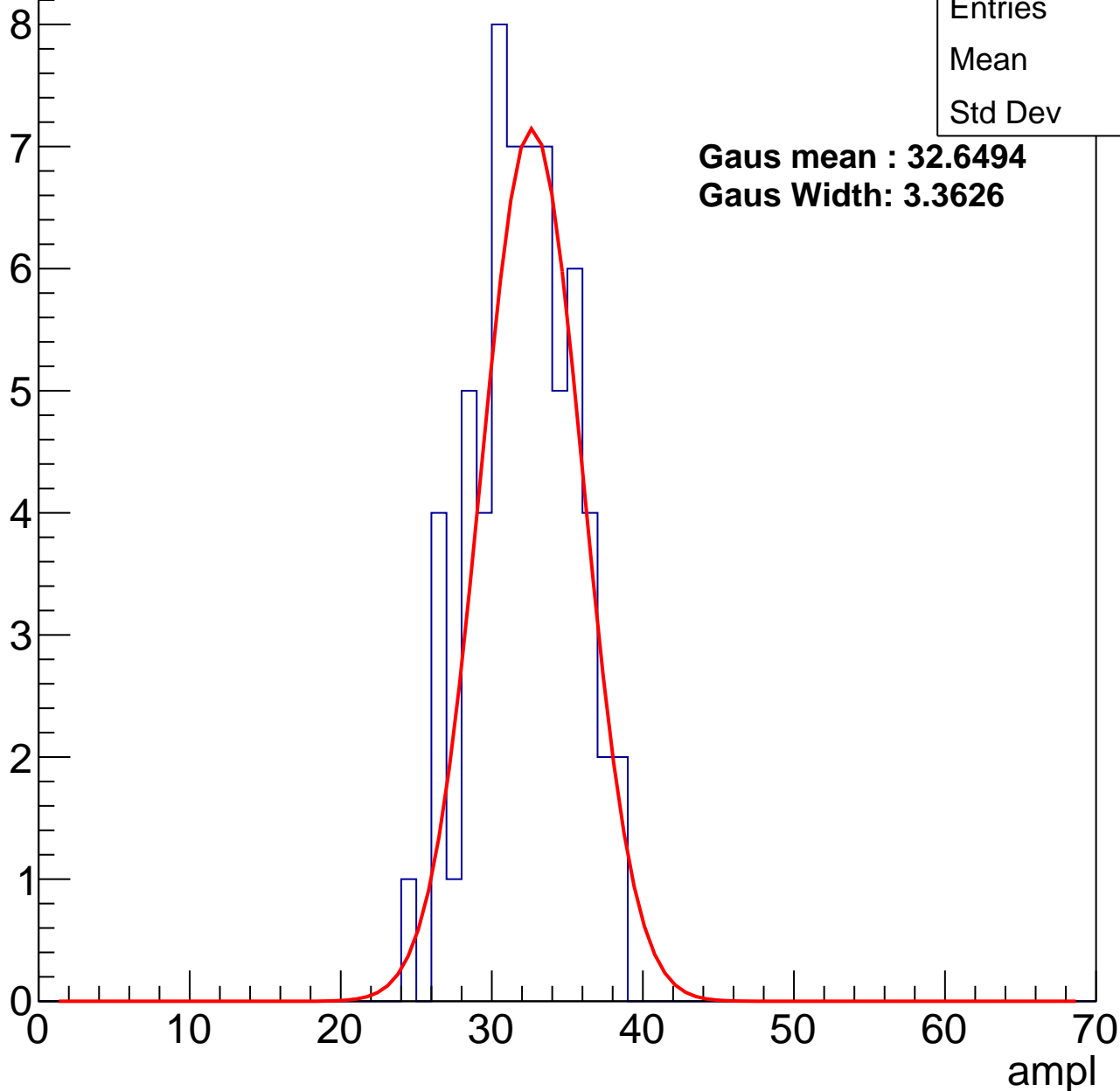
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	31.7
Std Dev	3.21

Gaus mean : 32.6494

Gaus Width: 3.3626



B1L100S, U12-ch39, adc1

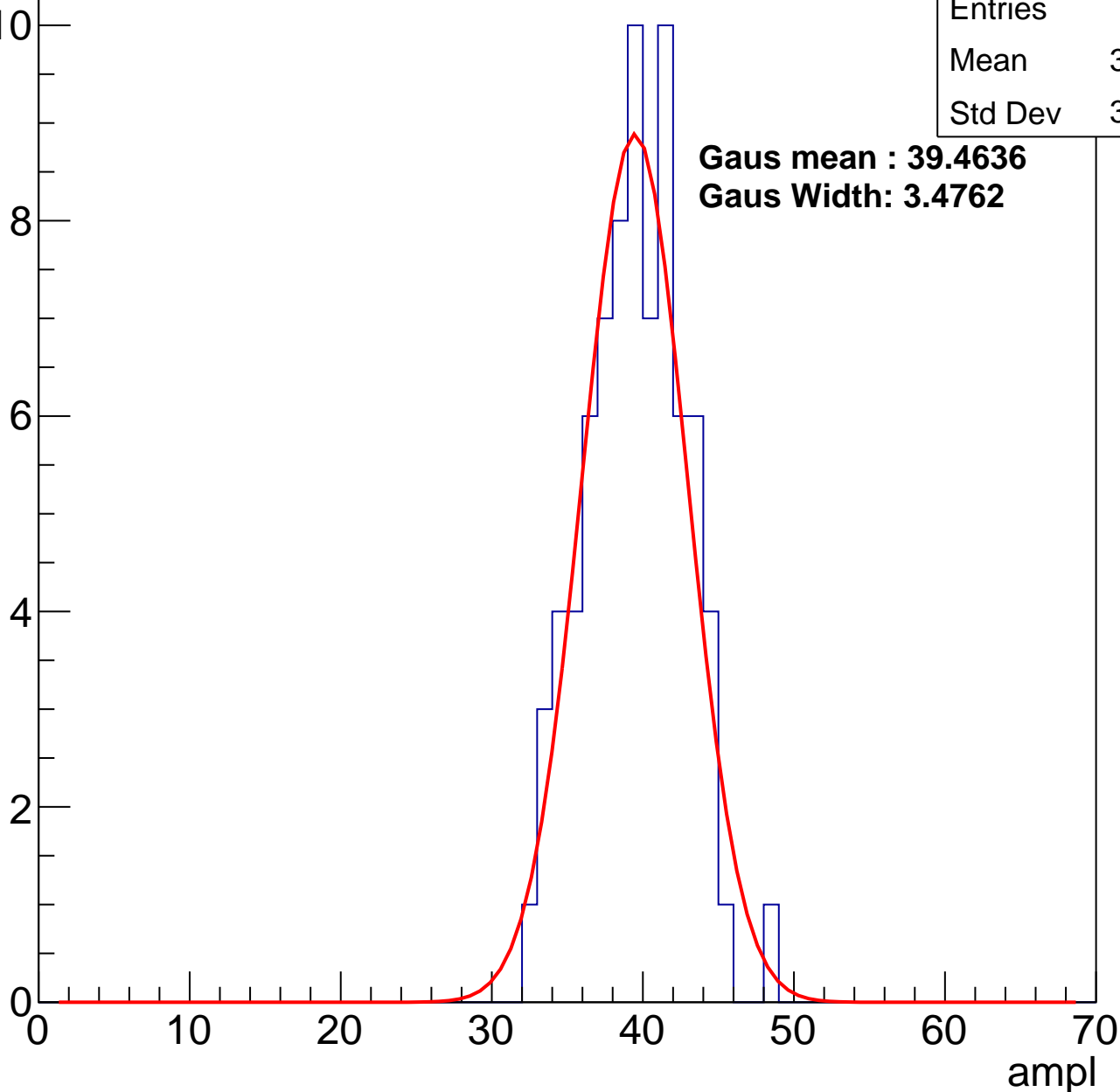
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	39.04
Std Dev	3.252

Gaus mean : 39.4636

Gaus Width: 3.4762



B1L100S, U12-ch39, adc2

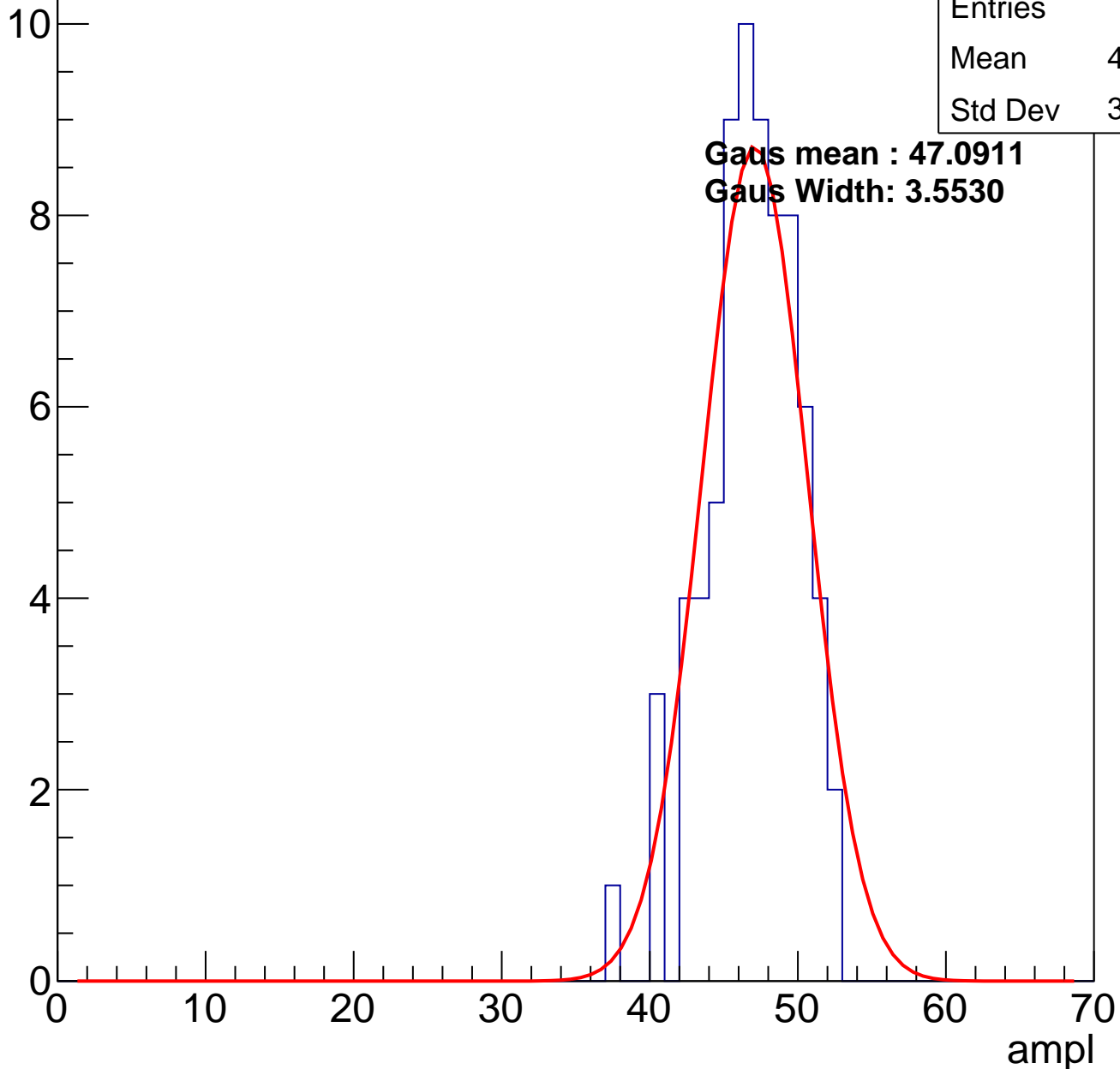
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	46.42
Std Dev	3.065

Gaus mean : 47.0911

Gaus Width: 3.5530

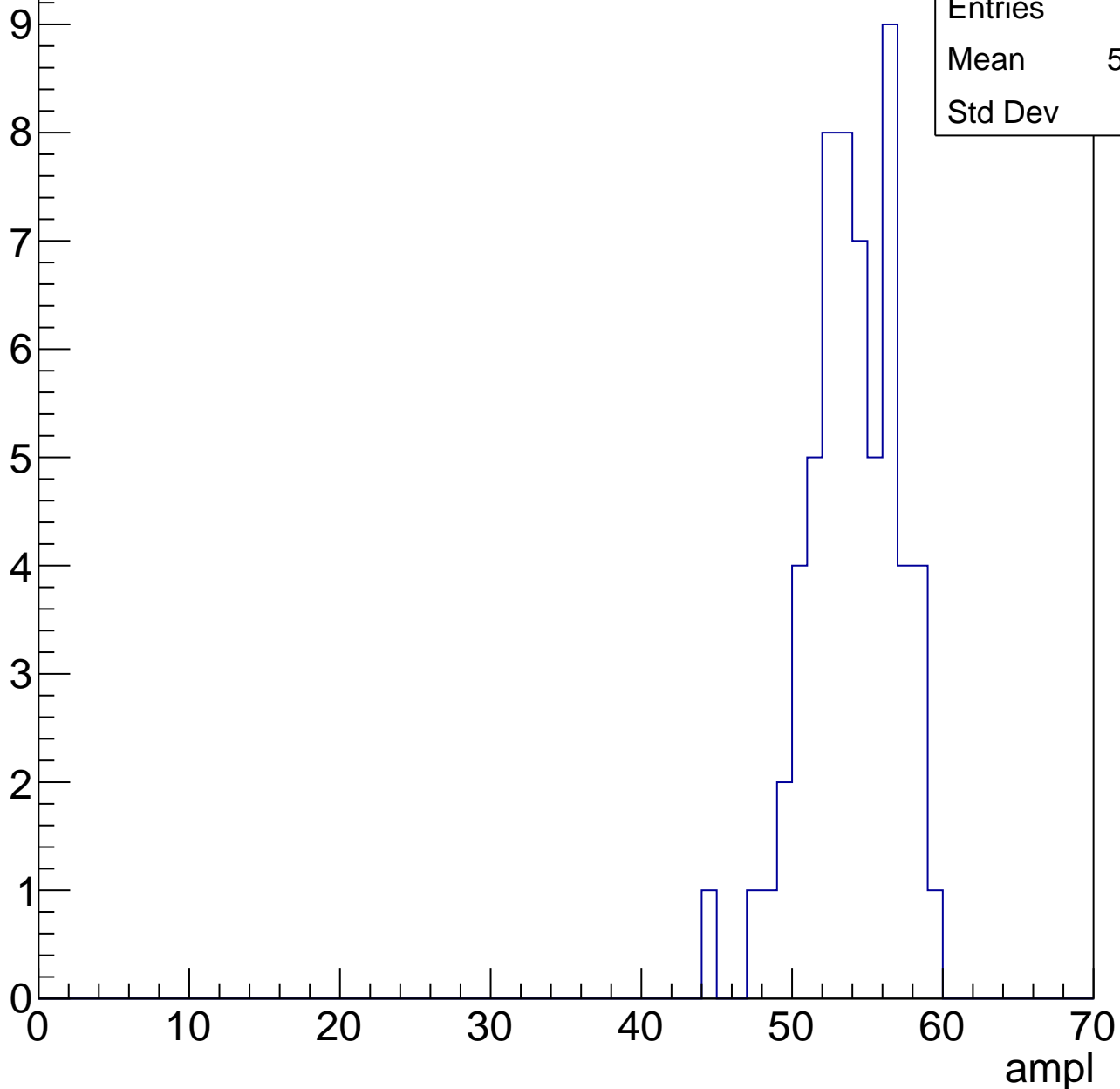
Entry



B1L100S, U12-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

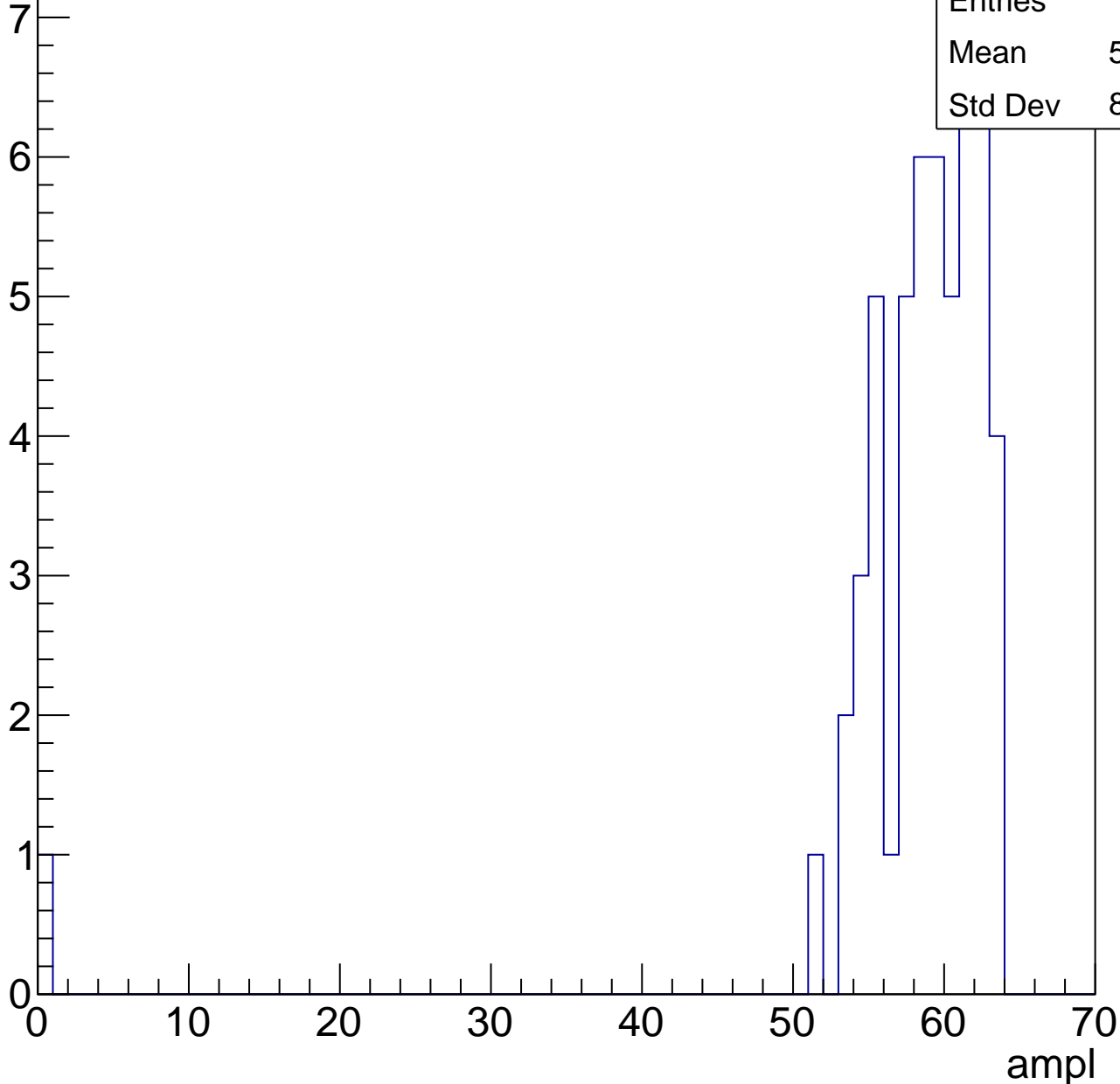


B1L100S, U12-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.55
Std Dev	8.529

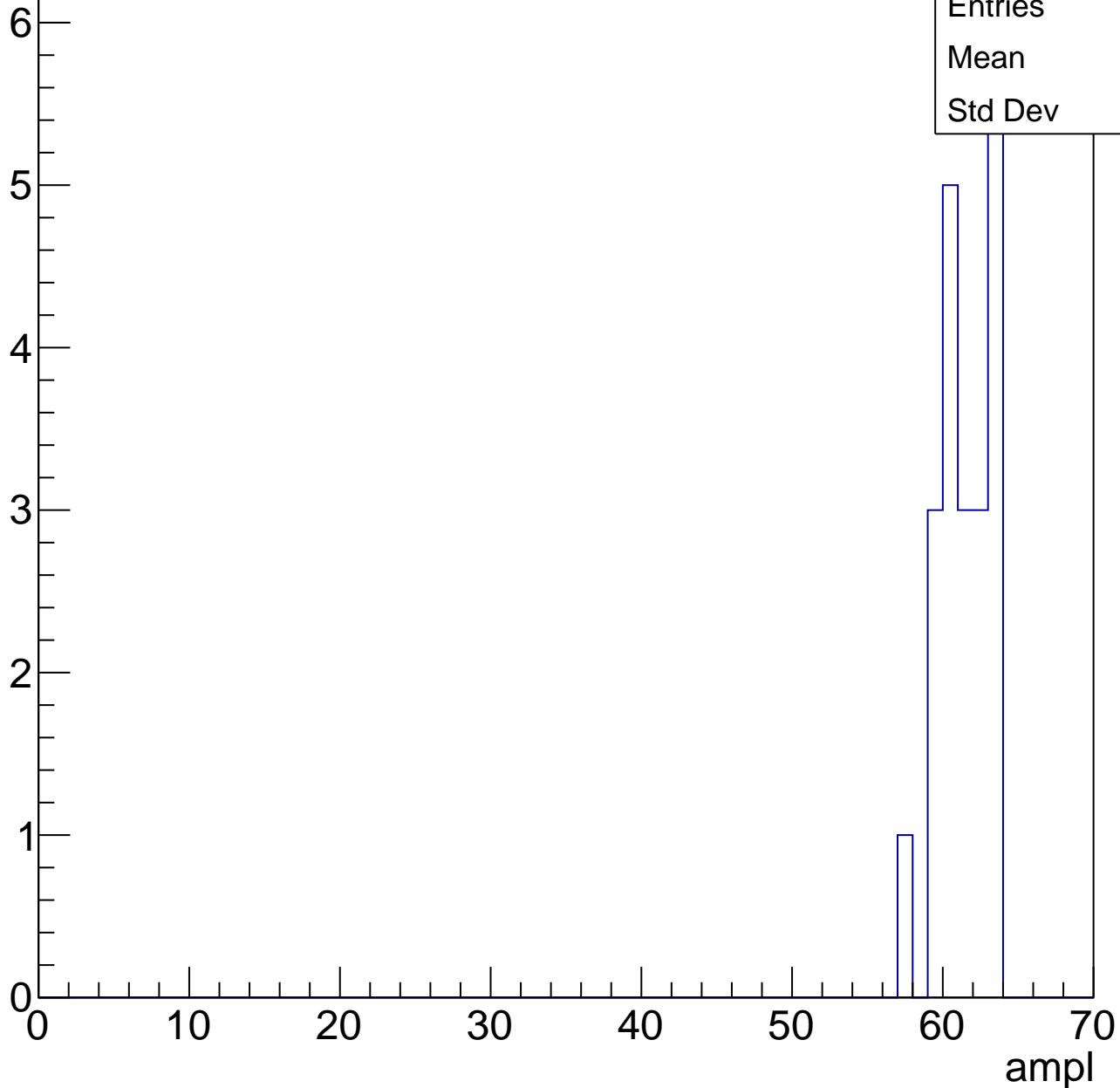


B1L100S, U12-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	61
Std Dev	1.69



B1L100S, U12-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

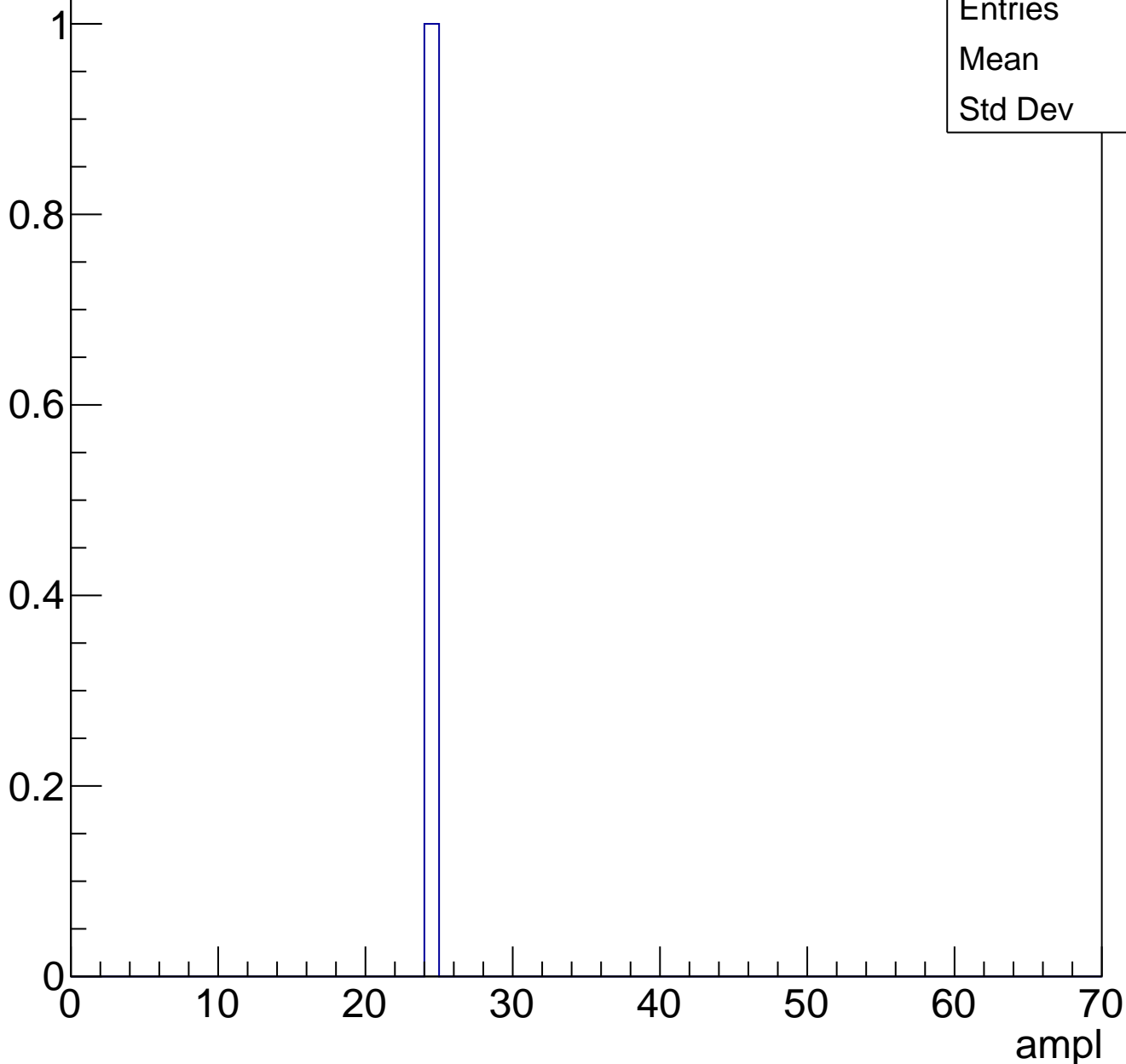


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	24
Std Dev	0

B1L100S, U12-ch40, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	31.13
Std Dev	5

Gaus mean : 32.0239

Gaus Width: 3.5162

10

8

6

4

2

0

0

10

20

30

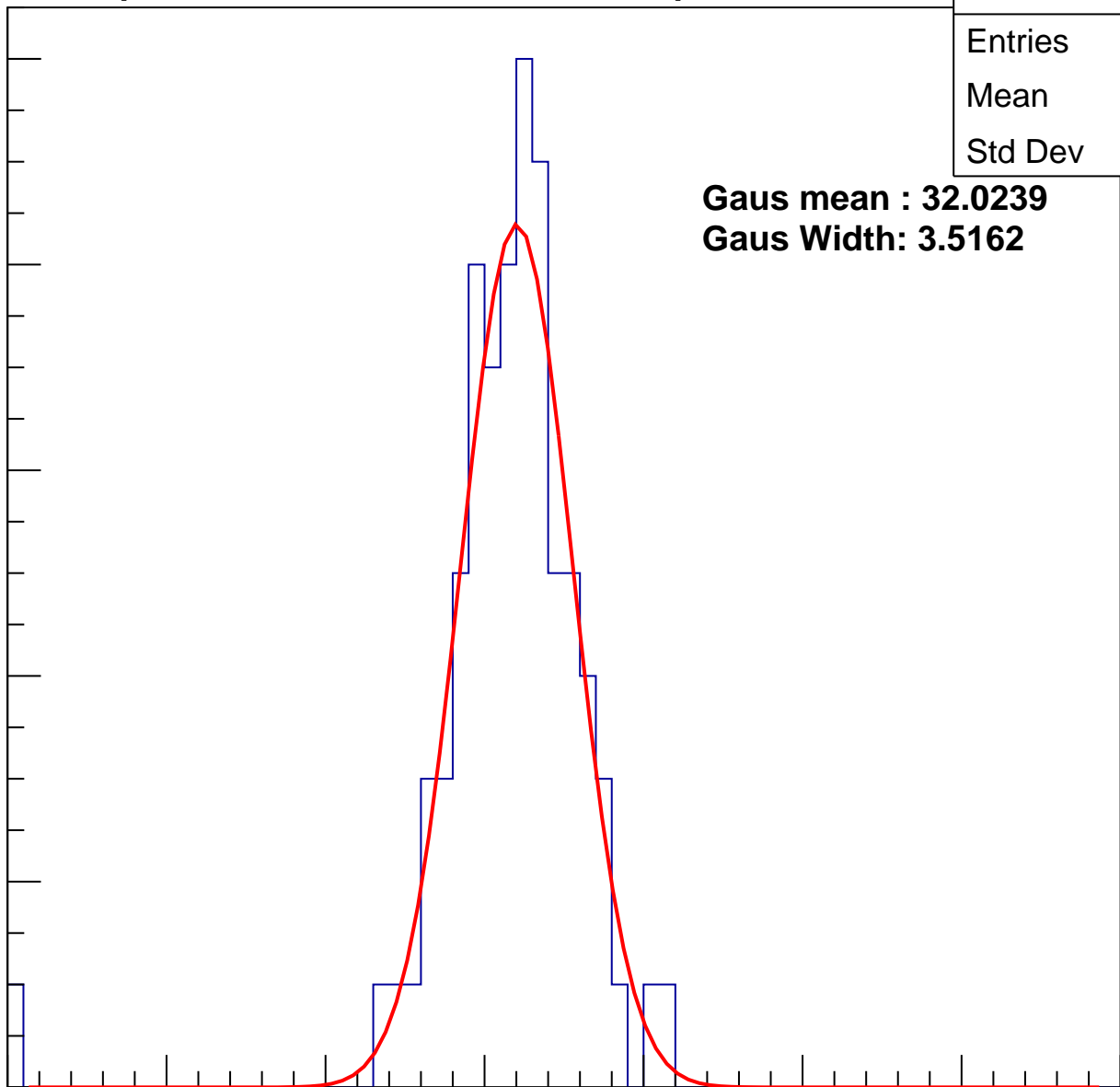
40

50

60

70

ampl



B1L100S, U12-ch40, adc1

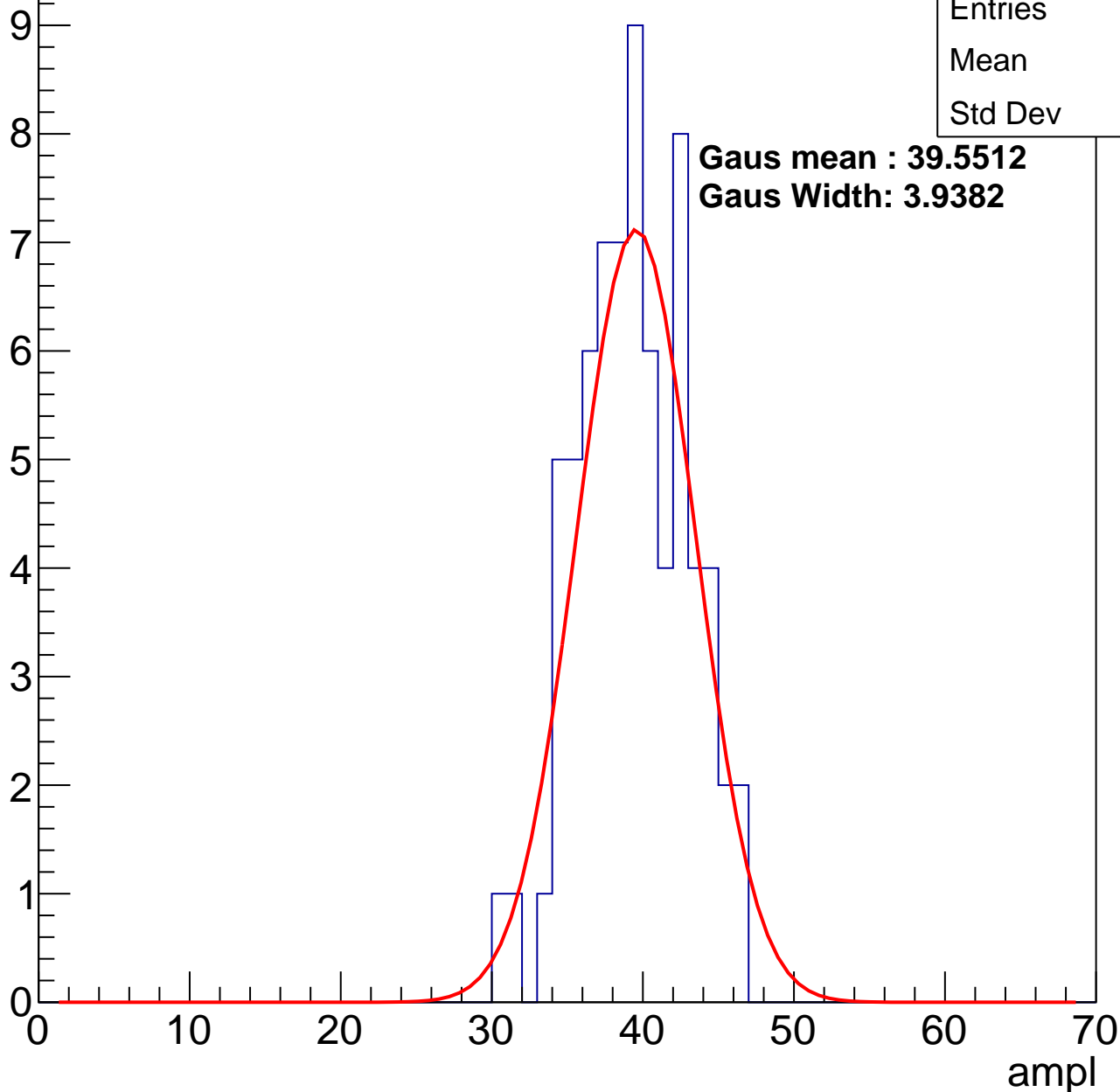
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.9
Std Dev	3.54

Gaus mean : 39.5512

Gaus Width: 3.9382



B1L100S, U12-ch40, adc2

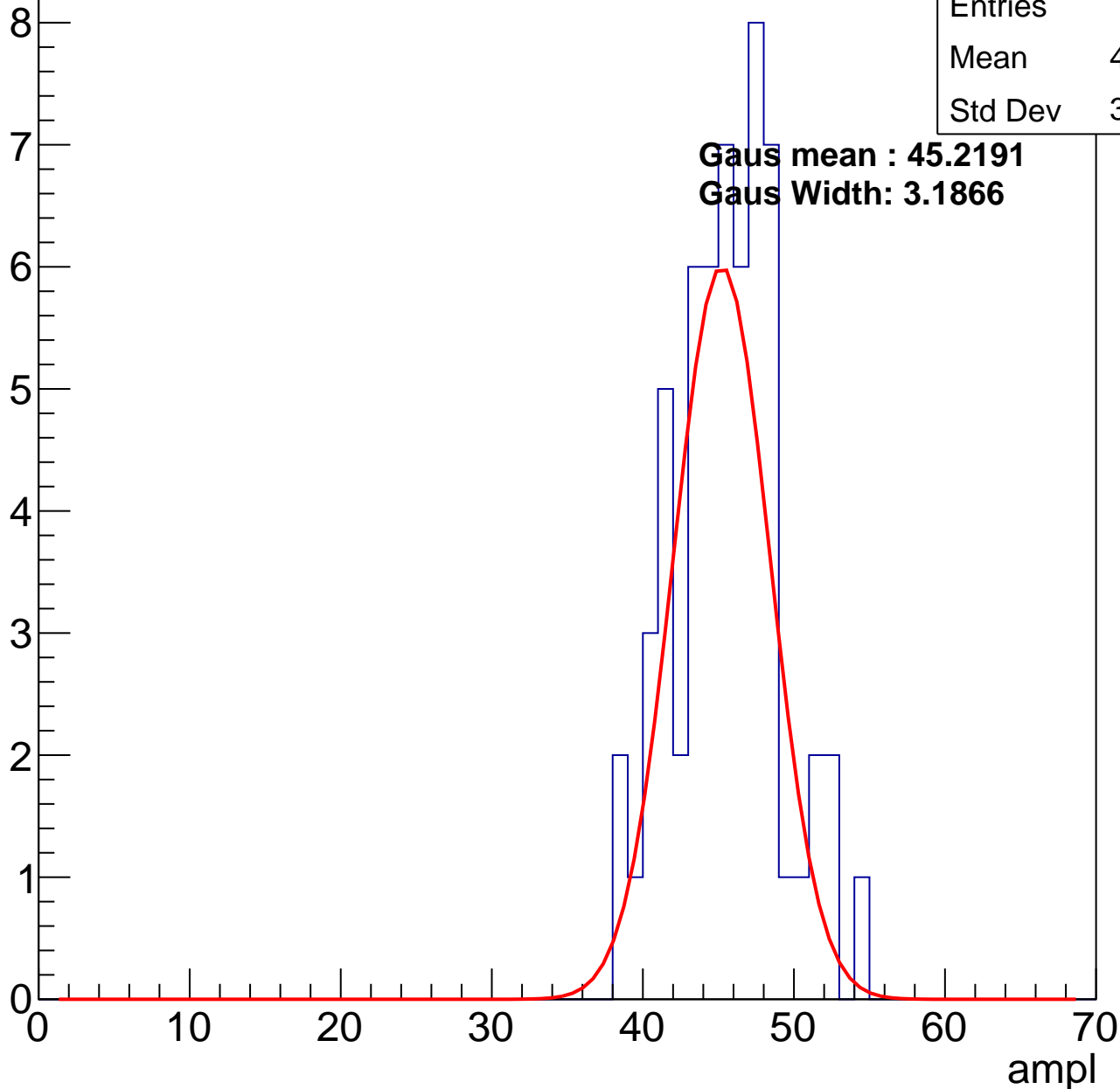
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	45.13
Std Dev	3.486

Gaus mean : 45.2191

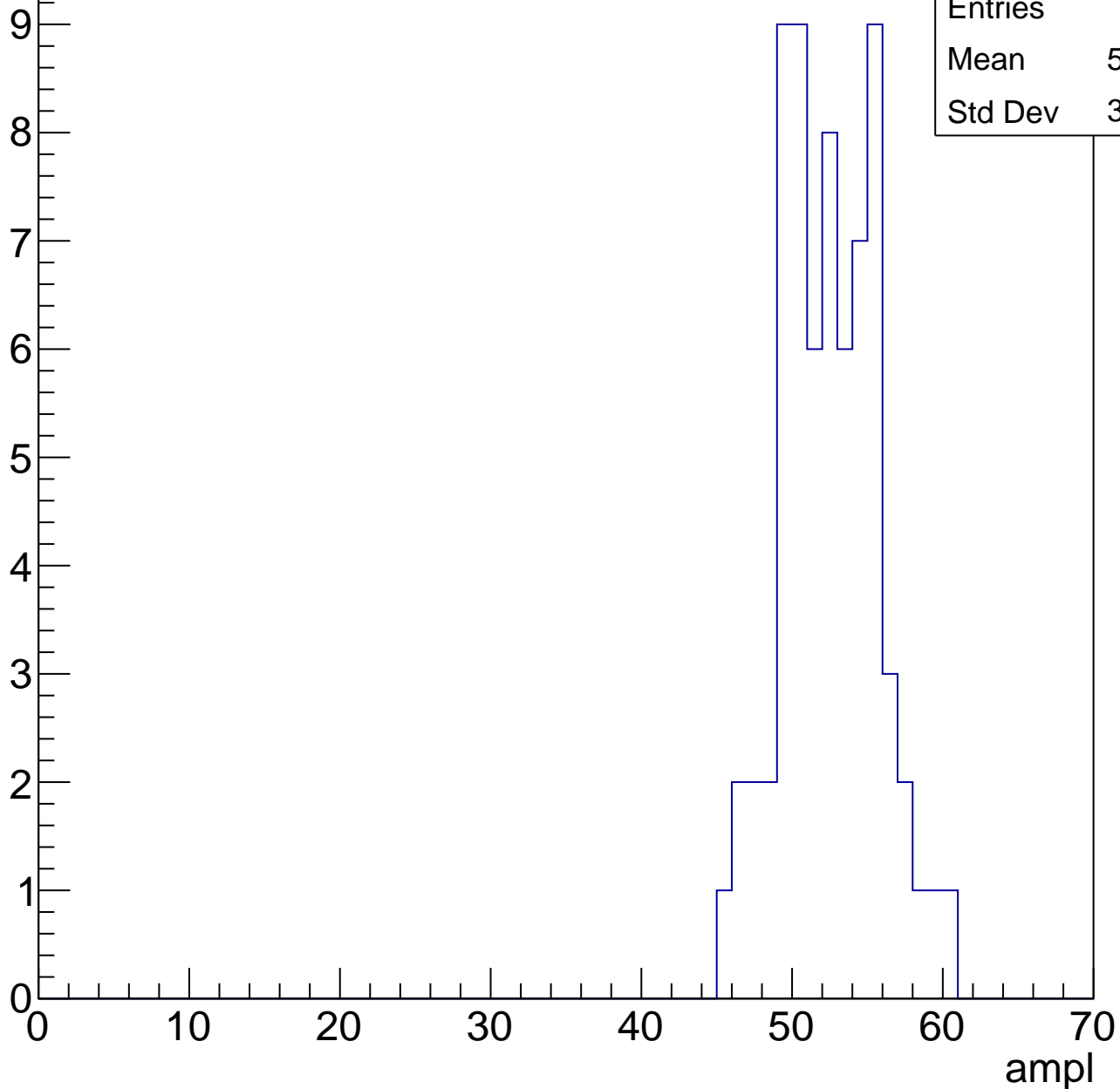
Gaus Width: 3.1866



B1L100S, U12-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

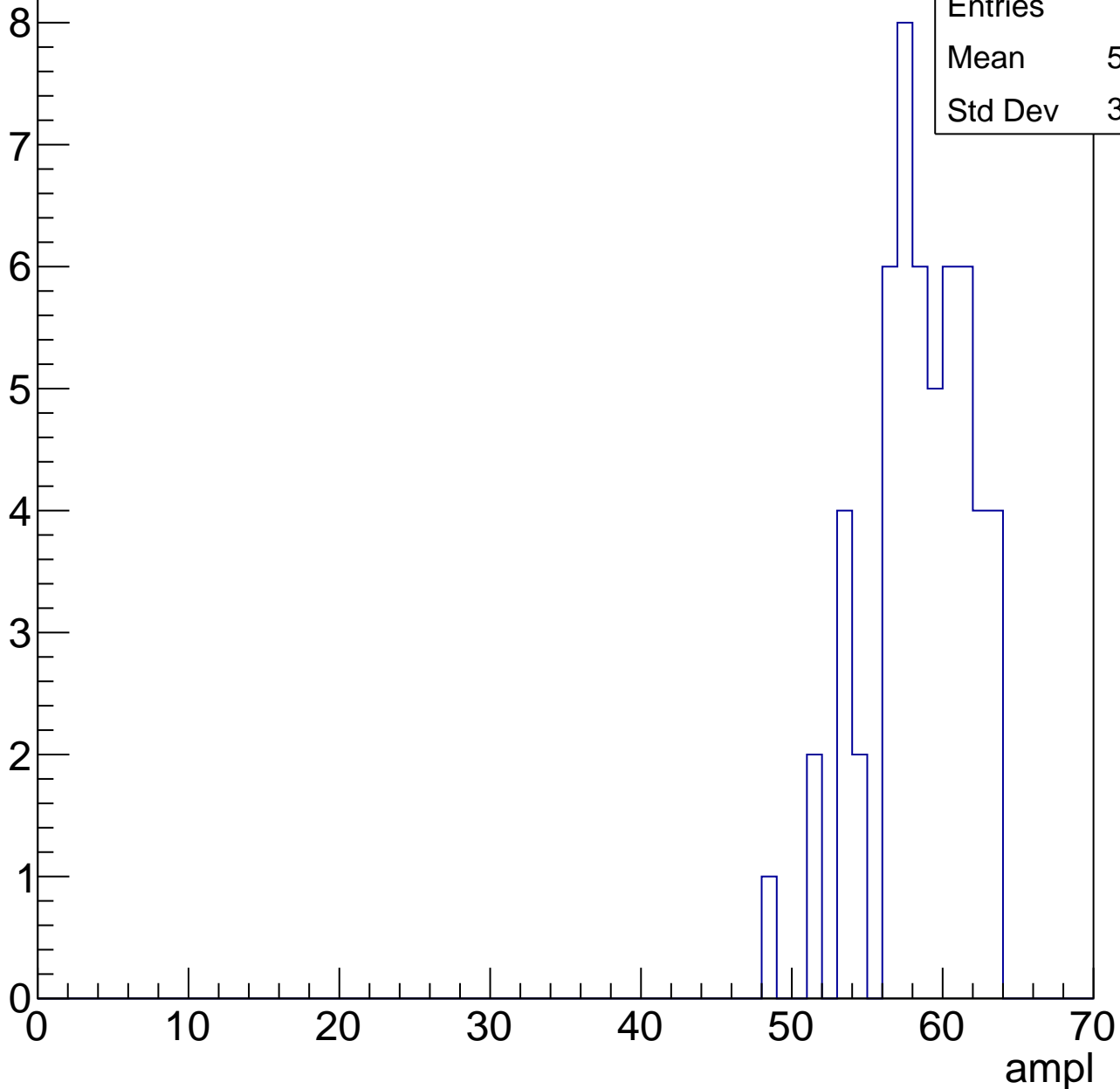


B1L100S, U12-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	57.98
Std Dev	3.358

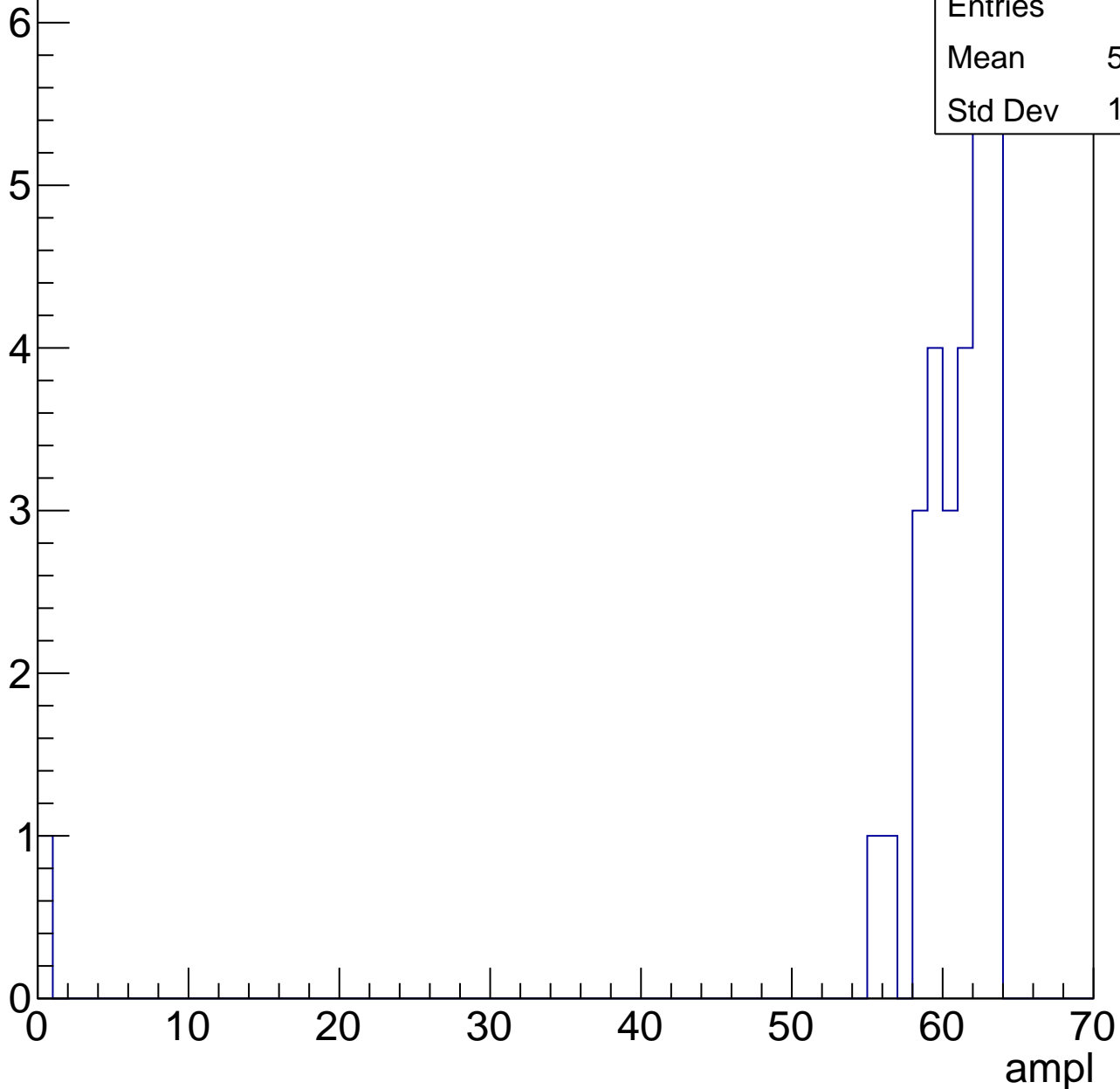


B1L100S, U12-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.45
Std Dev	11.25



B1L100S, U12-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

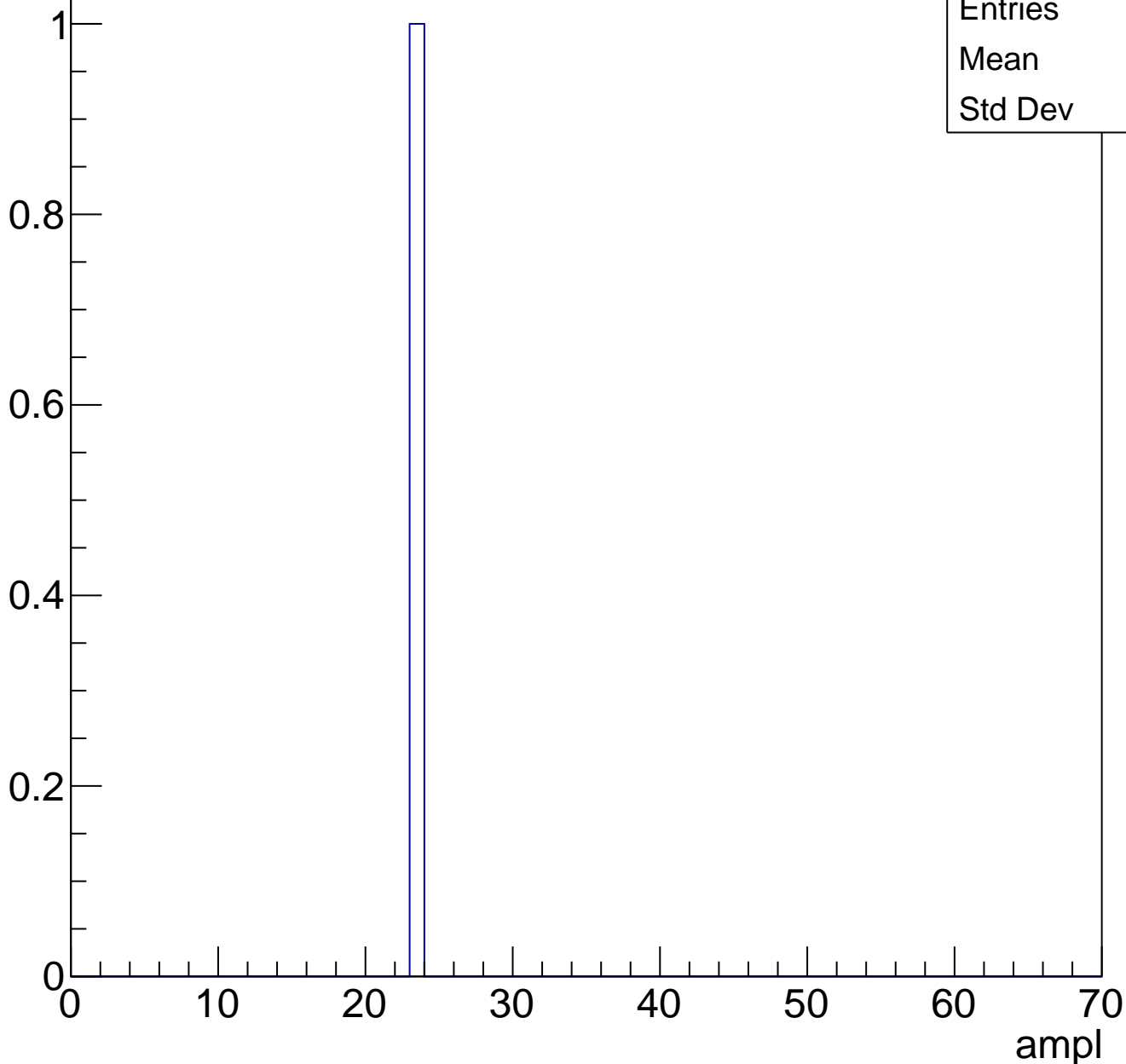


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch41, adc0

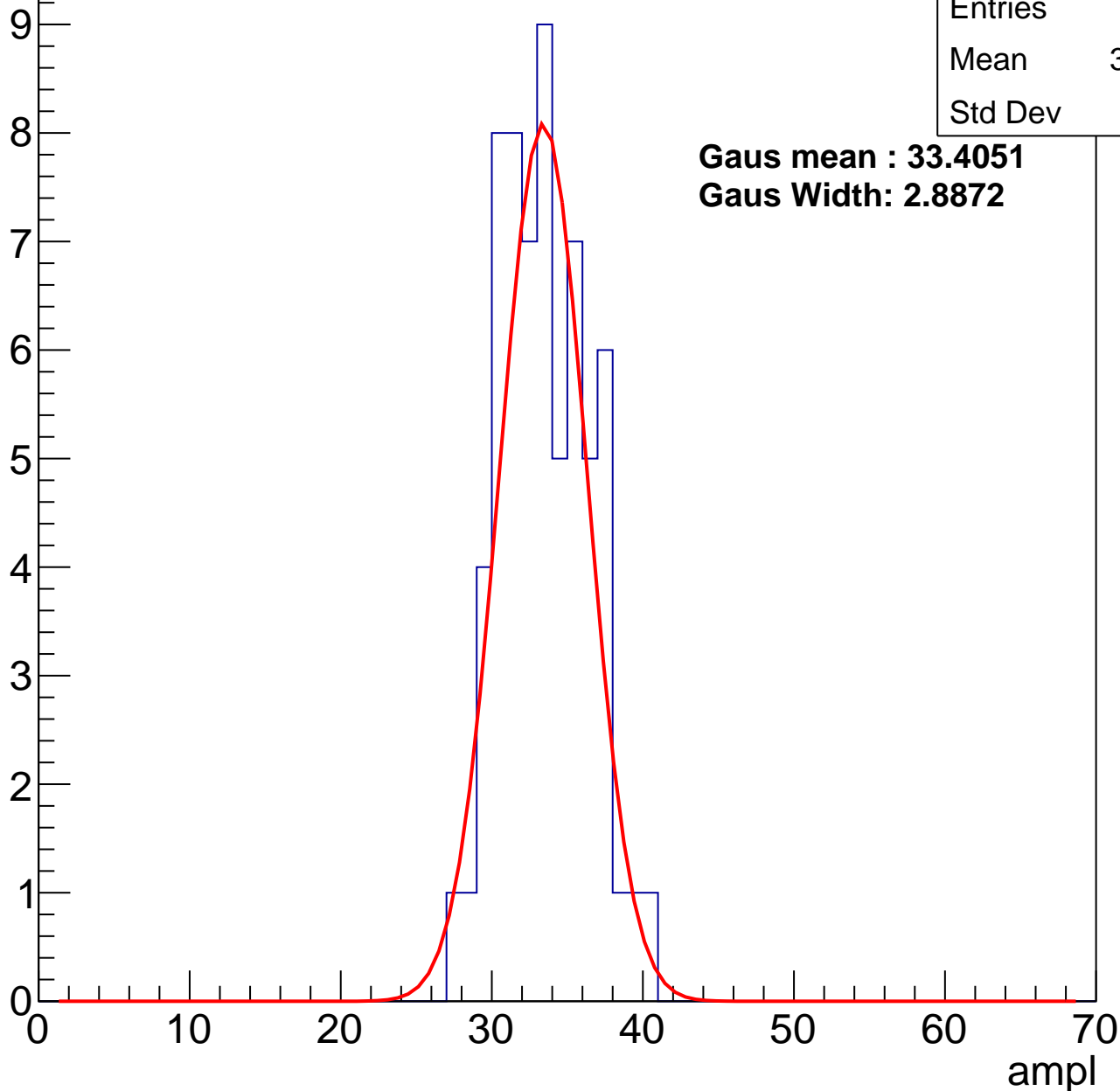
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	33.03
Std Dev	2.85

Gaus mean : 33.4051

Gaus Width: 2.8872



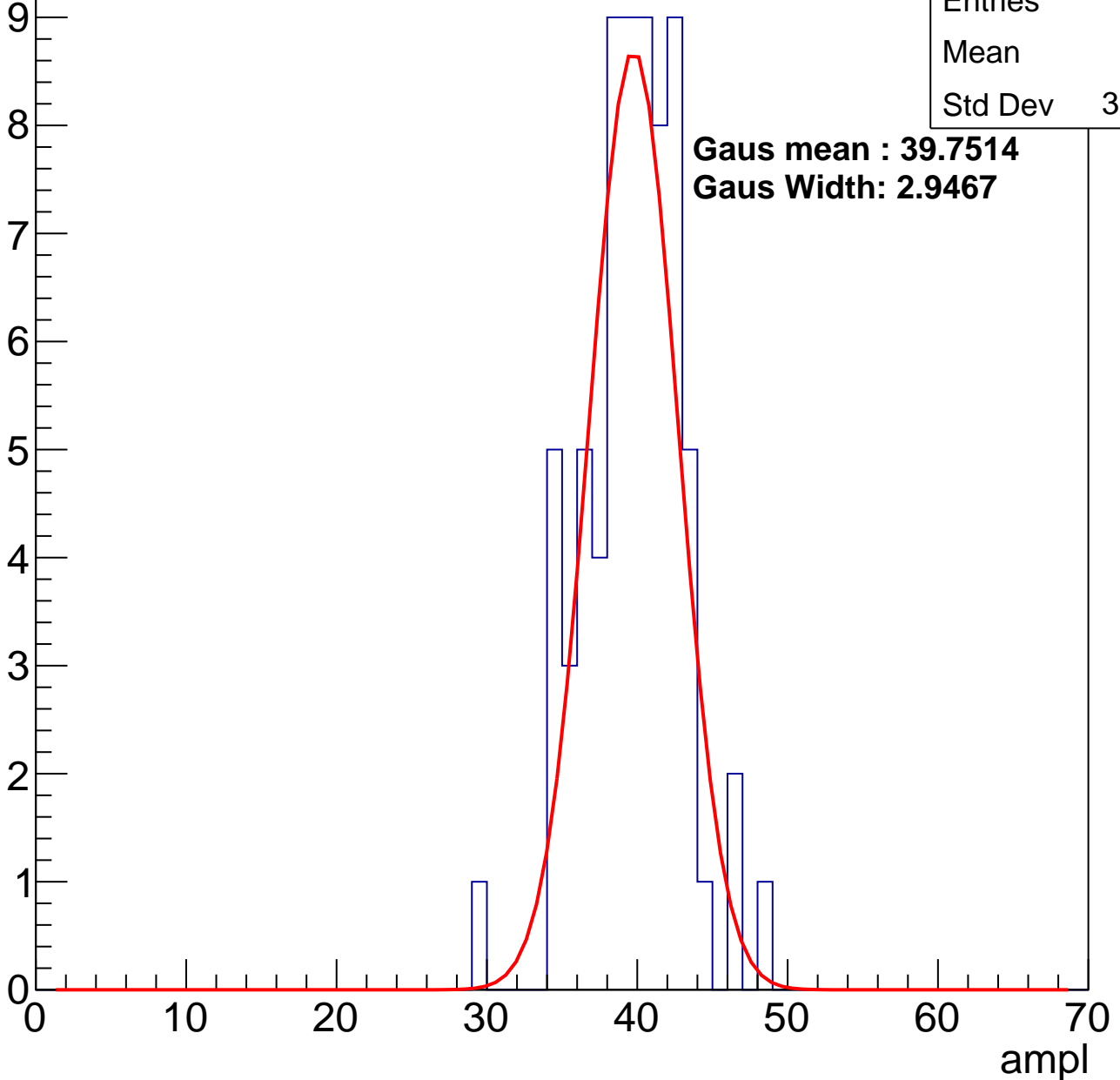
B1L100S, U12-ch41, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

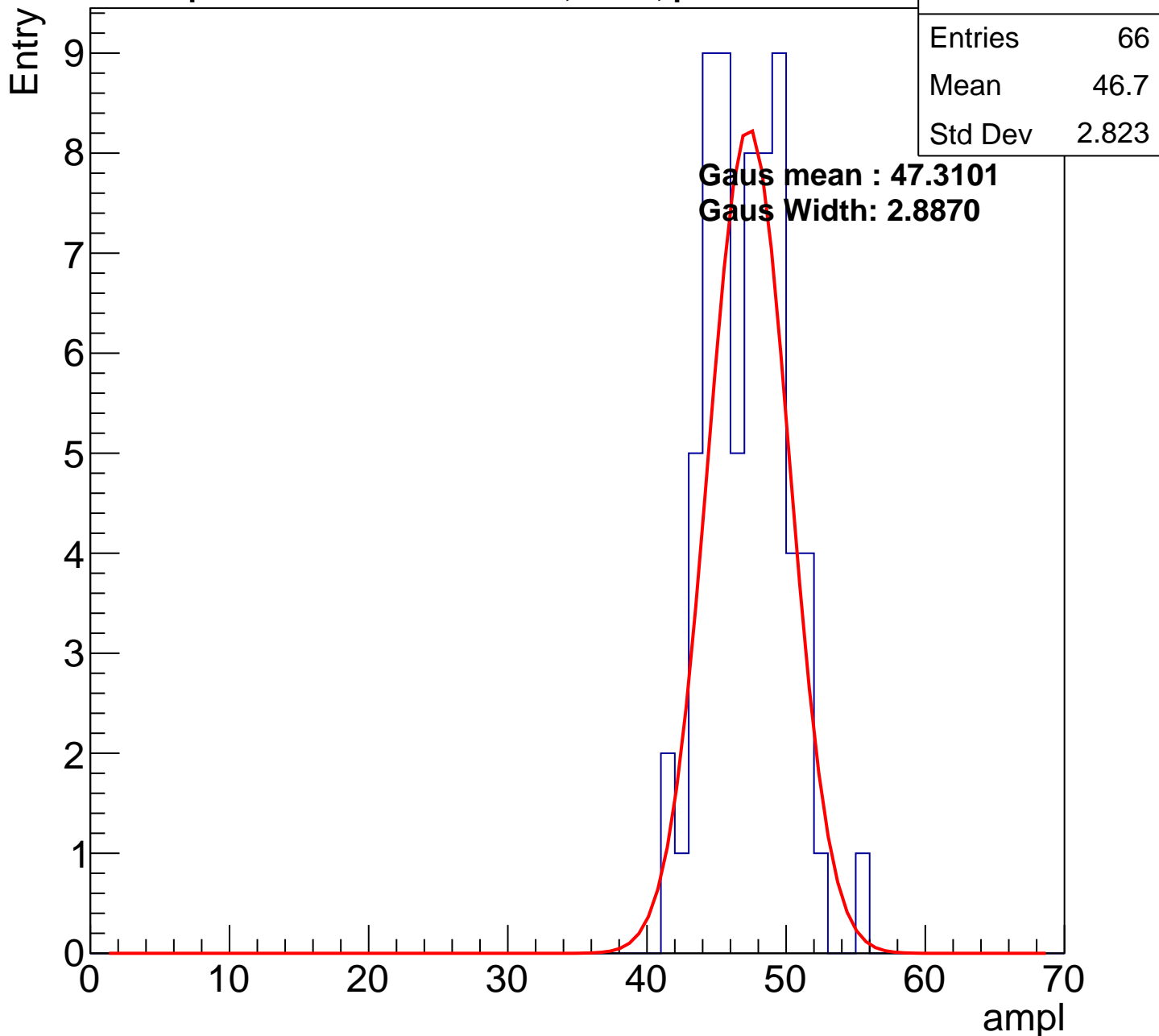
Entries	71
Mean	39.3
Std Dev	3.234

Gaus mean : 39.7514
Gaus Width: 2.9467



B1L100S, U12-ch41, adc2

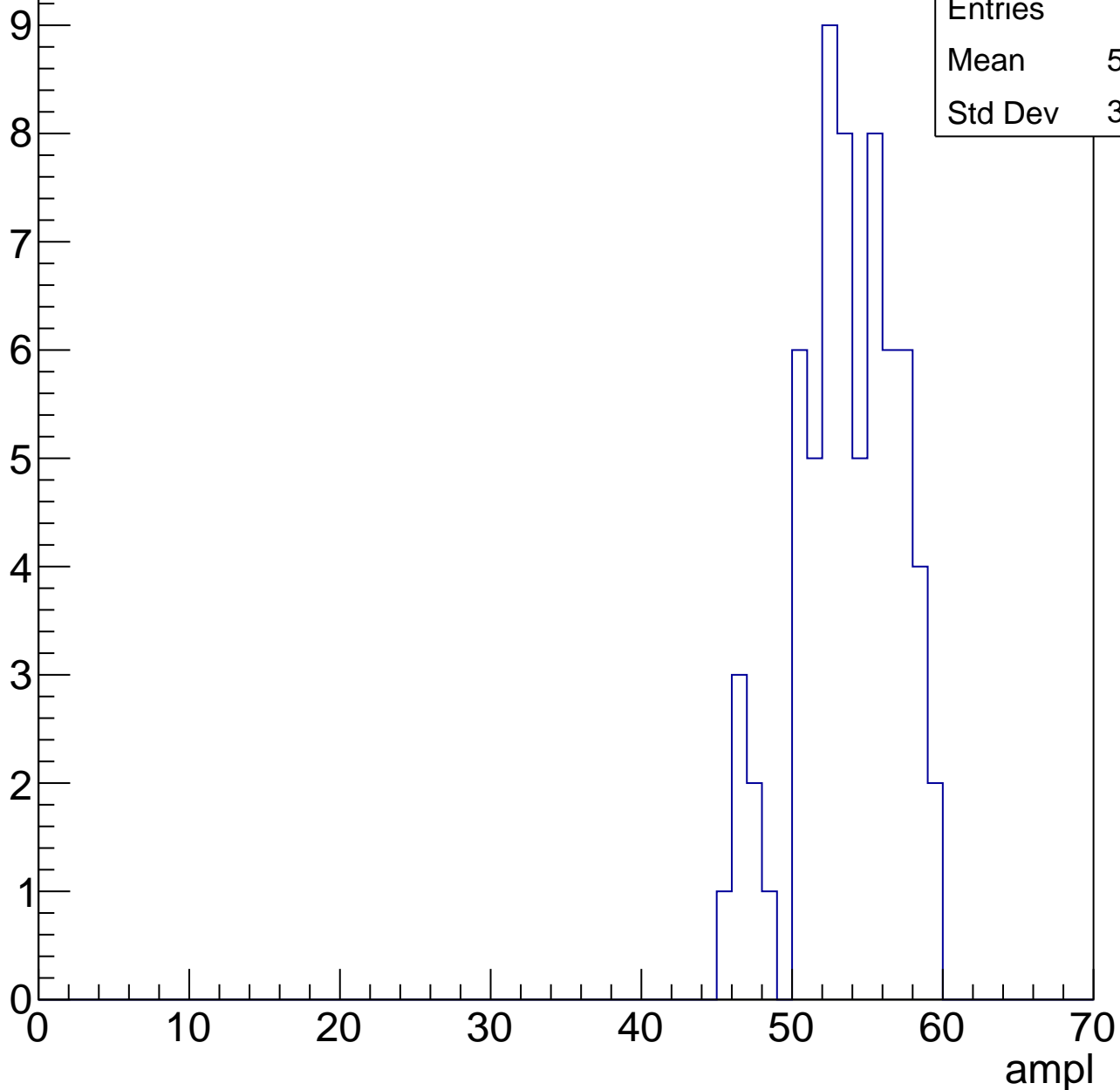
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	66
Mean	53.18
Std Dev	3.366

B1L100S, U12-ch41, adc4

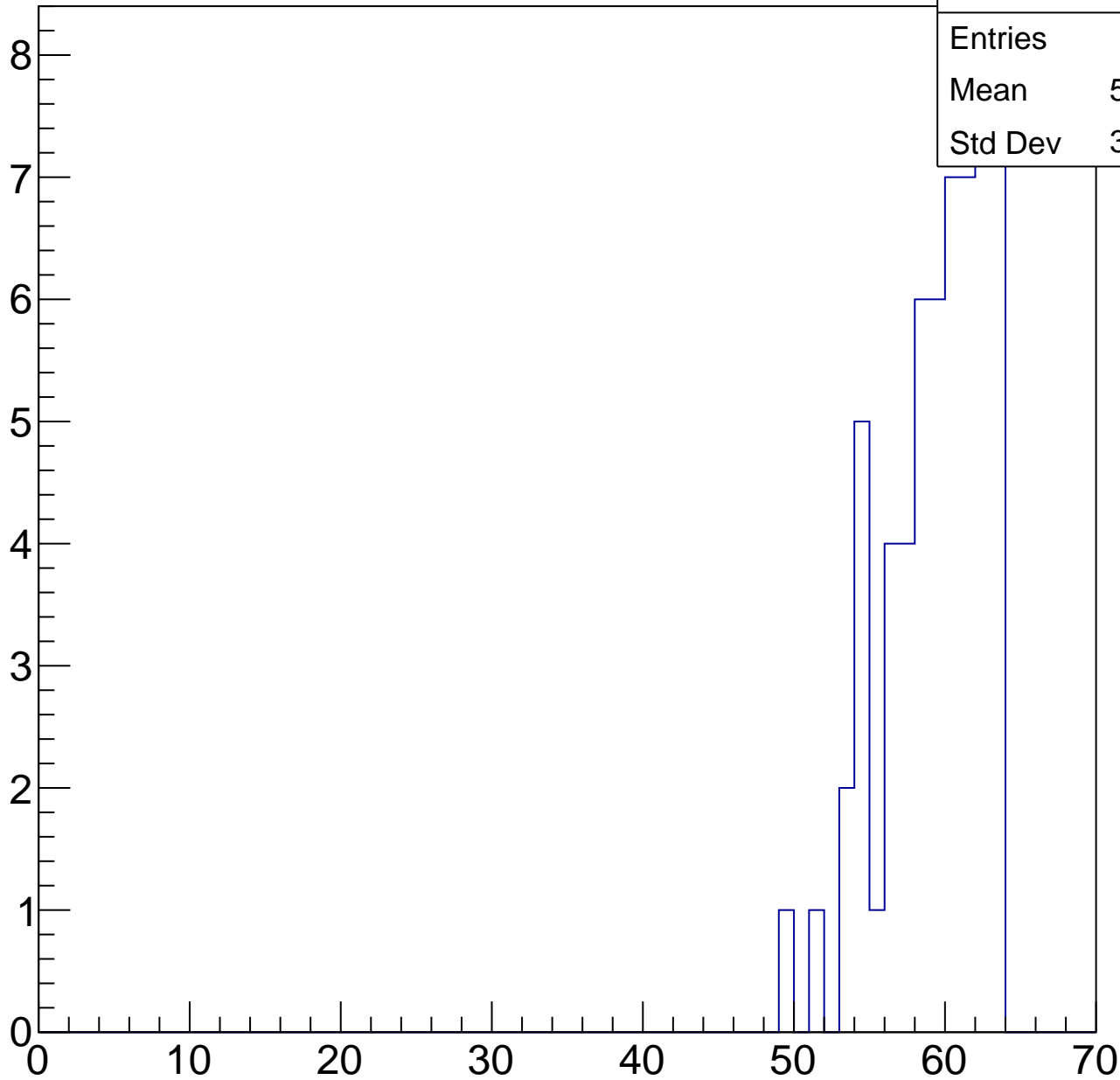
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.87
Std Dev	3.339

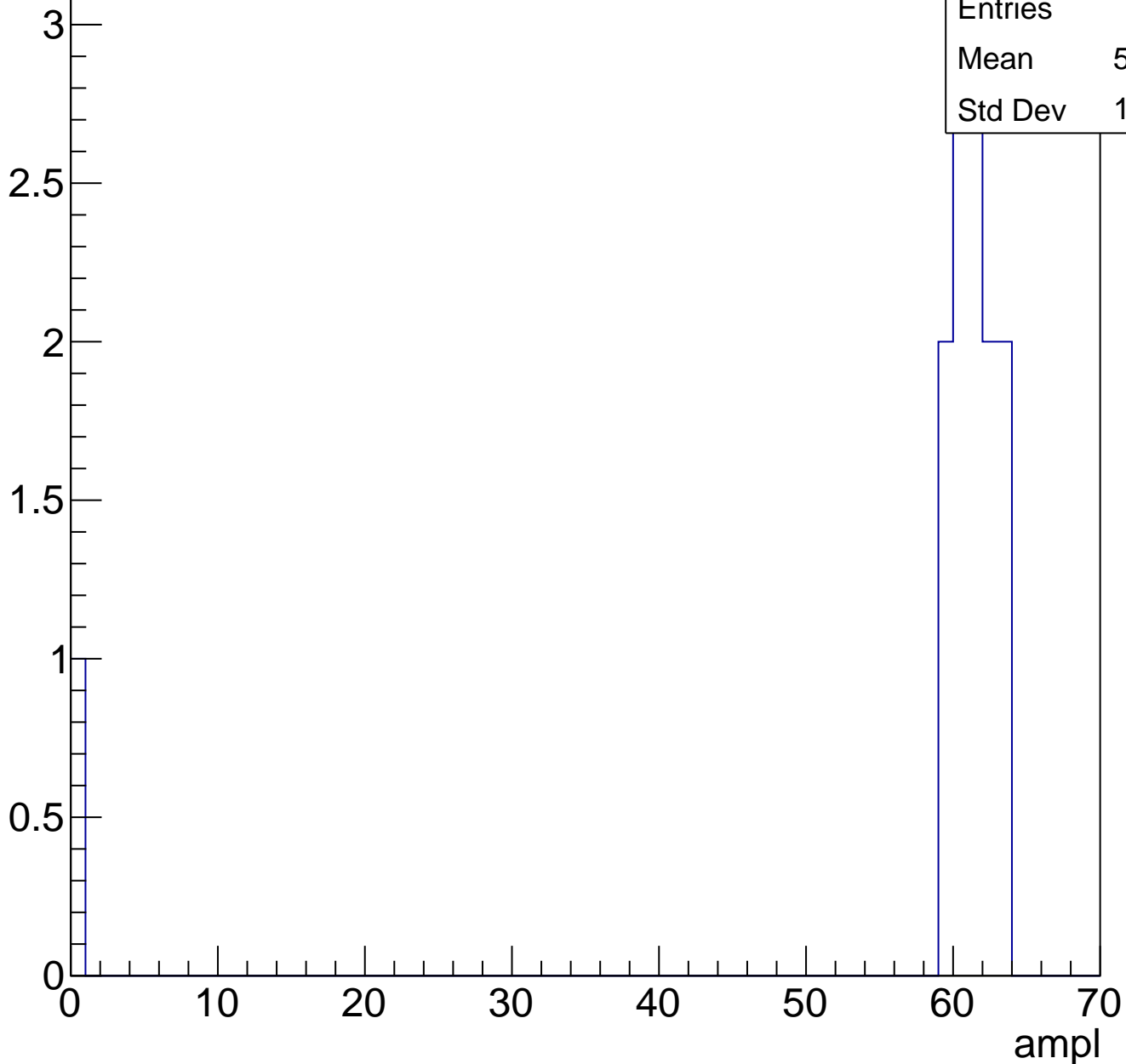
ampl



B1L100S, U12-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

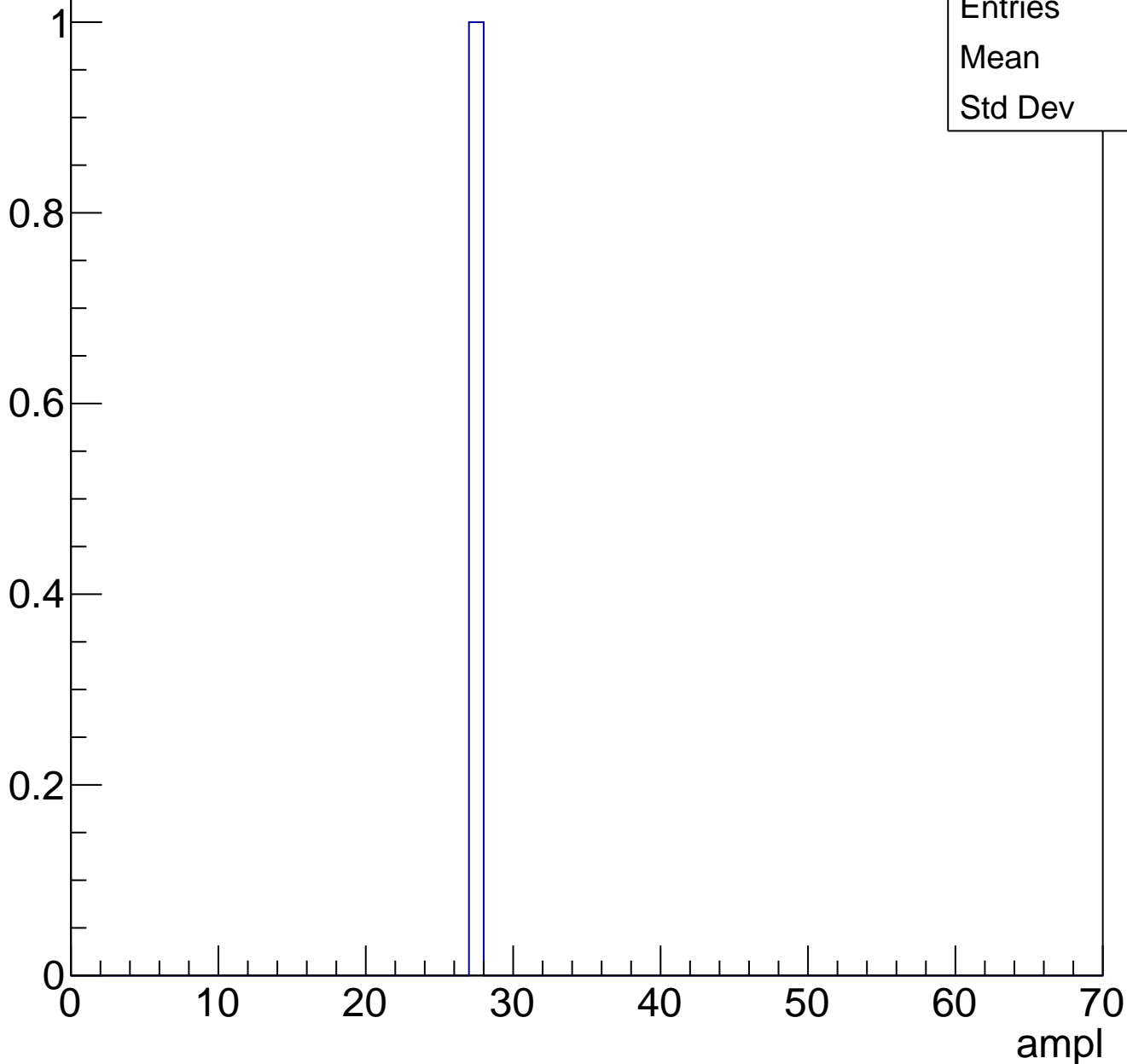


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch42, adc0

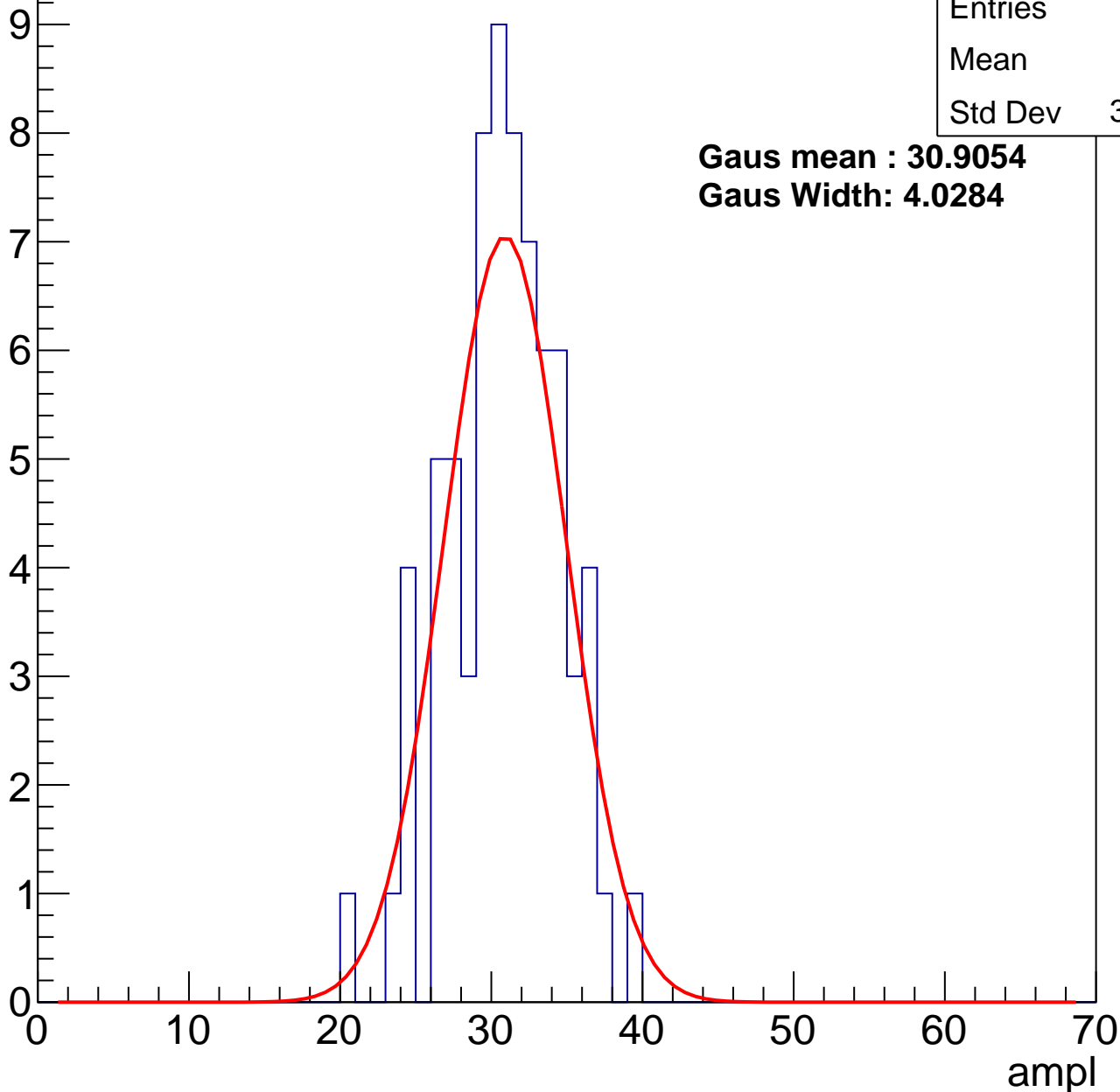
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.4
Std Dev	3.654

Gaus mean : 30.9054

Gaus Width: 4.0284



B1L100S, U12-ch42, adc1

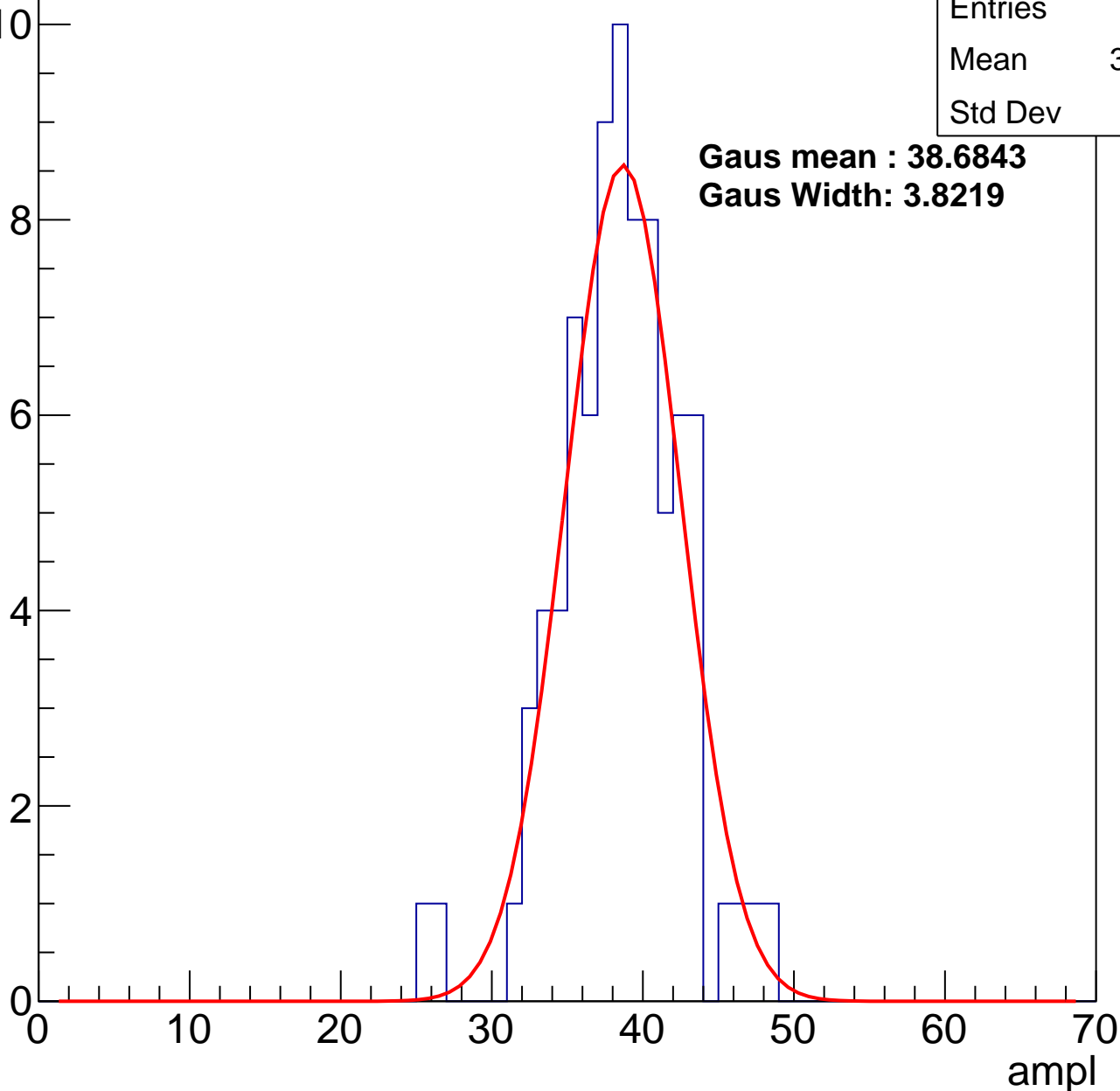
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	37.99
Std Dev	4.04

Gaus mean : 38.6843

Gaus Width: 3.8219



B1L100S, U12-ch42, adc2

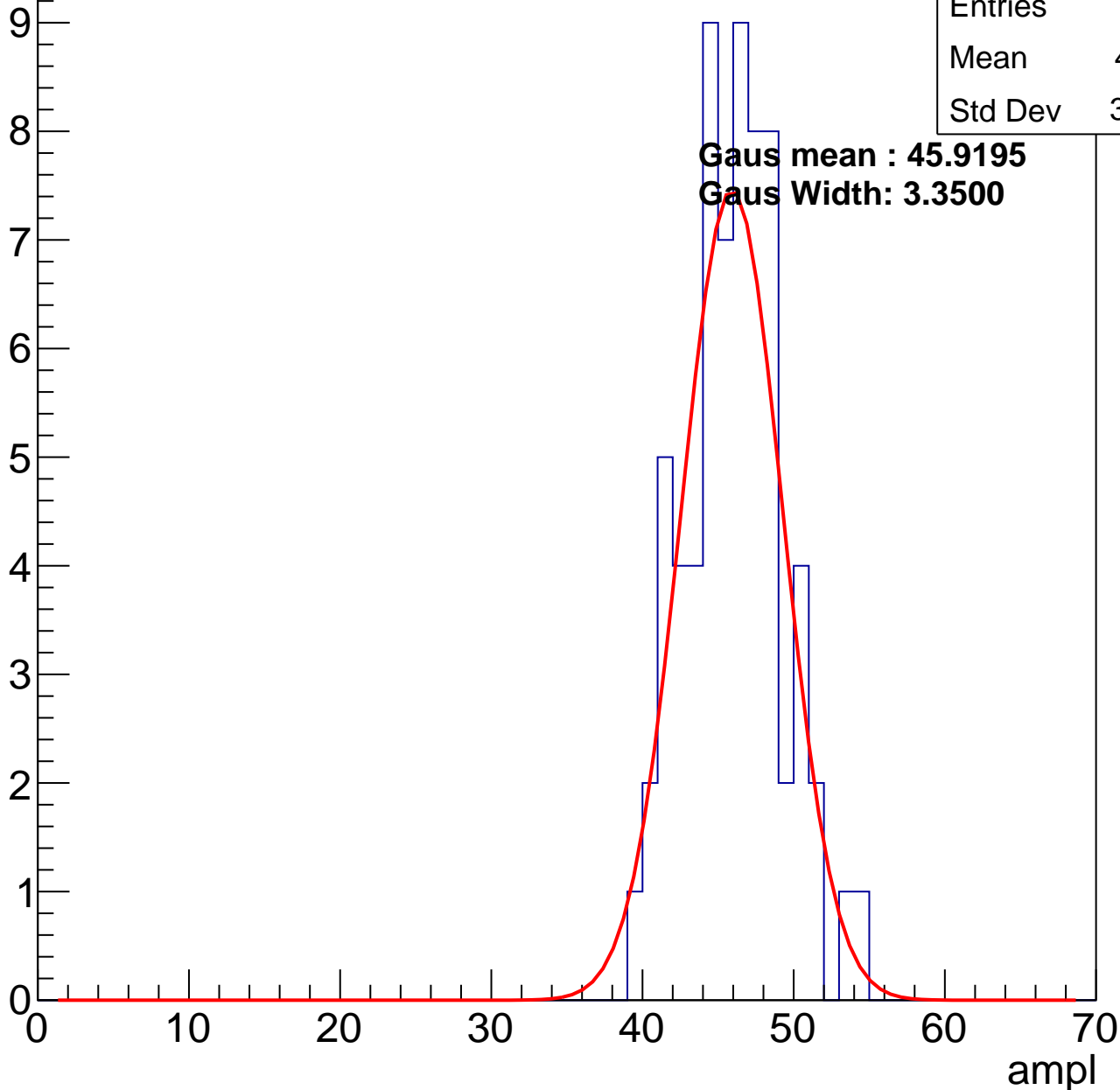
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	45.61
Std Dev	3.148

Gaus mean : 45.9195

Gaus Width: 3.3500

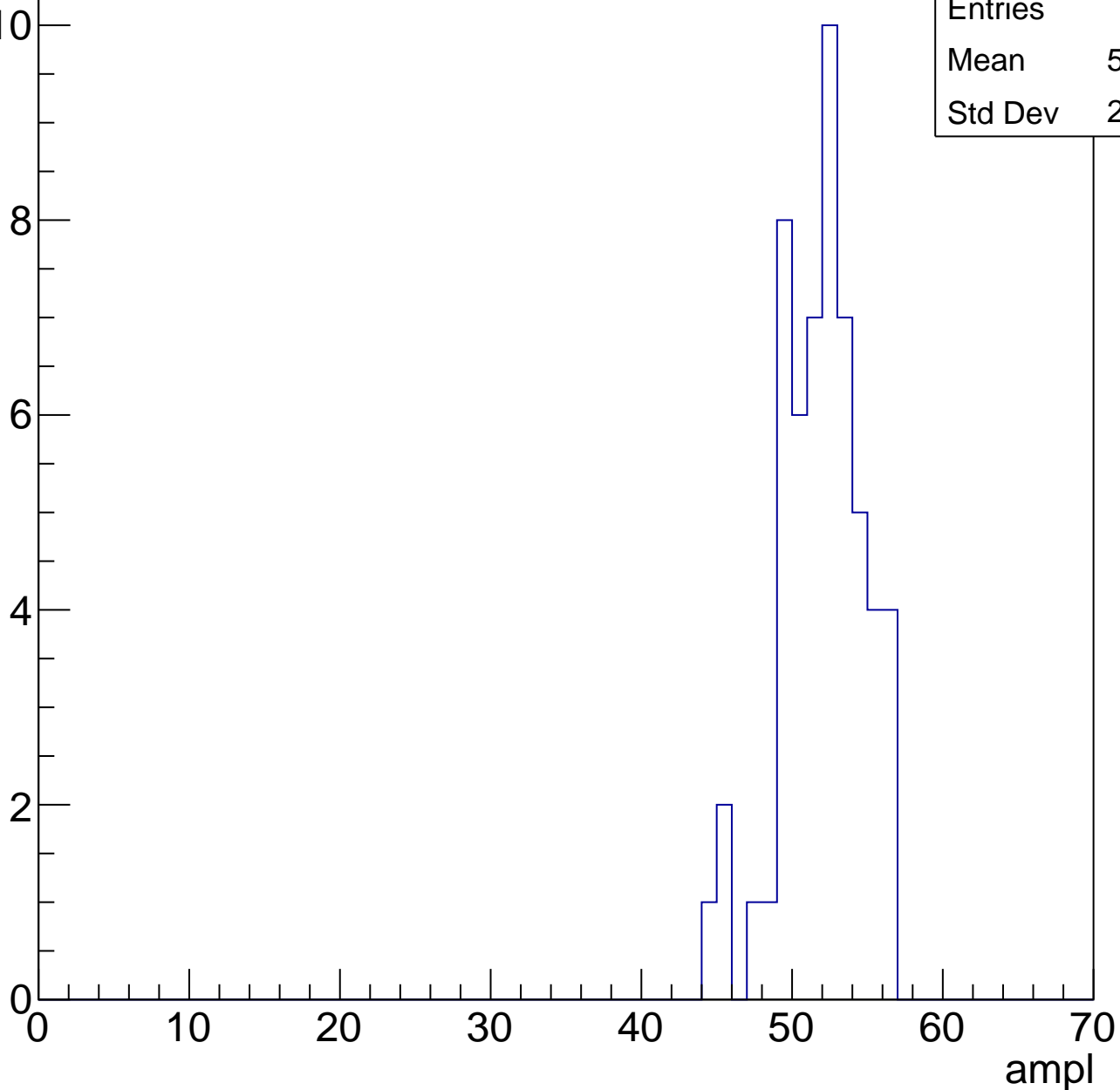


B1L100S, U12-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

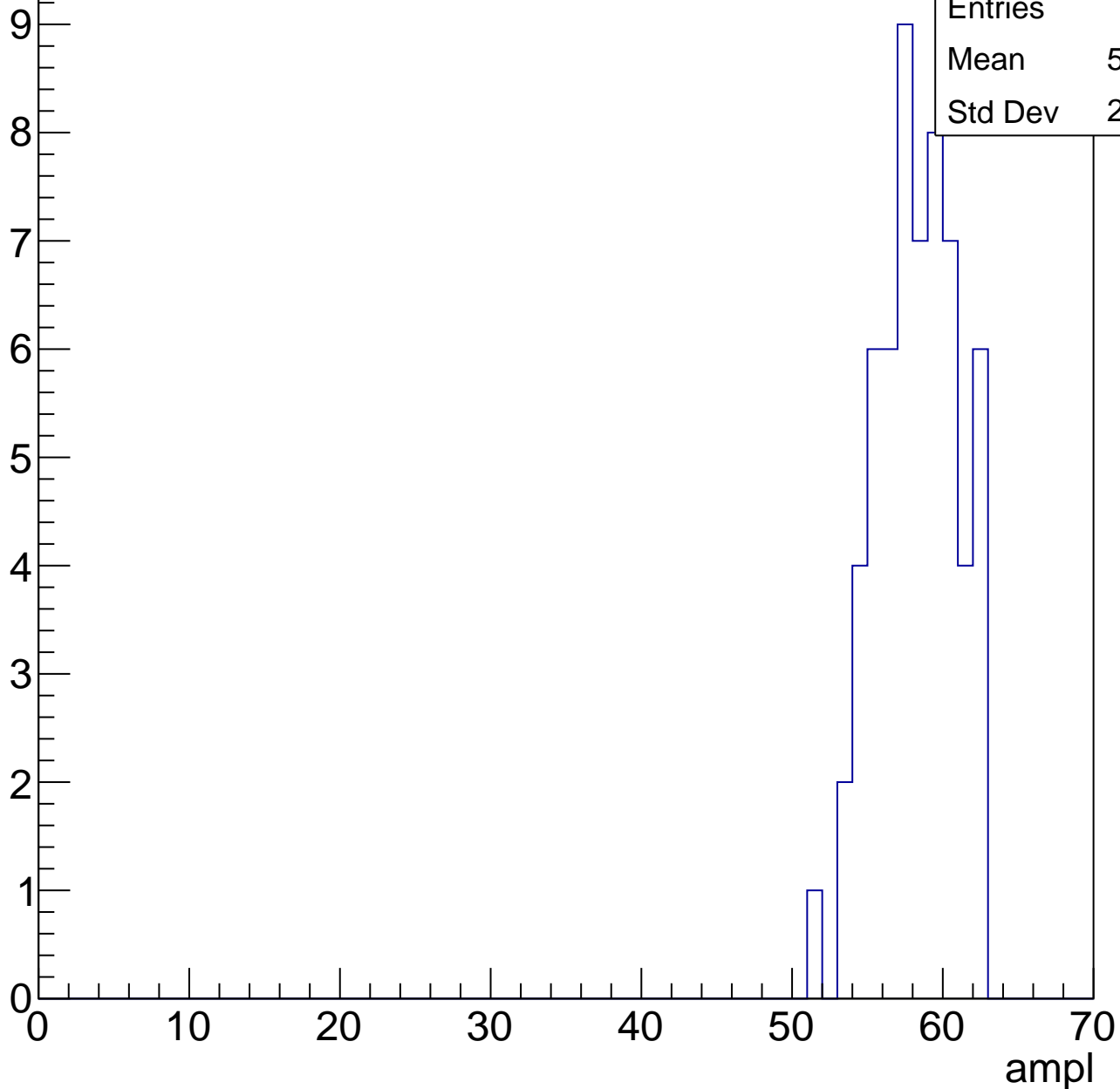
Entries	56
Mean	51.48
Std Dev	2.732



B1L100S, U12-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

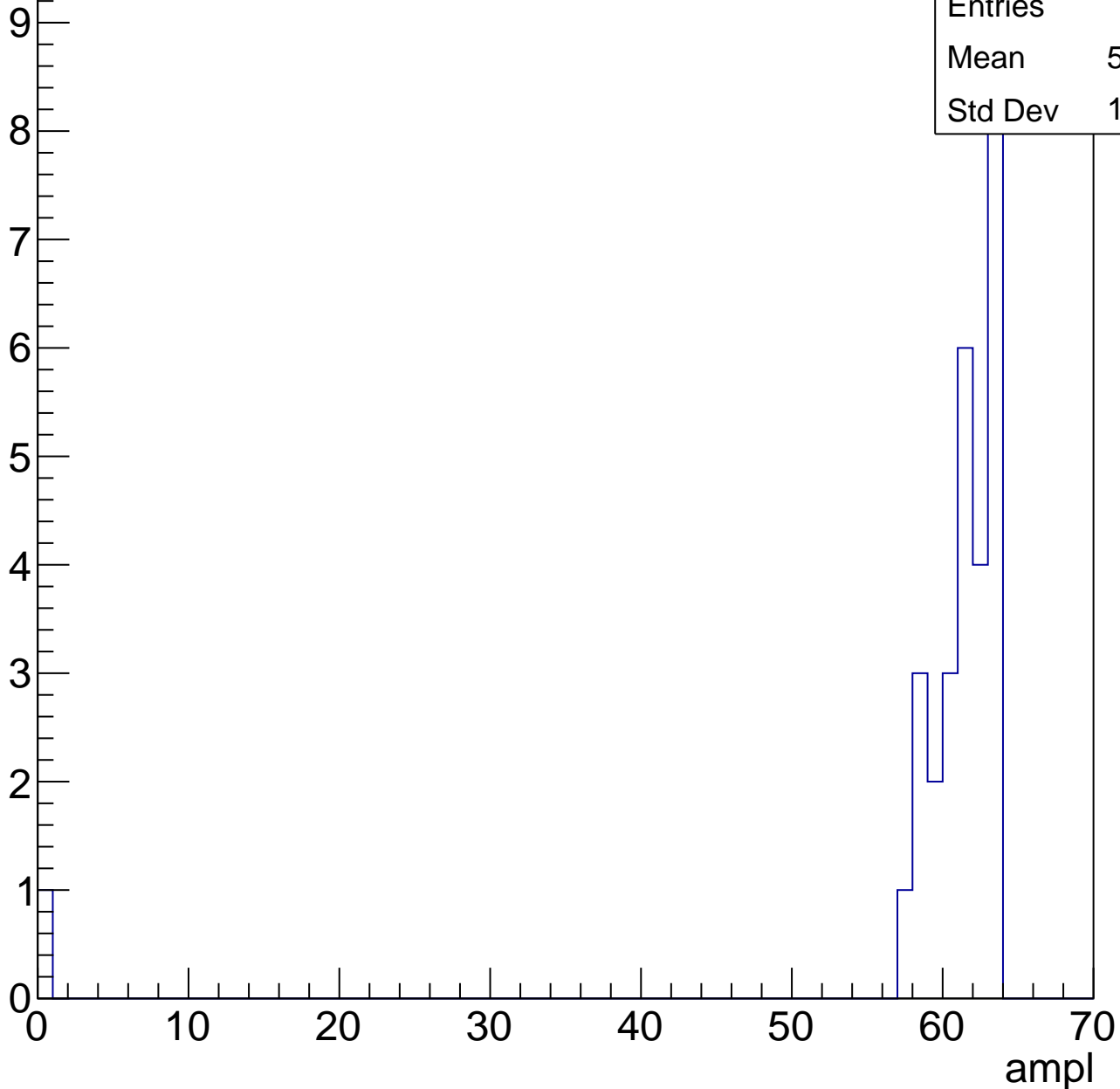


B1L100S, U12-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.97
Std Dev	11.29



B1L100S, U12-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U12-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch43, adc0

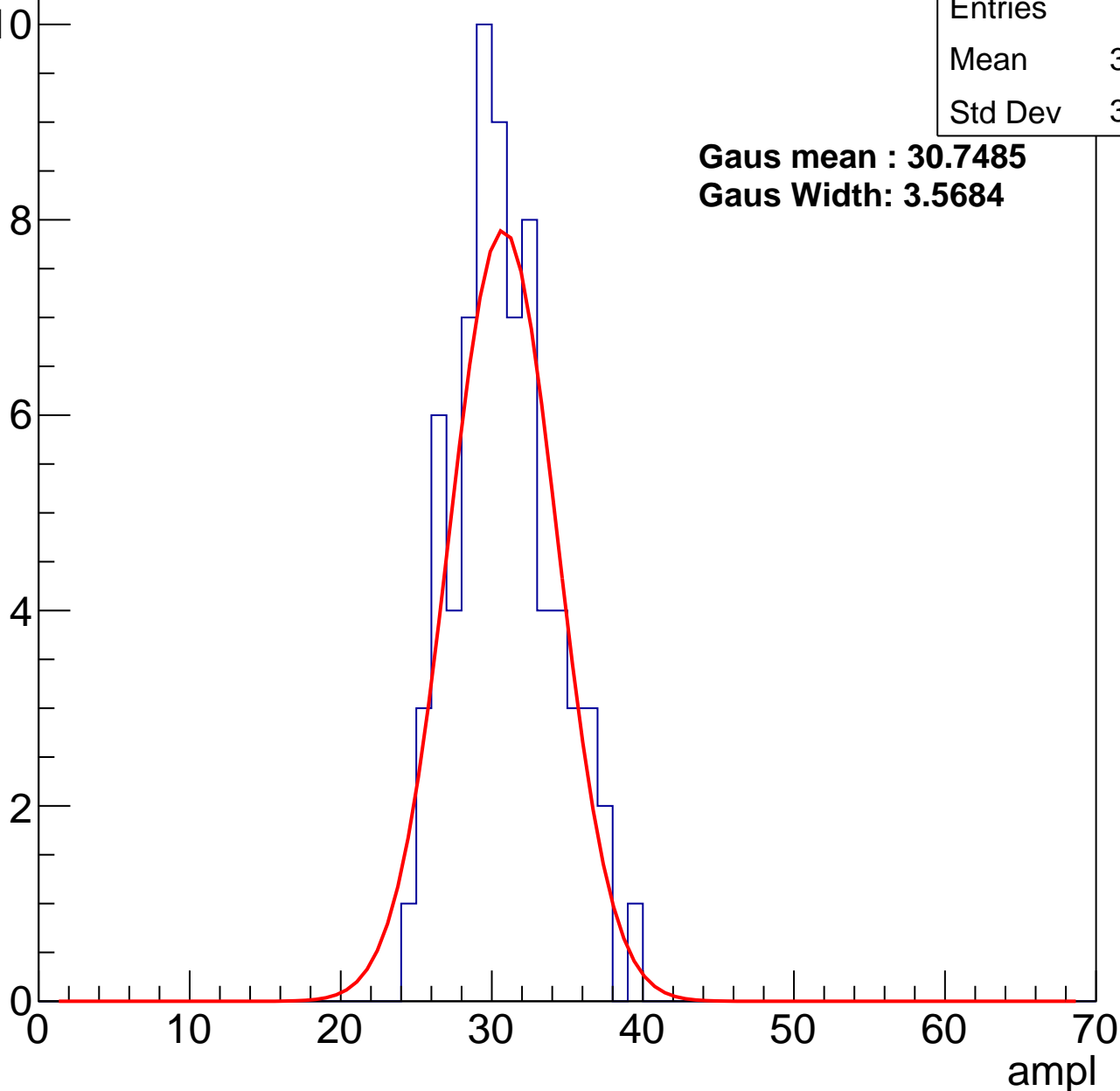
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.36
Std Dev	3.276

Gaus mean : 30.7485

Gaus Width: 3.5684



B1L100S, U12-ch43, adc1

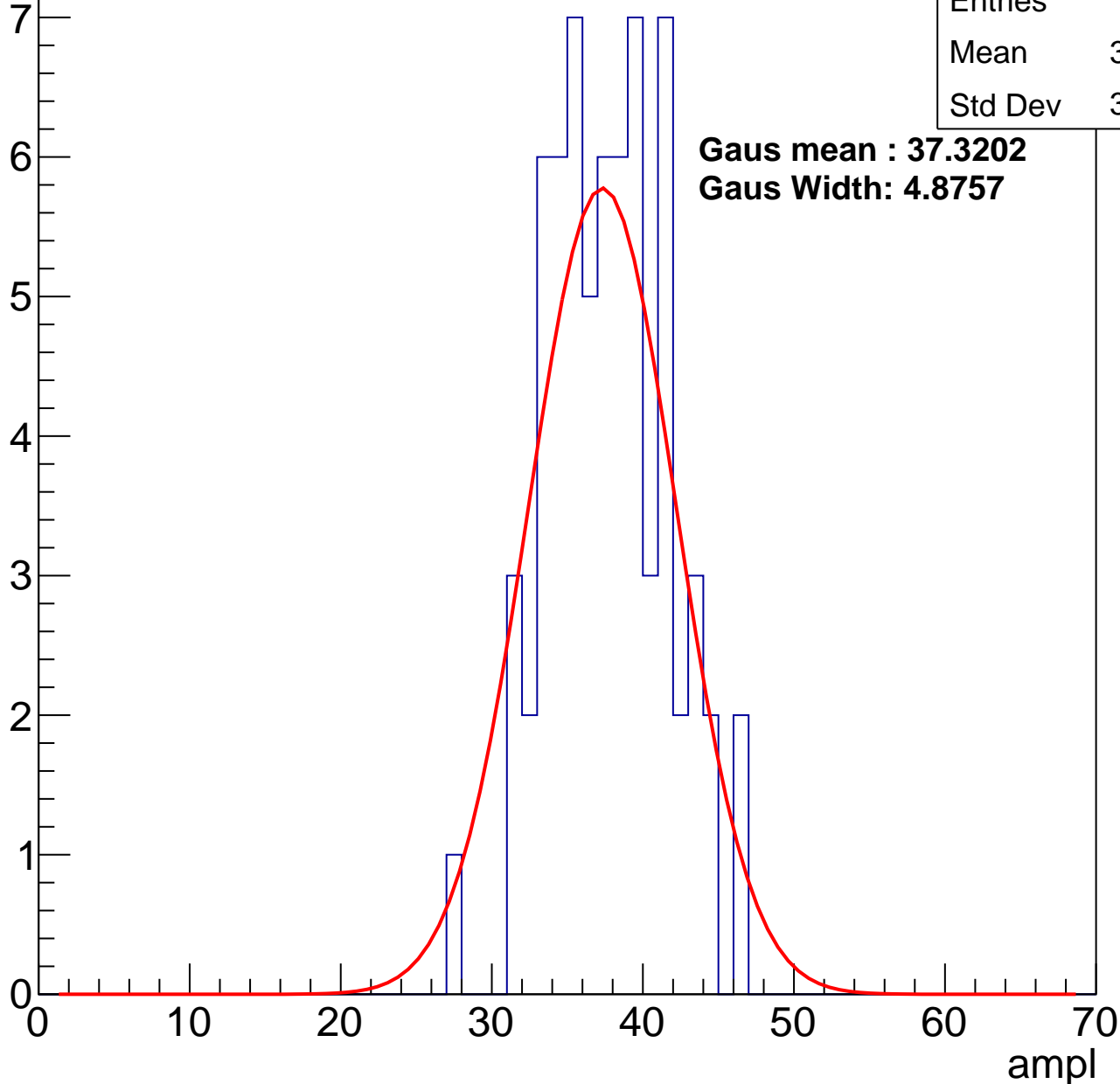
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	37.26
Std Dev	3.887

Gaus mean : 37.3202

Gaus Width: 4.8757



B1L100S, U12-ch43, adc2

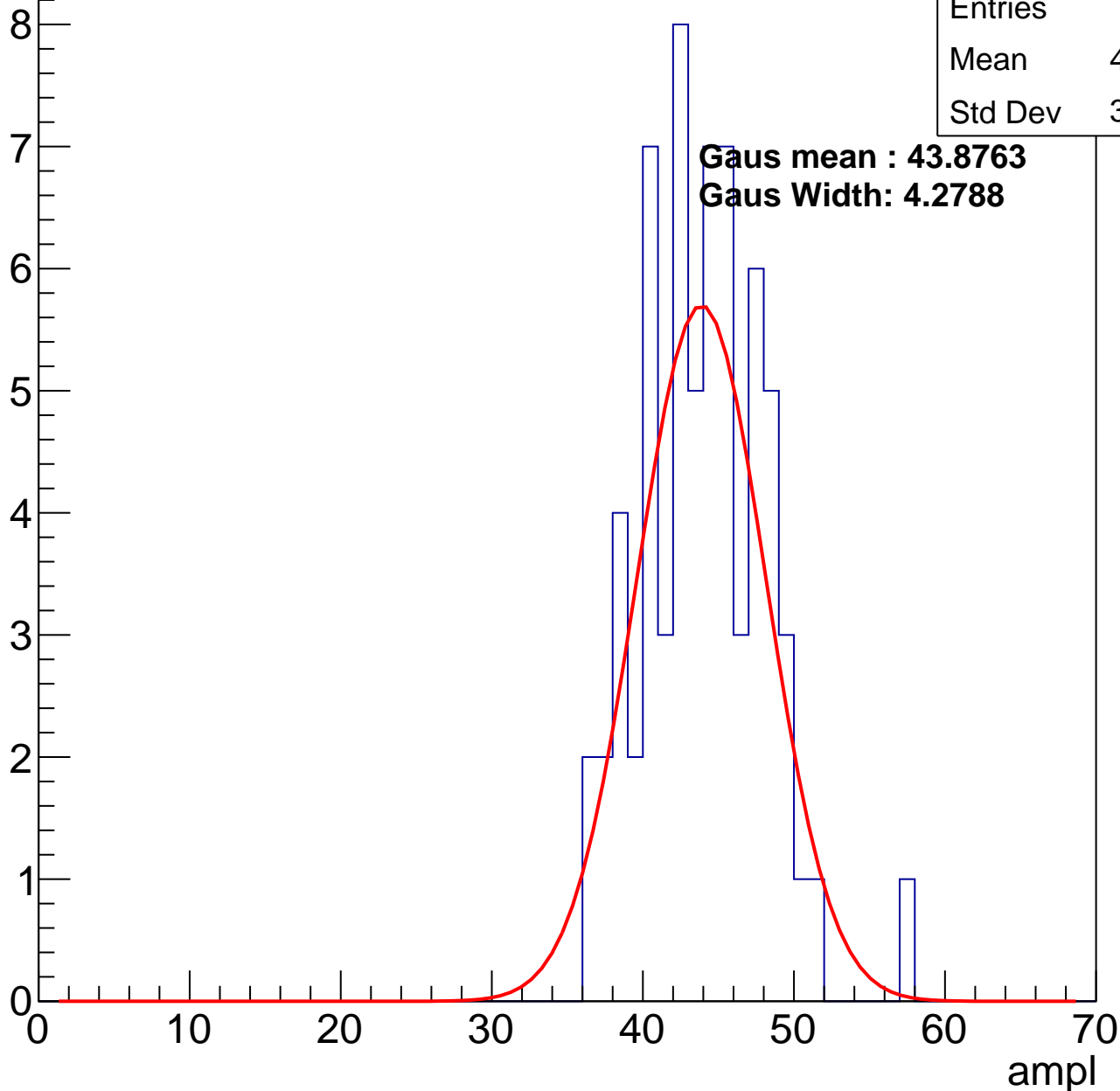
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	43.55
Std Dev	3.986

Gaus mean : 43.8763

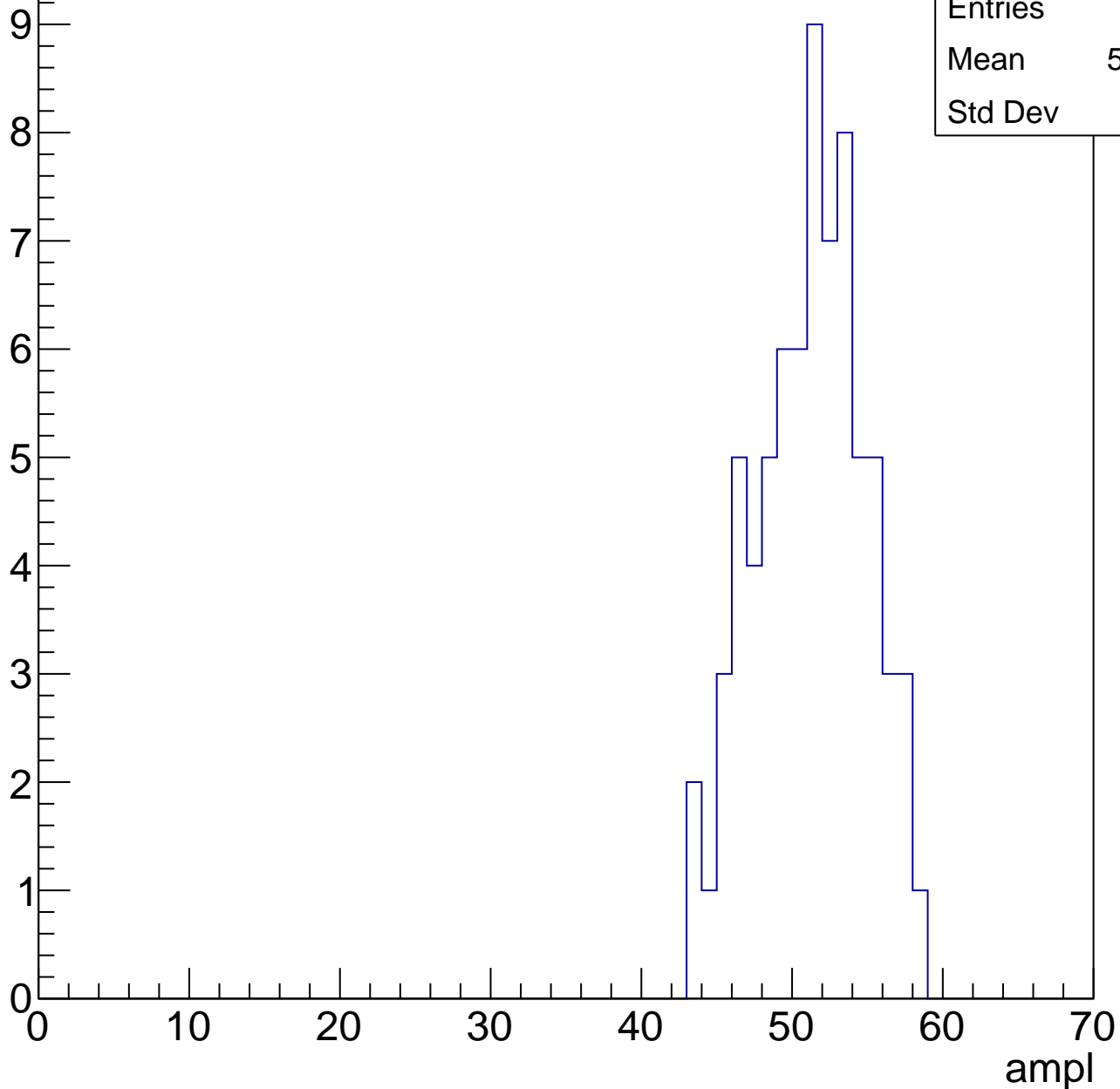
Gaus Width: 4.2788



B1L100S, U12-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



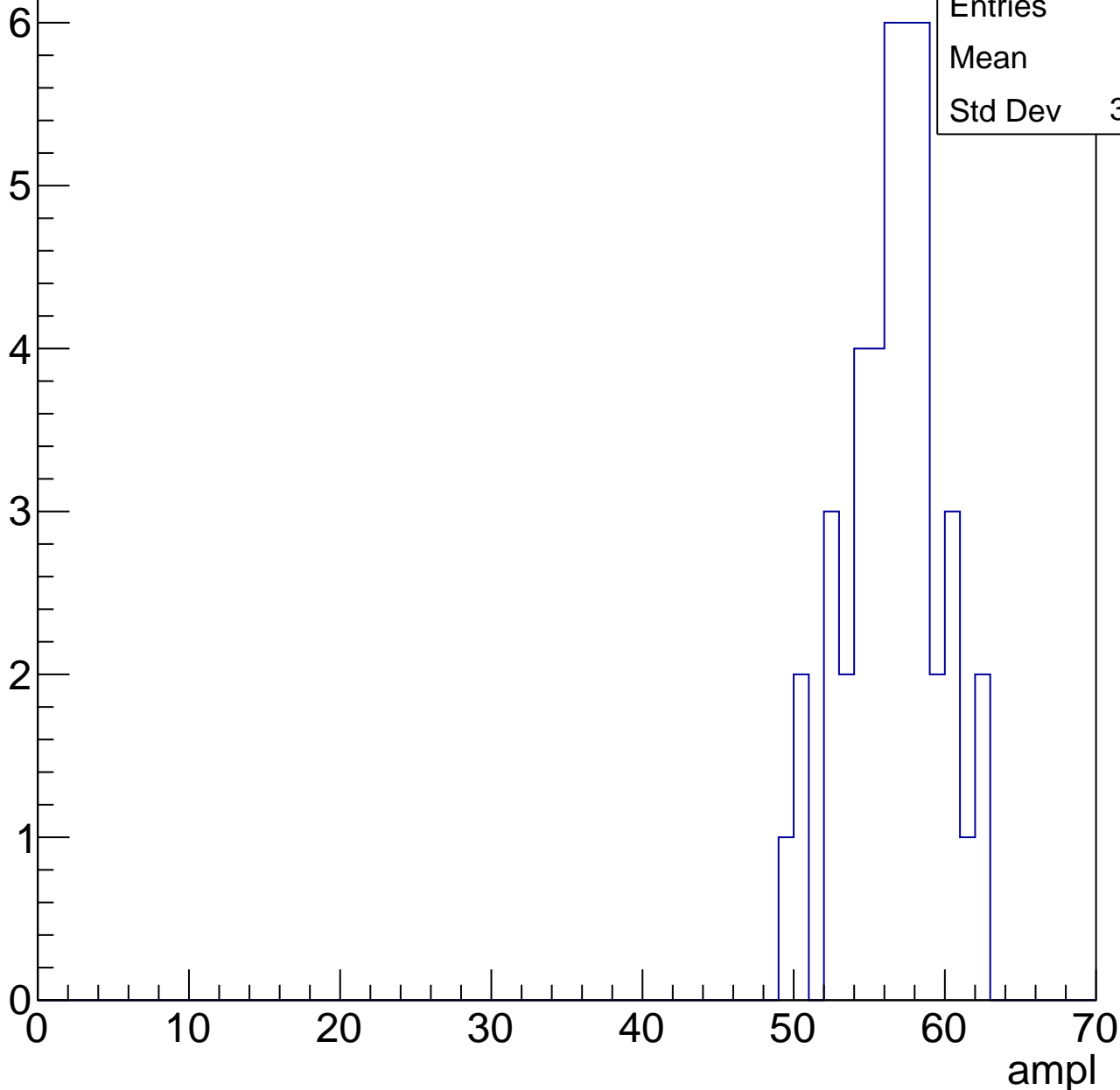
Entries	73
Mean	50.77
Std Dev	3.59

B1L100S, U12-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	56.1
Std Dev	3.085

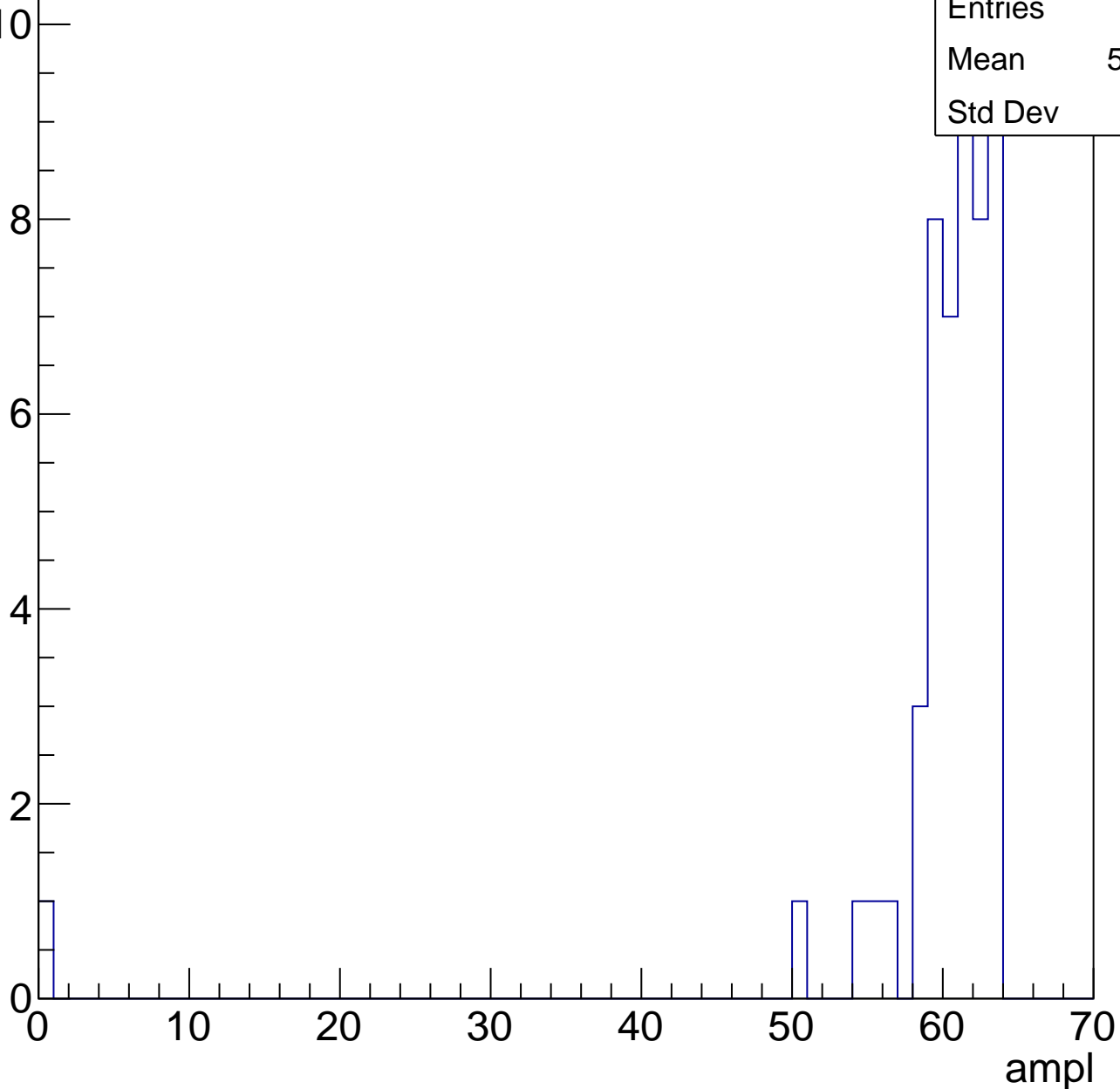


B1L100S, U12-ch43, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	59.12
Std Dev	8.82



B1L100S, U12-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch44, adc0

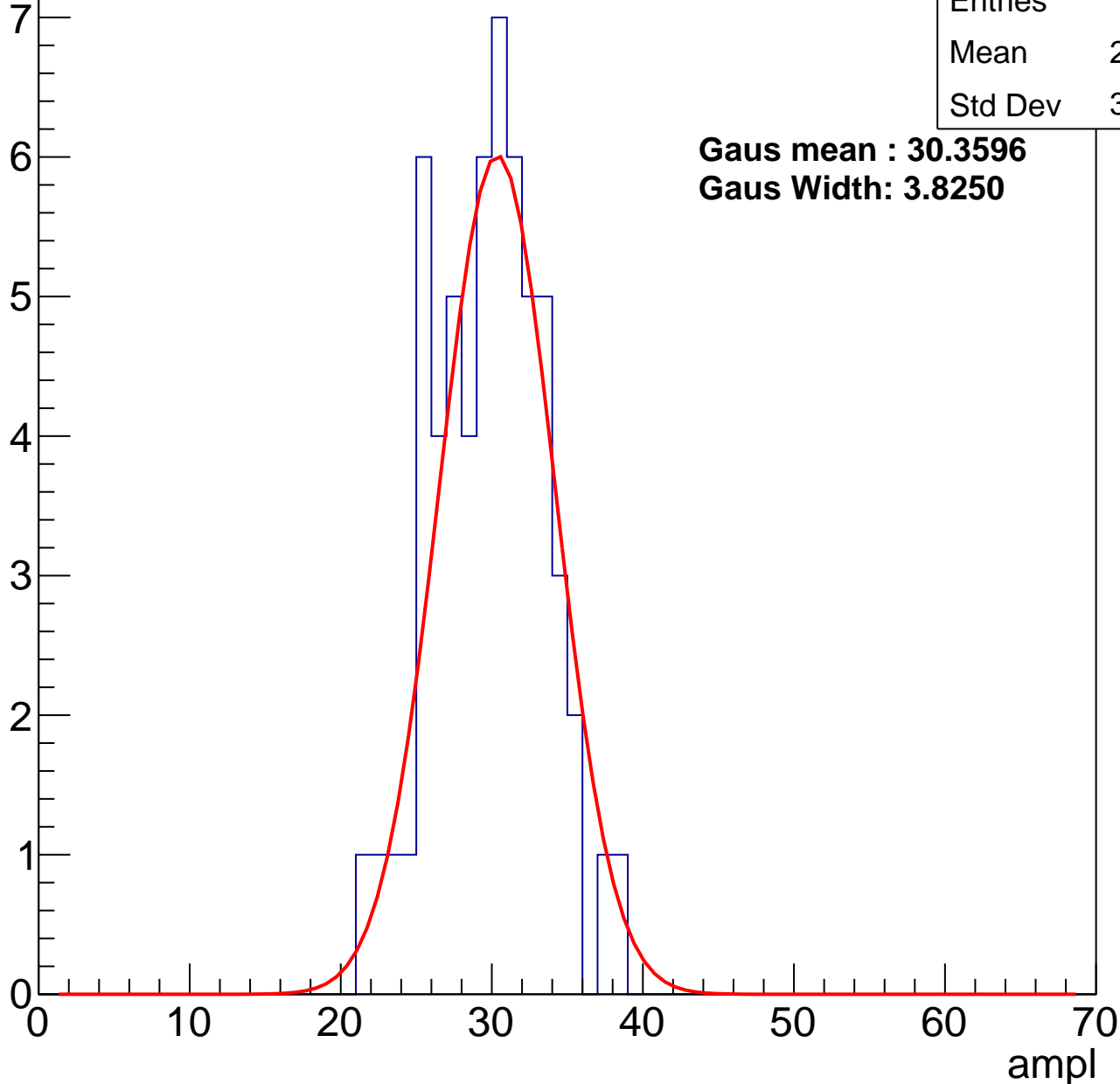
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.37
Std Dev	3.612

Gaus mean : 30.3596

Gaus Width: 3.8250



B1L100S, U12-ch44, adc1

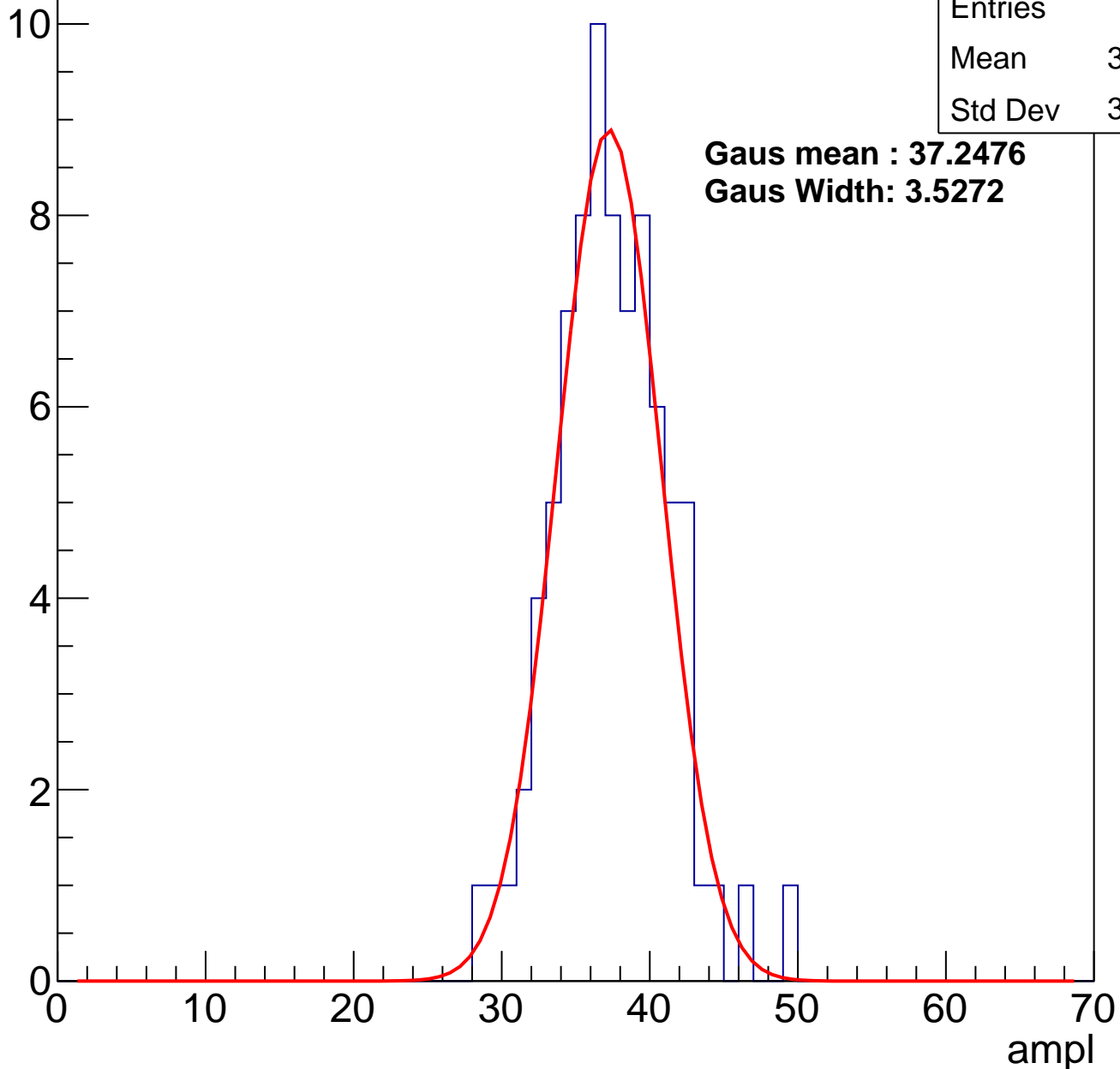
calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	36.96
Std Dev	3.759

Gaus mean : 37.2476

Gaus Width: 3.5272

Entry



B1L100S, U12-ch44, adc2

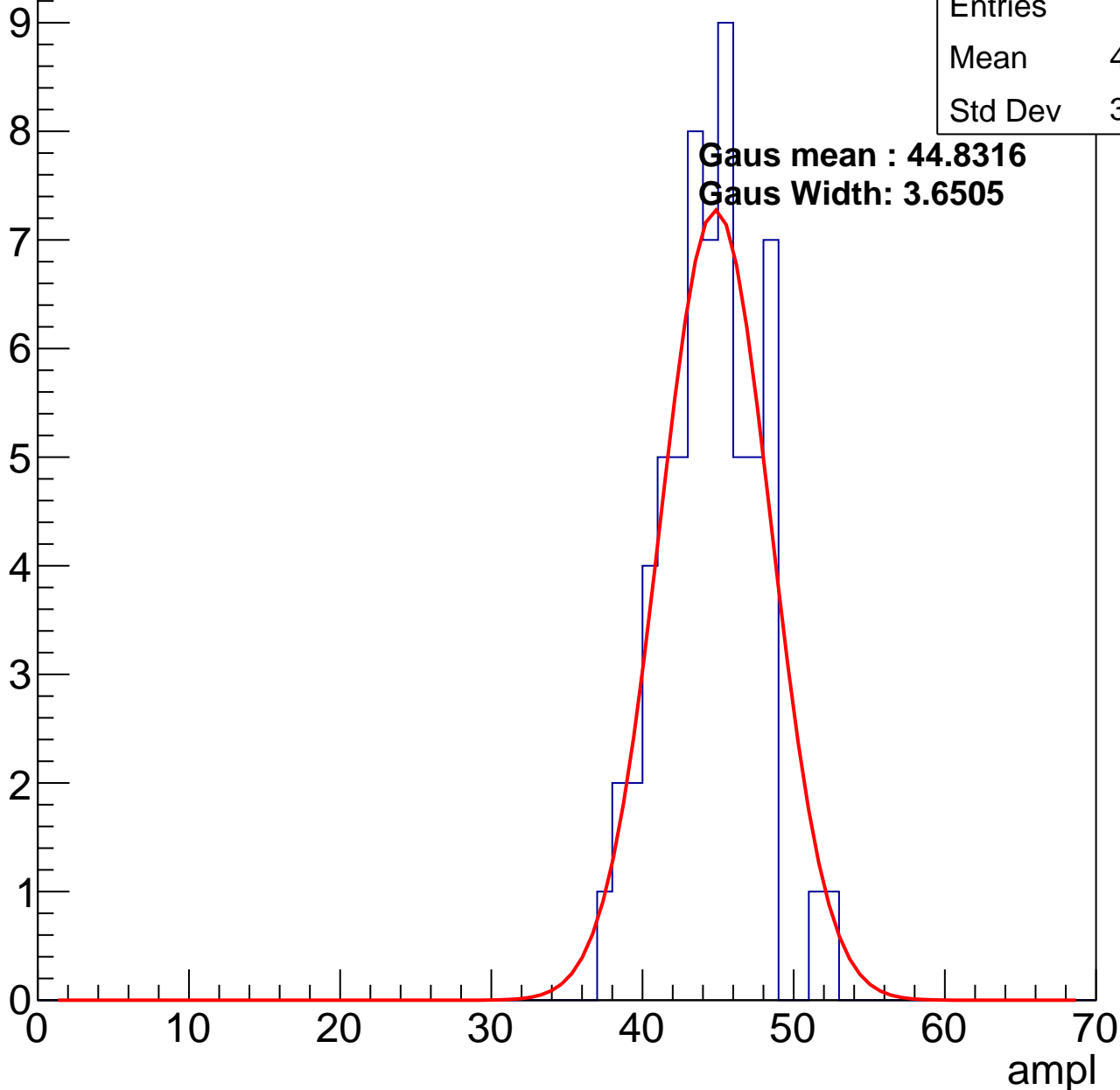
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	43.98
Std Dev	3.129

Gaus mean : 44.8316

Gaus Width: 3.6505



B1L100S, U12-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

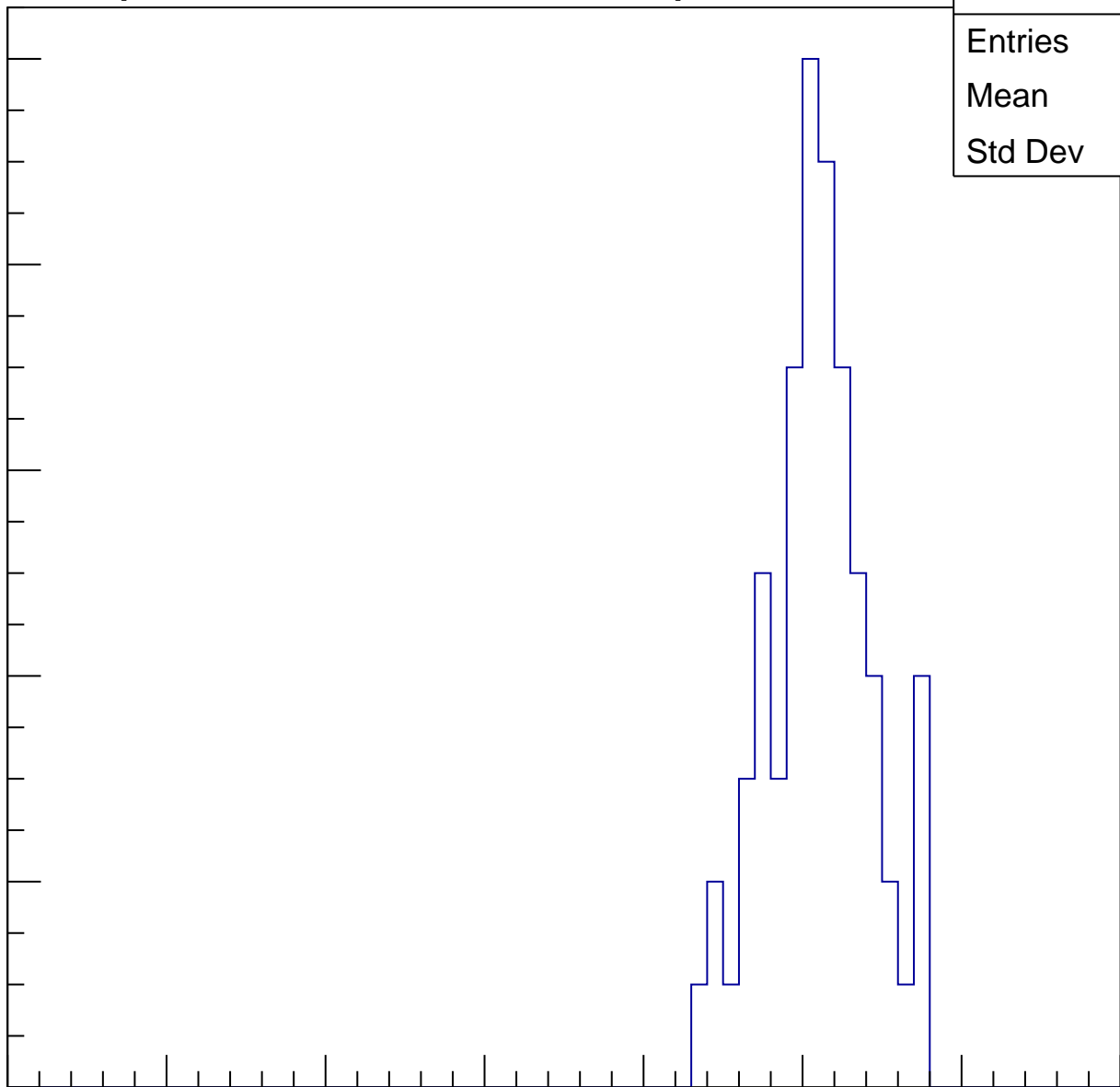
Entries	64
Mean	50.53
Std Dev	3.231

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

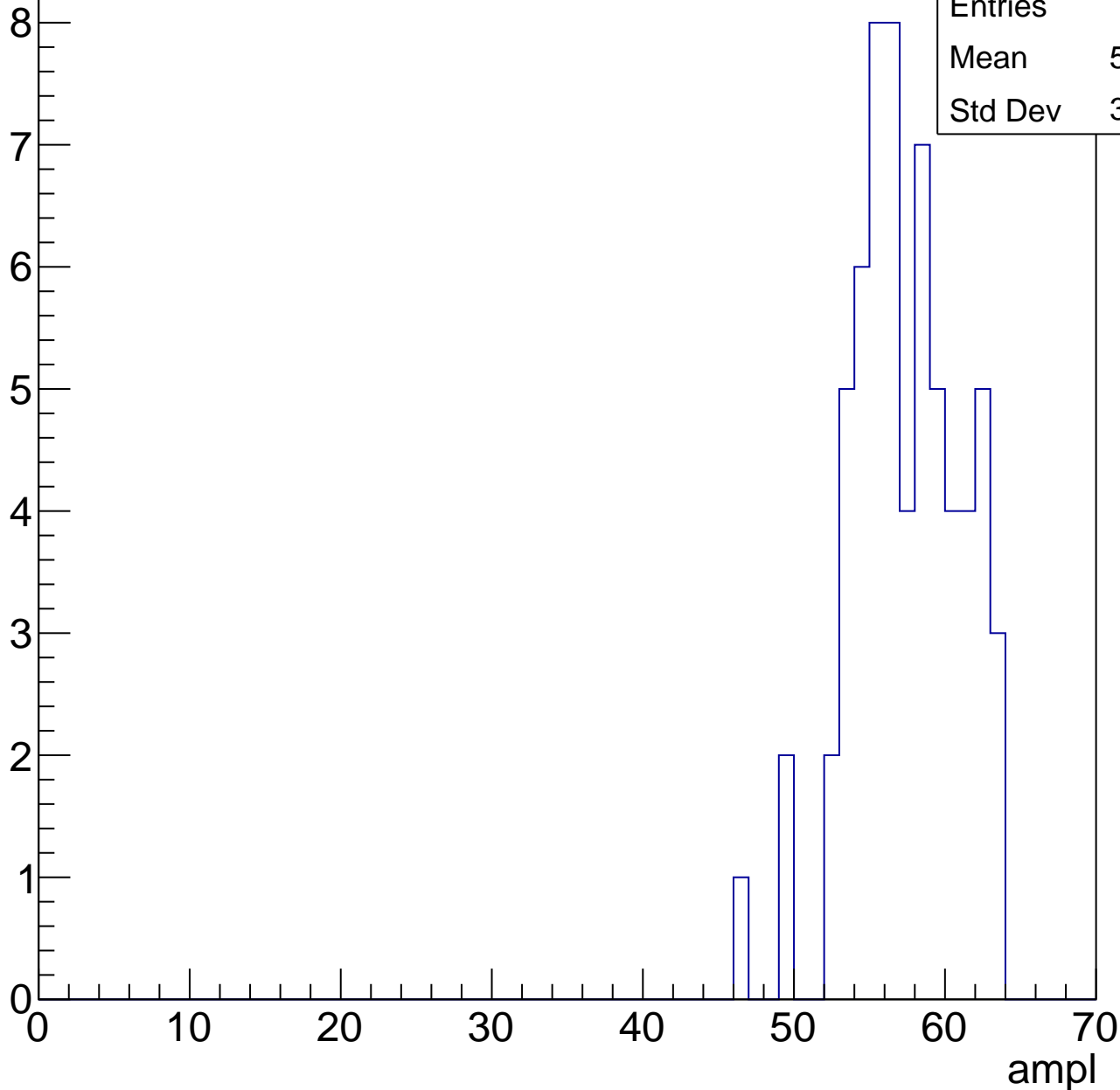


B1L100S, U12-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	56.83
Std Dev	3.604



B1L100S, U12-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	34
Mean	58.62
Std Dev	10.39

0

10

20

30

40

50

60

70

ampl

B1L100S, U12-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch45, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	33.1
Std Dev	3.466

Gaus mean : 33.6733

Gaus Width: 4.0356

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

7

8

9

10

B1L100S, U12-ch45, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	40.46
Std Dev	3.816

Gaus mean : 40.7440

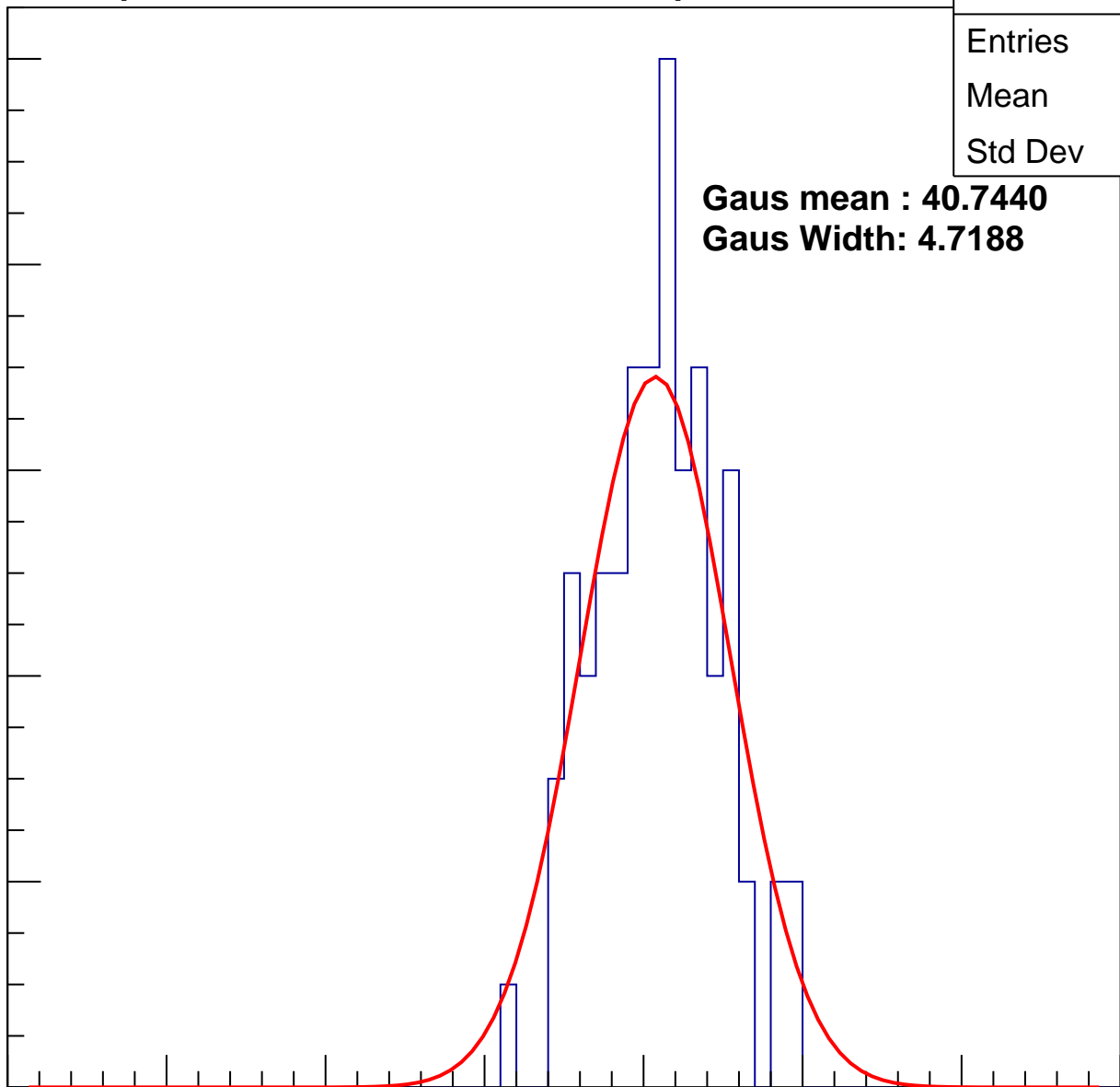
Gaus Width: 4.7188

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch45, adc2

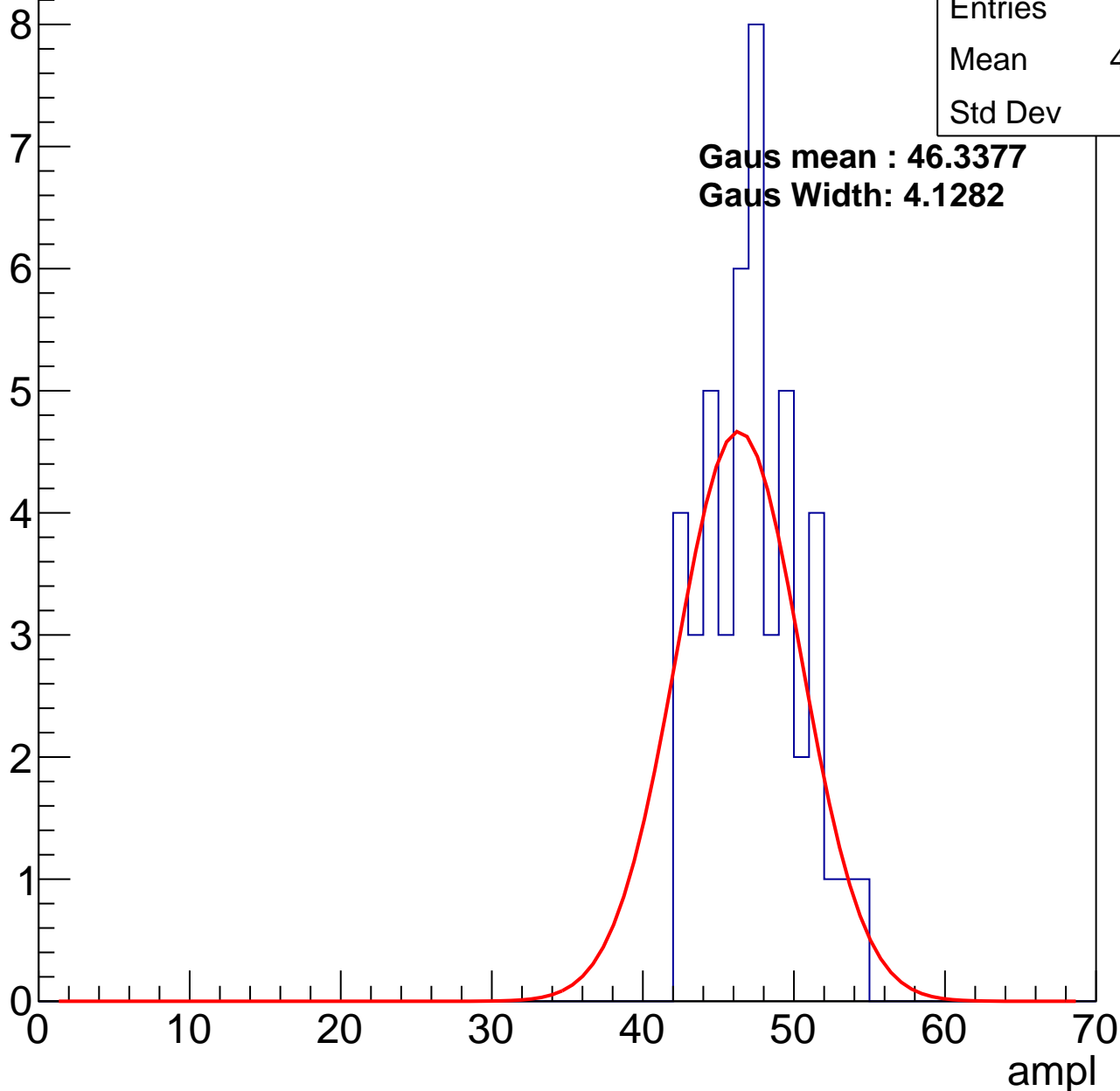
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	46.87
Std Dev	3.04

Gaus mean : 46.3377

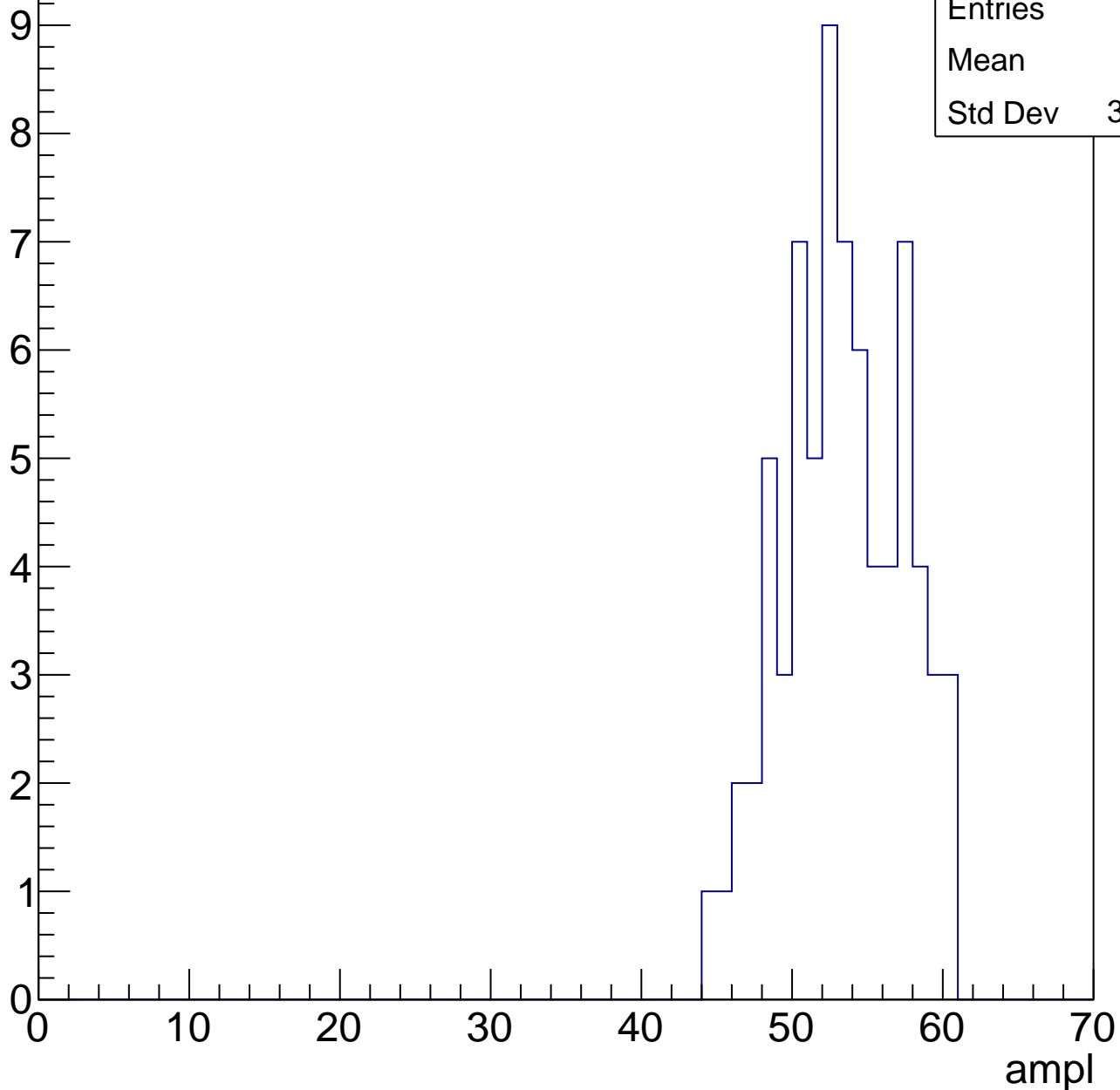
Gaus Width: 4.1282



B1L100S, U12-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



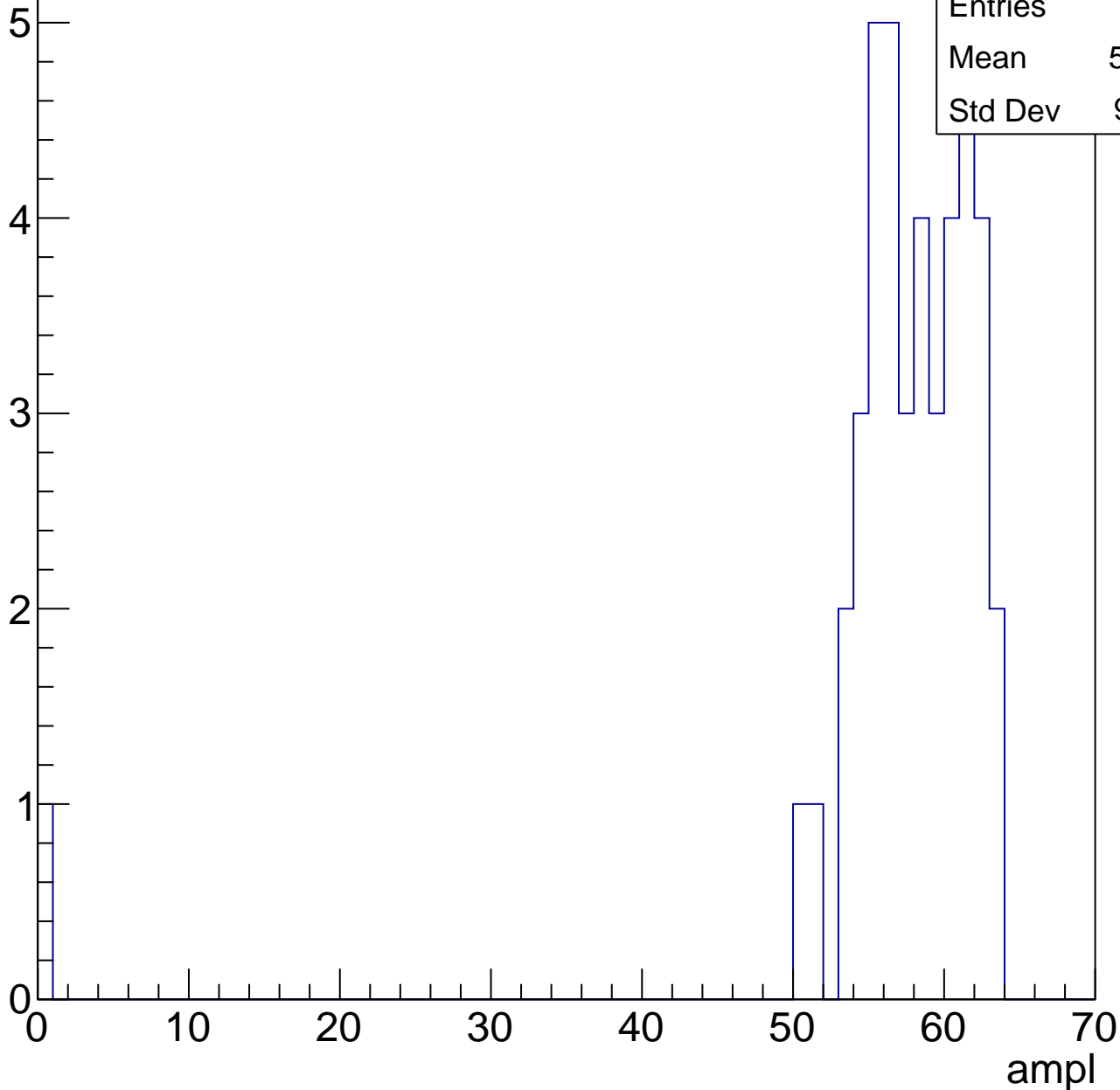
Entries	73
Mean	52.9
Std Dev	3.889

B1L100S, U12-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	56.35
Std Dev	9.281

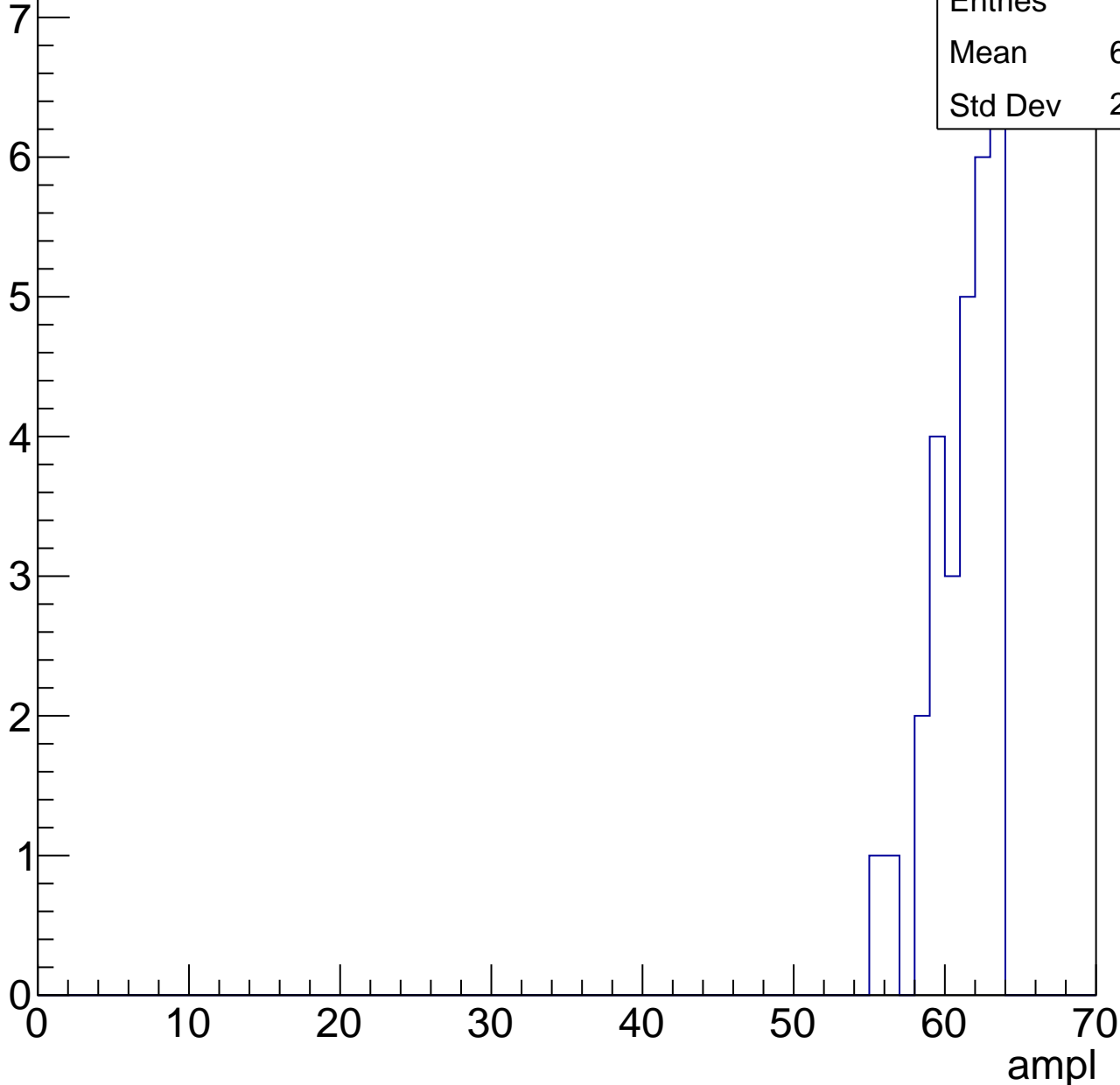


B1L100S, U12-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	60.72
Std Dev	2.116



B1L100S, U12-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch46, adc0

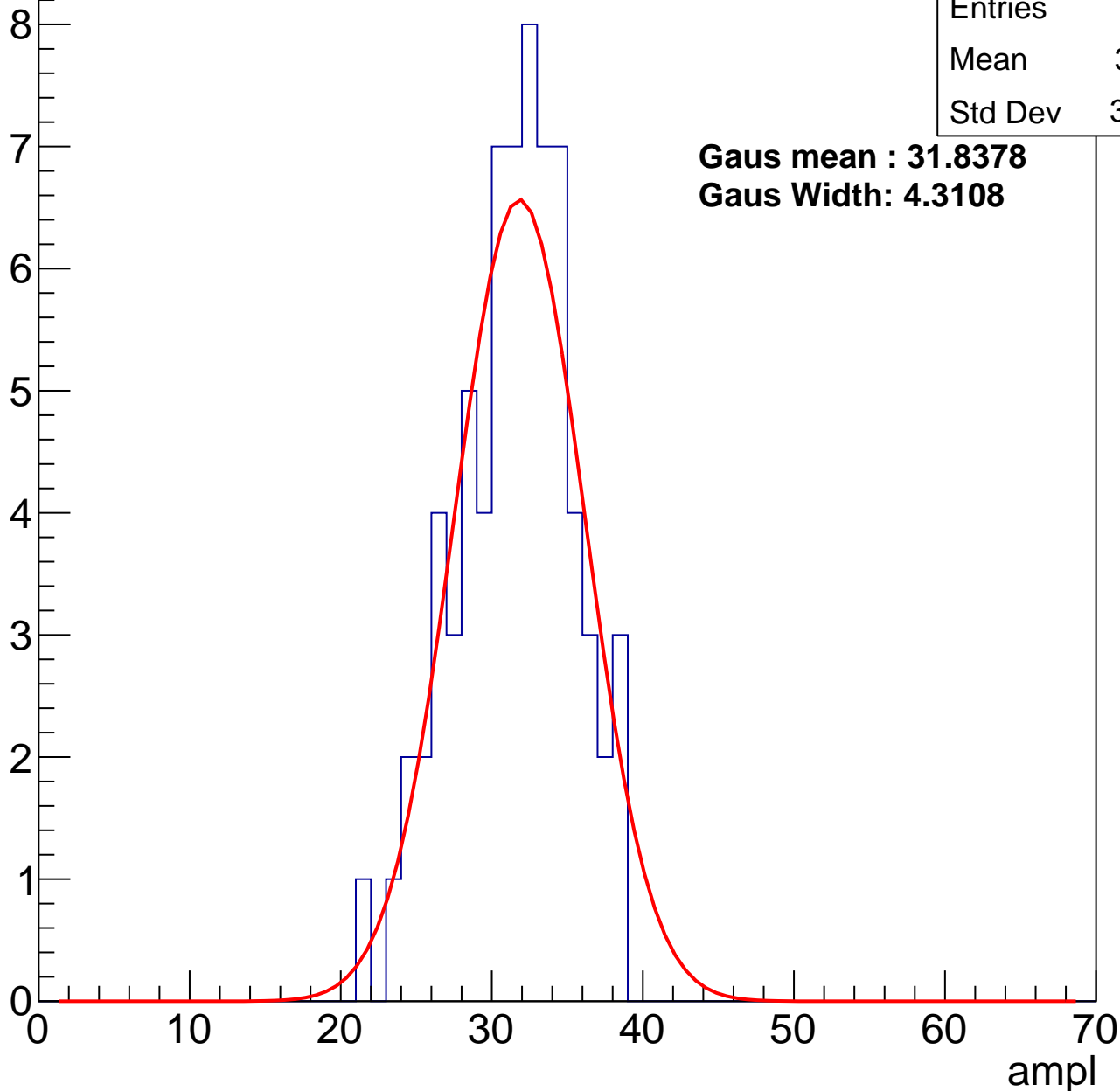
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	31.01
Std Dev	3.793

Gaus mean : 31.8378

Gaus Width: 4.3108



B1L100S, U12-ch46, adc1

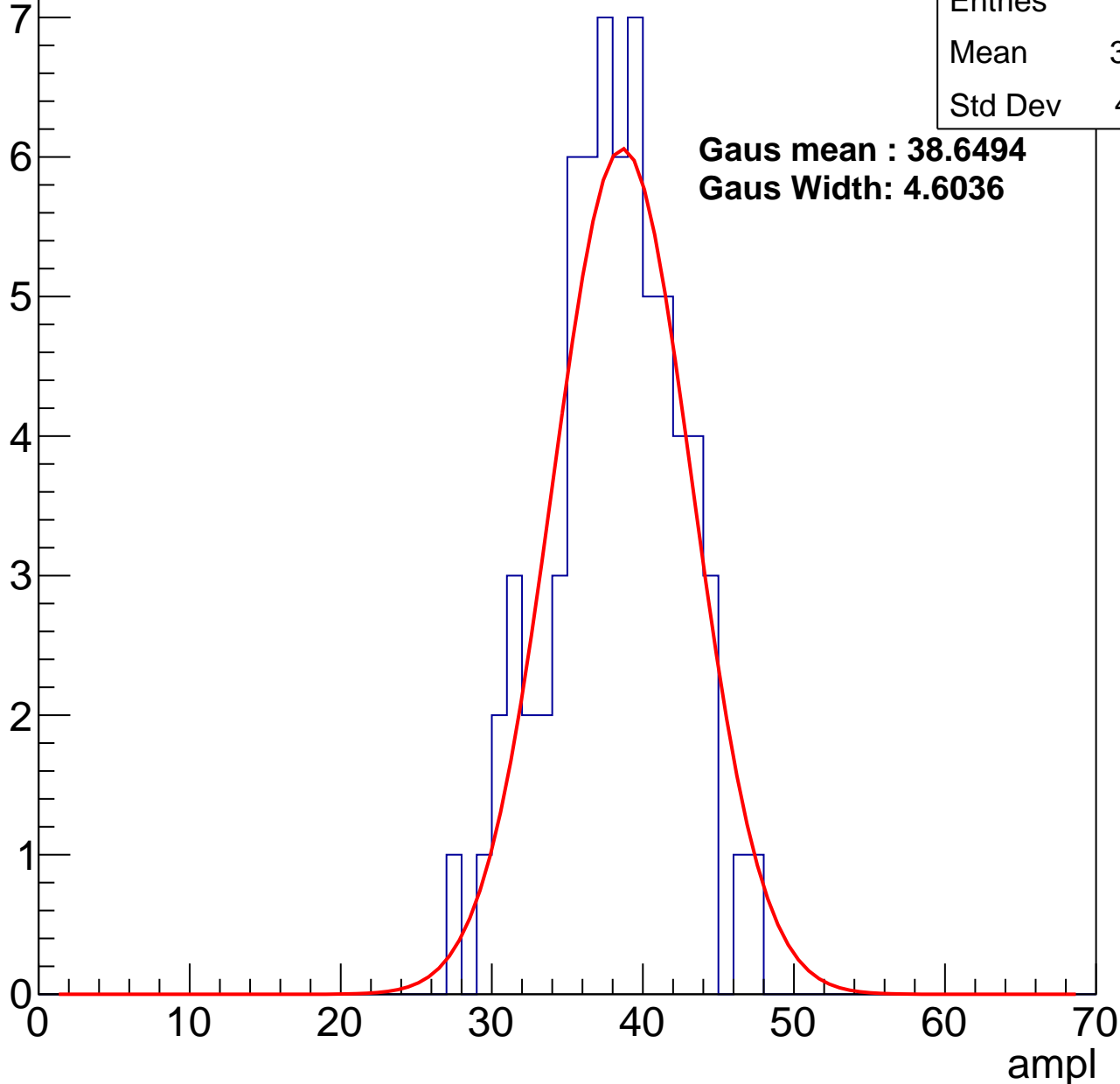
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.64
Std Dev	4.191

Gaus mean : 38.6494

Gaus Width: 4.6036

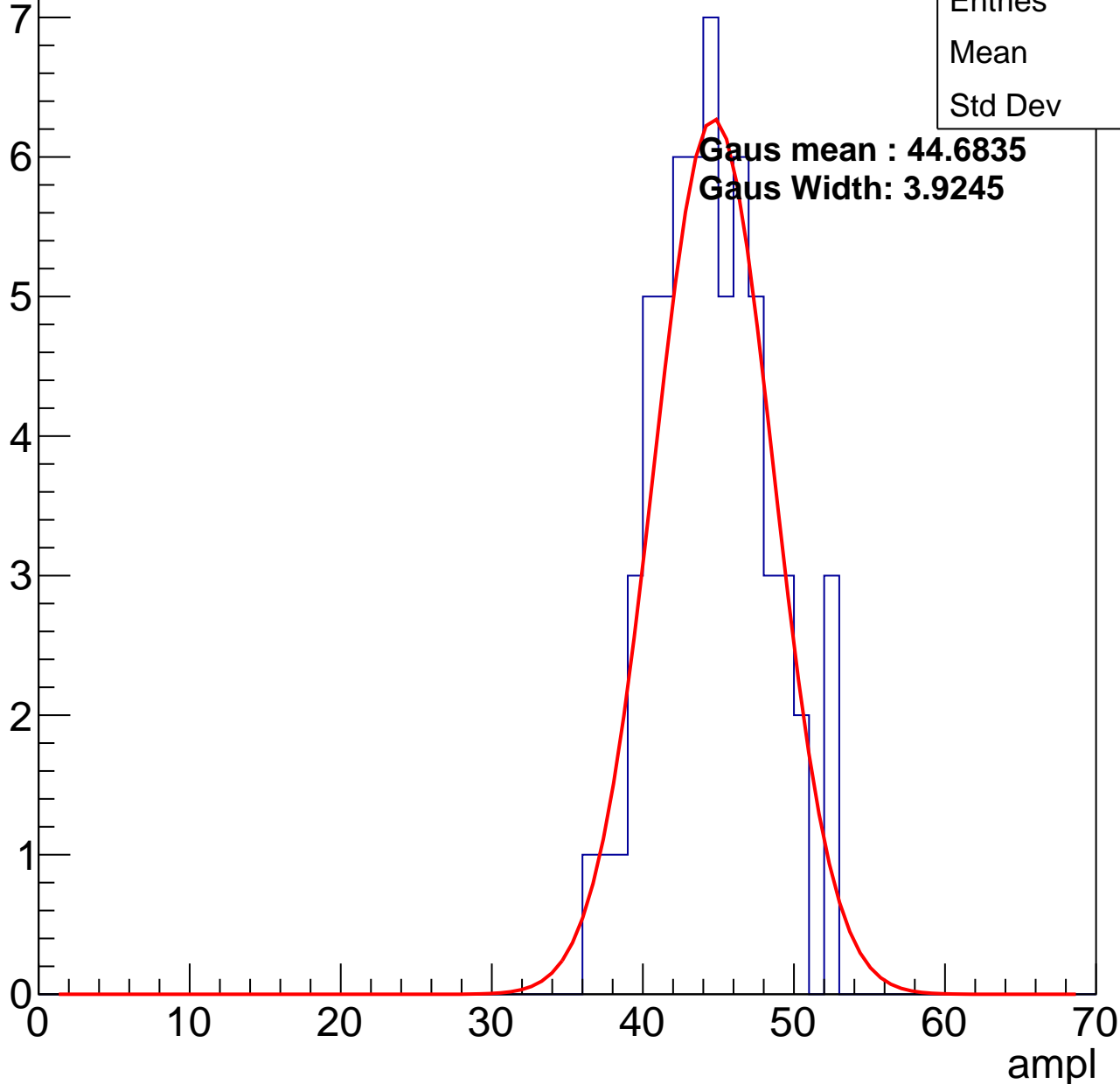


B1L100S, U12-ch46, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

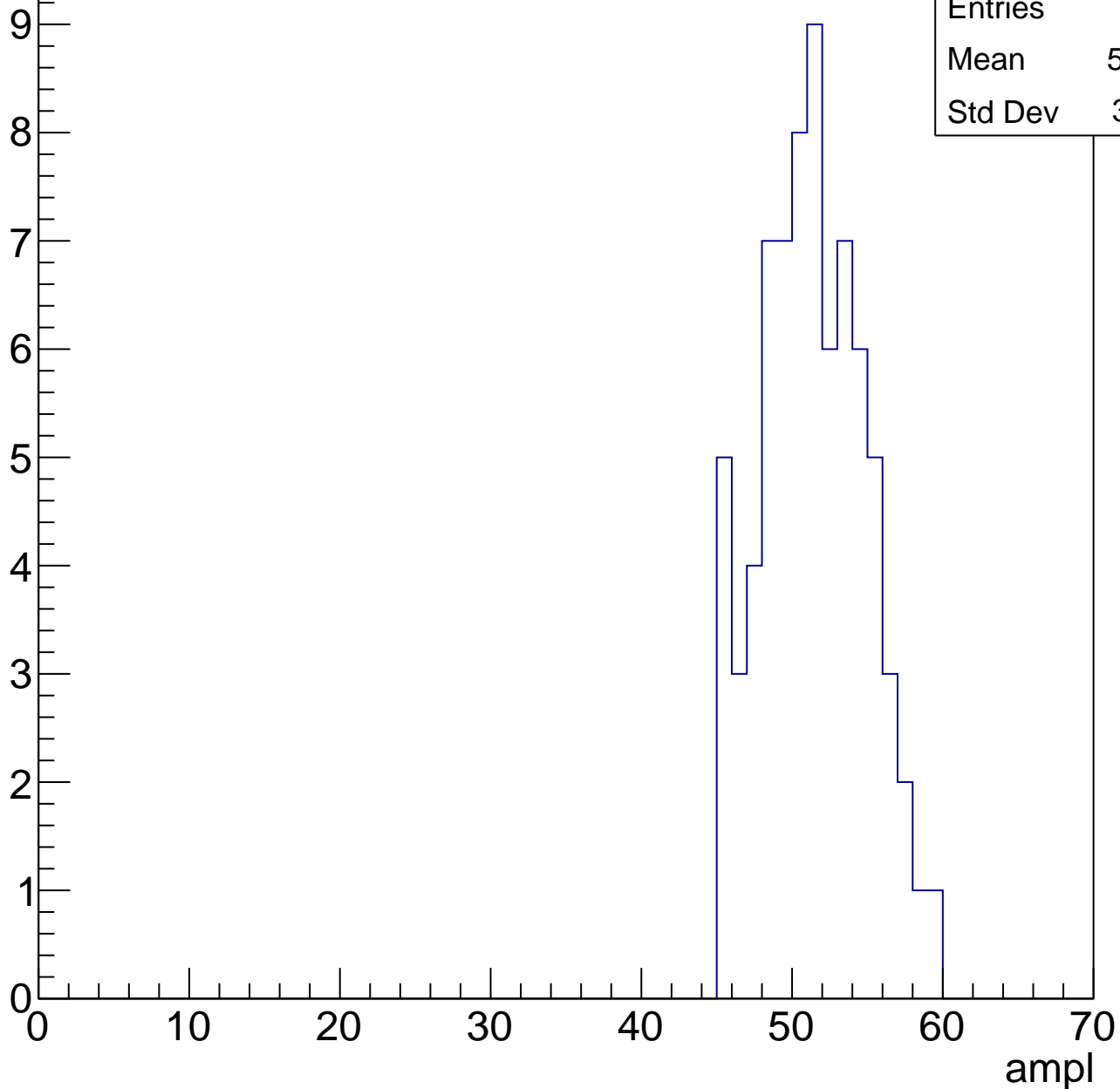
Entries	62
Mean	44.1
Std Dev	3.68



B1L100S, U12-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

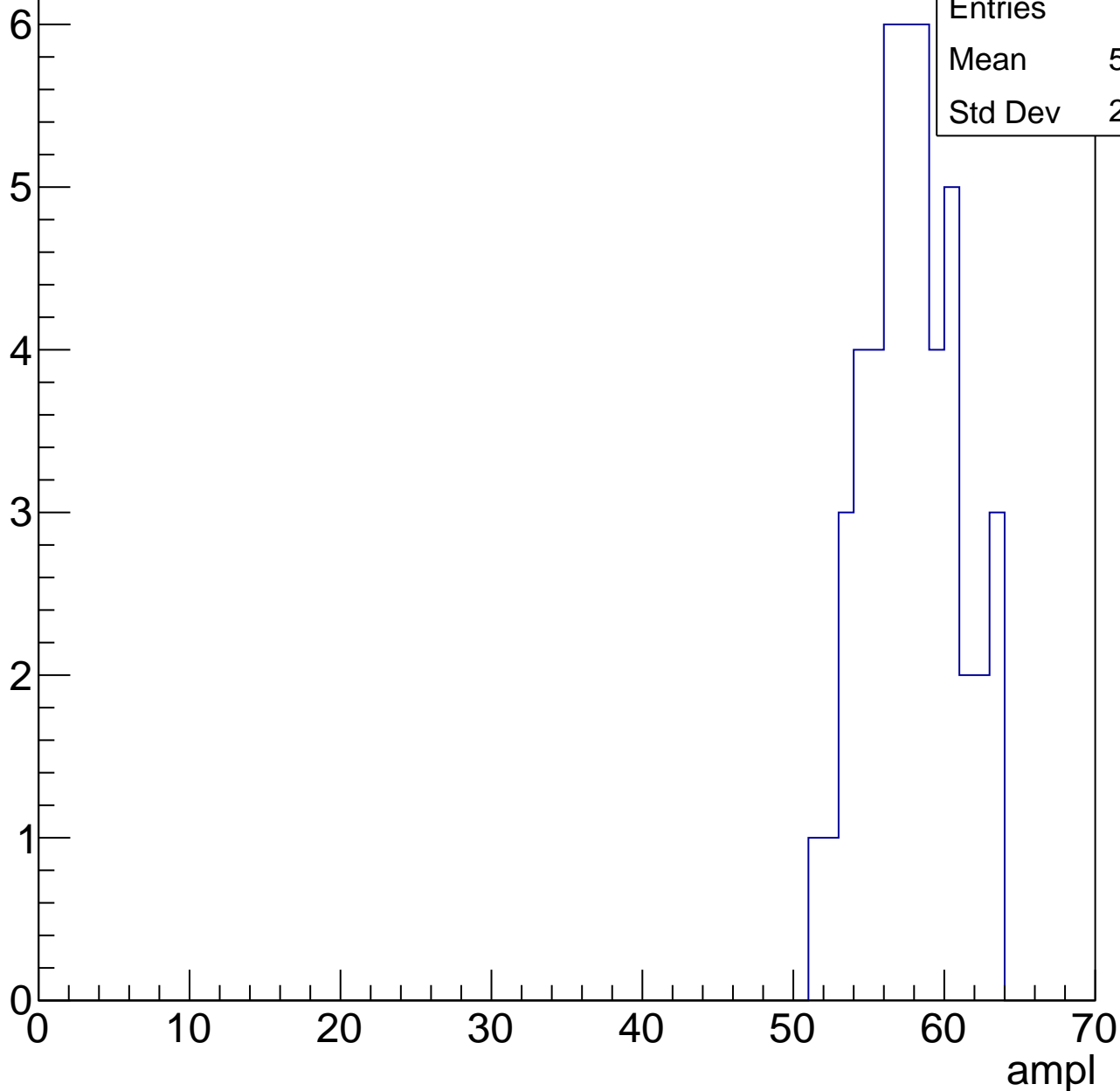


B1L100S, U12-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	57.34
Std Dev	2.984

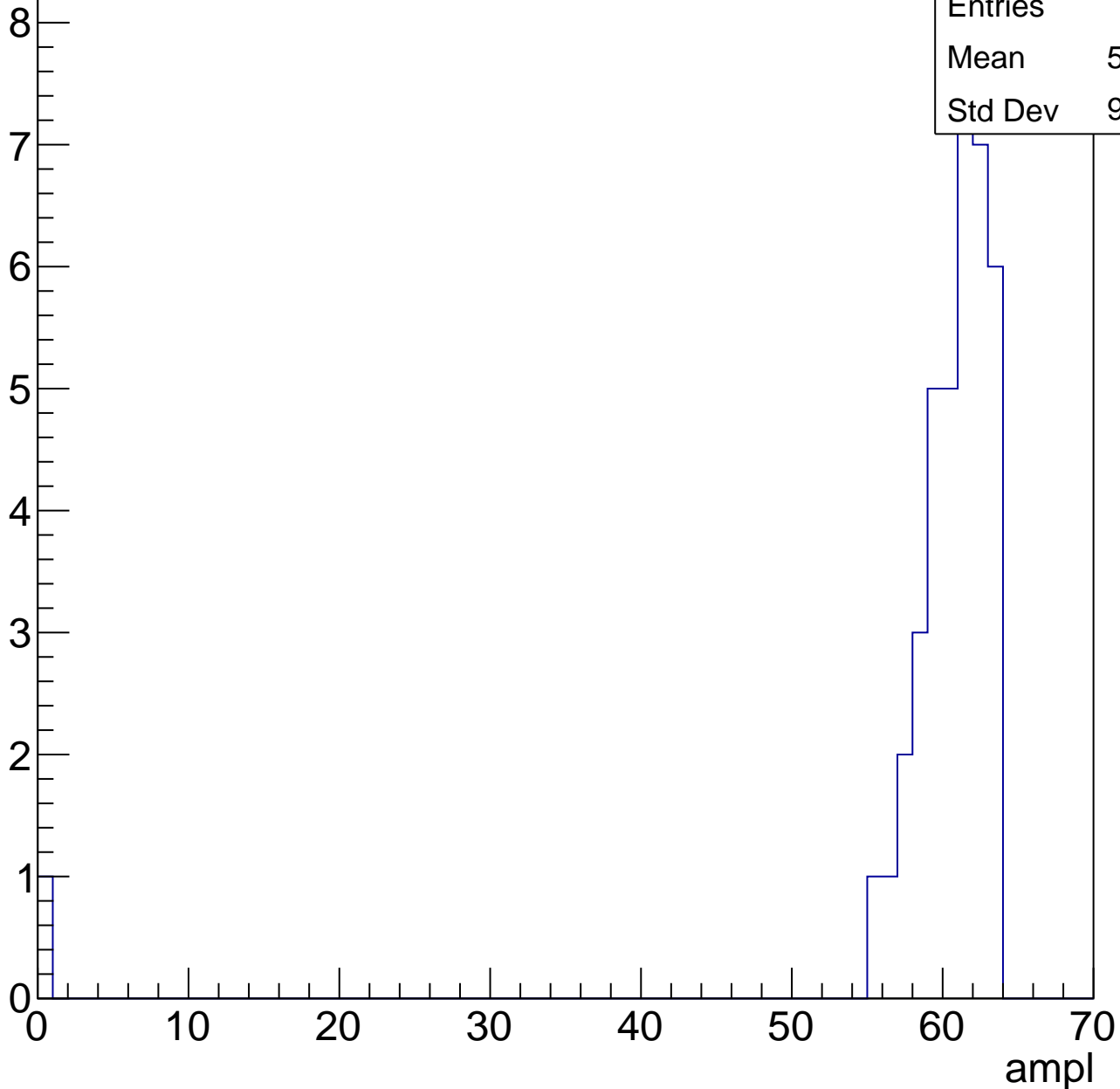


B1L100S, U12-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	58.82
Std Dev	9.756



B1L100S, U12-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch47, adc0

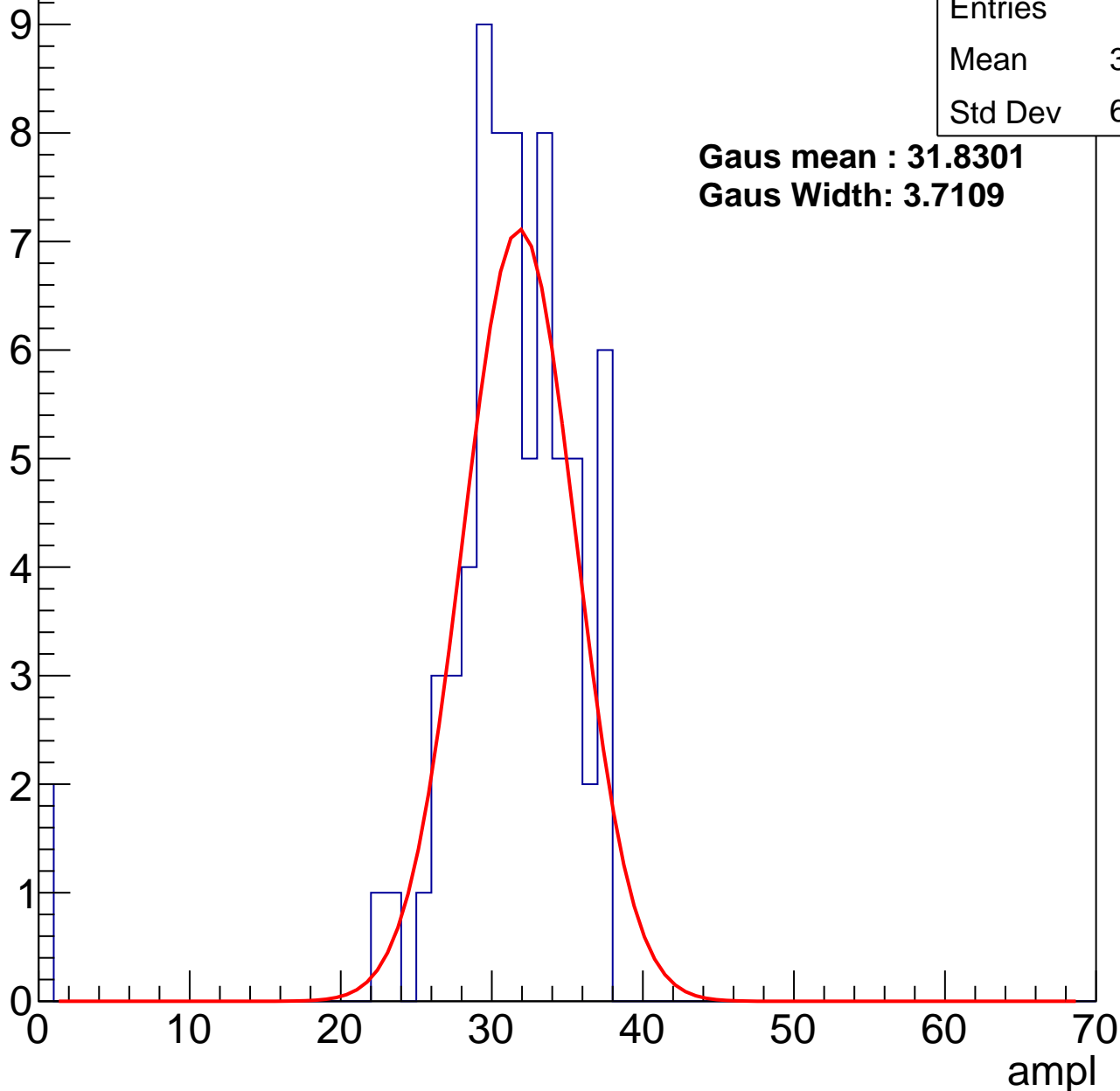
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.32
Std Dev	6.168

Gaus mean : 31.8301

Gaus Width: 3.7109



B1L100S, U12-ch47, adc1

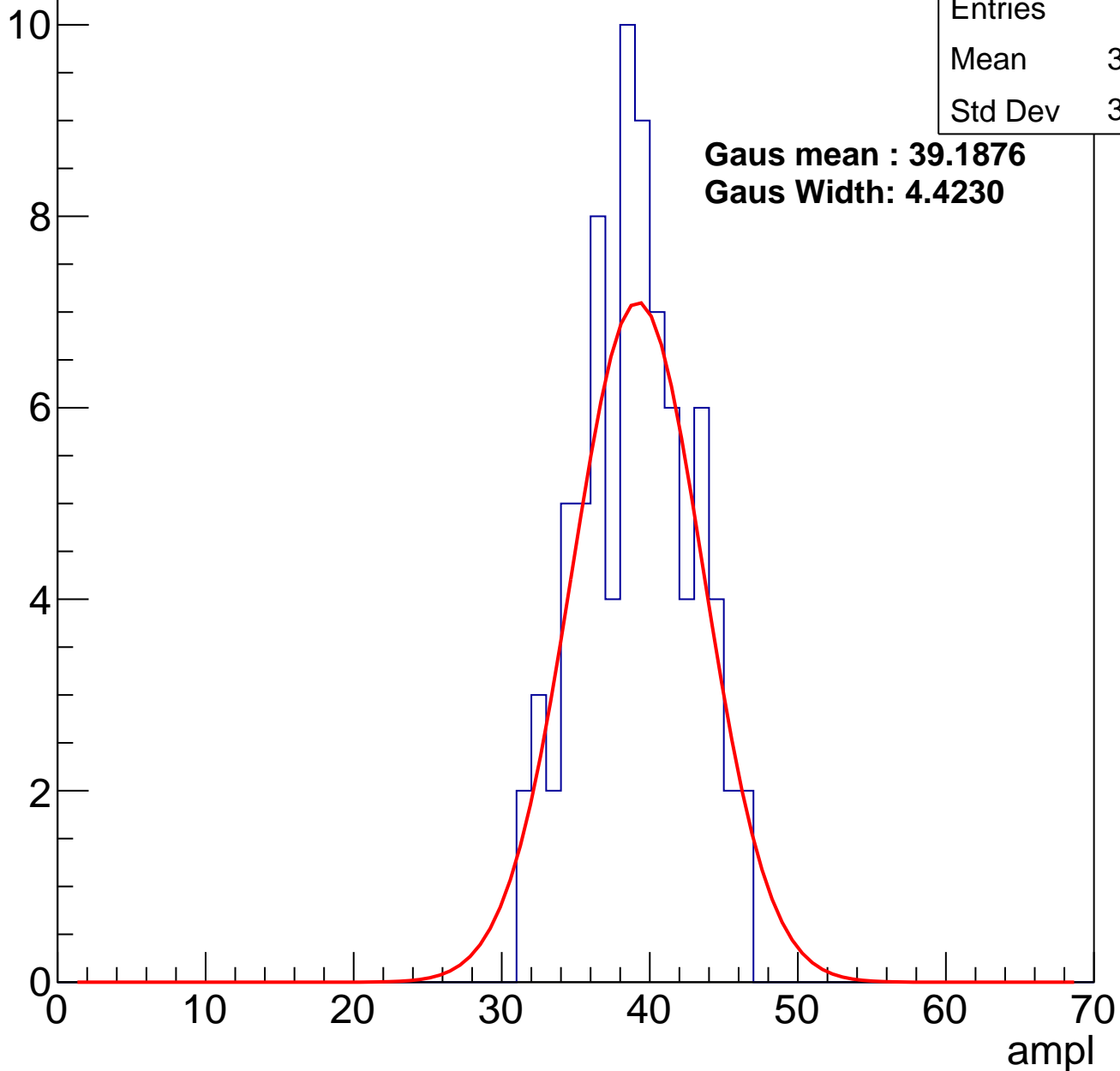
calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	38.56
Std Dev	3.676

Gaus mean : 39.1876

Gaus Width: 4.4230

Entry



B1L100S, U12-ch47, adc2

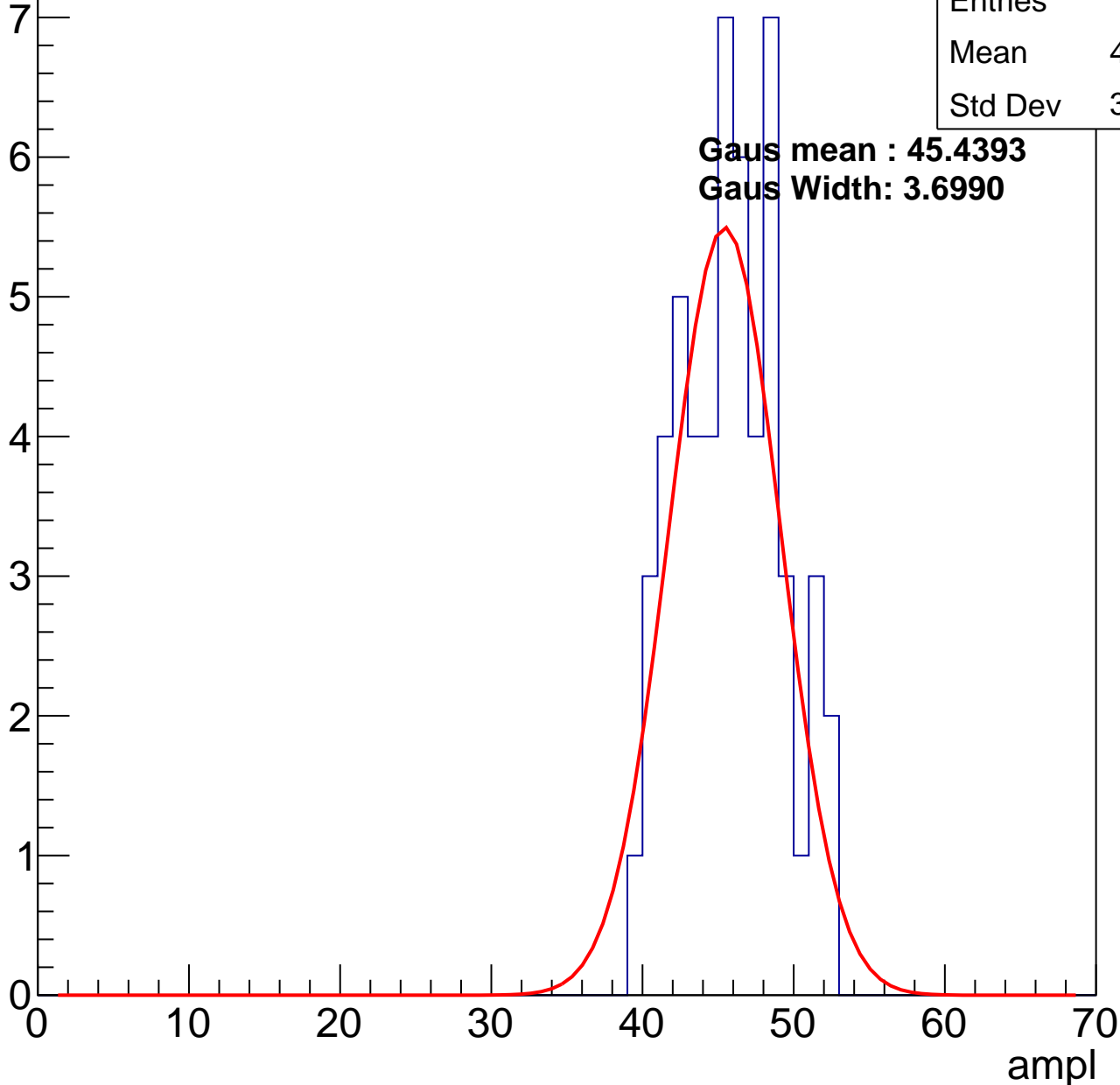
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	45.37
Std Dev	3.324

Gaus mean : 45.4393

Gaus Width: 3.6990

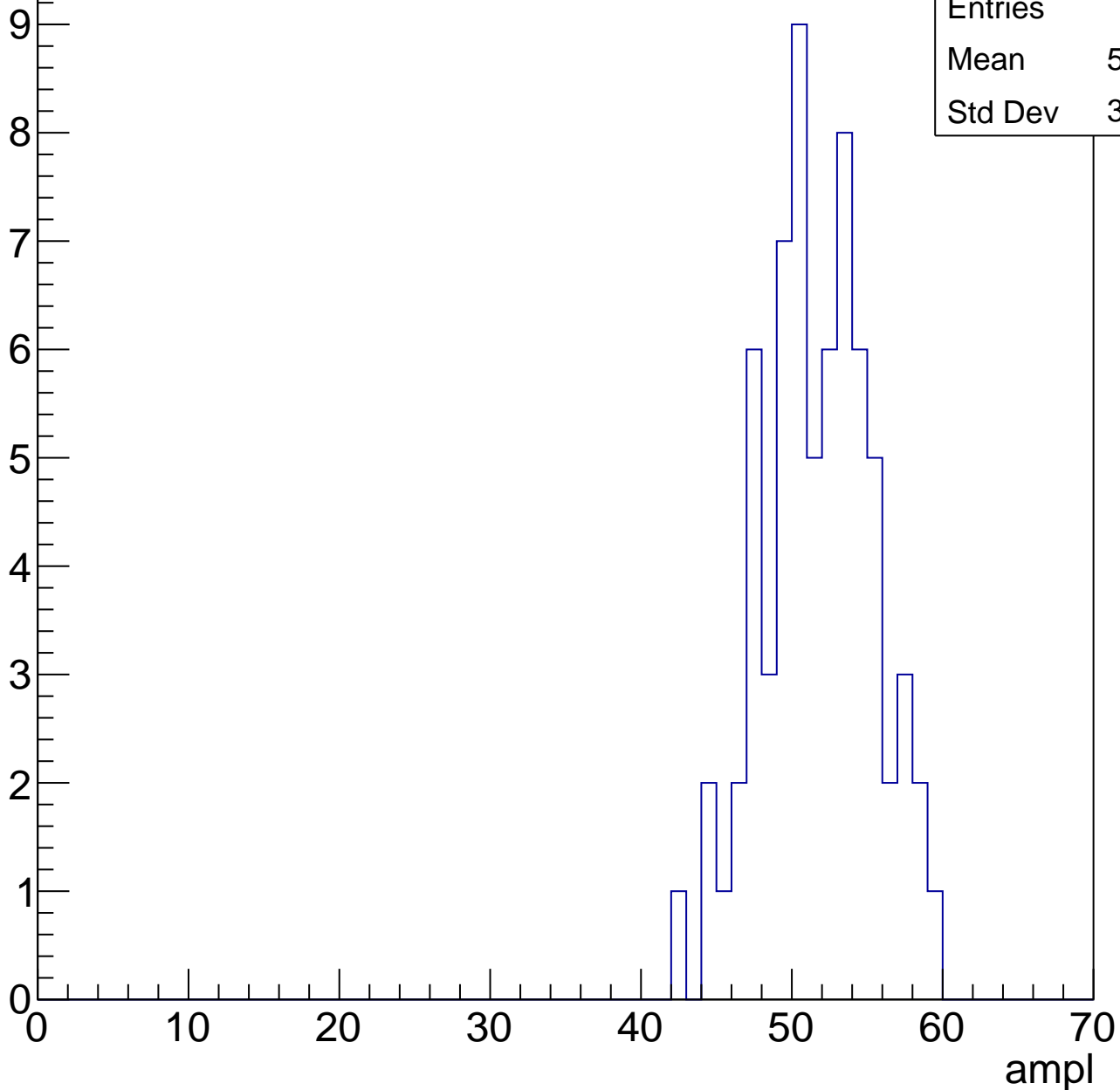


B1L100S, U12-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	51.22
Std Dev	3.639



B1L100S, U12-ch47, adc4

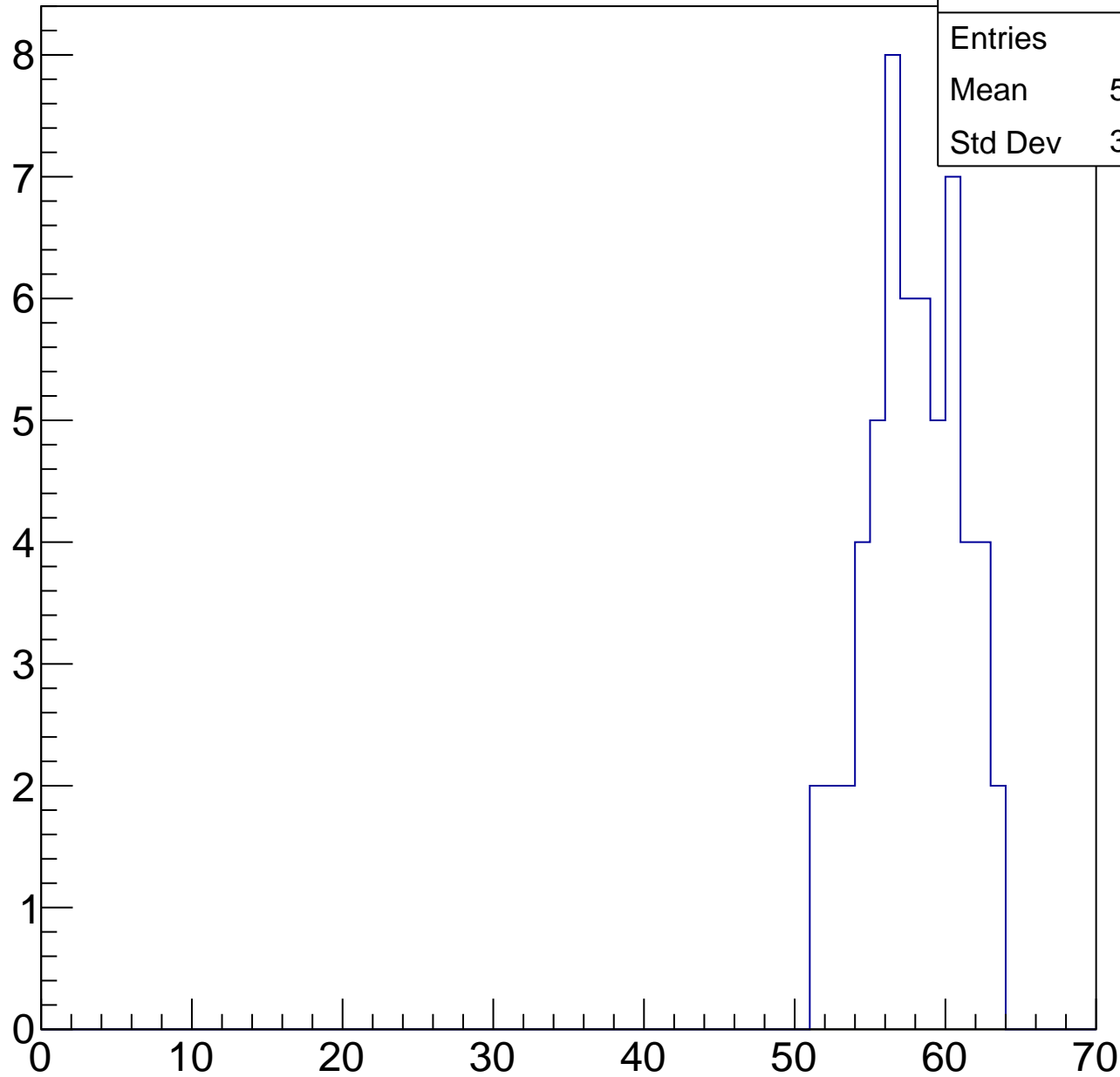
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	57.44
Std Dev	3.055

ampl

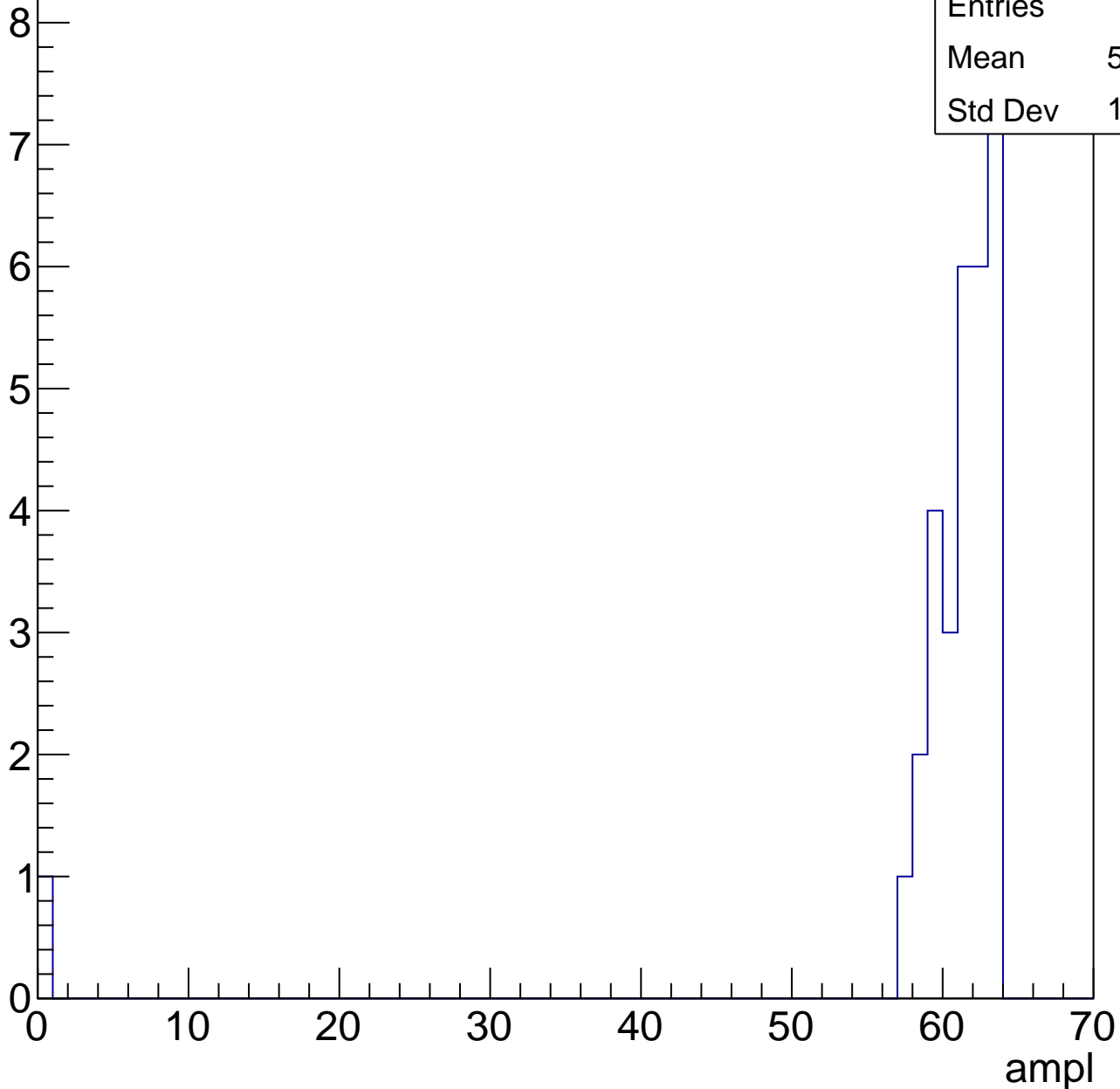


B1L100S, U12-ch47, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	59.06
Std Dev	10.92



B1L100S, U12-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L100S, U12-ch48, adc0

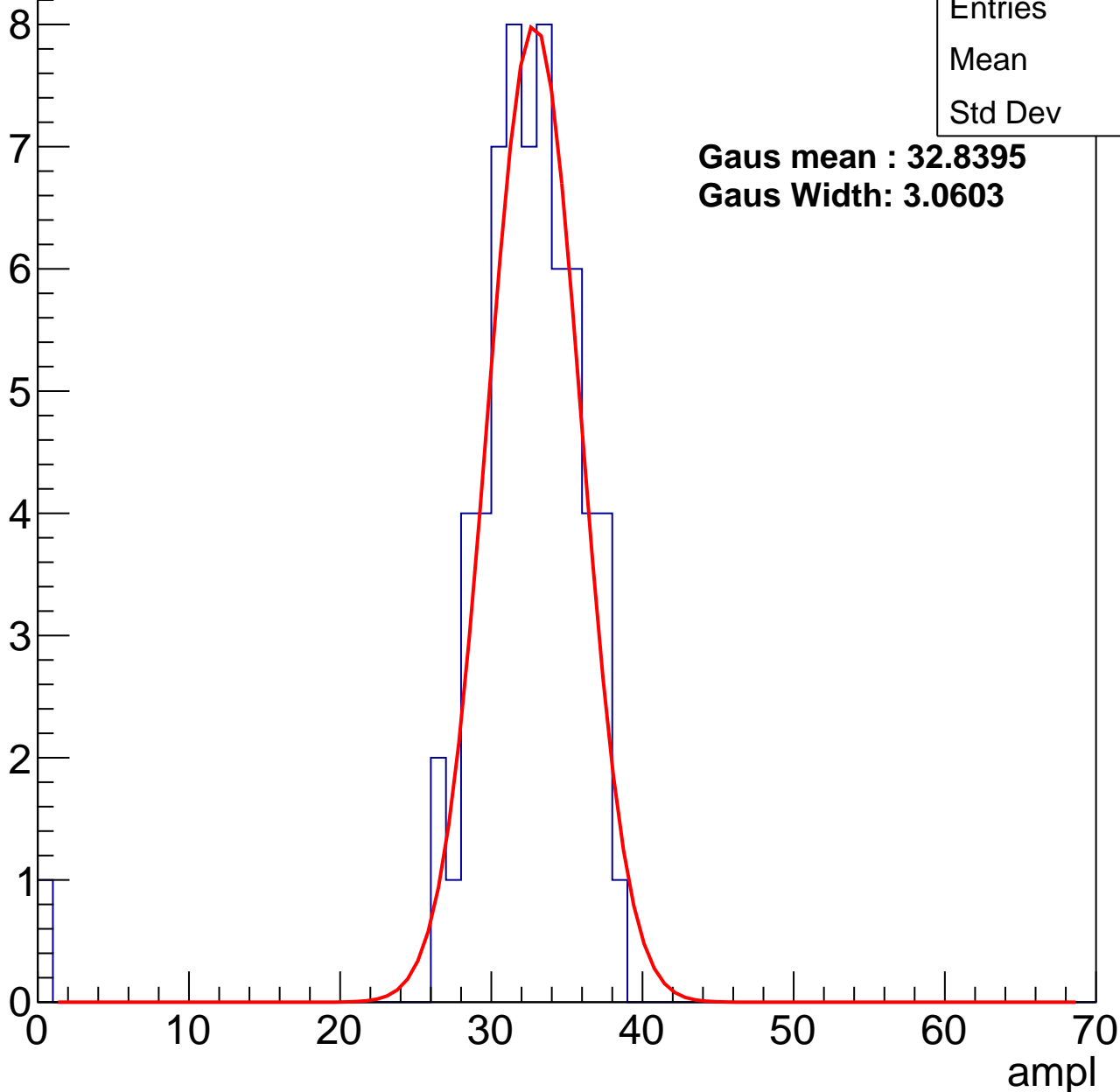
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	31.7
Std Dev	4.94

Gaus mean : 32.8395

Gaus Width: 3.0603



B1L100S, U12-ch48, adc1

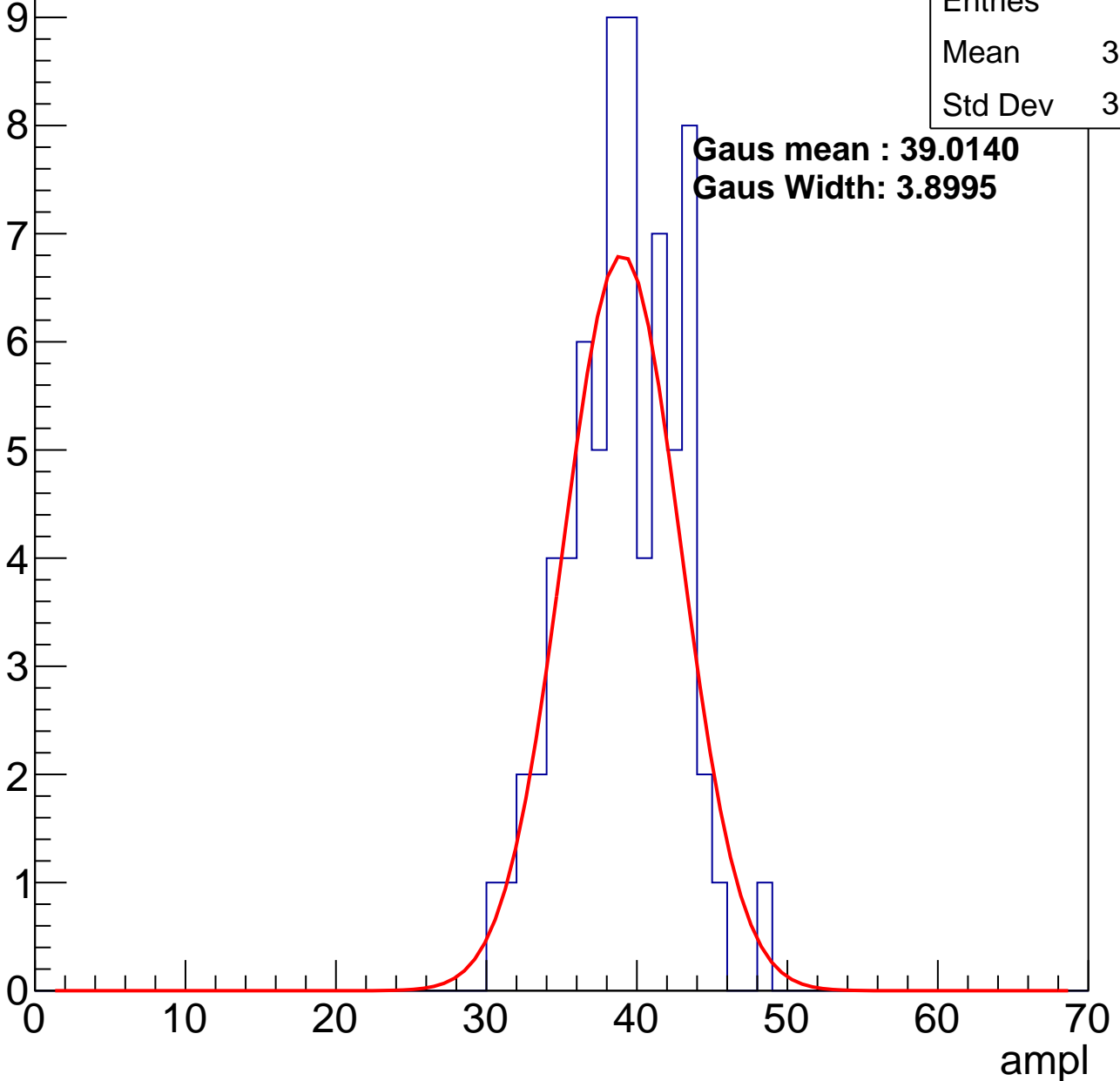
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	38.63
Std Dev	3.608

Gaus mean : 39.0140

Gaus Width: 3.8995



B1L100S, U12-ch48, adc2

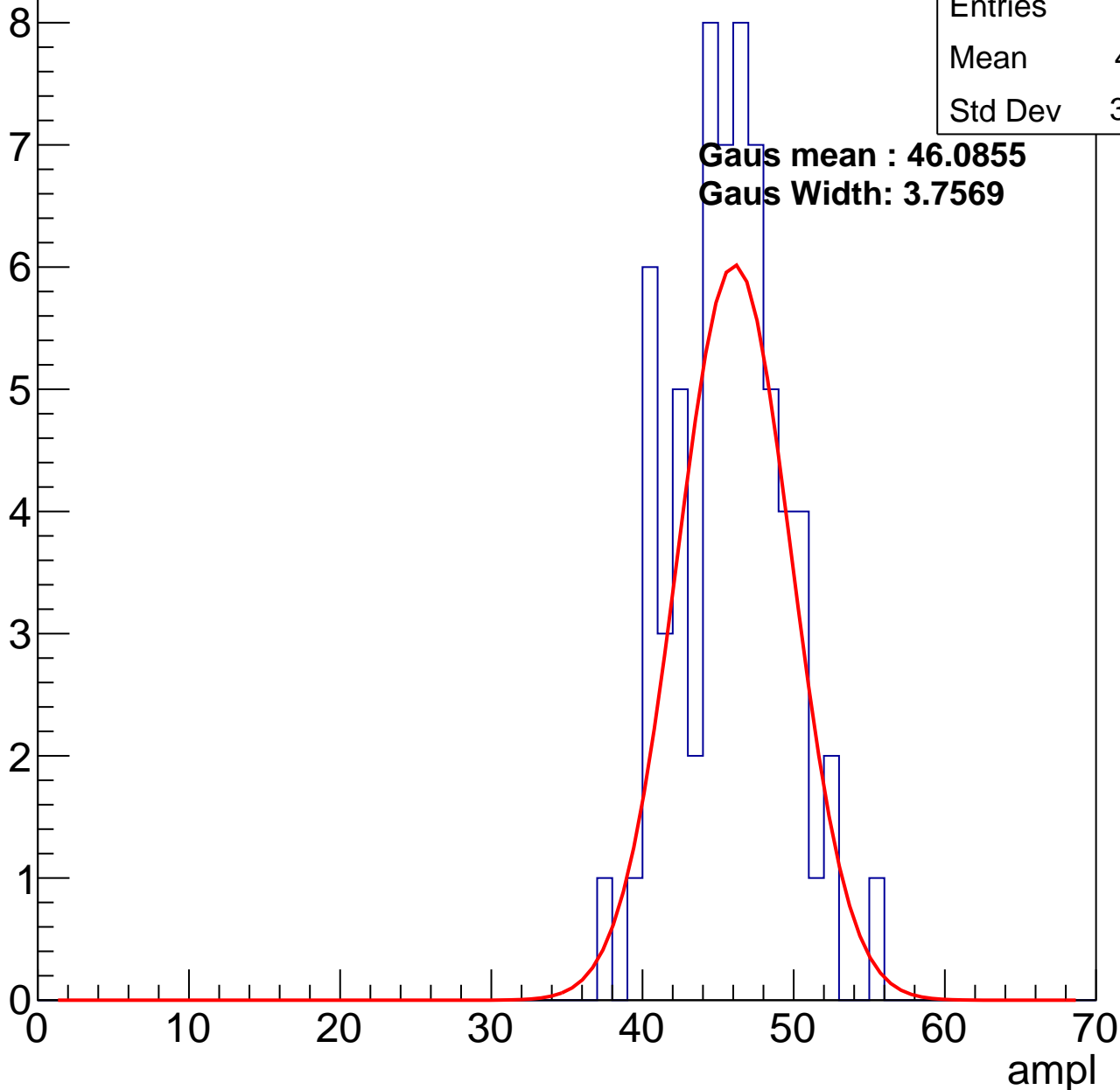
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	45.31
Std Dev	3.577

Gaus mean : 46.0855

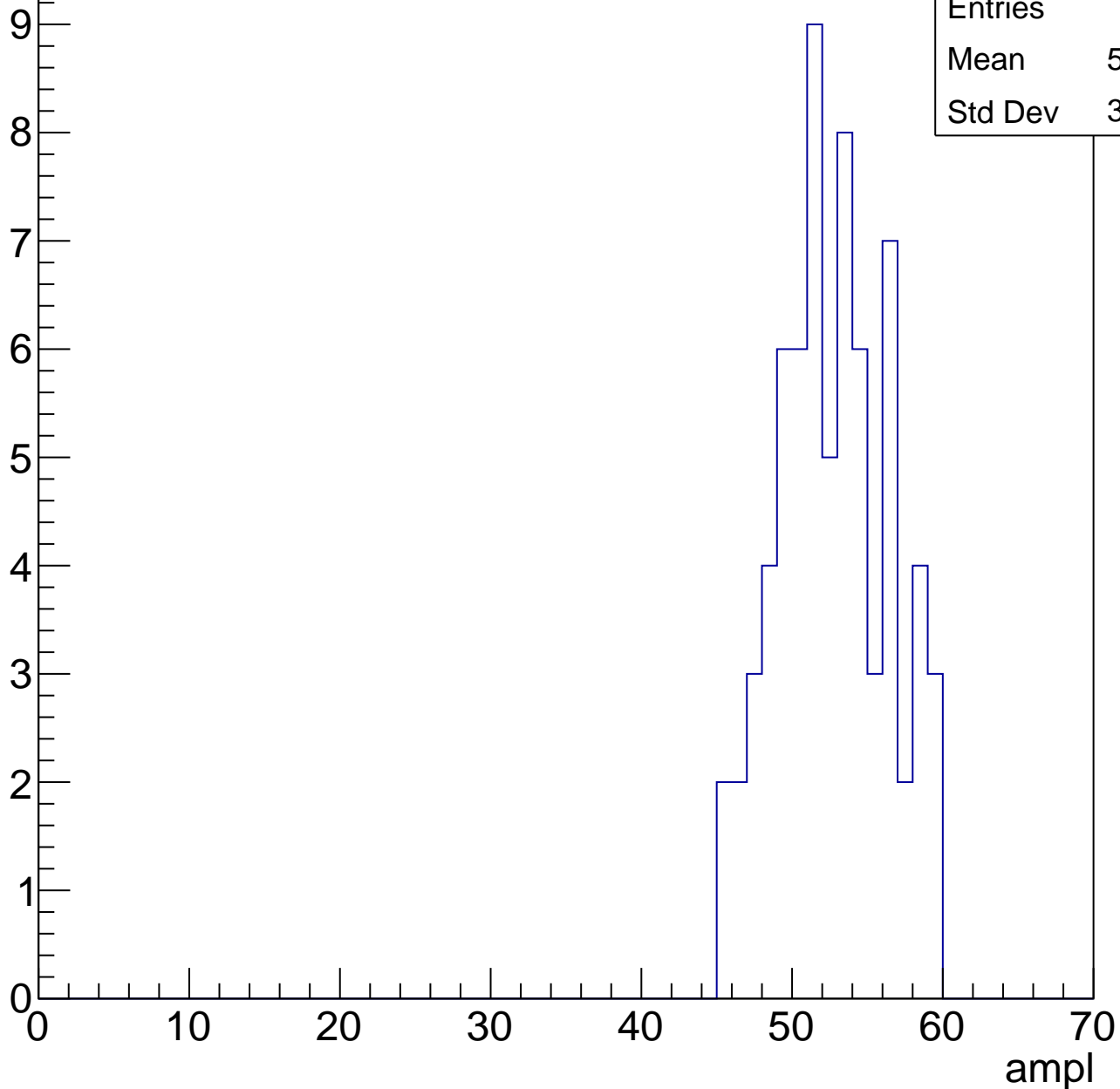
Gaus Width: 3.7569



B1L100S, U12-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

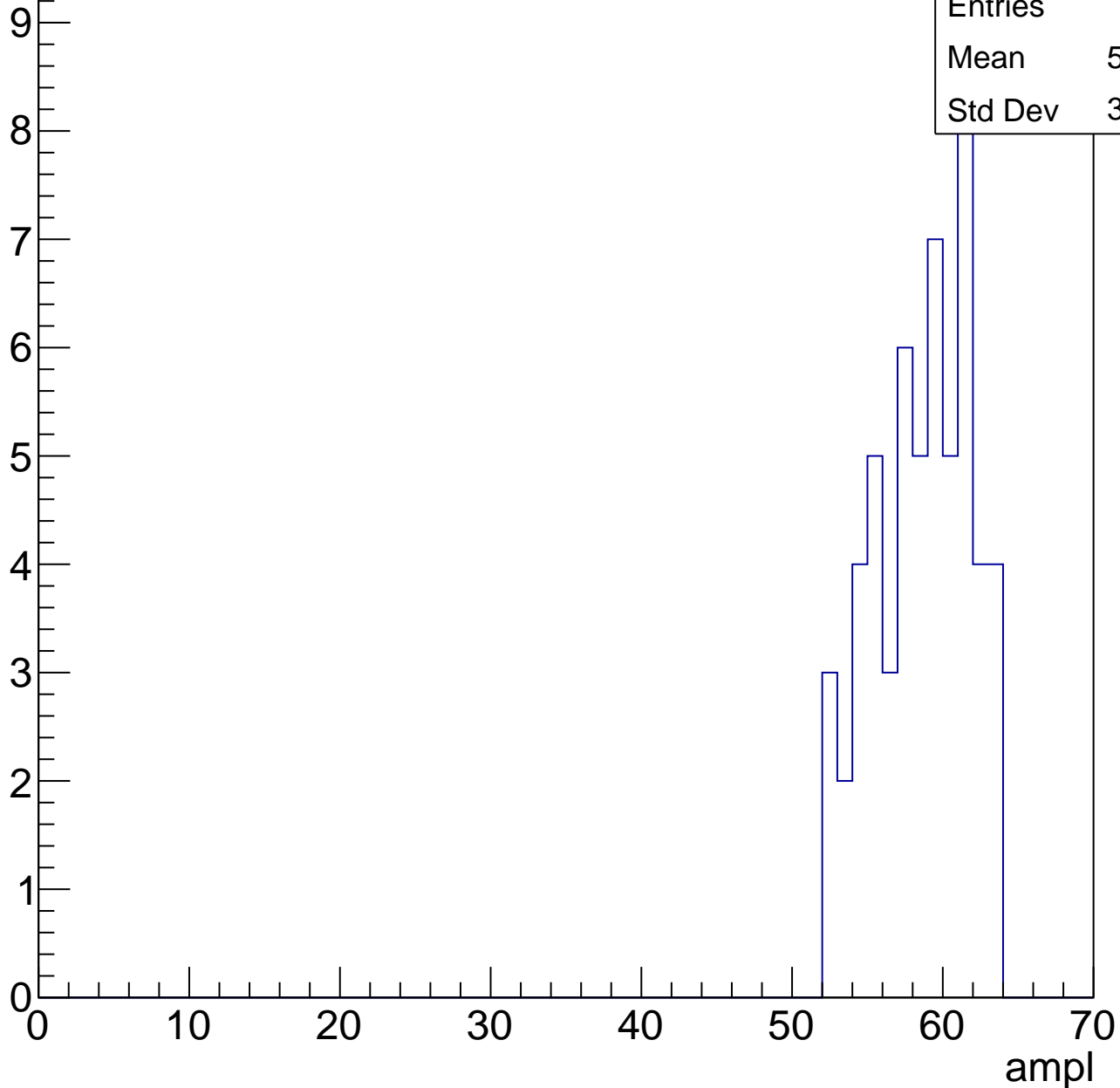
Entry



B1L100S, U12-ch48, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

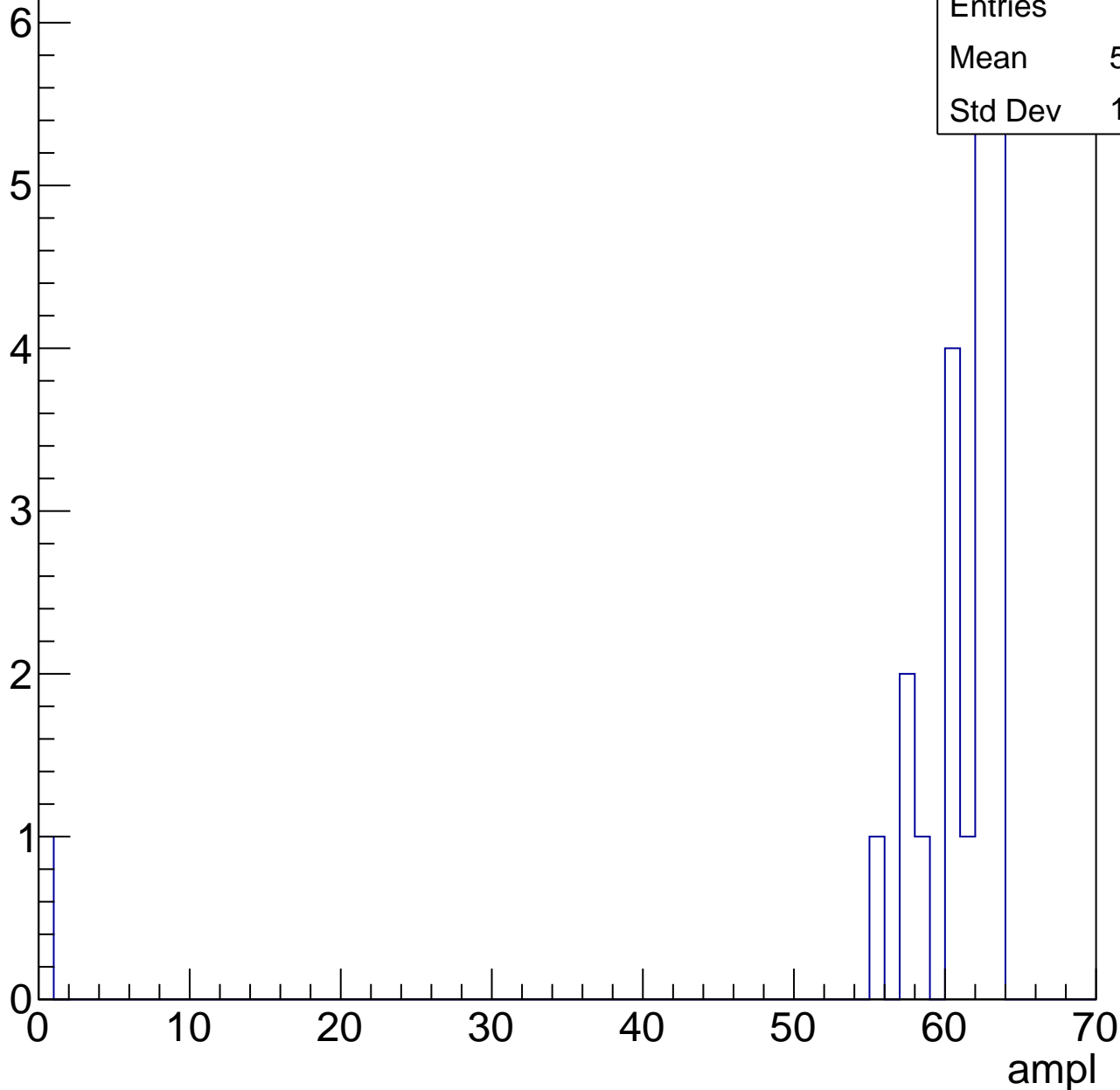


B1L100S, U12-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.09
Std Dev	12.87



B1L100S, U12-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch49, adc0

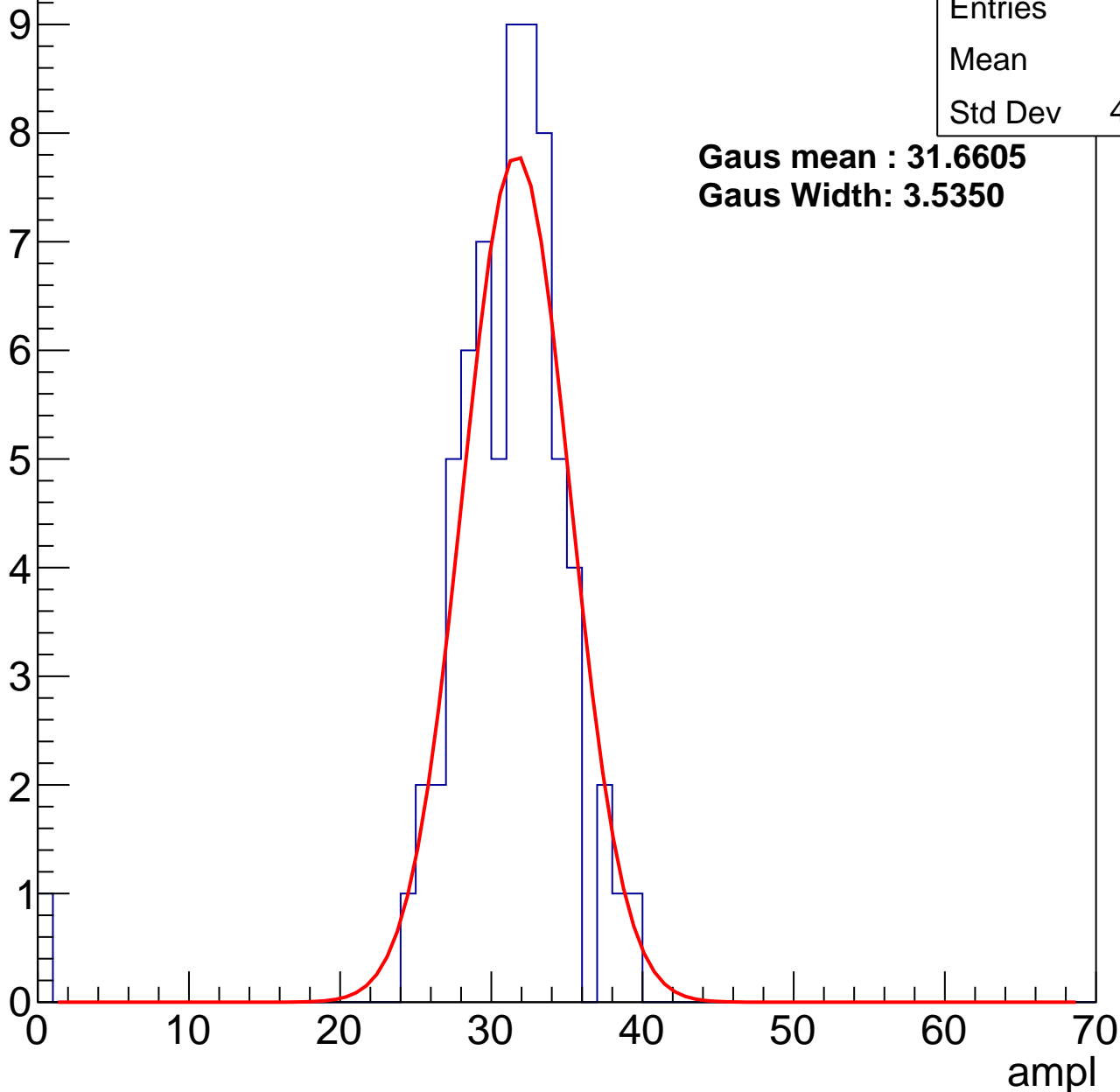
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.5
Std Dev	4.876

Gaus mean : 31.6605

Gaus Width: 3.5350



B1L100S, U12-ch49, adc1

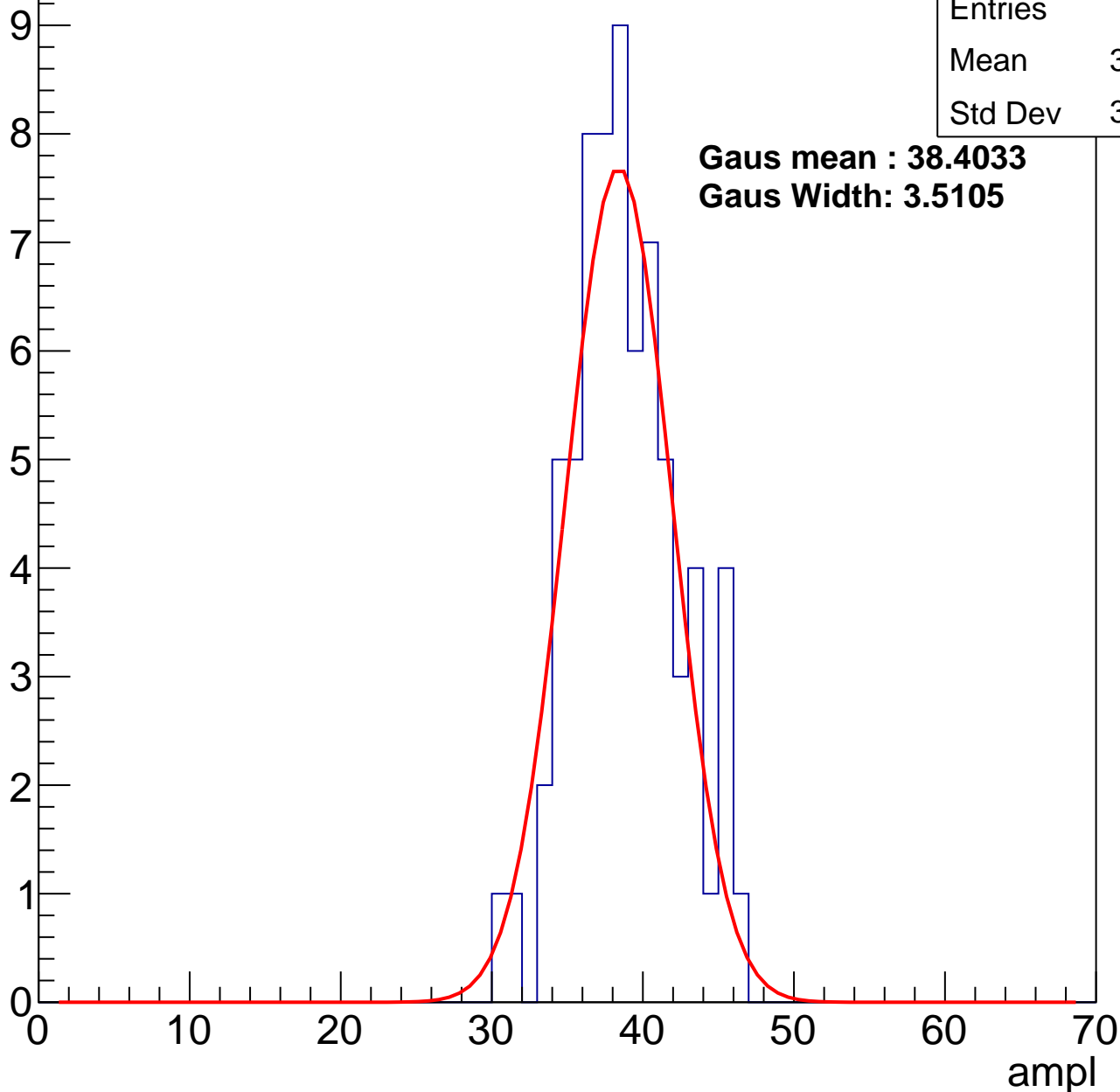
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	38.36
Std Dev	3.468

Gaus mean : 38.4033

Gaus Width: 3.5105



B1L100S, U12-ch49, adc2

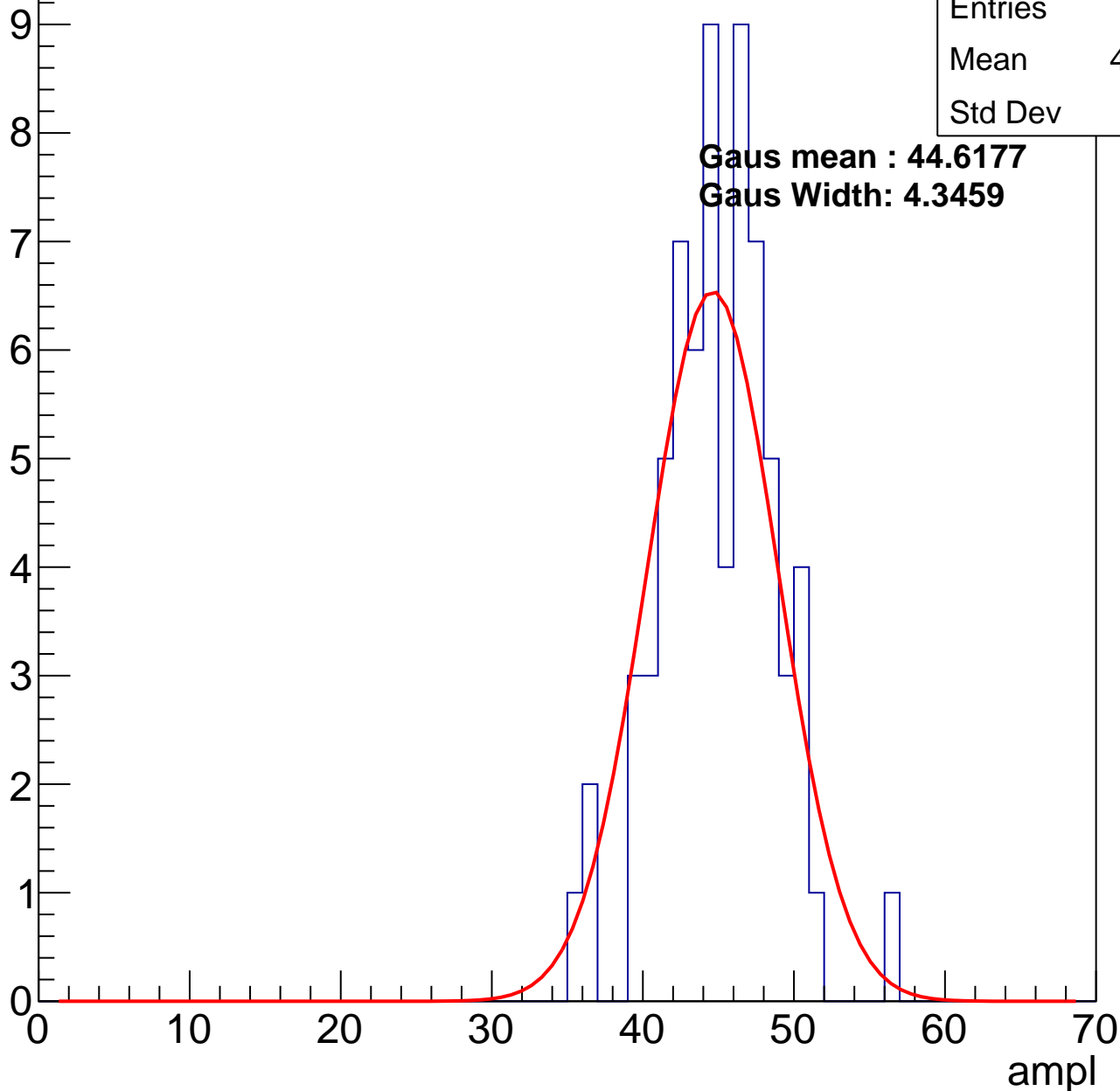
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.49
Std Dev	3.76

Gaus mean : 44.6177

Gaus Width: 4.3459

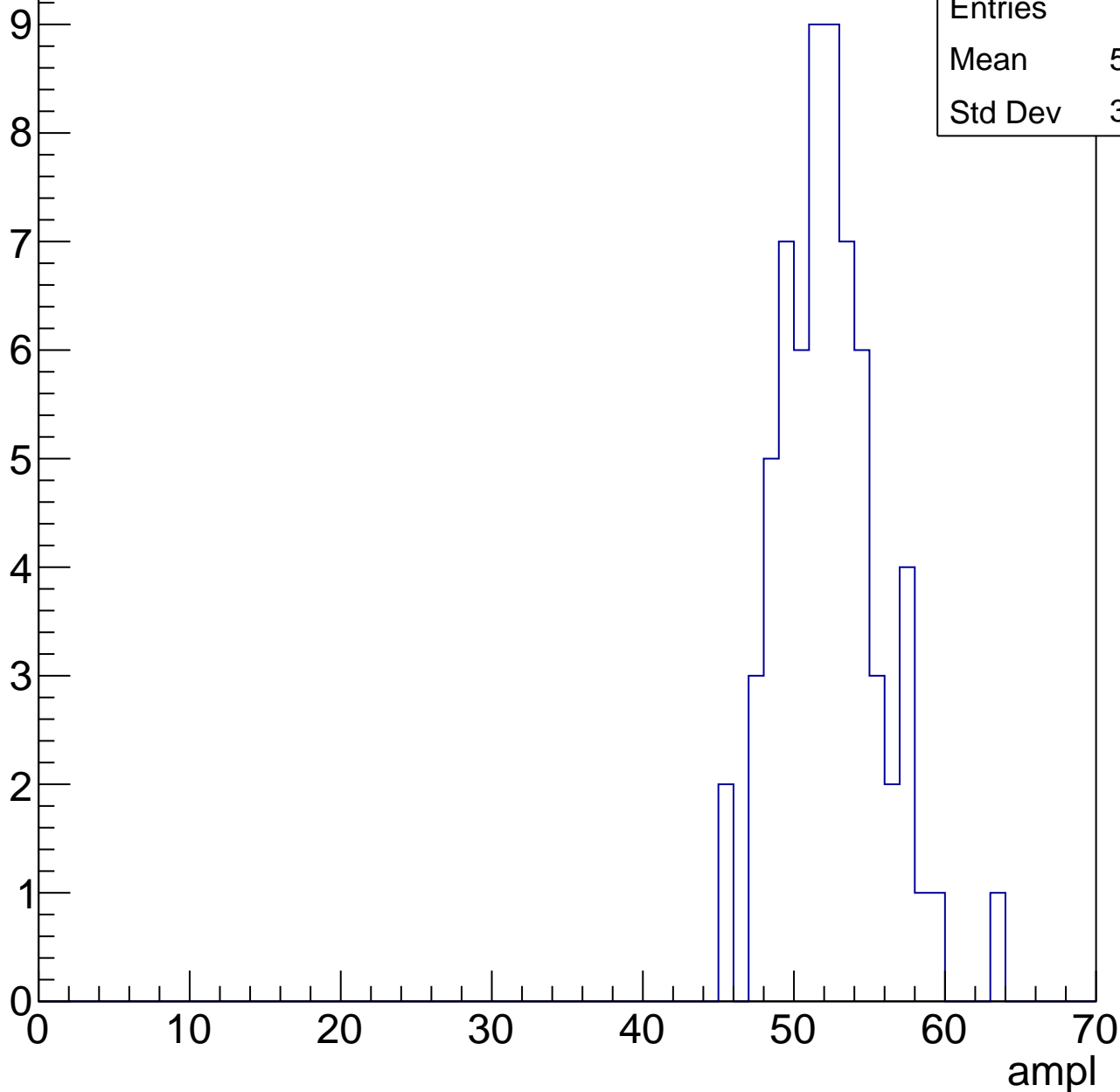


B1L100S, U12-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

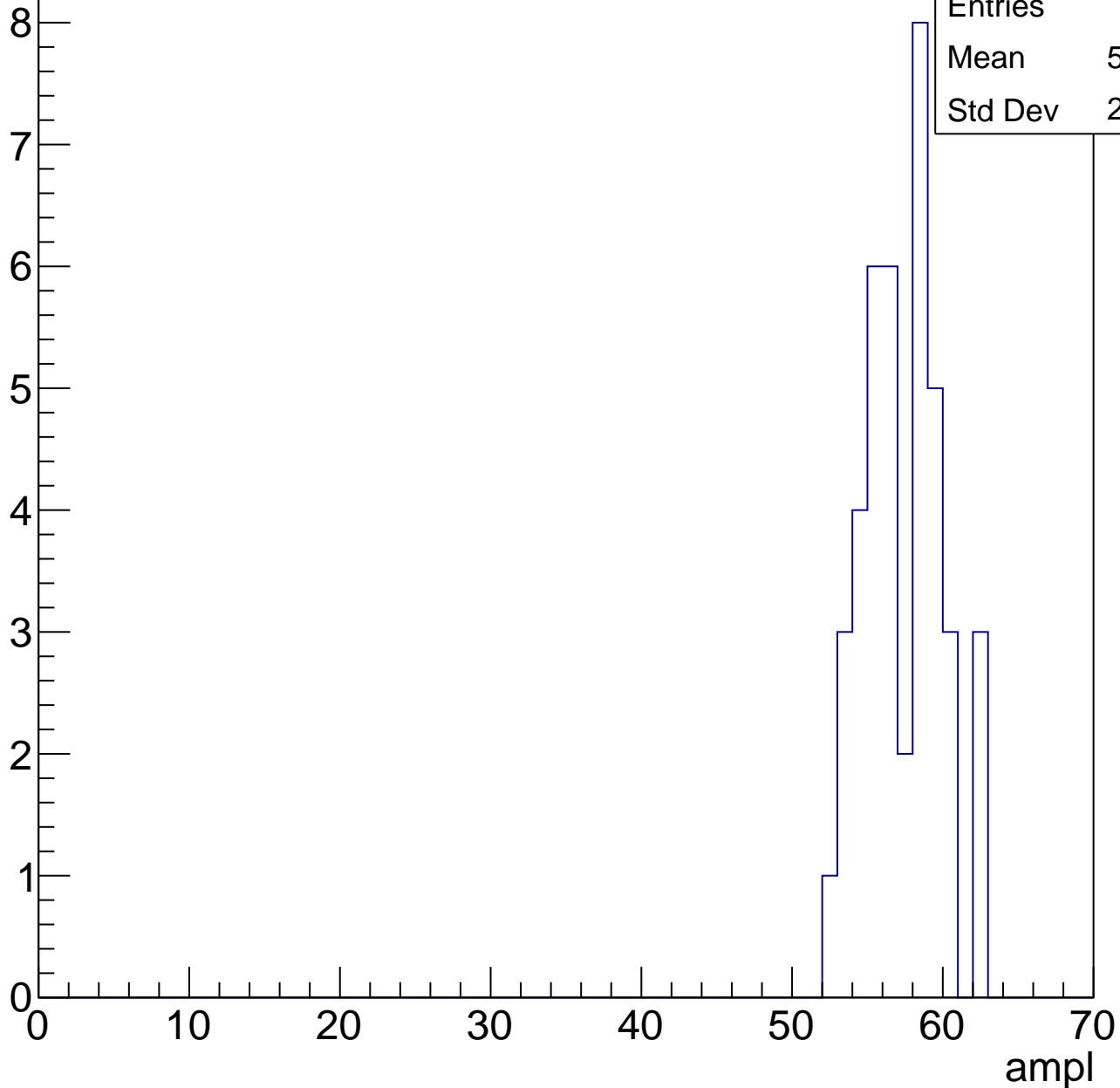
Entries	66
Mean	51.83
Std Dev	3.356



B1L100S, U12-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

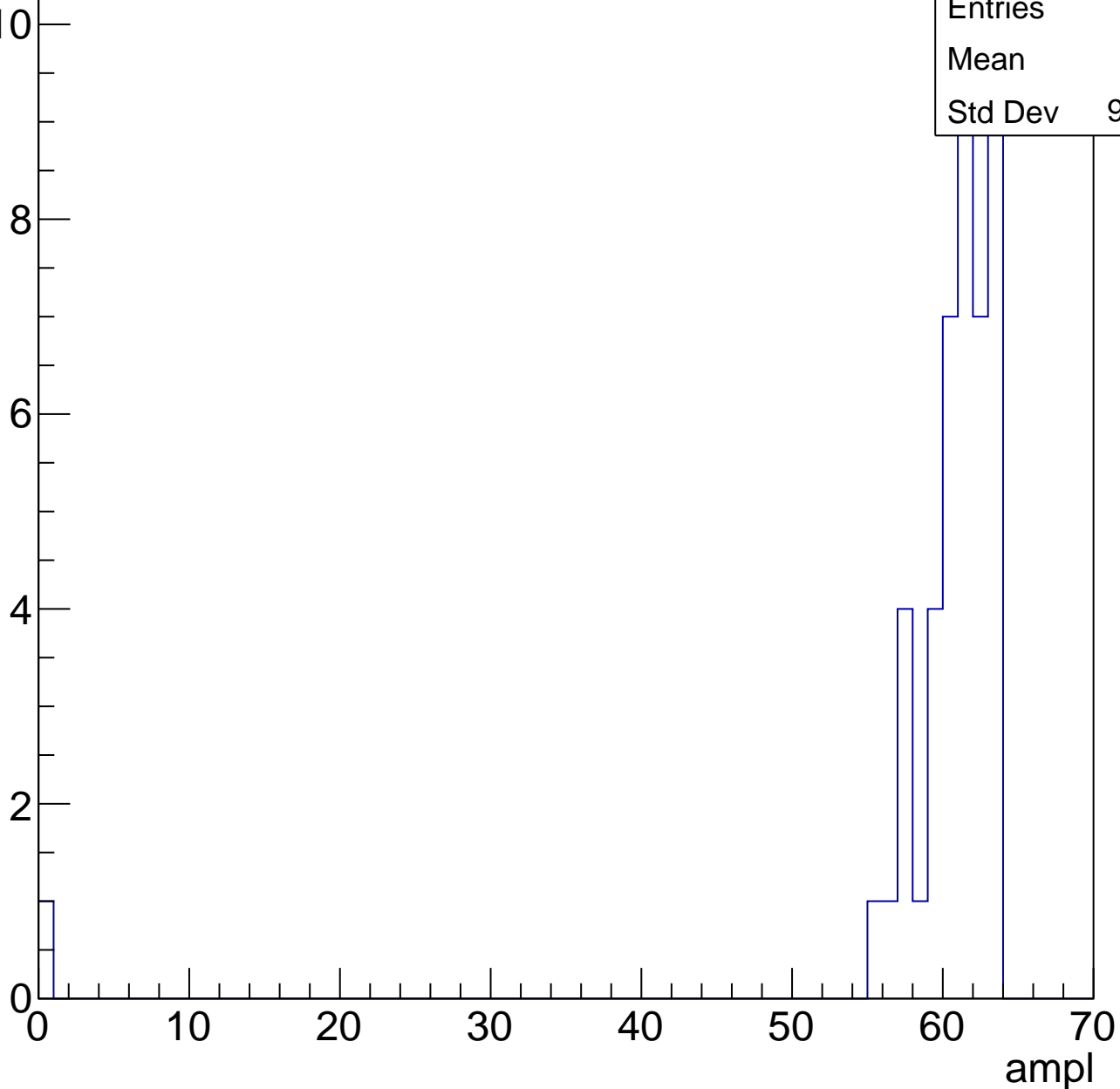


B1L100S, U12-ch49, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	59.2
Std Dev	9.159



B1L100S, U12-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch50, adc0

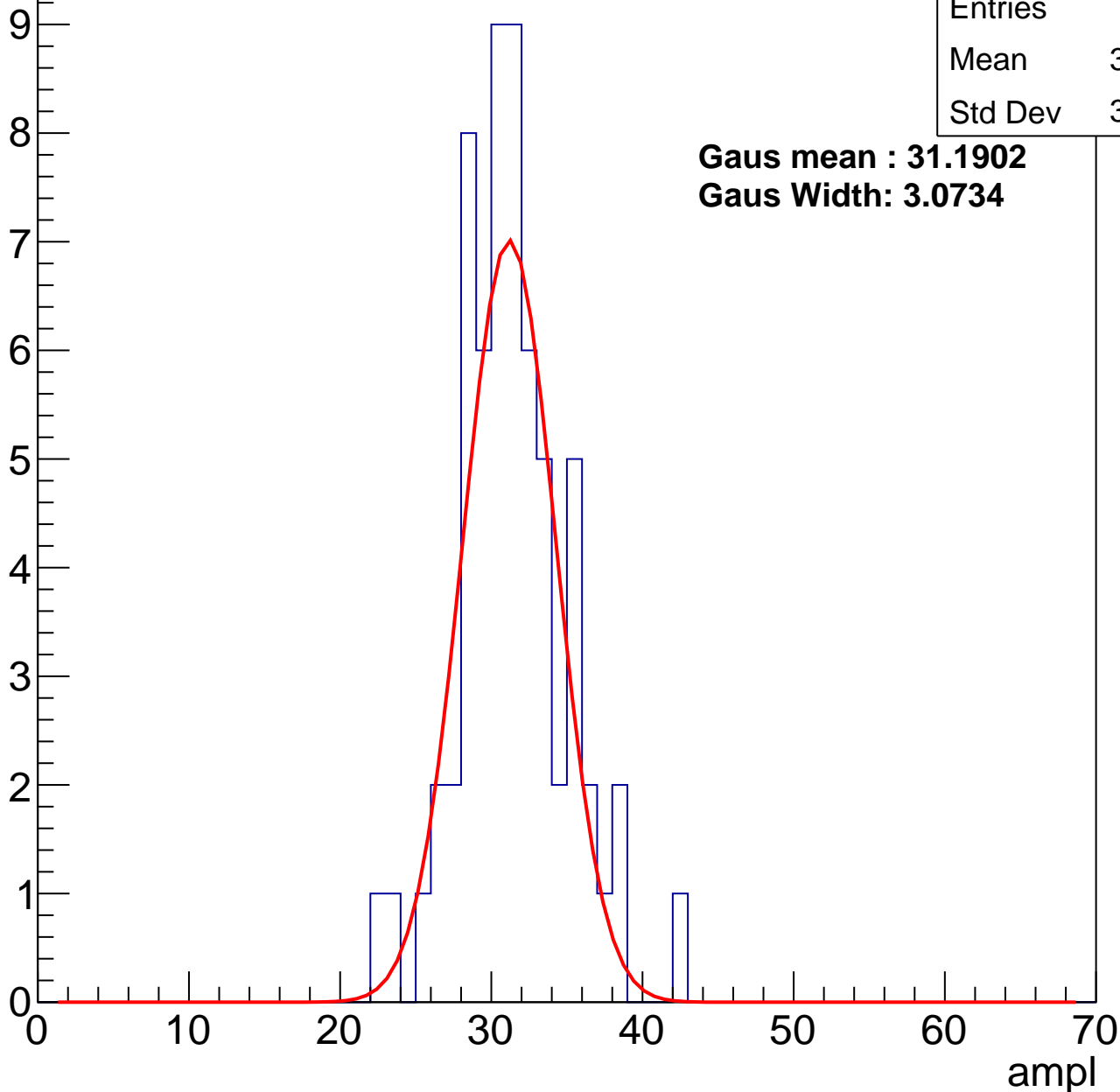
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	30.95
Std Dev	3.556

Gaus mean : 31.1902

Gaus Width: 3.0734



B1L100S, U12-ch50, adc1

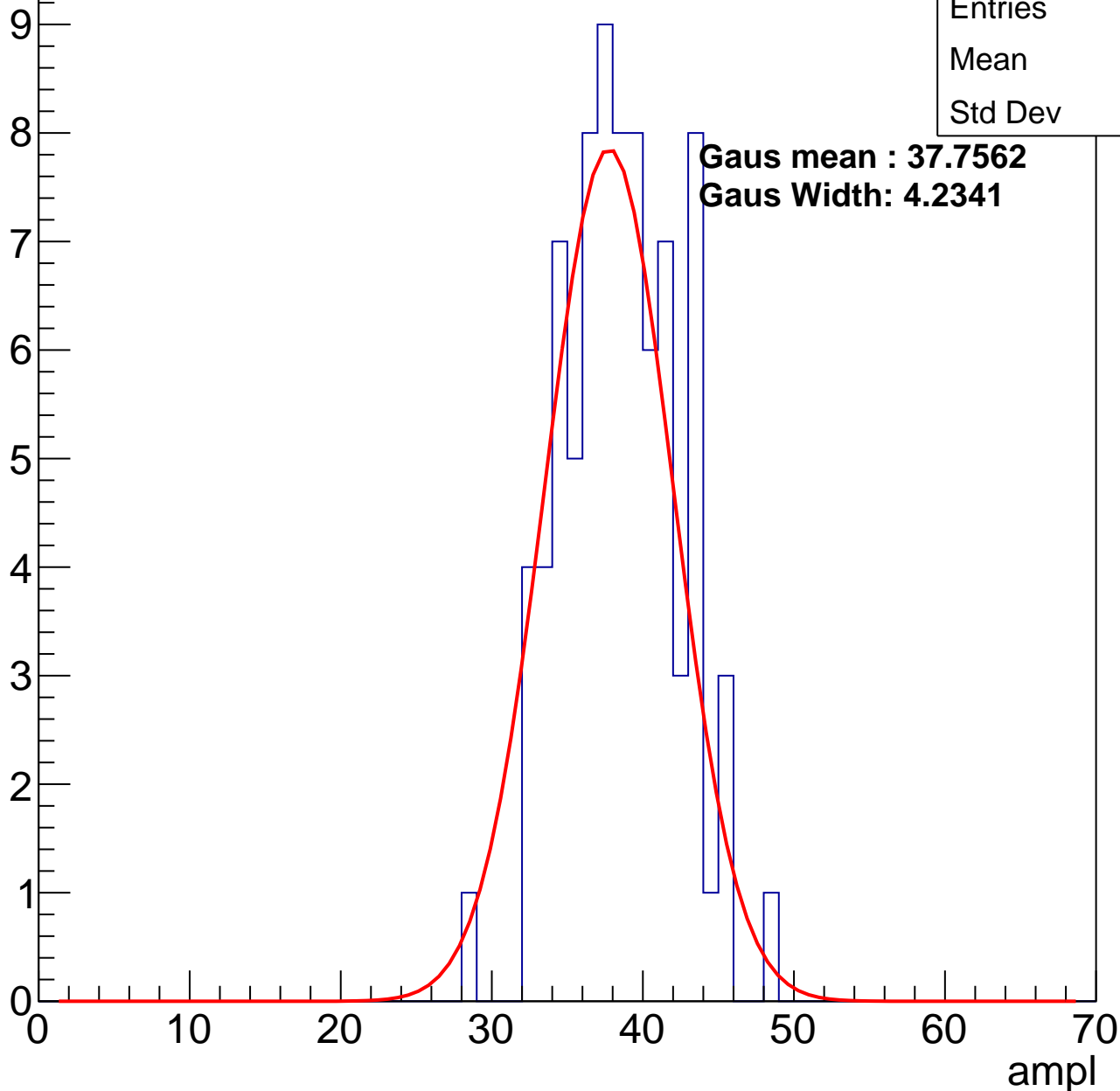
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	38.1
Std Dev	3.75

Gaus mean : 37.7562

Gaus Width: 4.2341



B1L100S, U12-ch50, adc2

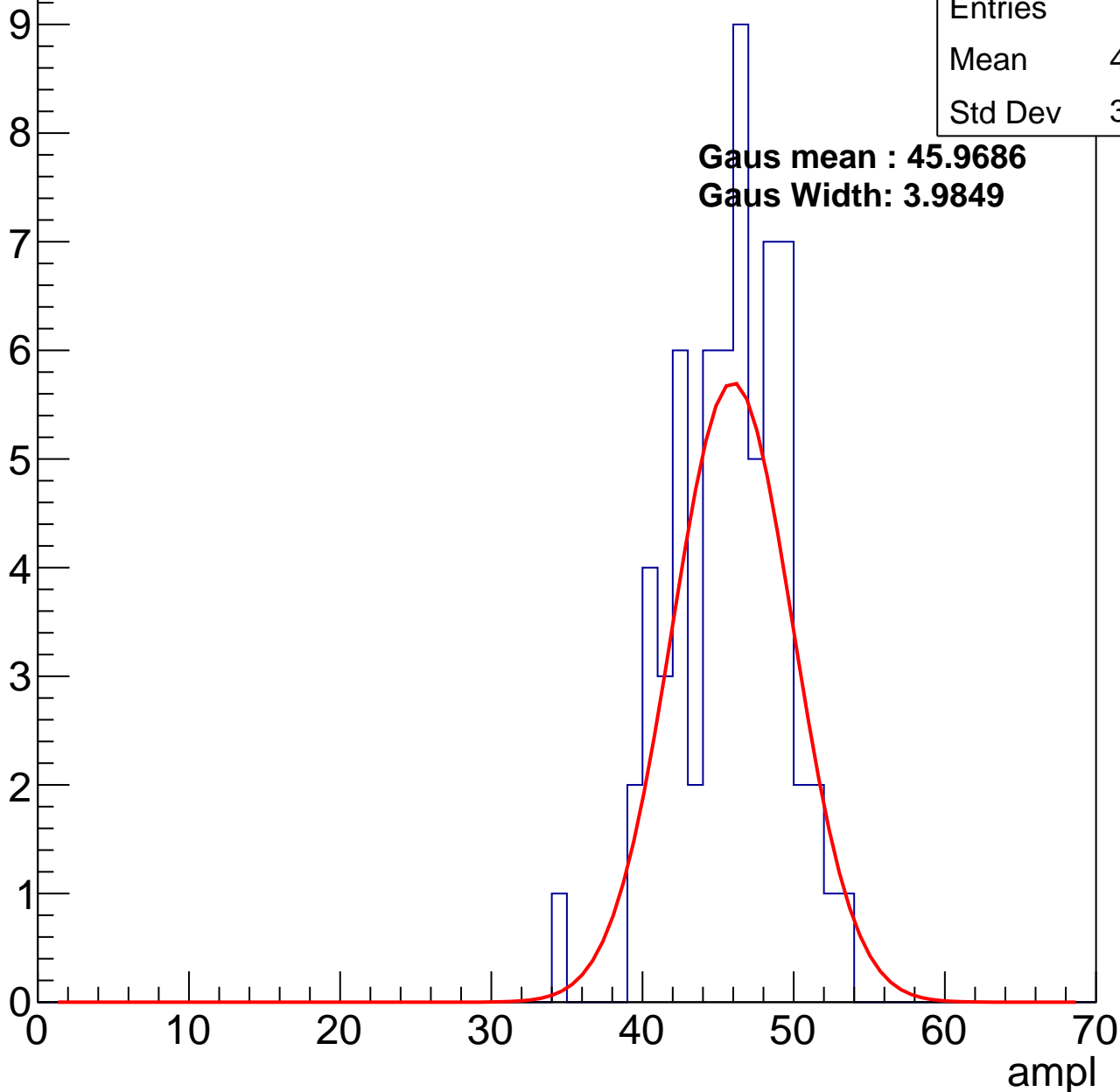
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	45.34
Std Dev	3.624

Gaus mean : 45.9686

Gaus Width: 3.9849

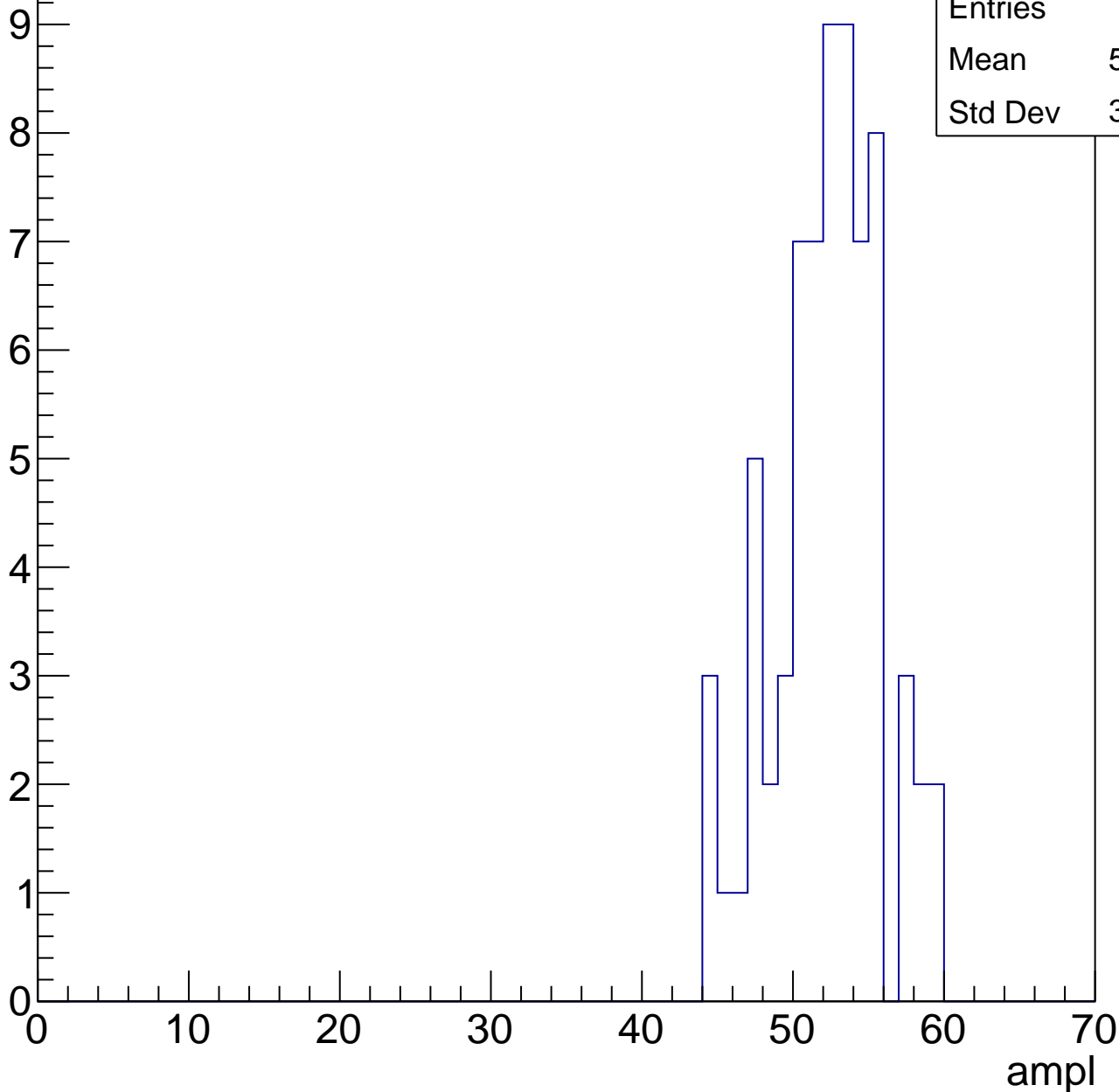


B1L100S, U12-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

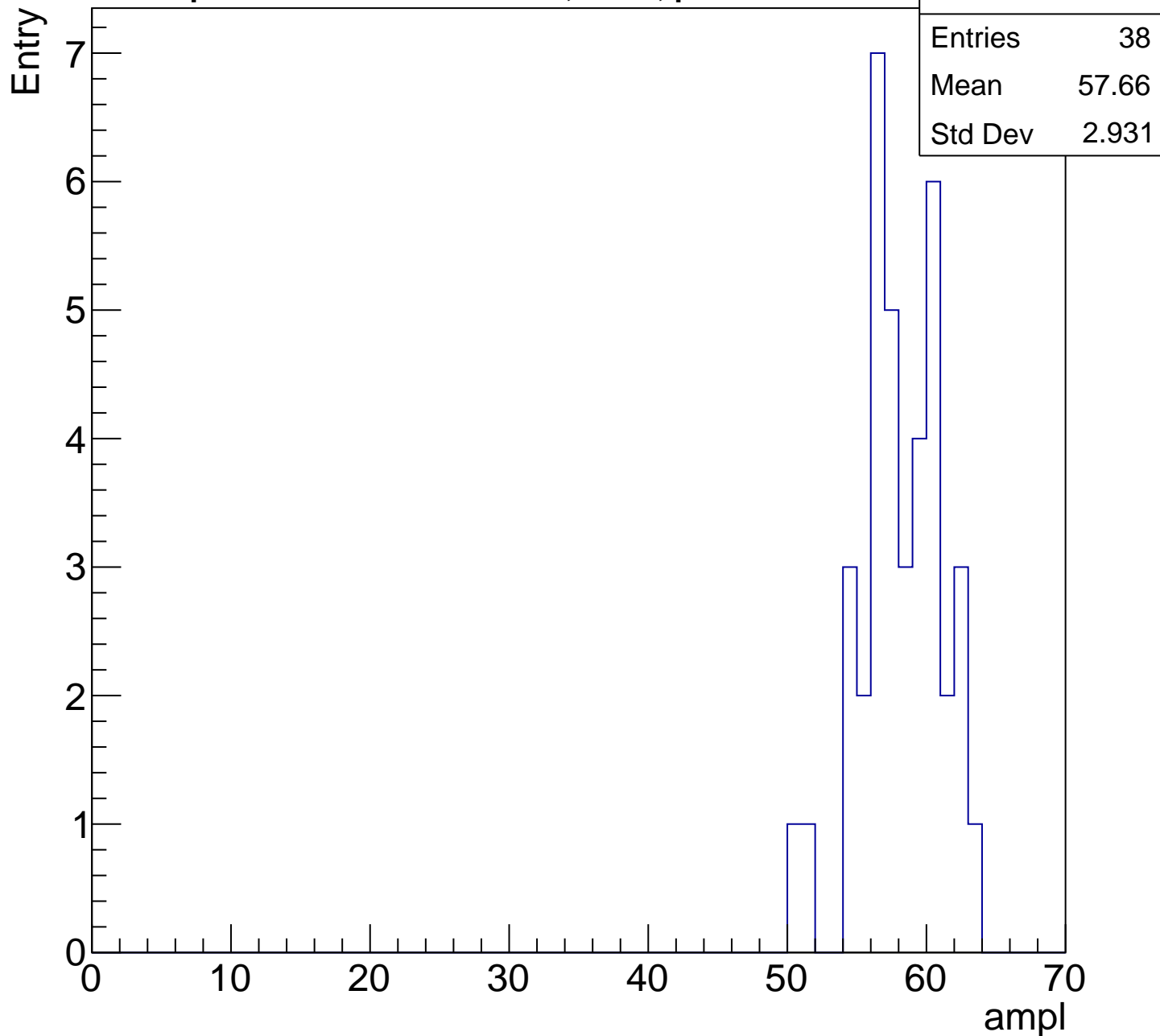
Entry

Entries	69
Mean	51.83
Std Dev	3.506



B1L100S, U12-ch50, adc4

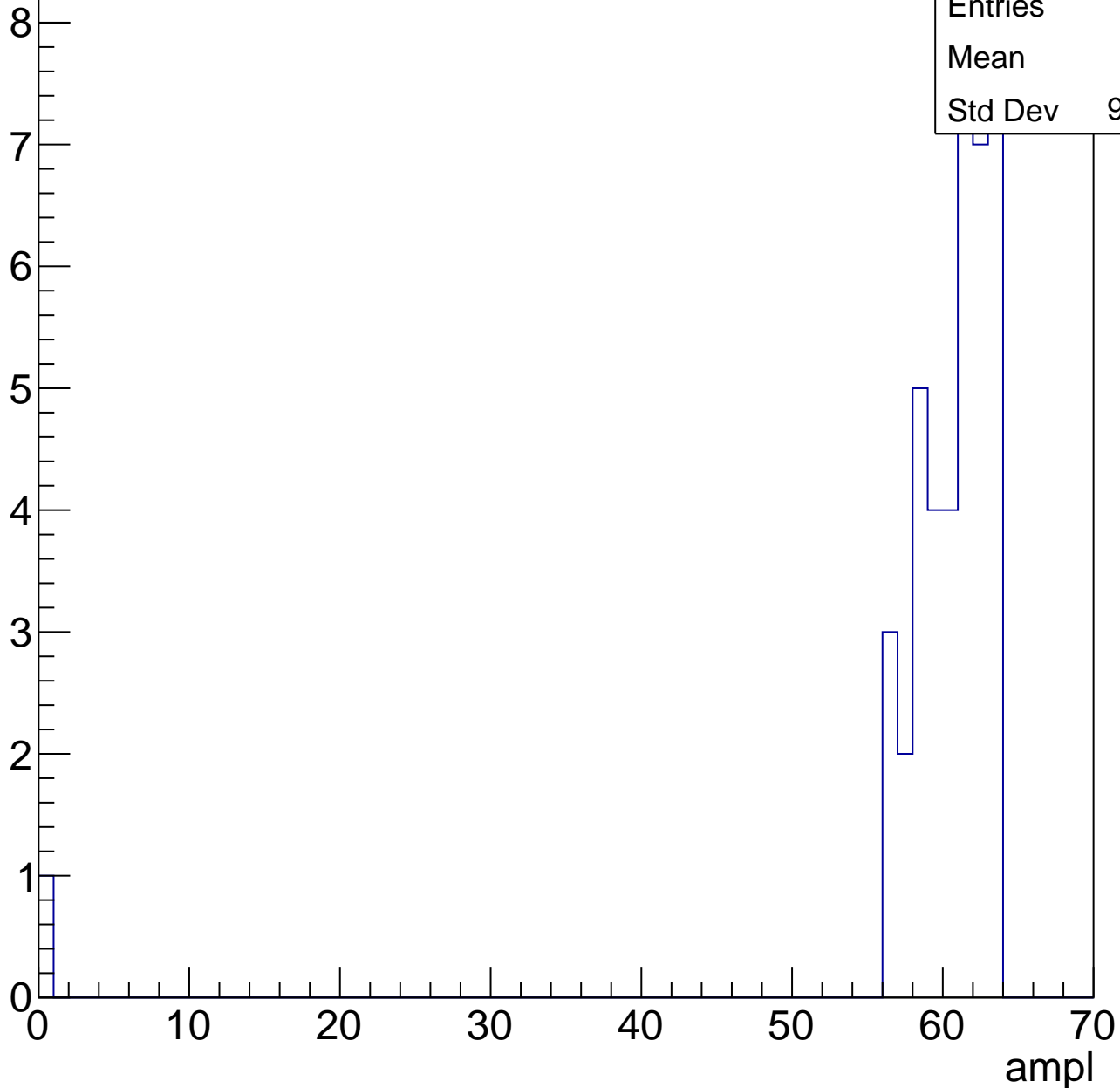
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U12-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch51, adc0

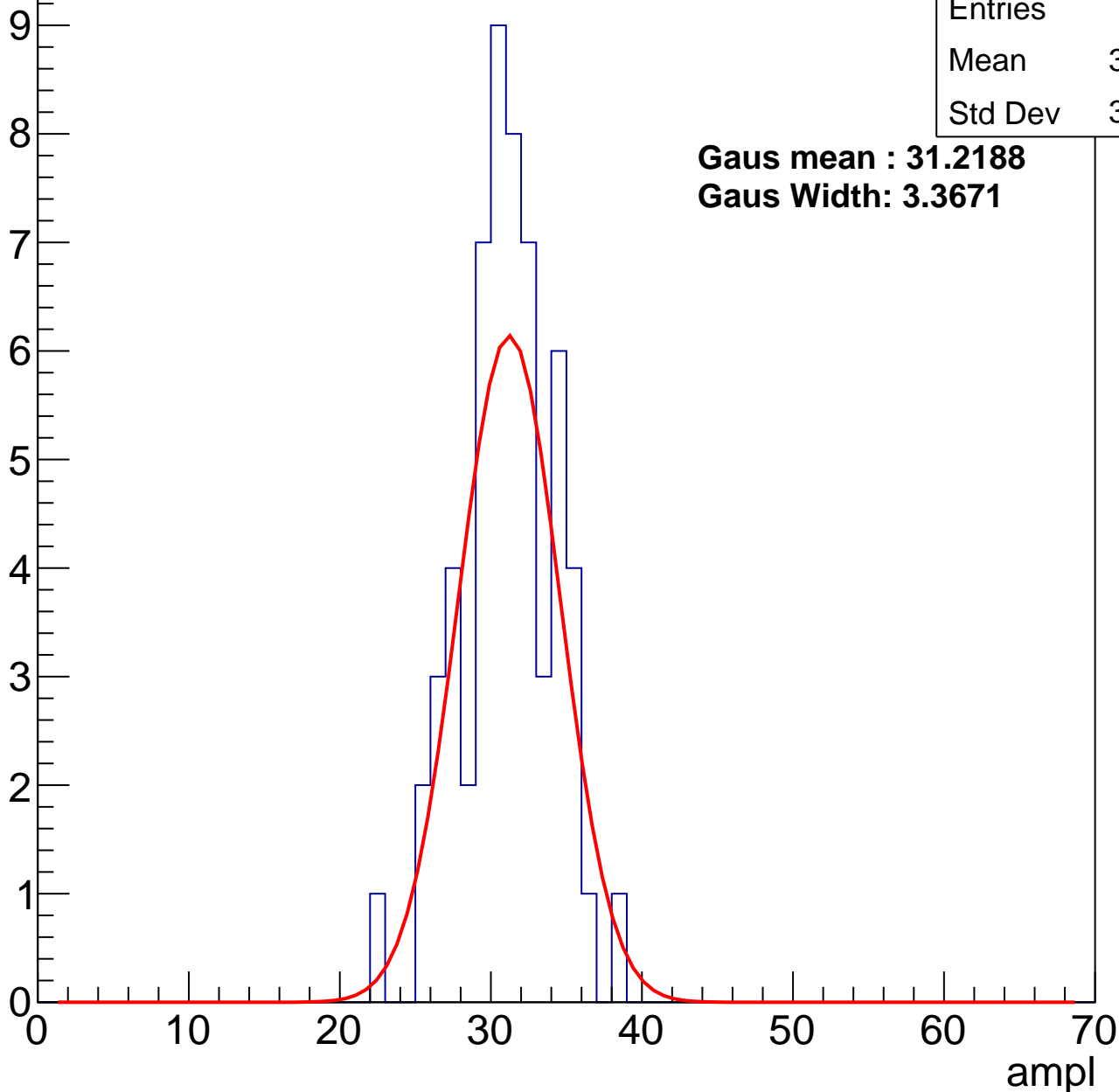
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	30.62
Std Dev	3.078

Gaus mean : 31.2188

Gaus Width: 3.3671



B1L100S, U12-ch51, adc1

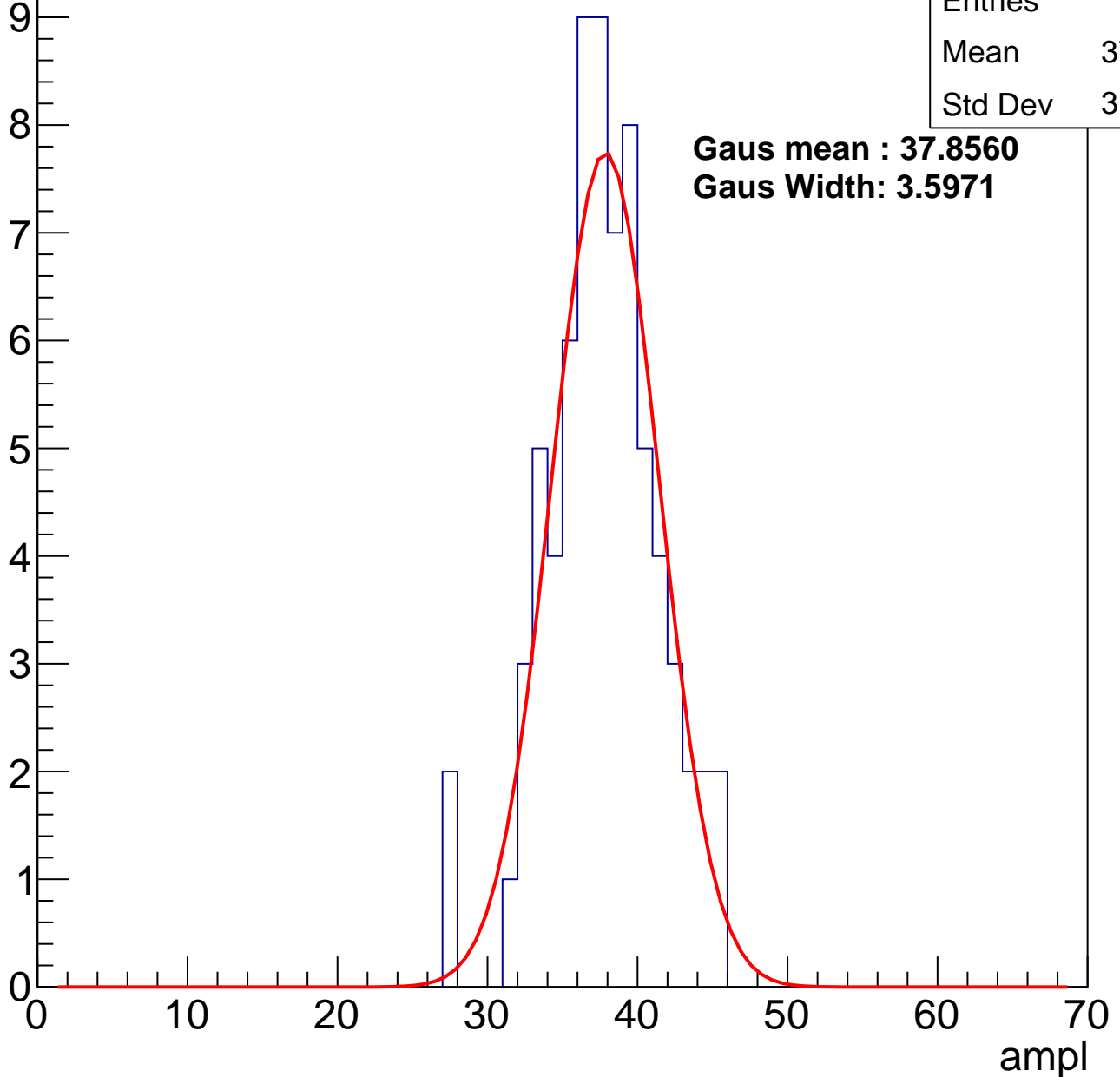
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.24
Std Dev	3.684

Gaus mean : 37.8560

Gaus Width: 3.5971



B1L100S, U12-ch51, adc2

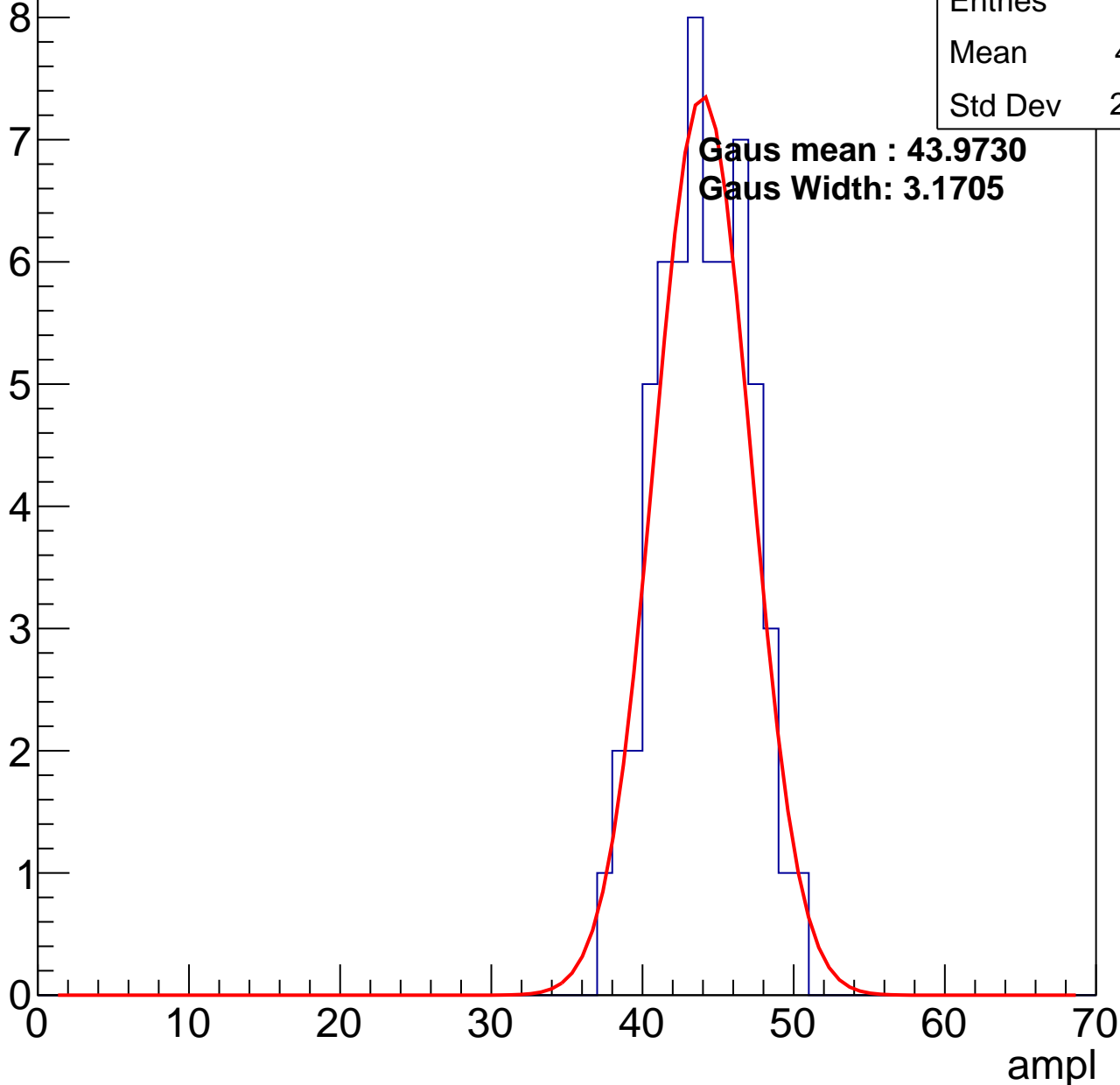
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.51
Std Dev	2.942

Gaus mean : 43.9730

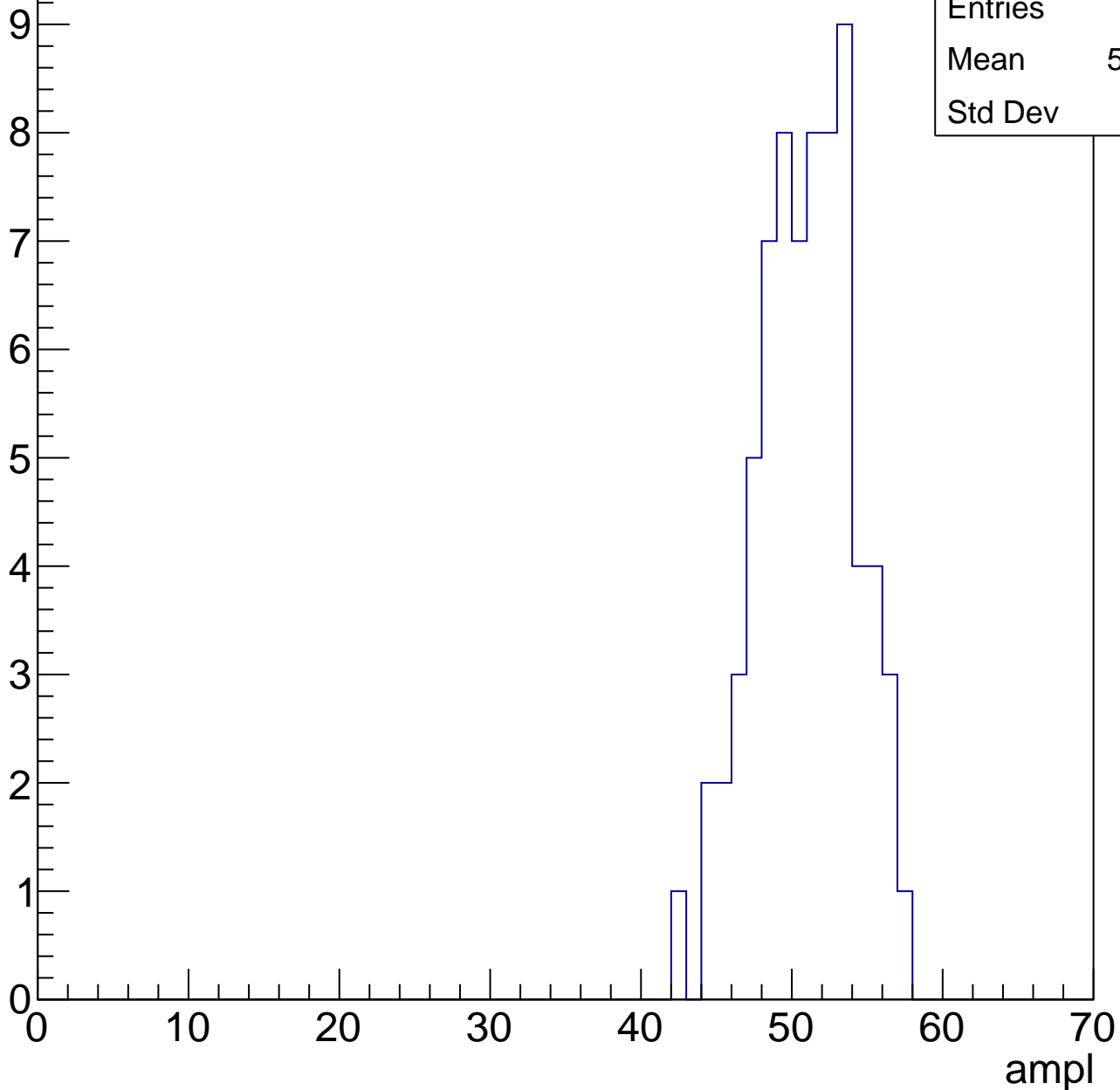
Gaus Width: 3.1705



B1L100S, U12-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2

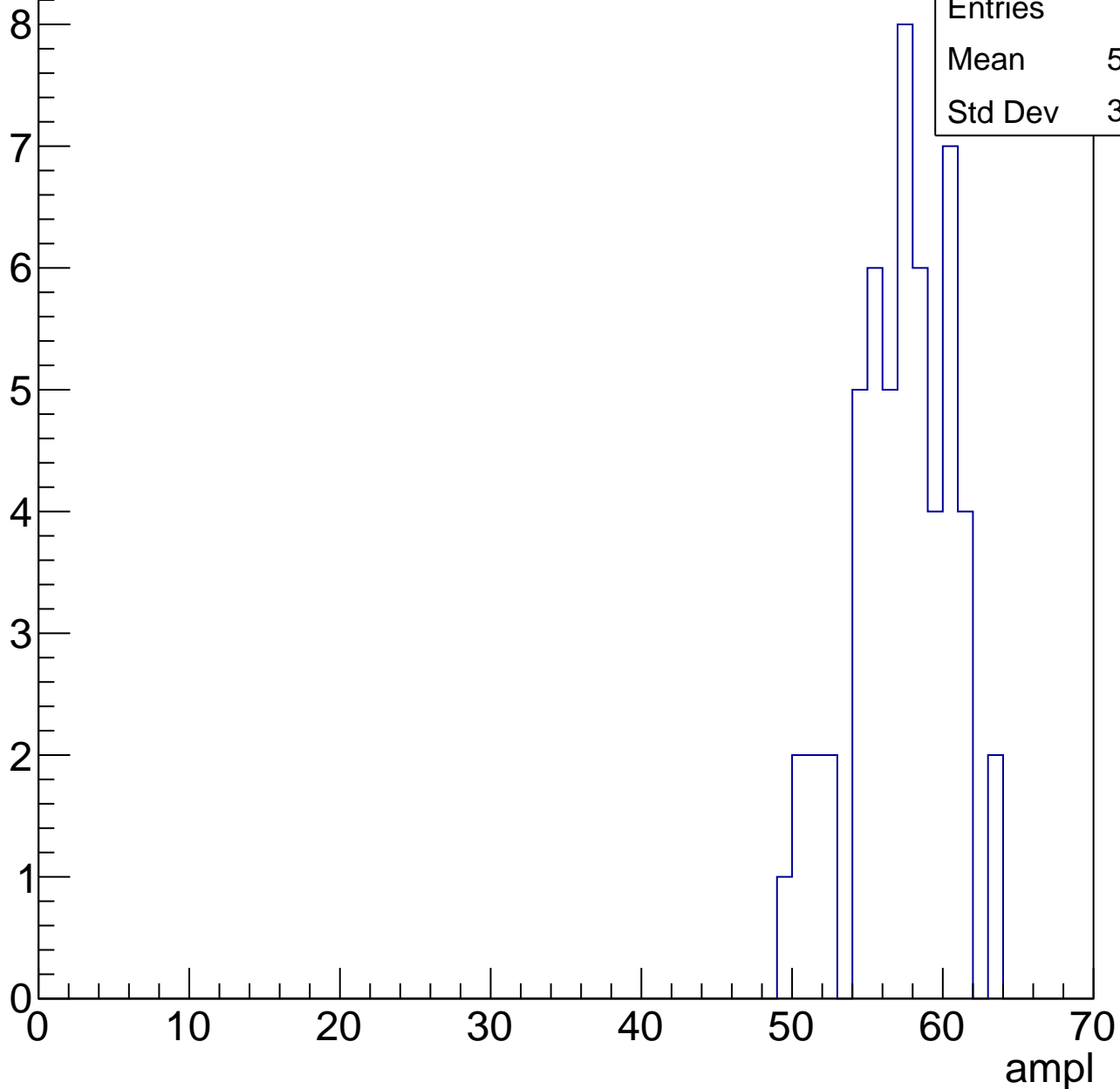
Entry



B1L100S, U12-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

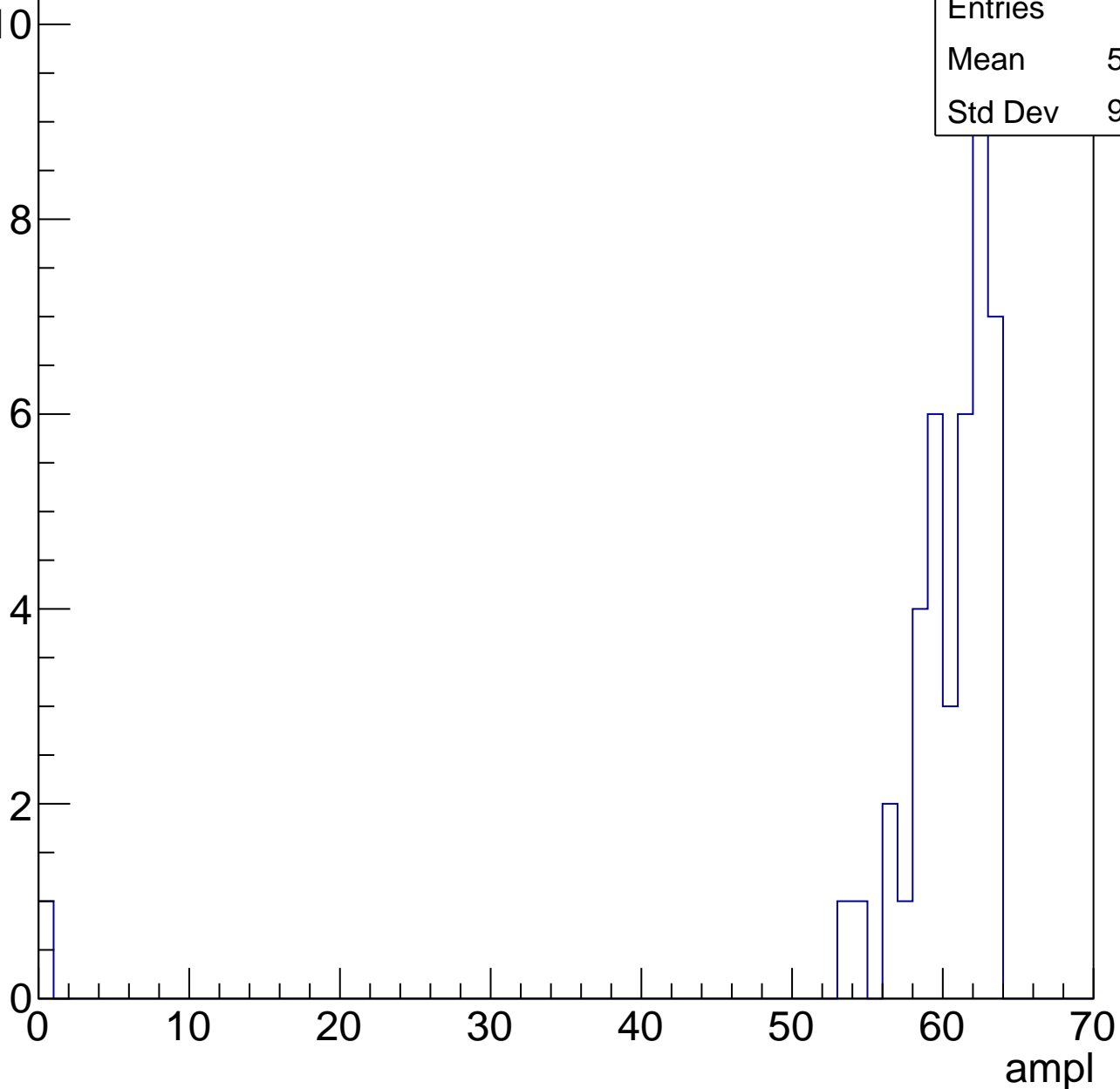


B1L100S, U12-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	58.79
Std Dev	9.506



B1L100S, U12-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch52, adc0

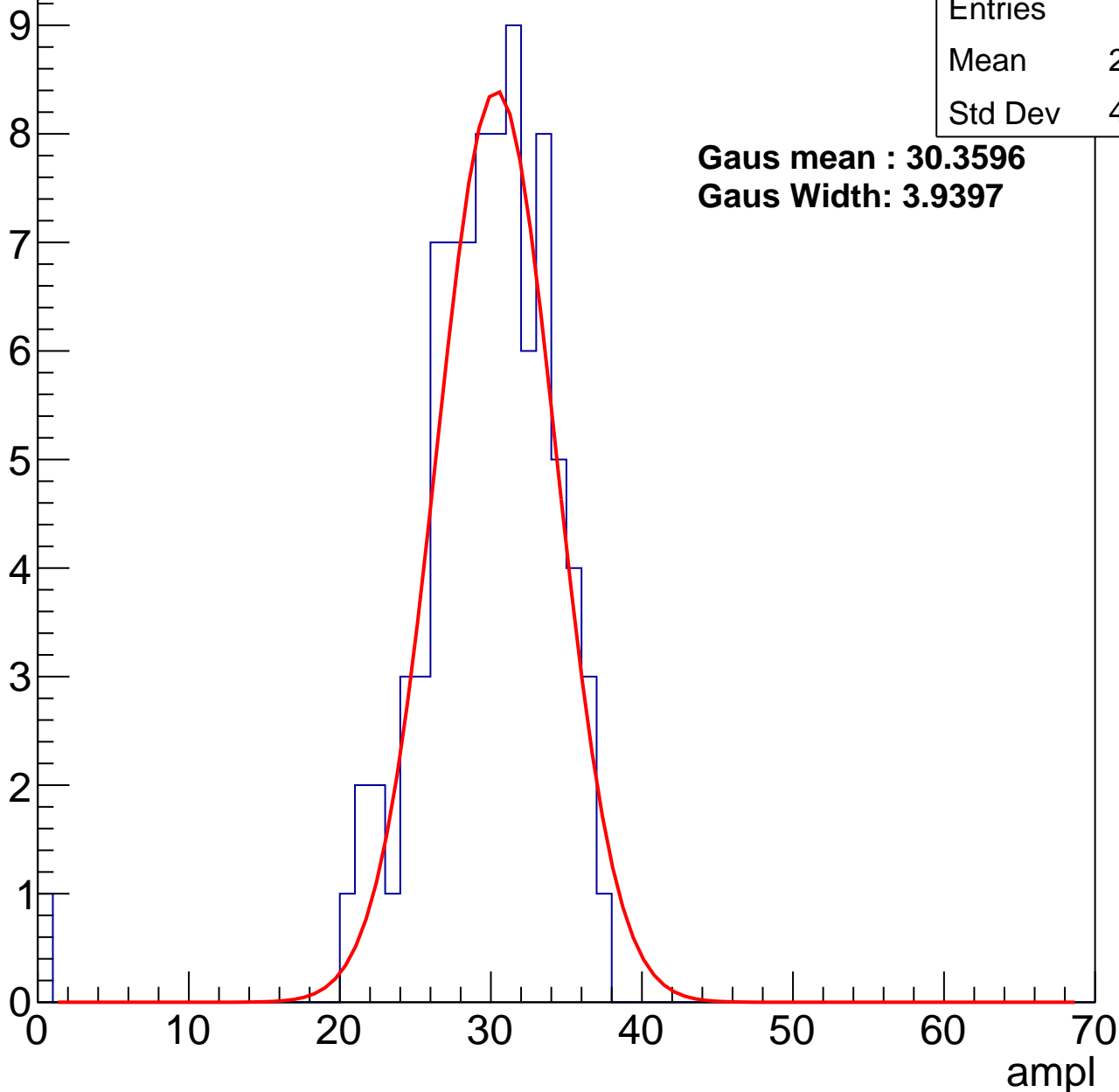
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	86
Mean	29.13
Std Dev	4.953

Gaus mean : 30.3596

Gaus Width: 3.9397



B1L100S, U12-ch52, adc1

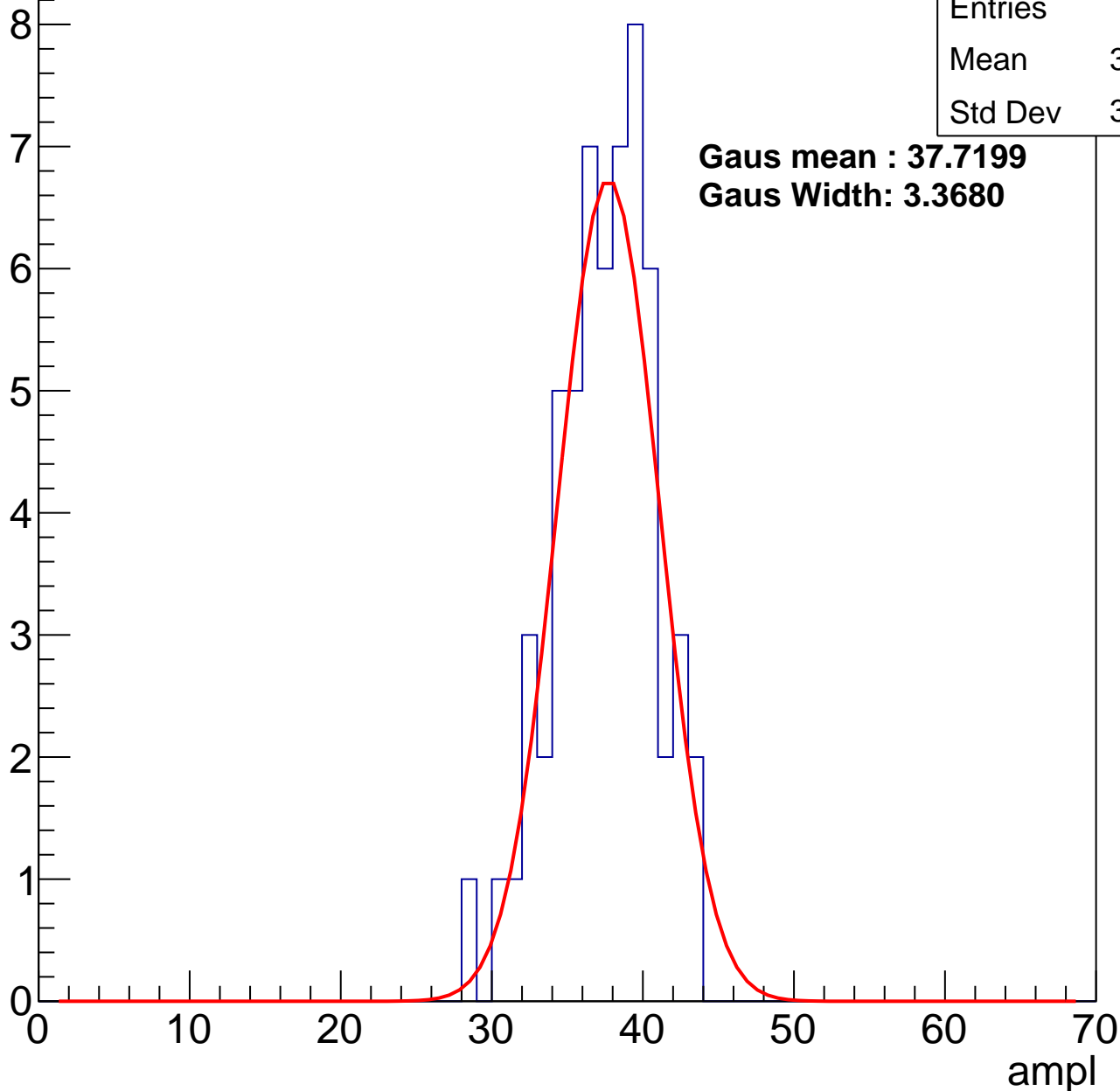
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.98
Std Dev	3.234

Gaus mean : 37.7199

Gaus Width: 3.3680



B1L100S, U12-ch52, adc2

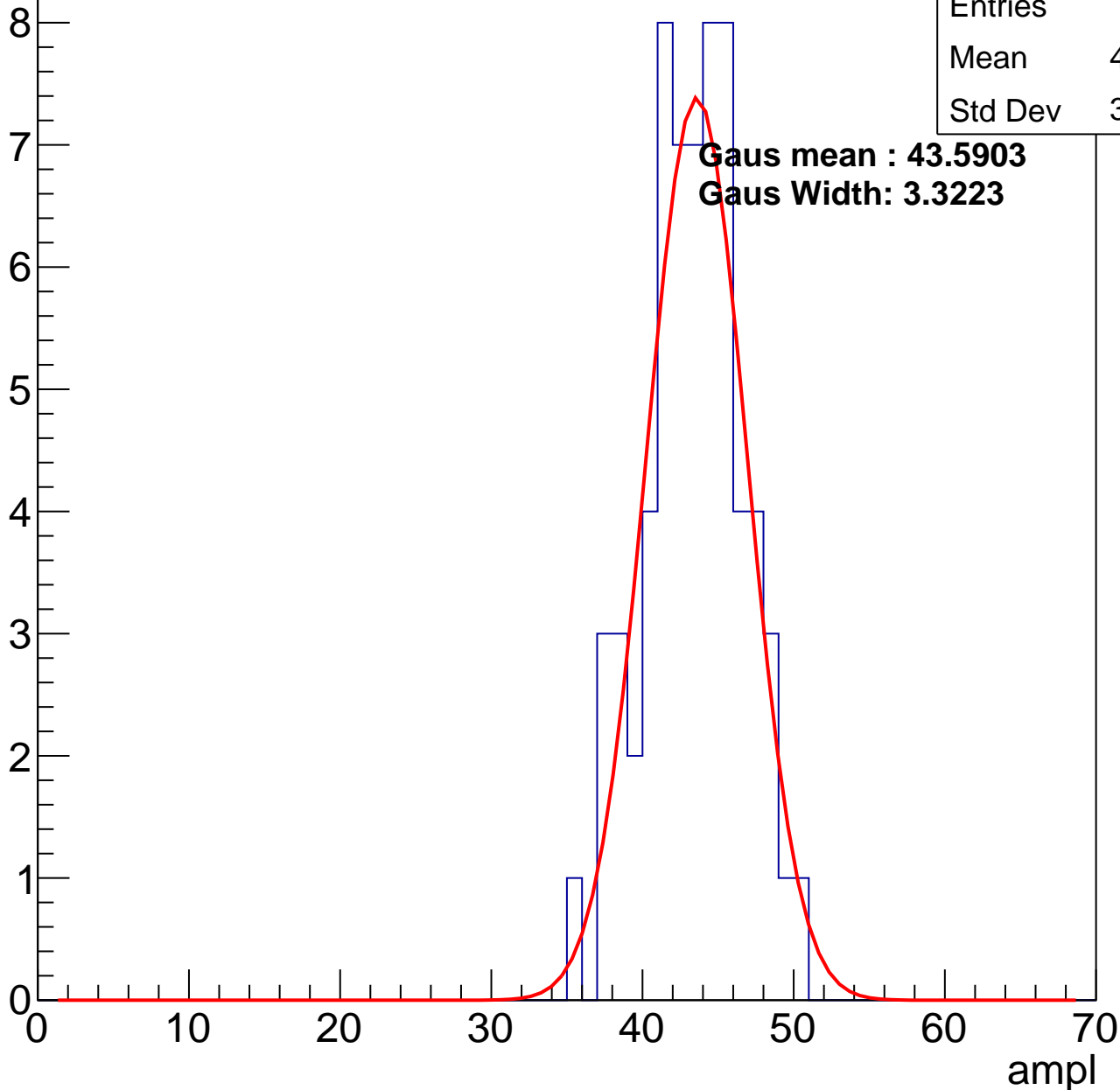
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.94
Std Dev	3.196

Gaus mean : 43.5903

Gaus Width: 3.3223

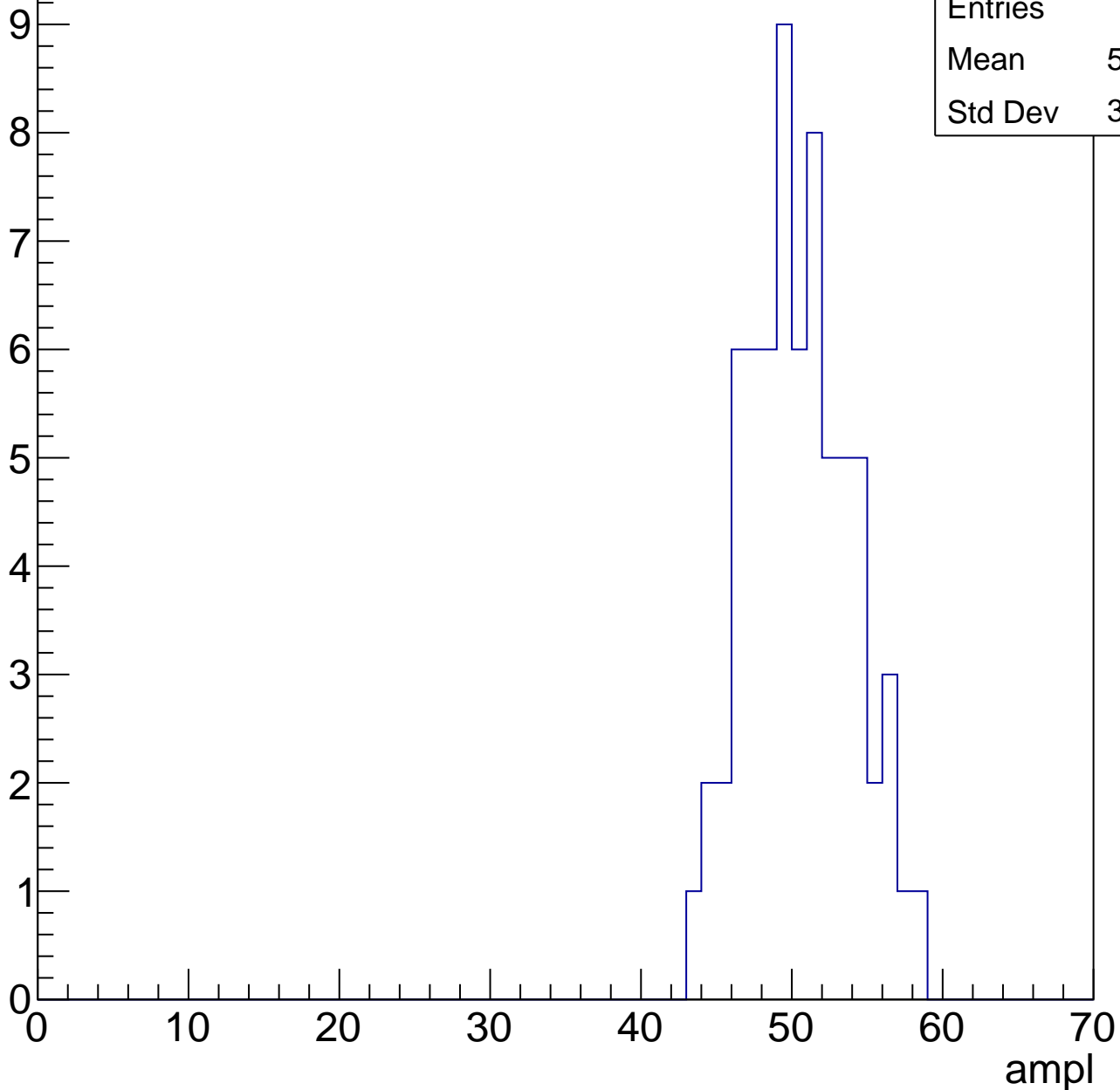


B1L100S, U12-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

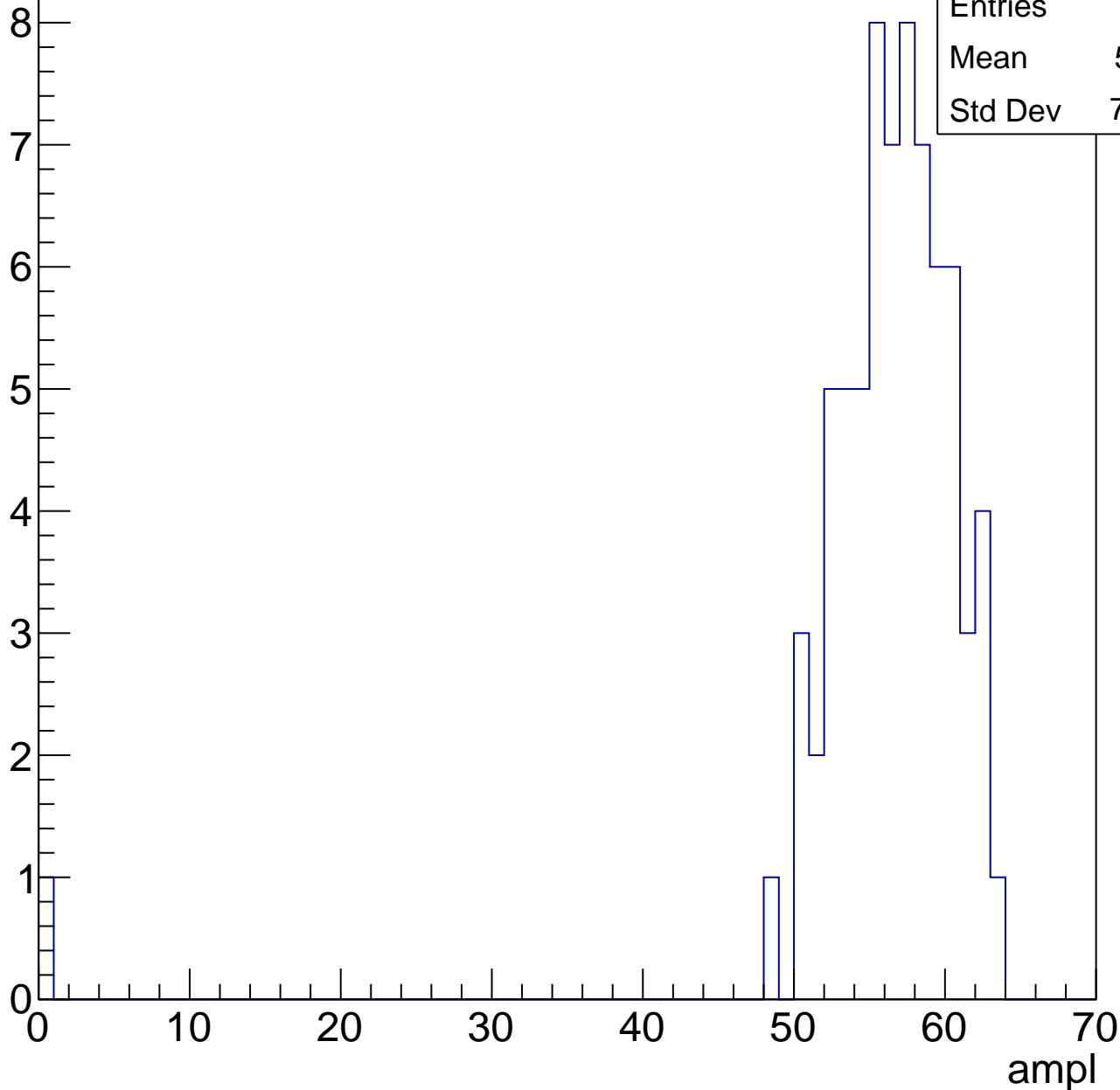
Entries	68
Mean	50.06
Std Dev	3.382



B1L100S, U12-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

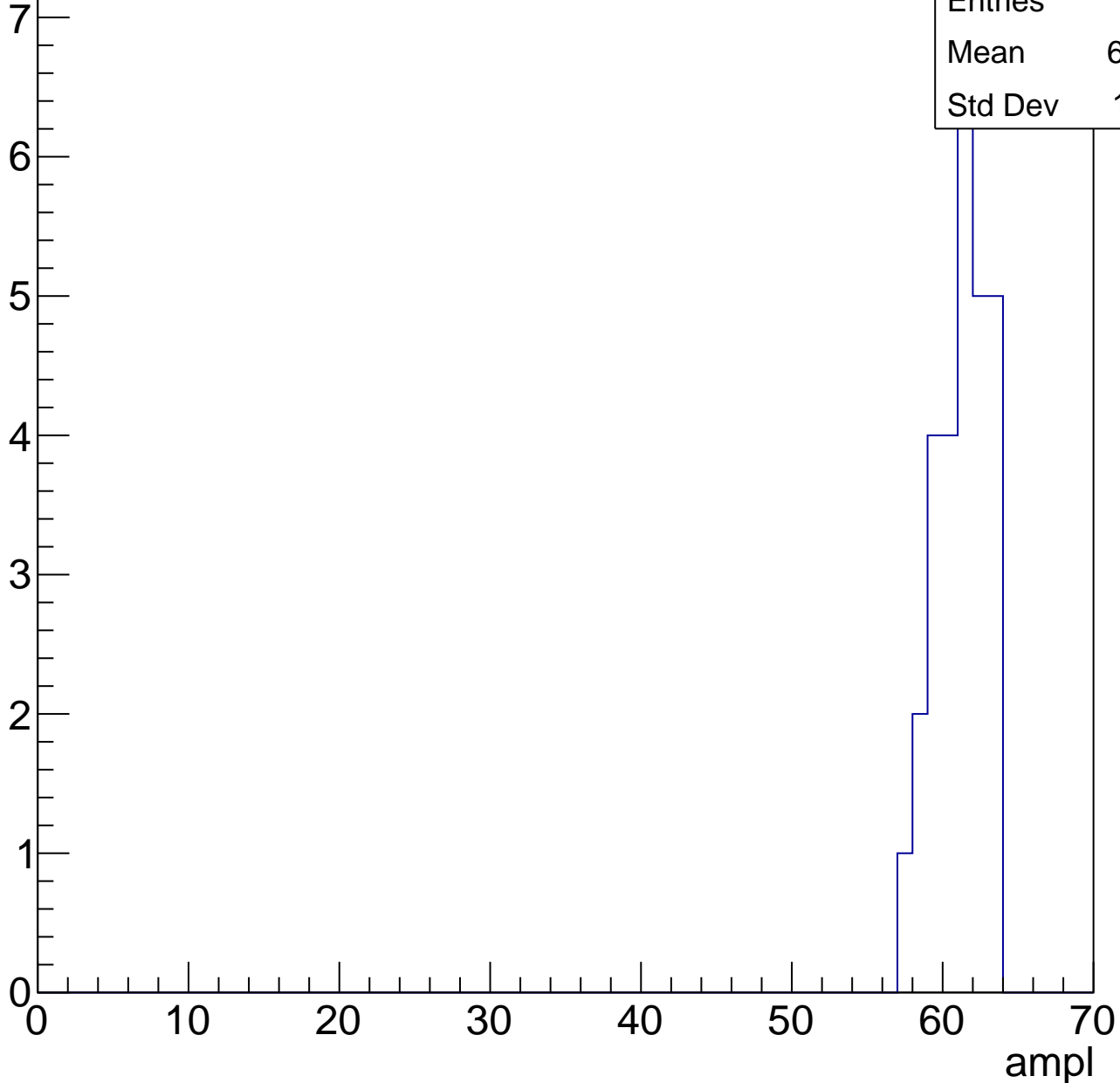
Entry



B1L100S, U12-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

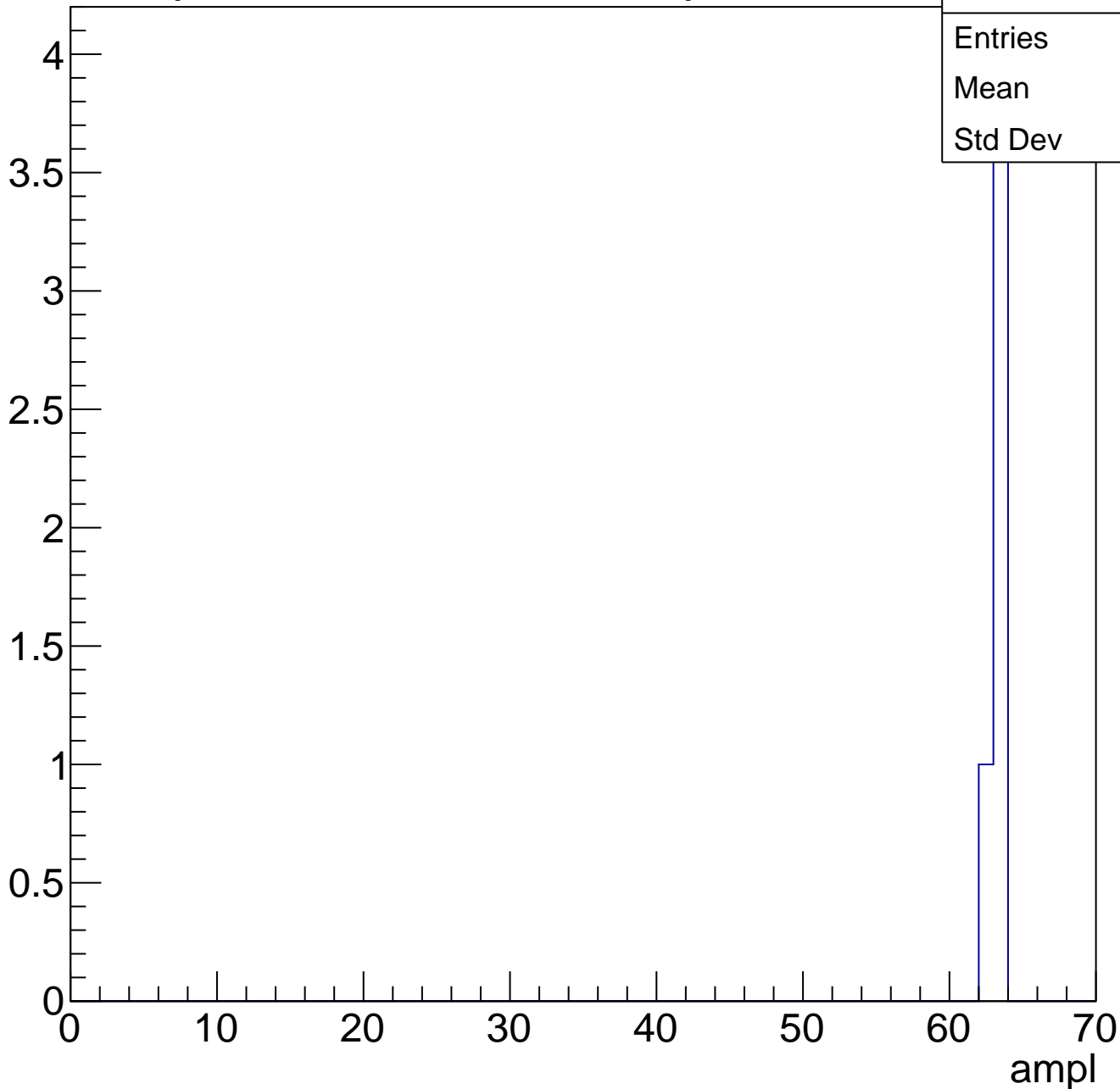


Entries	28
Mean	60.75
Std Dev	1.661

B1L100S, U12-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B1L100S, U12-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch53, adc0

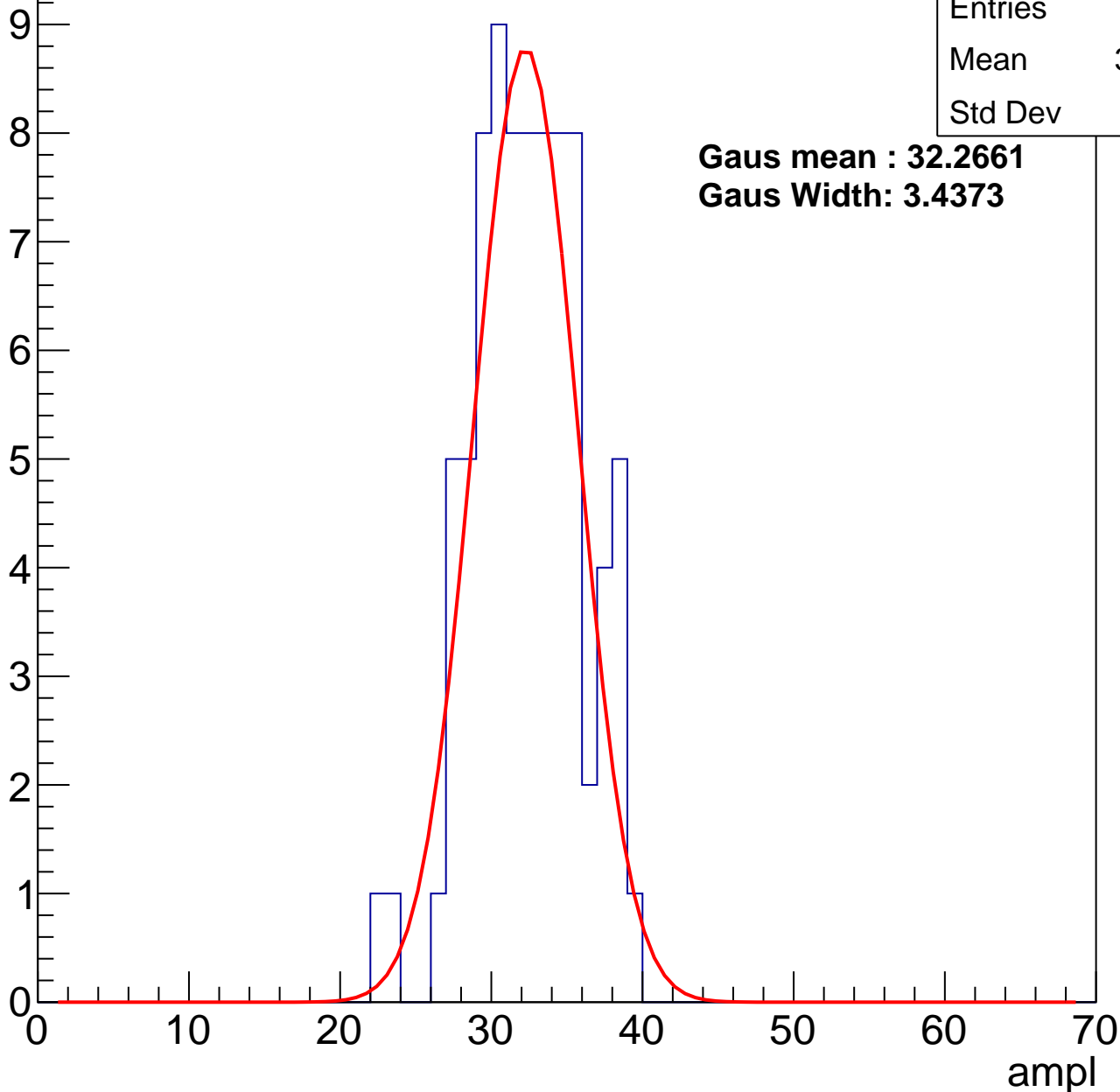
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	31.91
Std Dev	3.51

Gaus mean : 32.2661

Gaus Width: 3.4373



B1L100S, U12-ch53, adc1

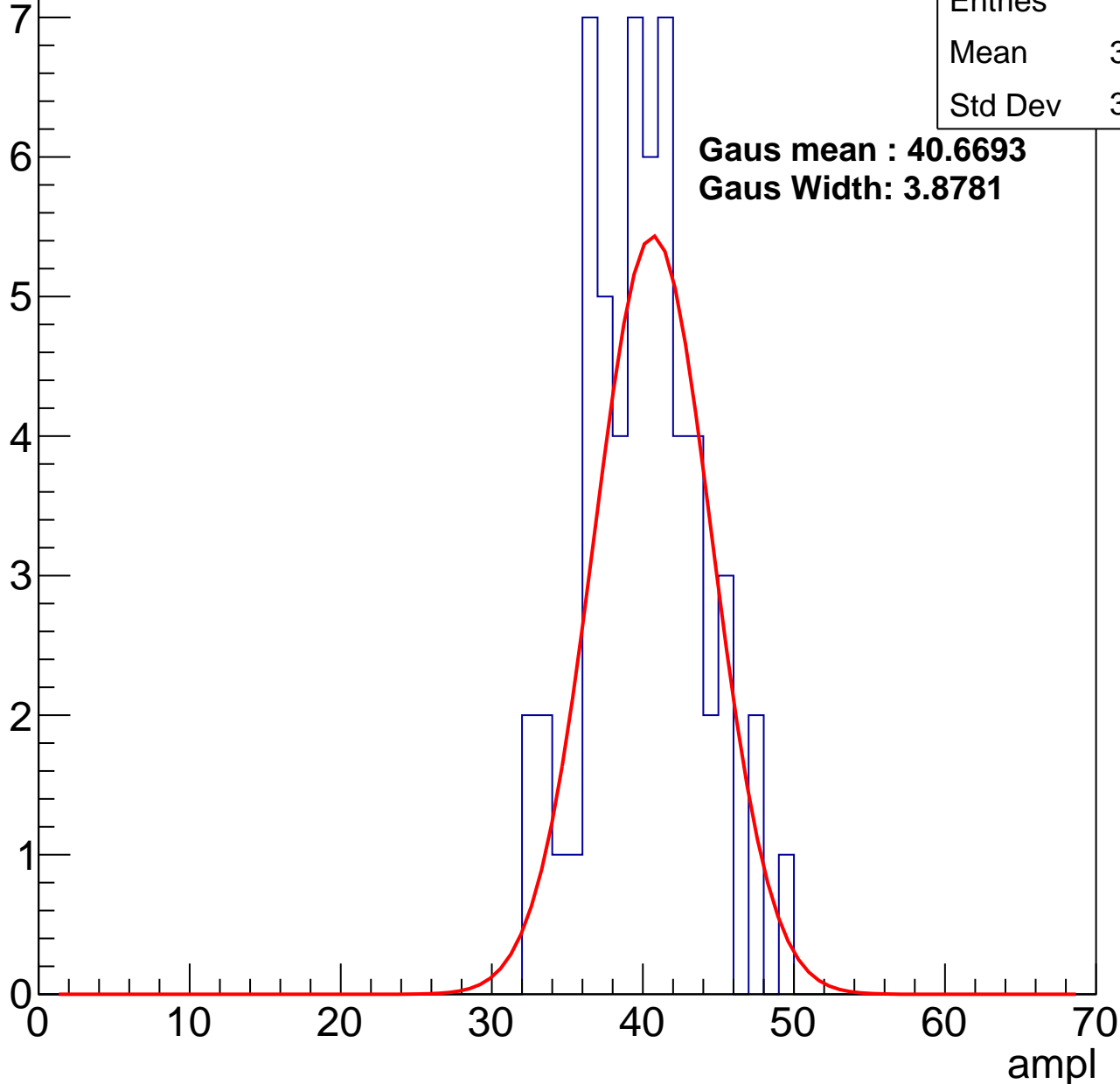
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	39.55
Std Dev	3.715

Gaus mean : 40.6693

Gaus Width: 3.8781



B1L100S, U12-ch53, adc2

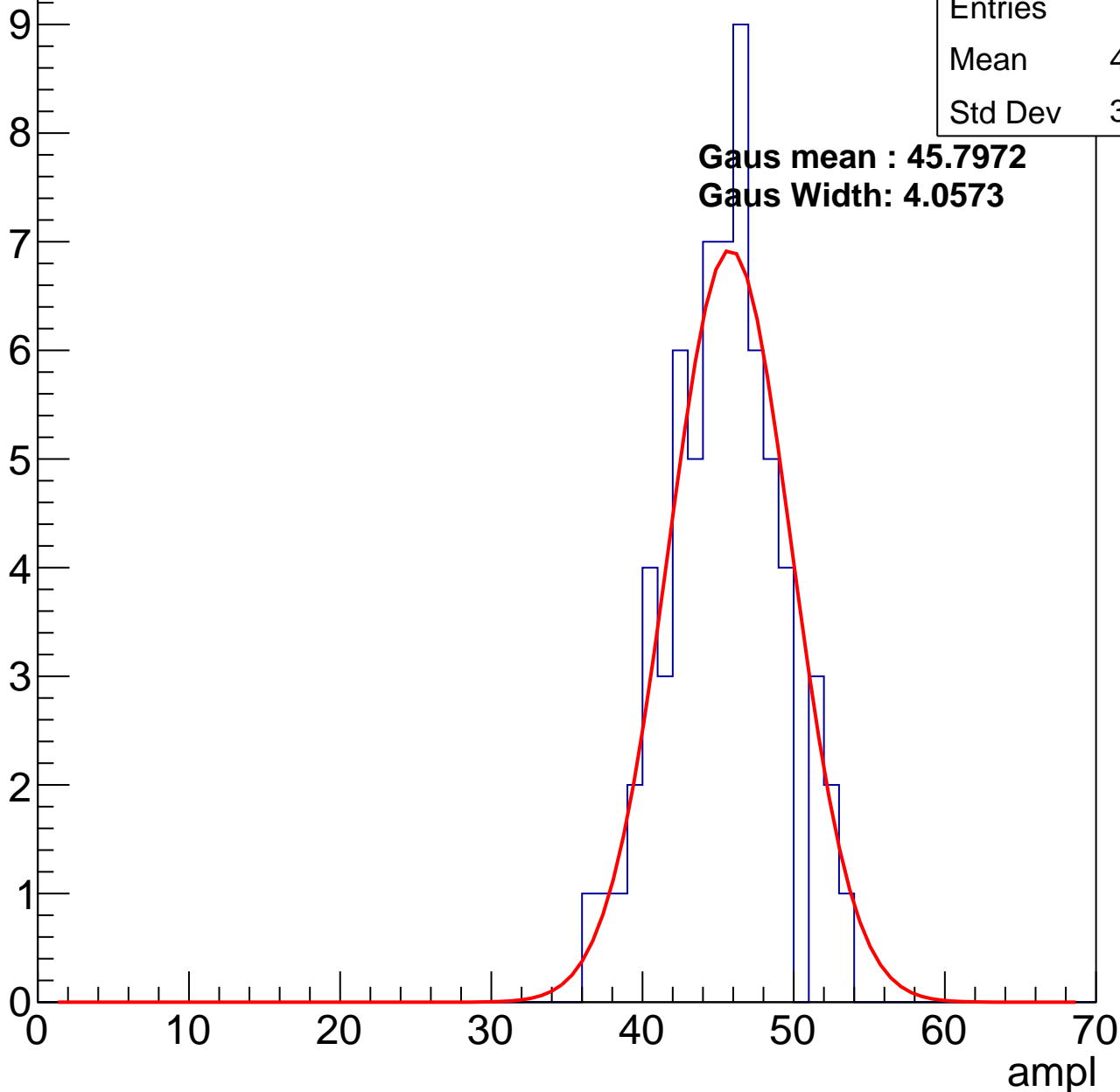
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.84
Std Dev	3.676

Gaus mean : 45.7972

Gaus Width: 4.0573

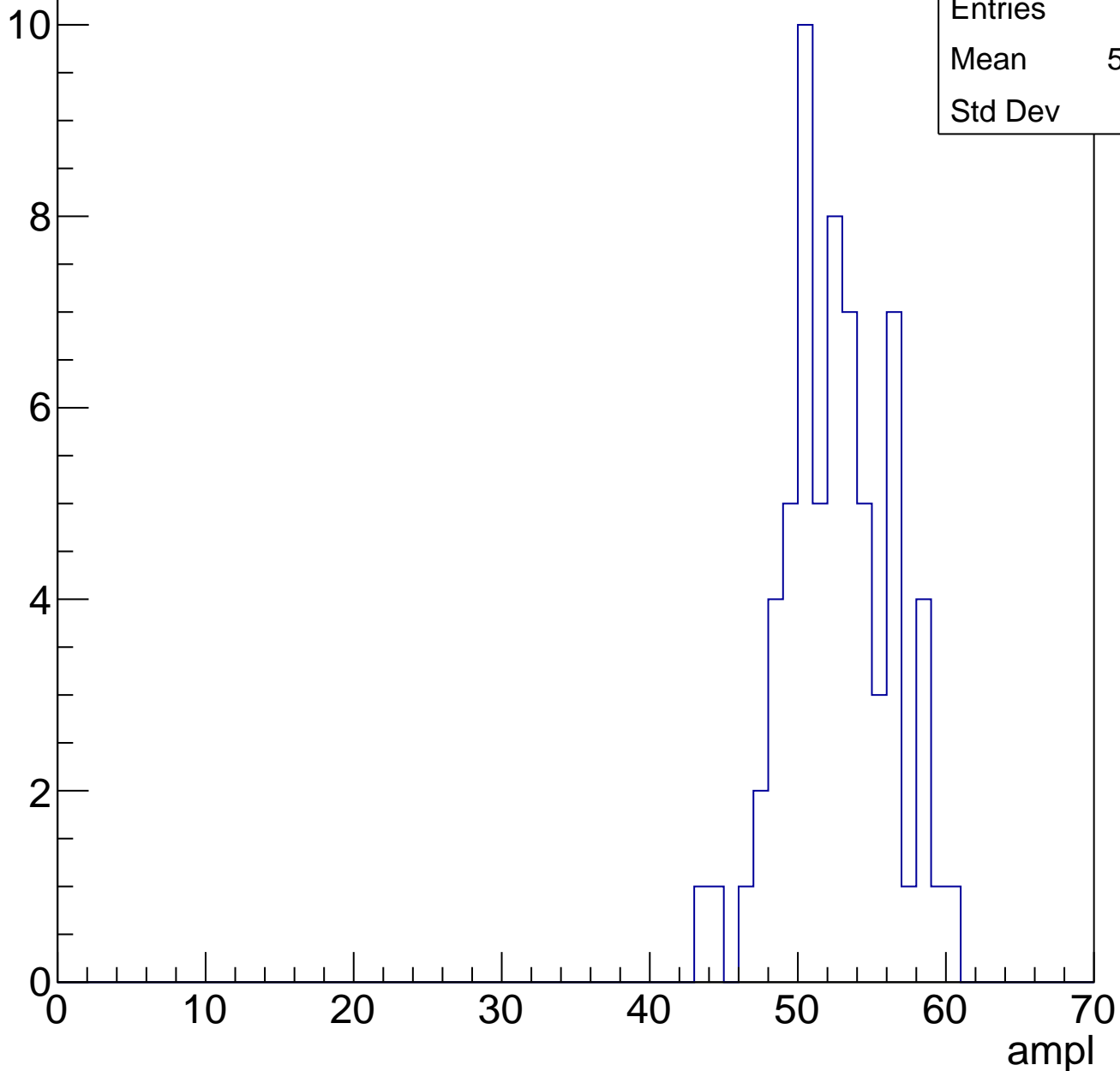


B1L100S, U12-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

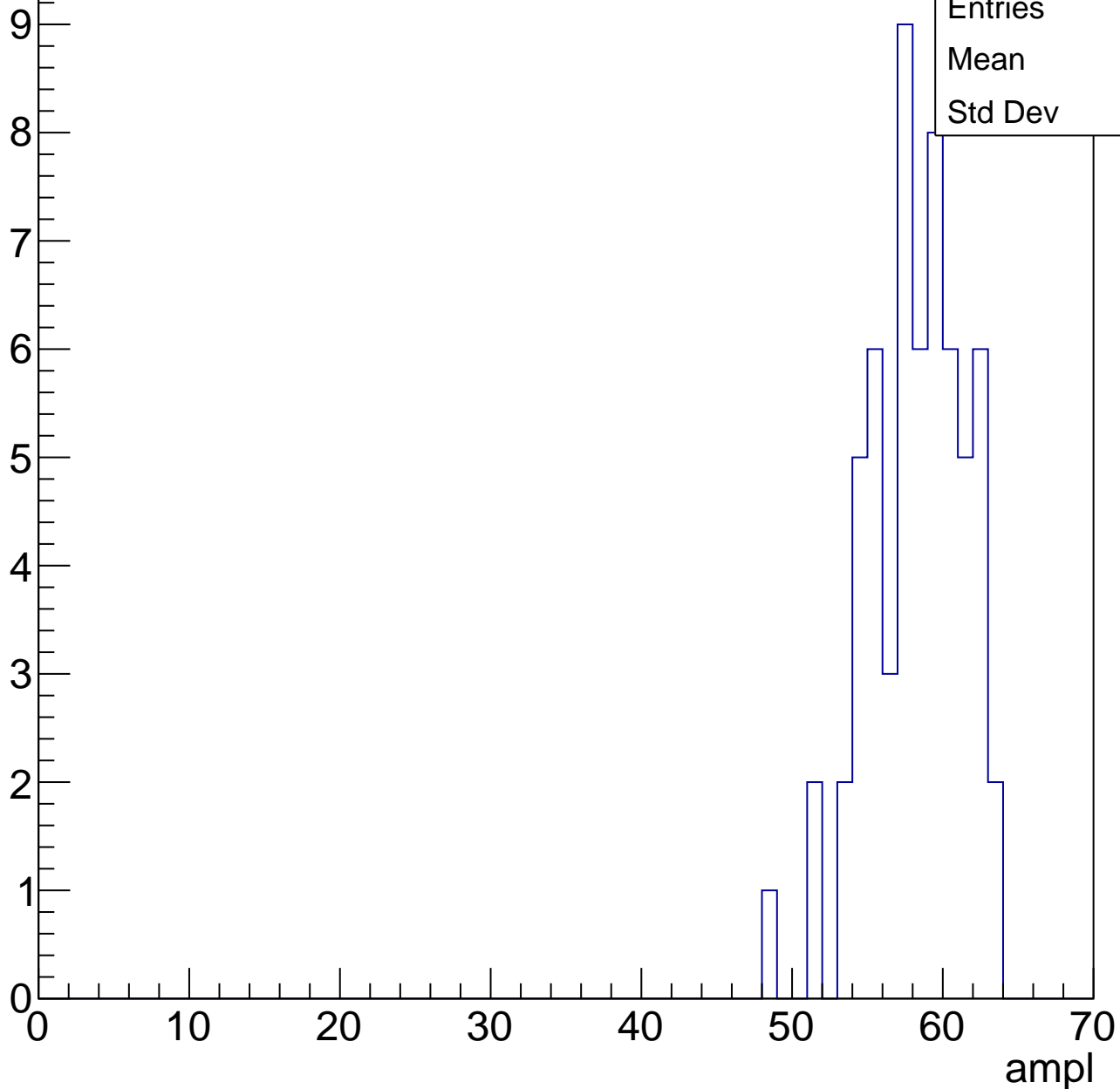
Entries	66
Mean	52.14
Std Dev	3.55



B1L100S, U12-ch53, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

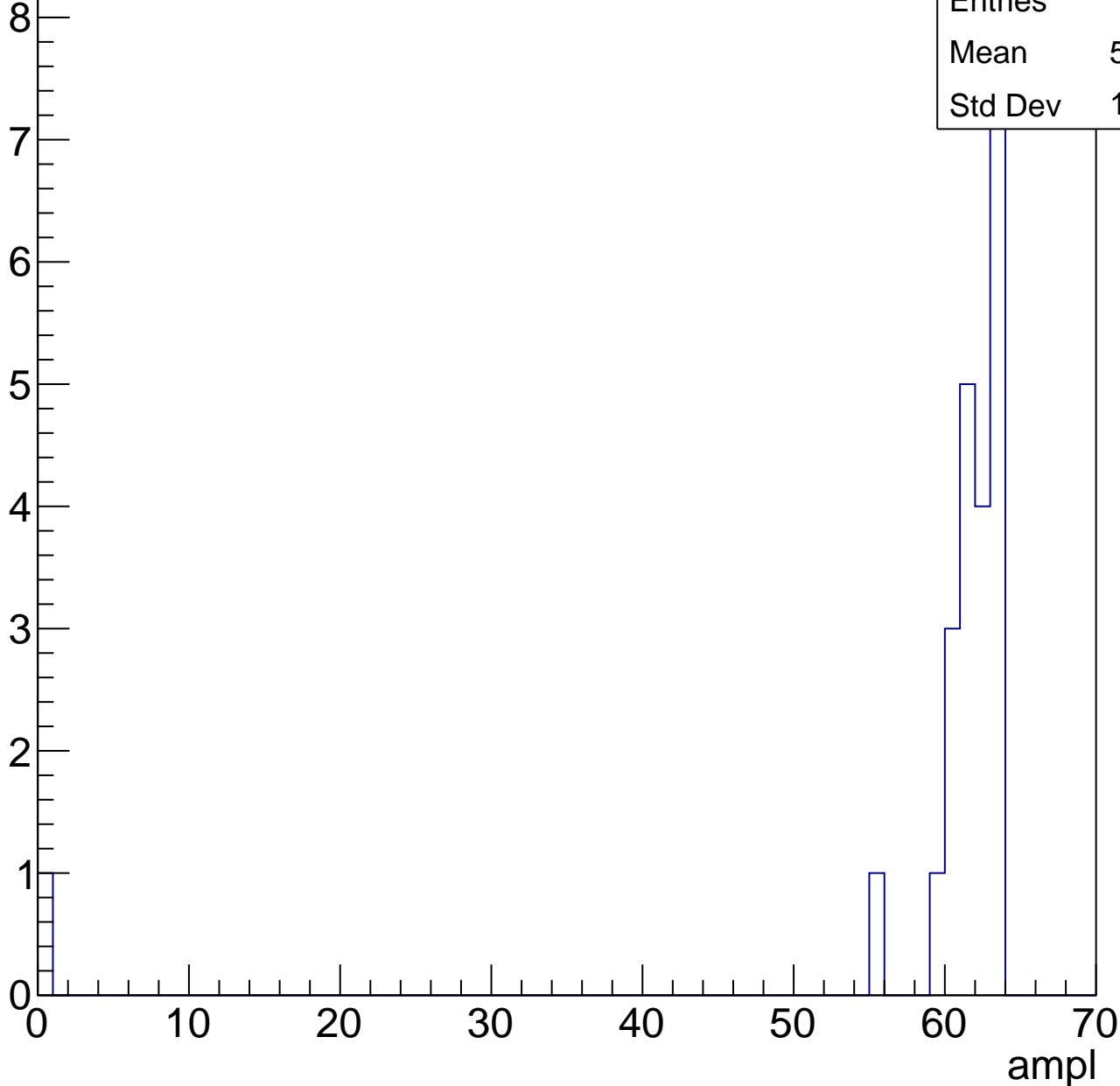


B1L100S, U12-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	23
Mean	58.74
Std Dev	12.65



B1L100S, U12-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch54, adc0

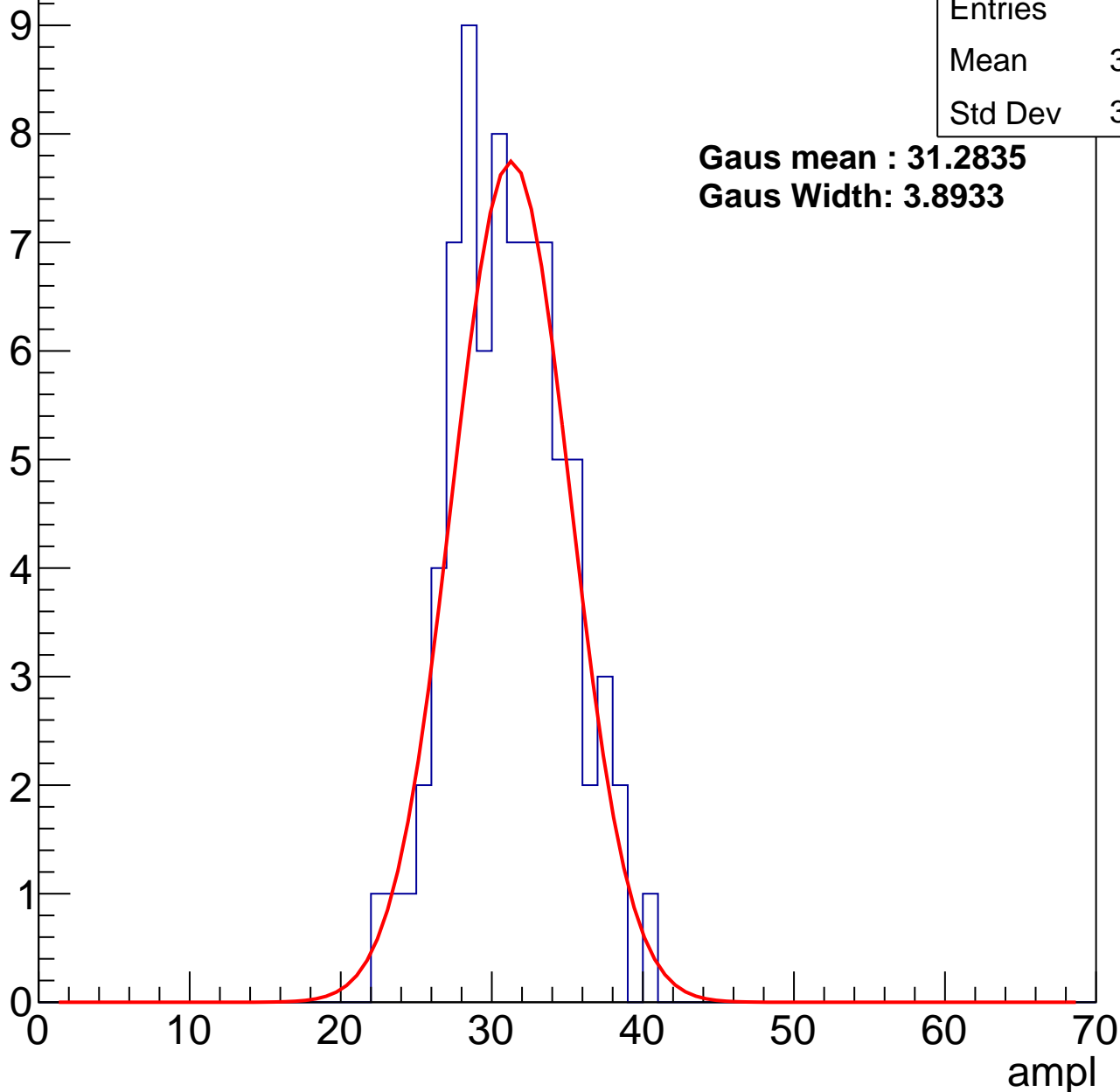
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	30.69
Std Dev	3.722

Gaus mean : 31.2835

Gaus Width: 3.8933



B1L100S, U12-ch54, adc1

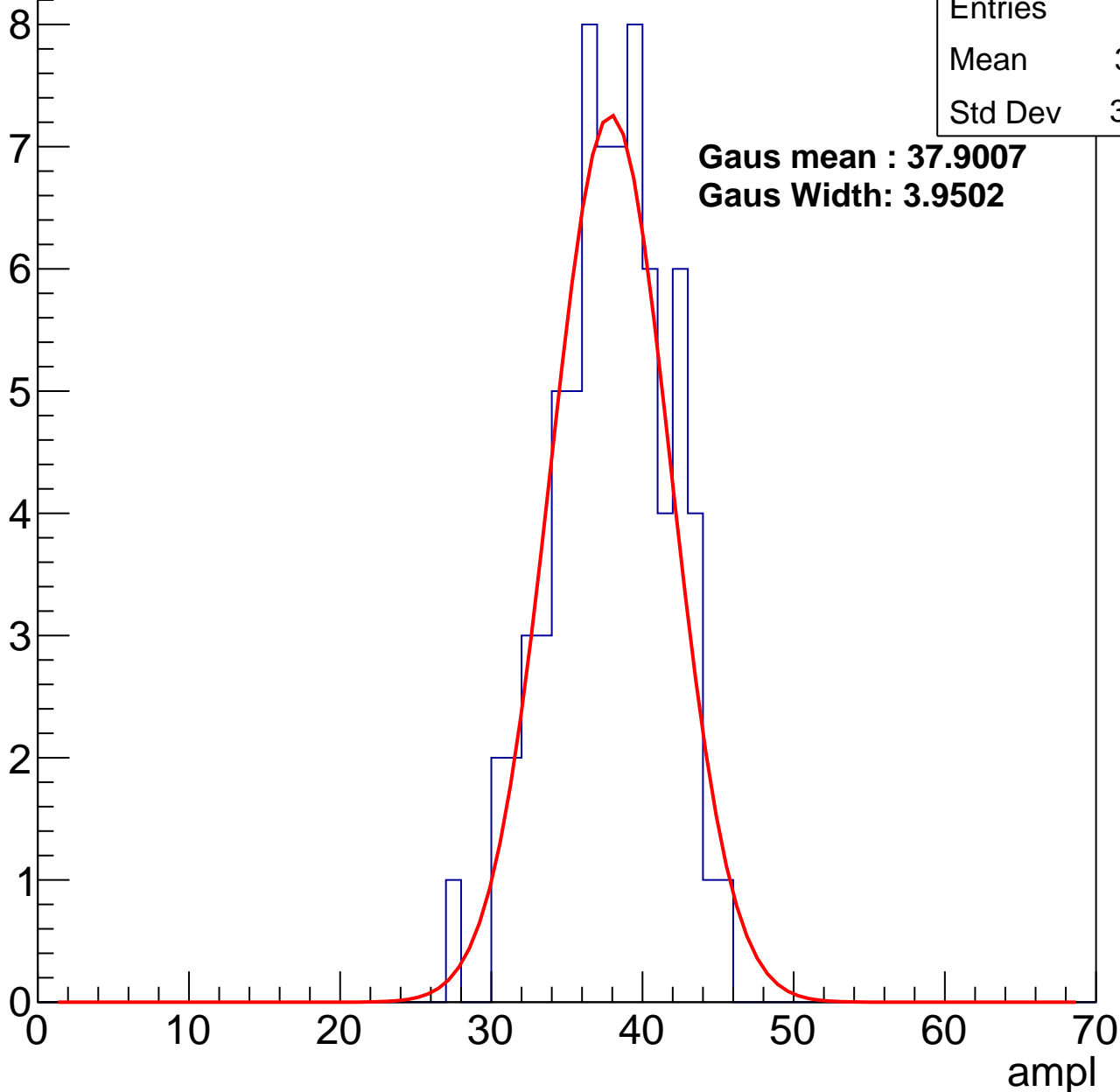
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.41
Std Dev	3.734

Gaus mean : 37.9007

Gaus Width: 3.9502



B1L100S, U12-ch54, adc2

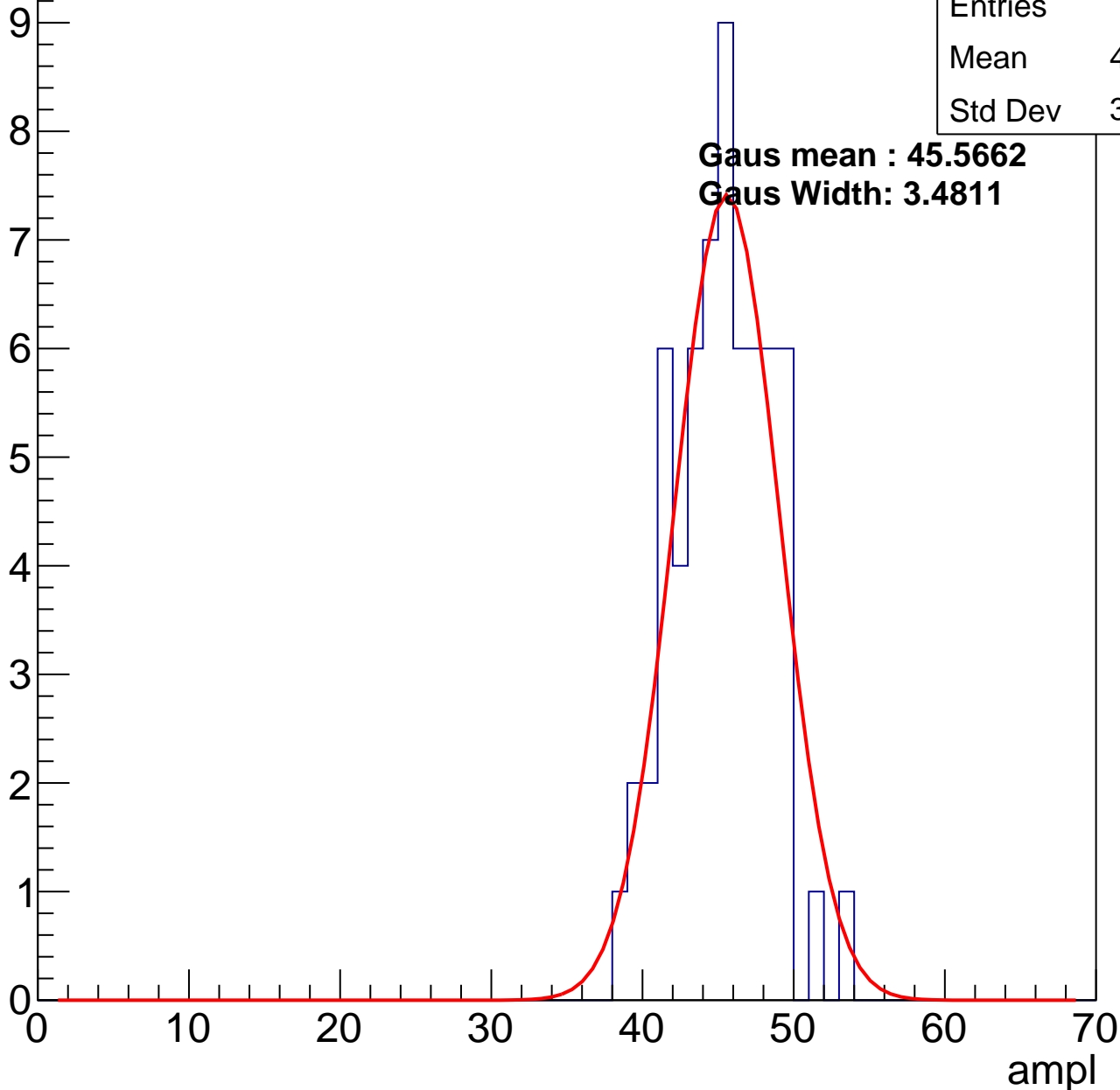
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.84
Std Dev	3.118

Gaus mean : 45.5662

Gaus Width: 3.4811

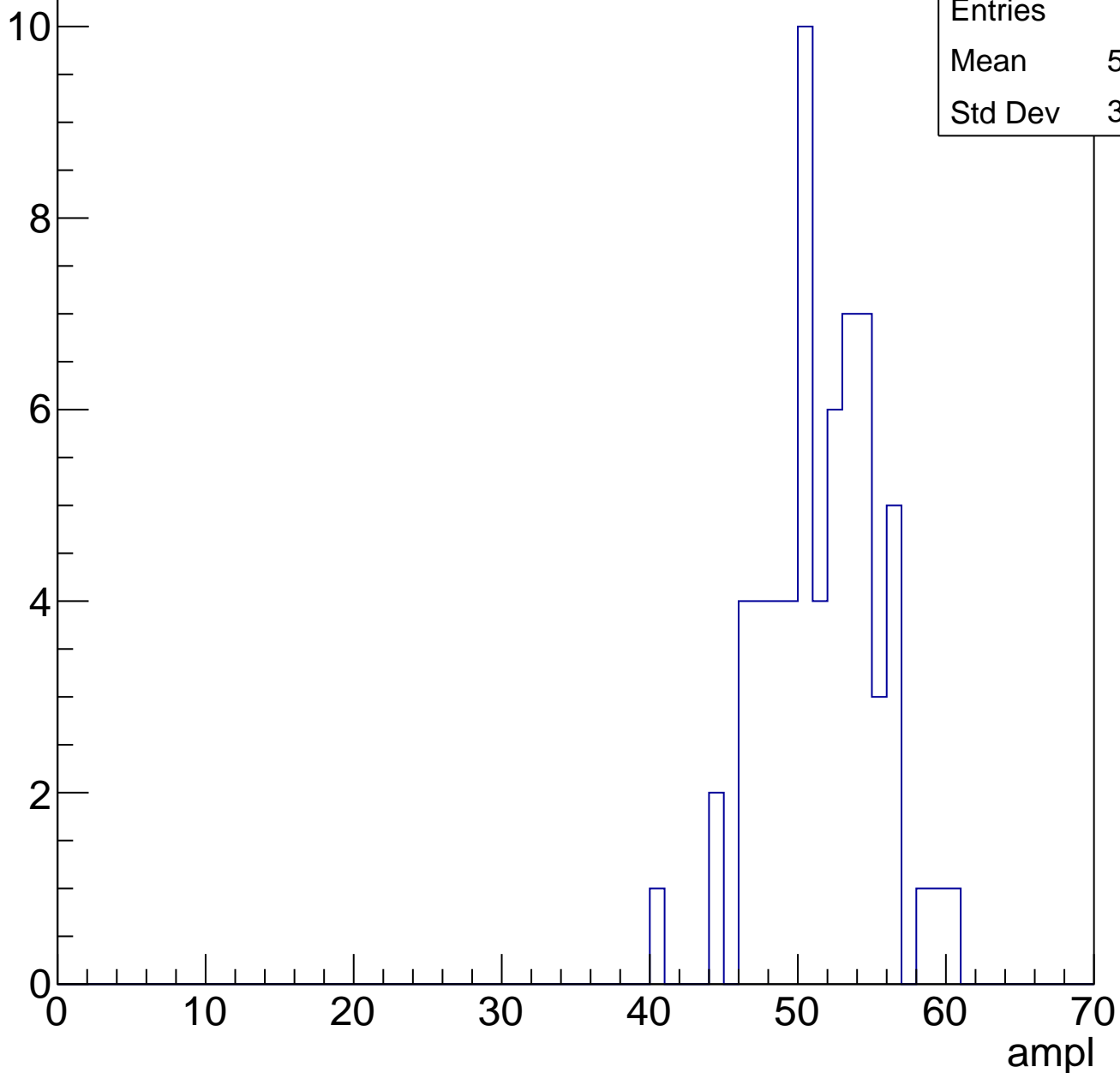


B1L100S, U12-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	51.17
Std Dev	3.765

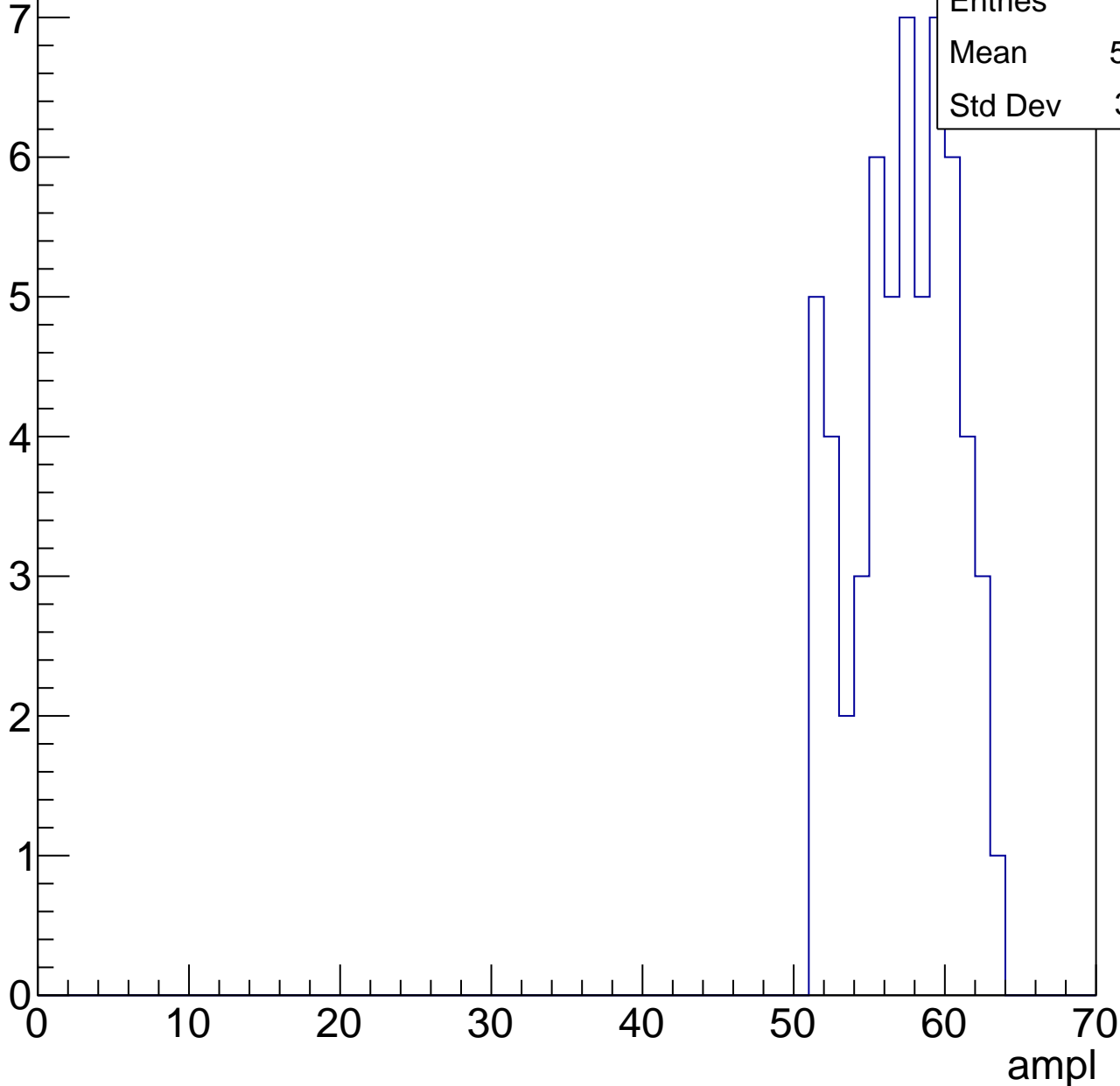


B1L100S, U12-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.83
Std Dev	3.291

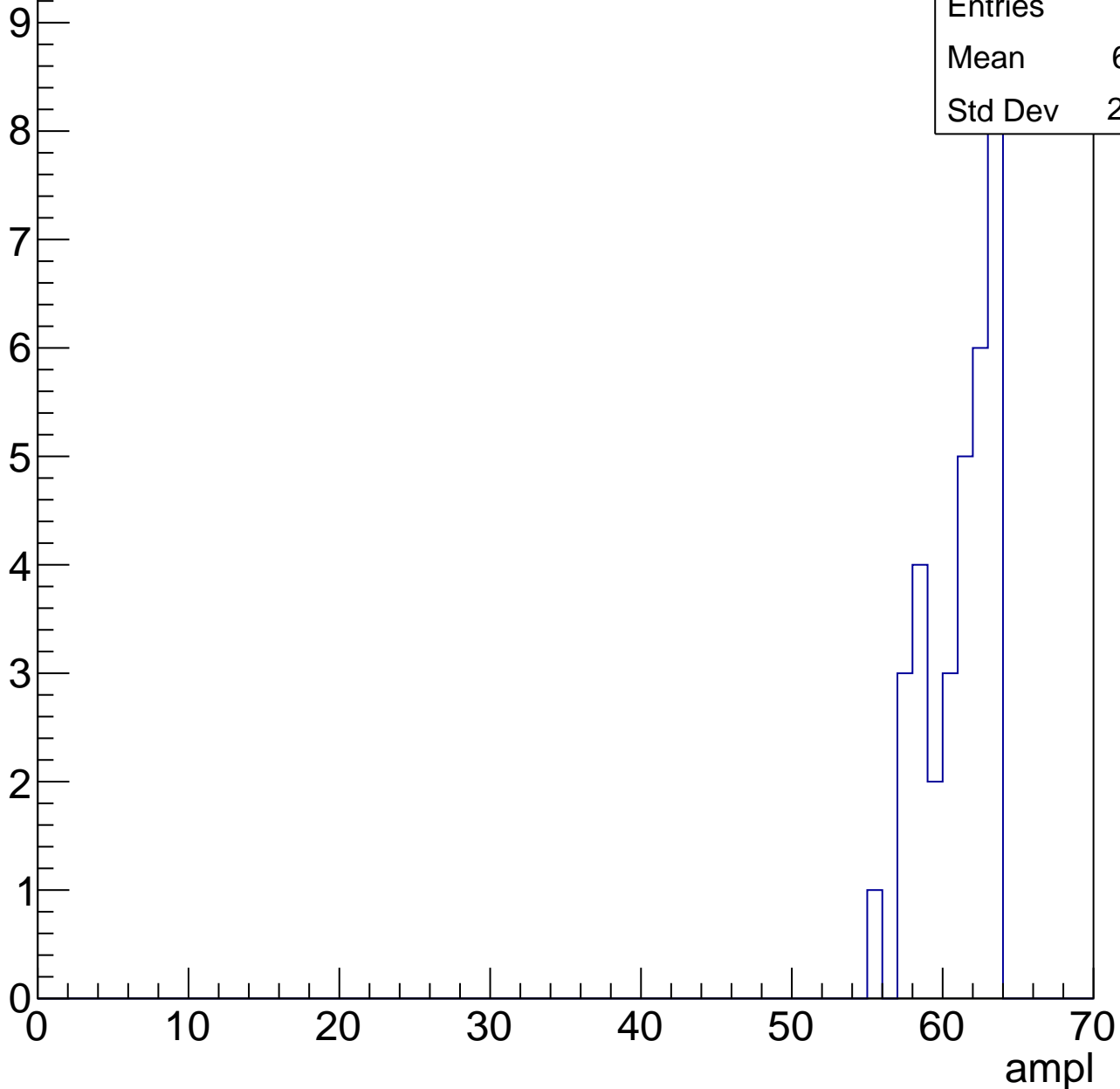


B1L100S, U12-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	60.61
Std Dev	2.255



B1L100S, U12-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch55, adc0

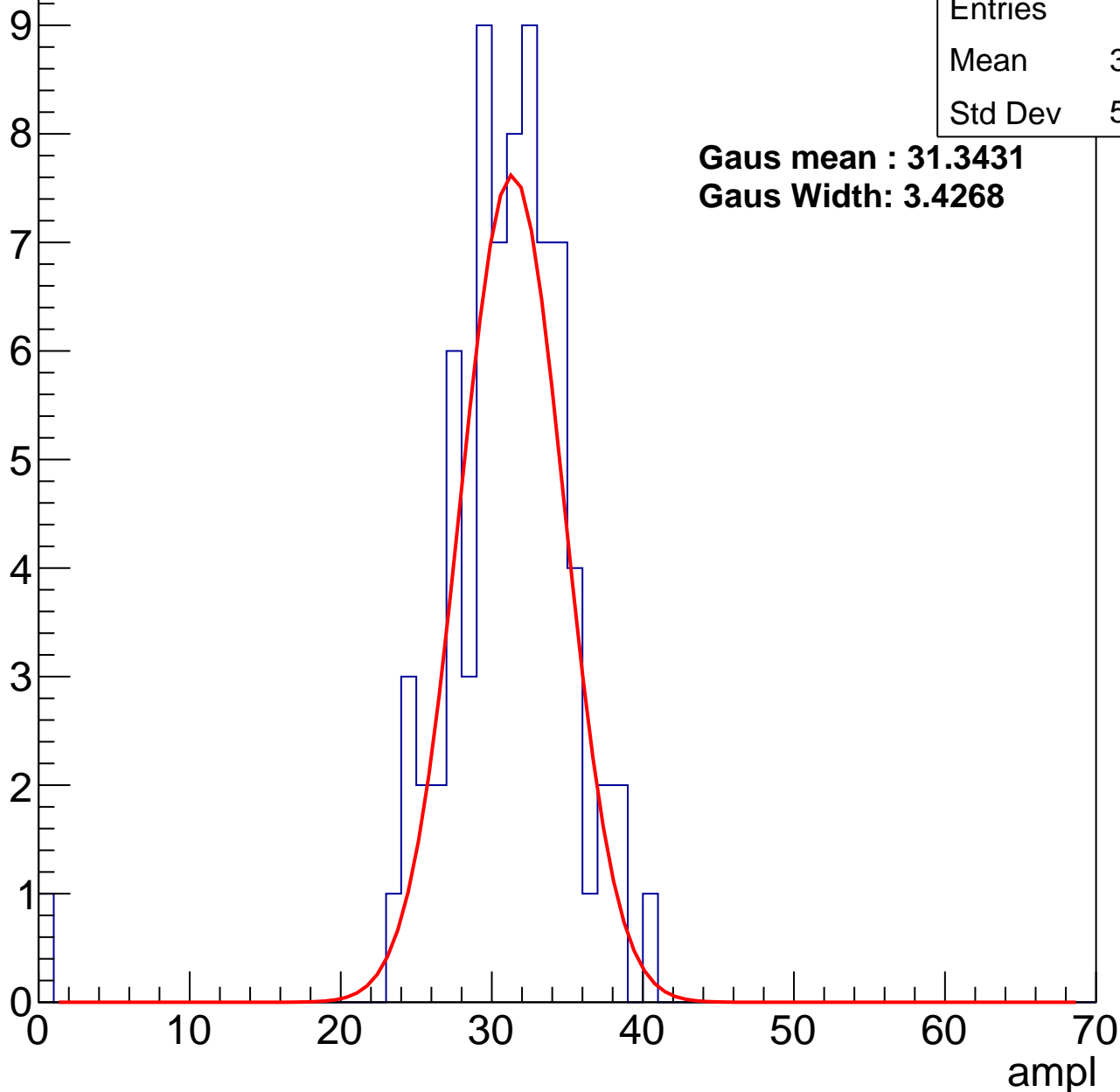
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.47
Std Dev	5.005

Gaus mean : 31.3431

Gaus Width: 3.4268



B1L100S, U12-ch55, adc1

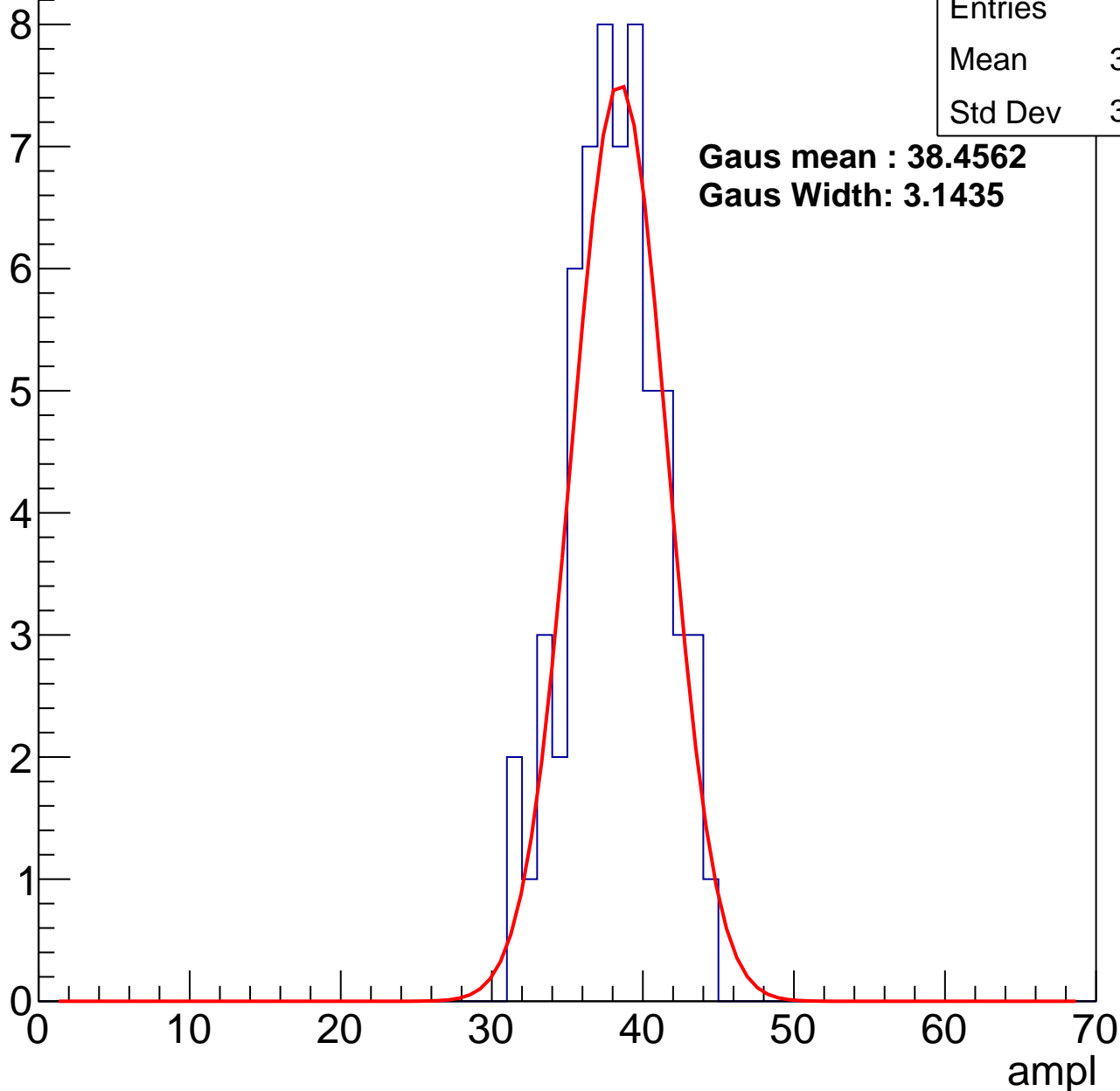
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	37.72
Std Dev	3.025

Gaus mean : 38.4562

Gaus Width: 3.1435



B1L100S, U12-ch55, adc2

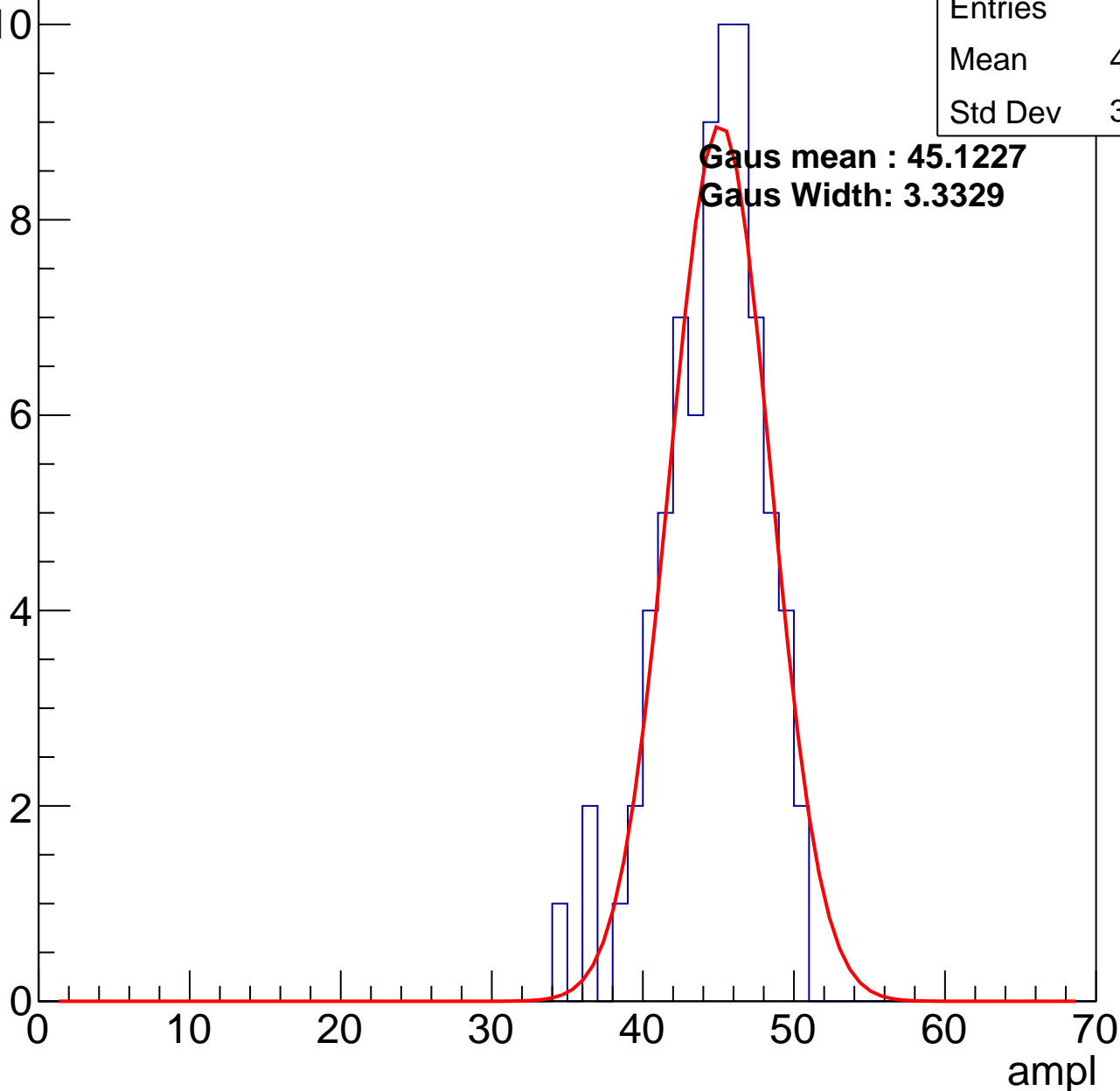
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.13
Std Dev	3.308

Gaus mean : 45.1227

Gaus Width: 3.3329

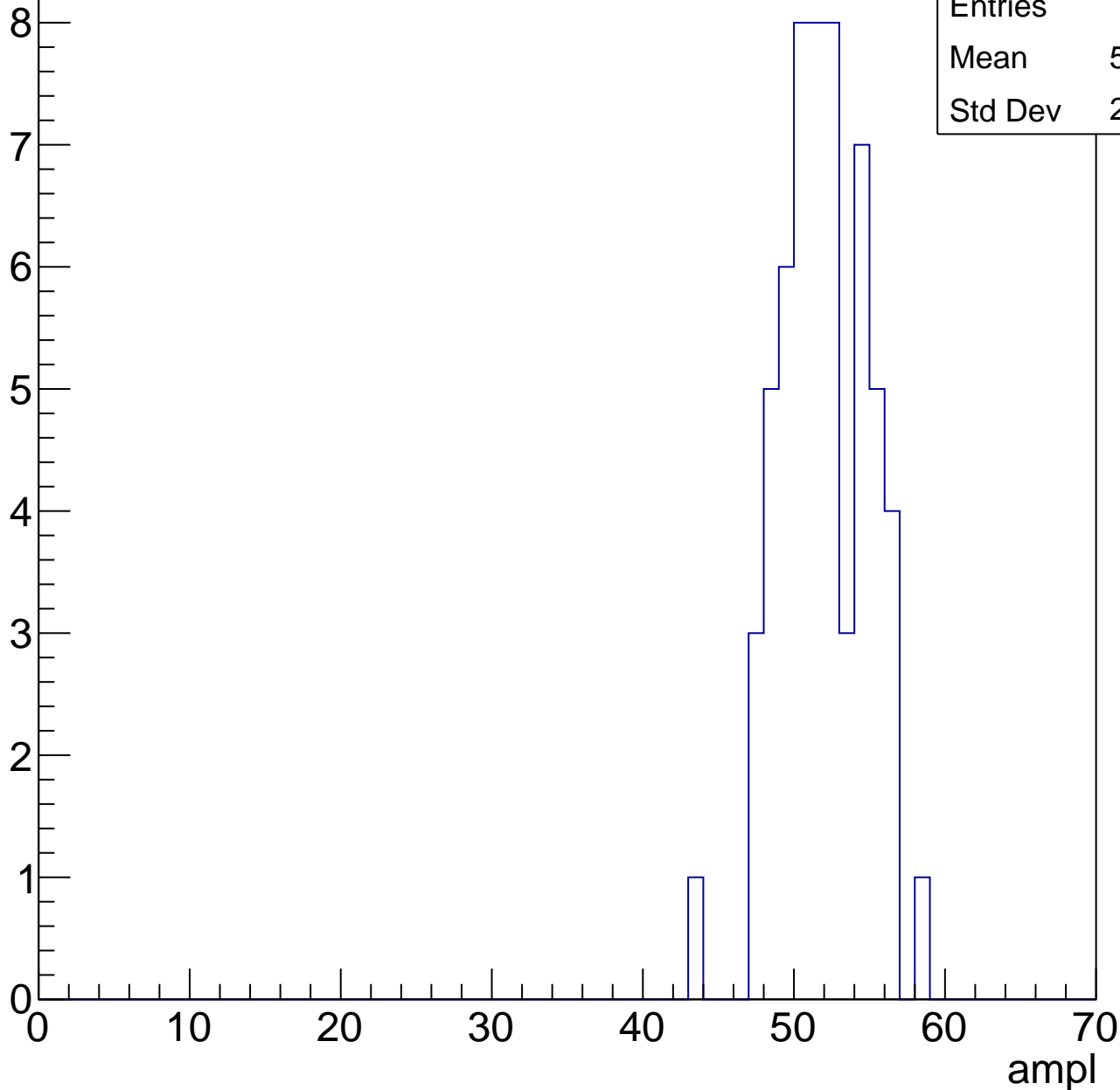


B1L100S, U12-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	51.46
Std Dev	2.878

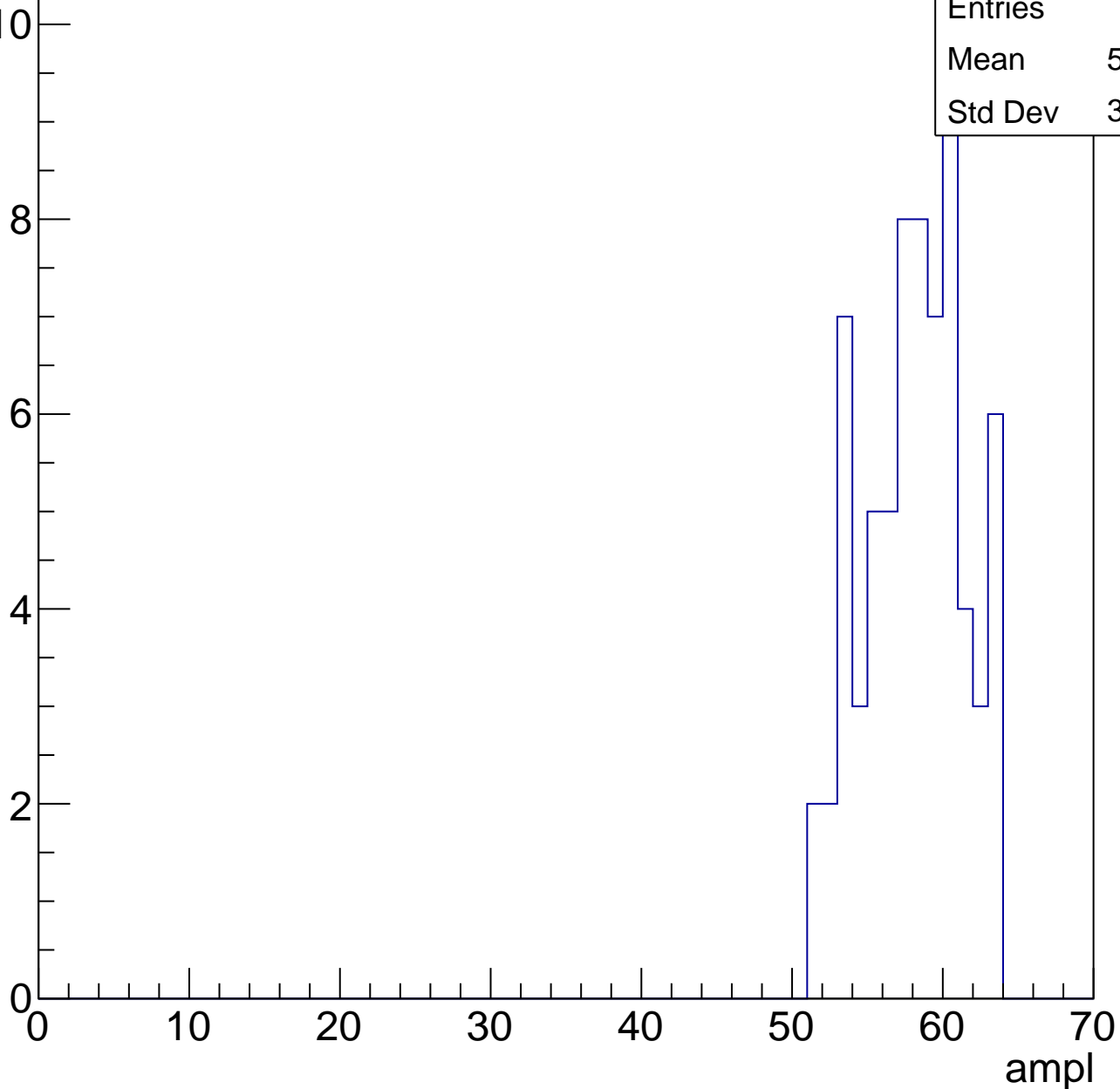


B1L100S, U12-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	57.64
Std Dev	3.247

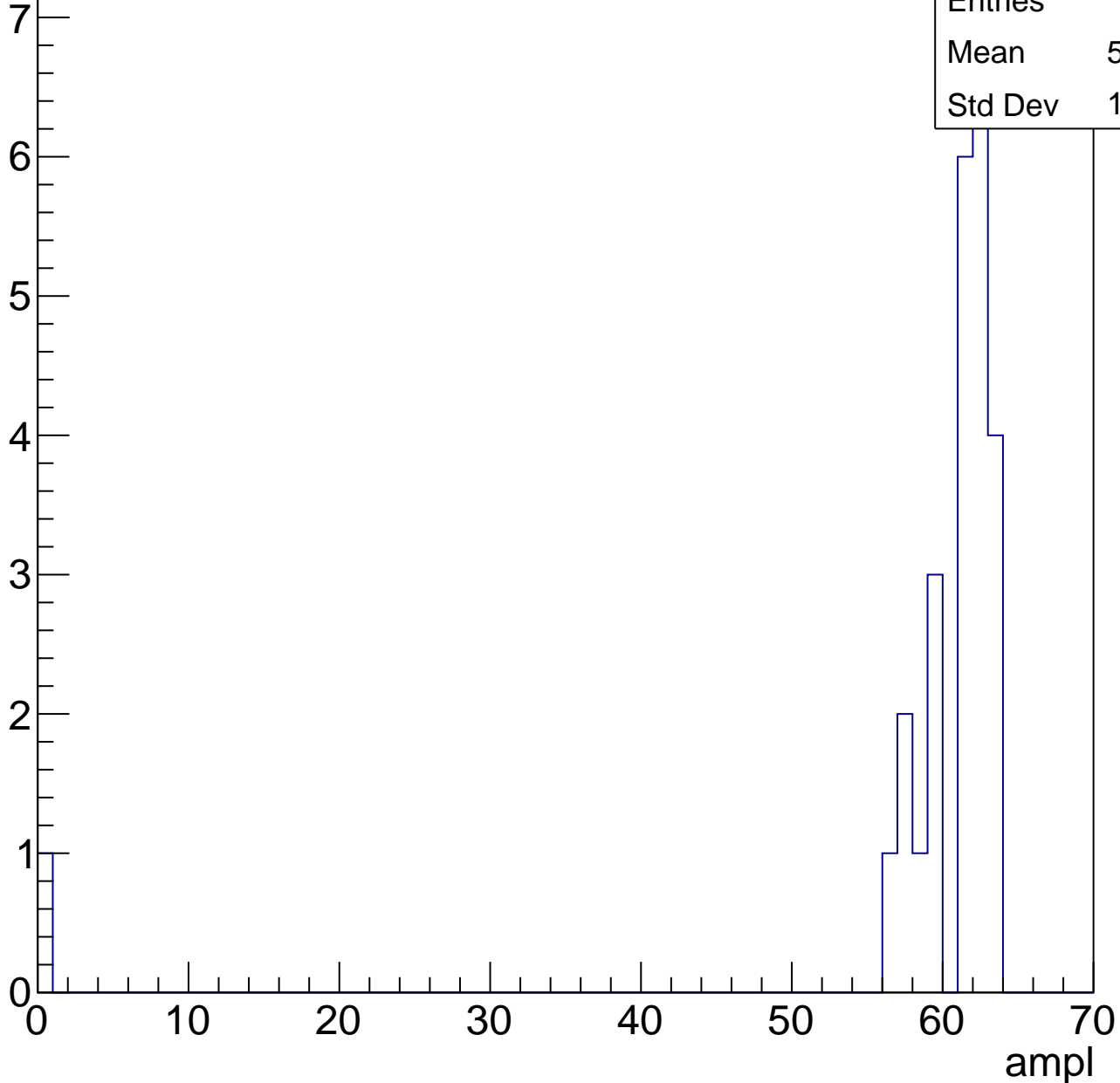


B1L100S, U12-ch55, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	25
Mean	58.28
Std Dev	12.06



B1L100S, U12-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L100S, U12-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch56, adc0

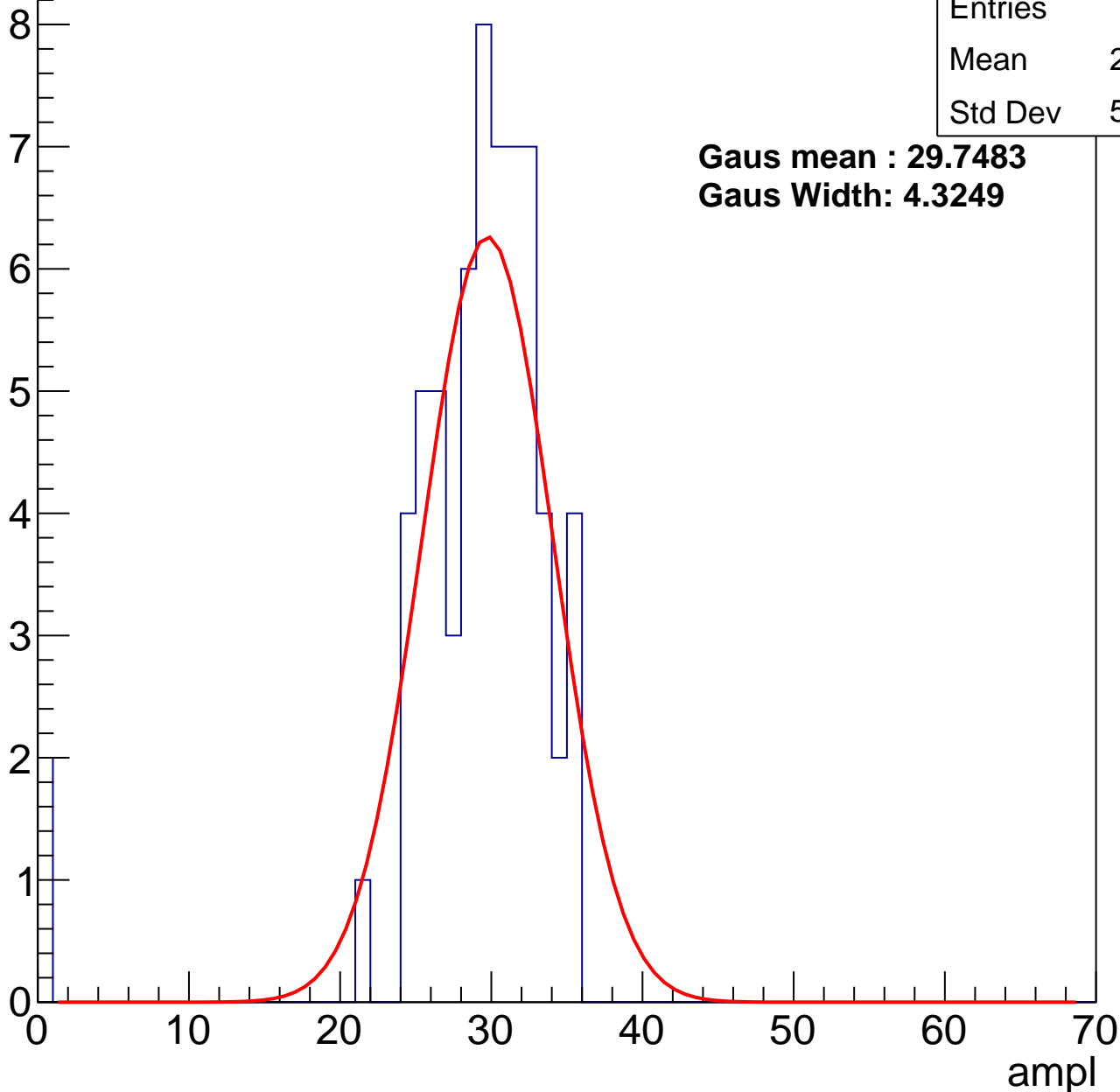
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.37
Std Dev	5.973

Gaus mean : 29.7483

Gaus Width: 4.3249



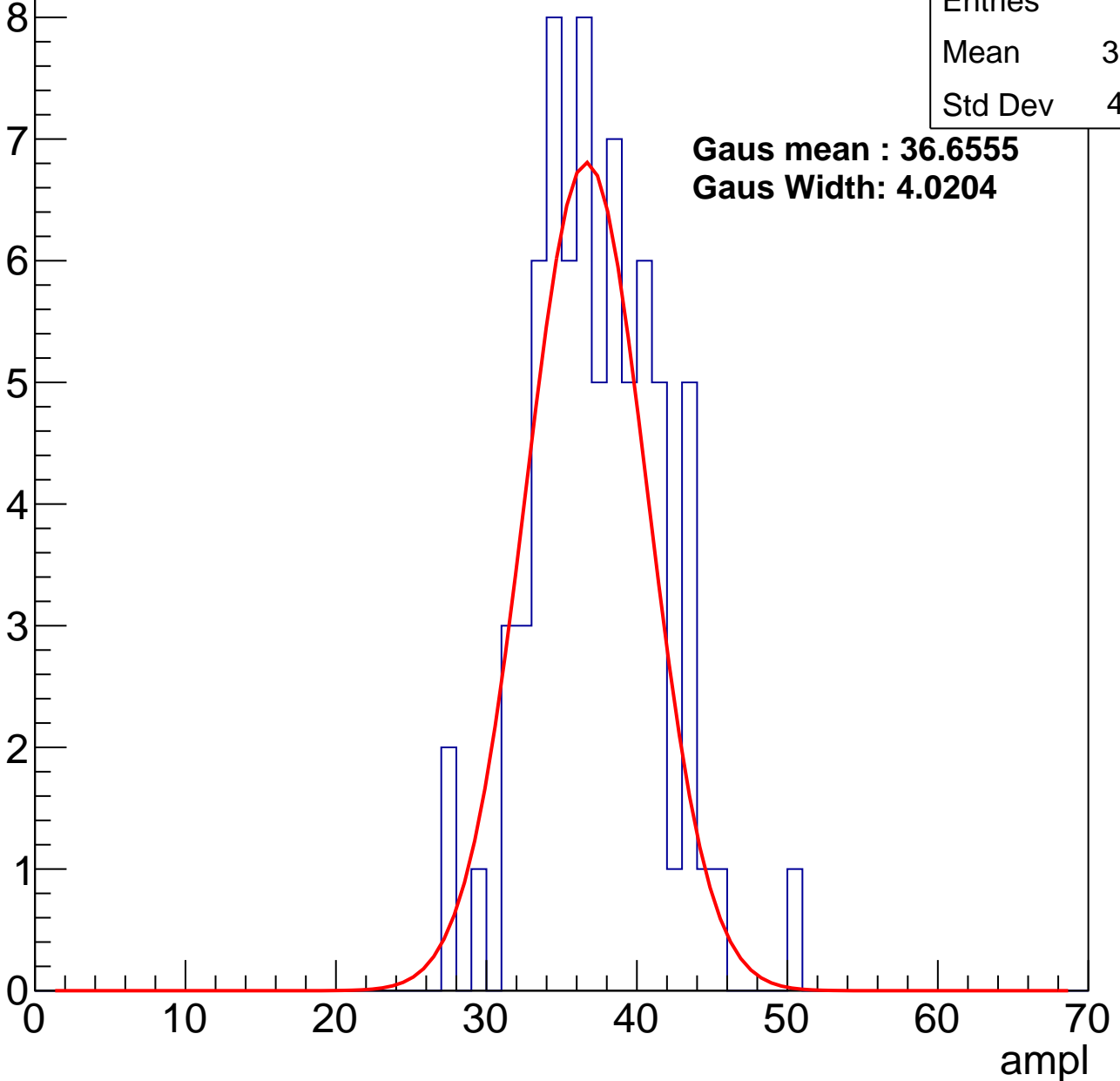
B1L100S, U12-ch56, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.85
Std Dev	4.181

Gaus mean : 36.6555
Gaus Width: 4.0204



B1L100S, U12-ch56, adc2

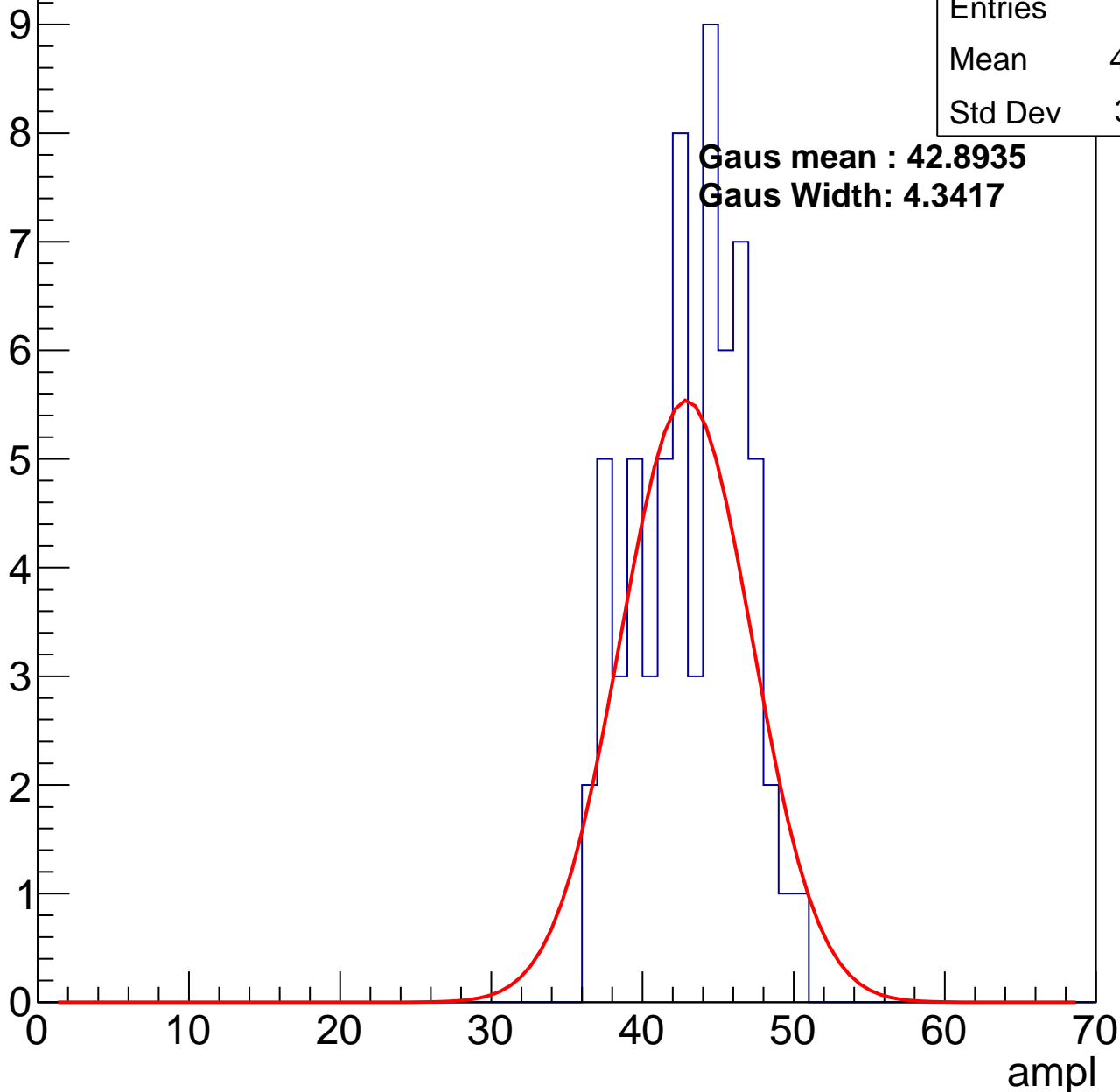
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.68
Std Dev	3.491

Gaus mean : 42.8935

Gaus Width: 4.3417

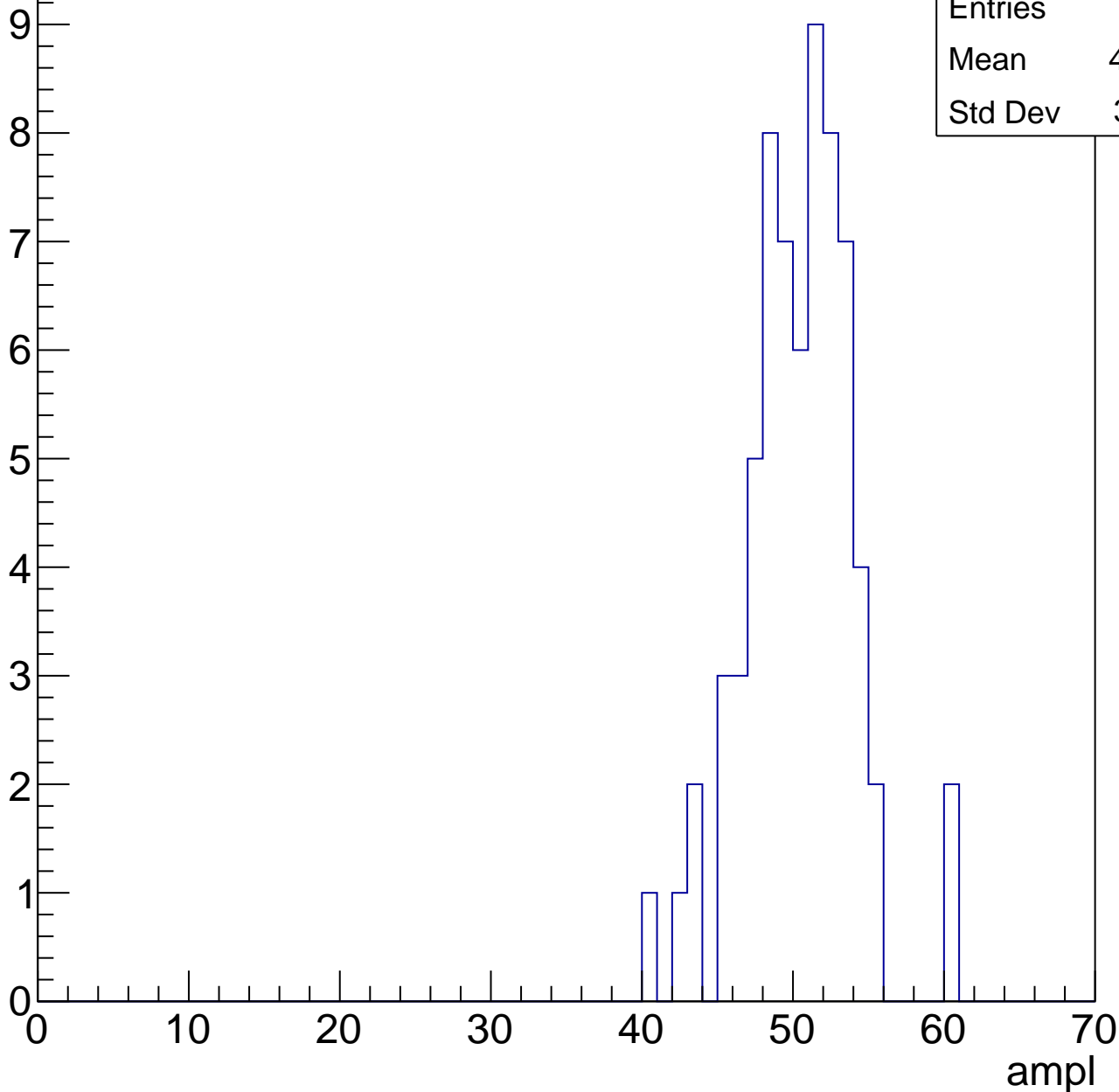


B1L100S, U12-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.93
Std Dev	3.611



B1L100S, U12-ch56, adc4

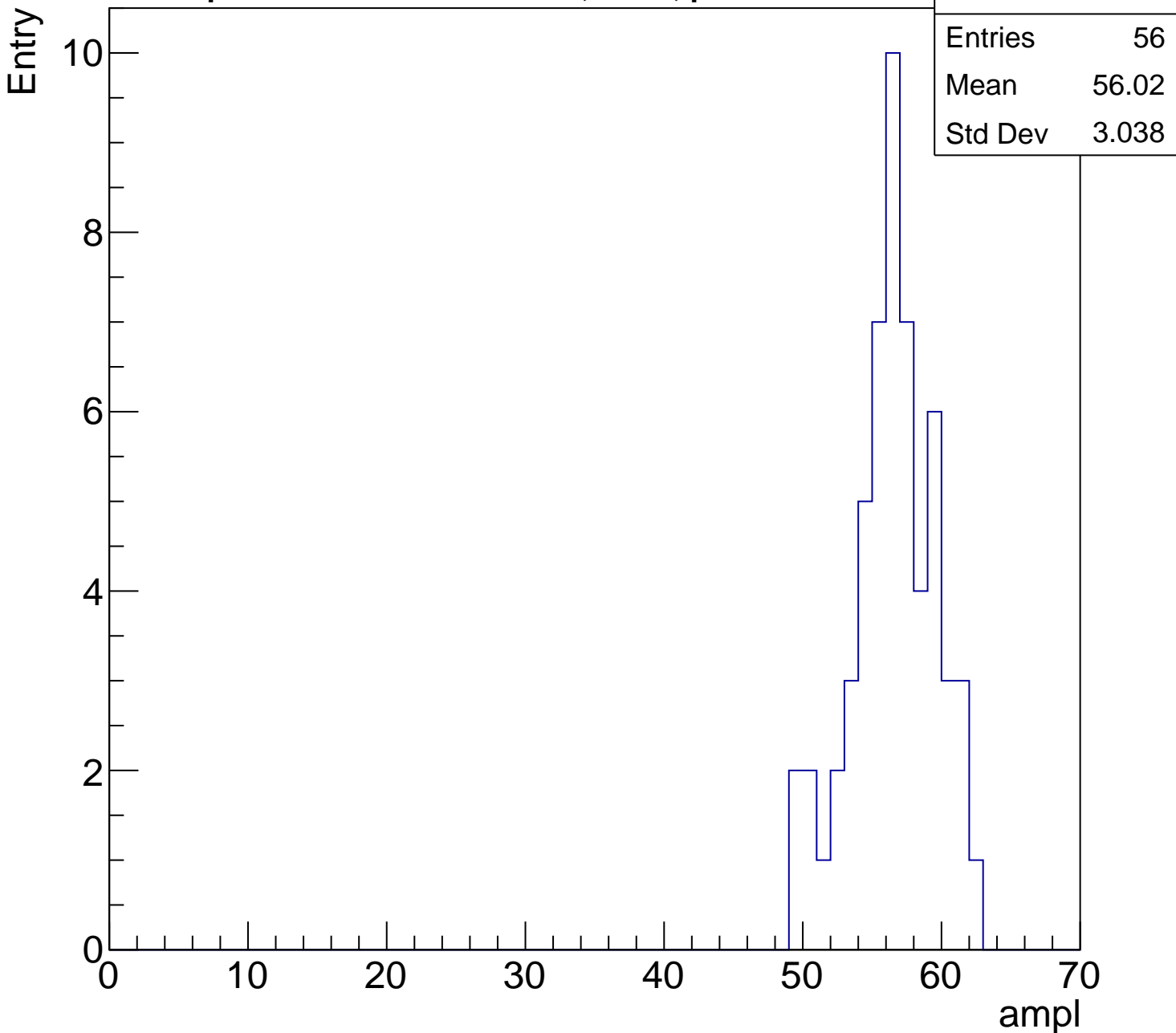
calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	56.02
Std Dev	3.038

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70
ampl

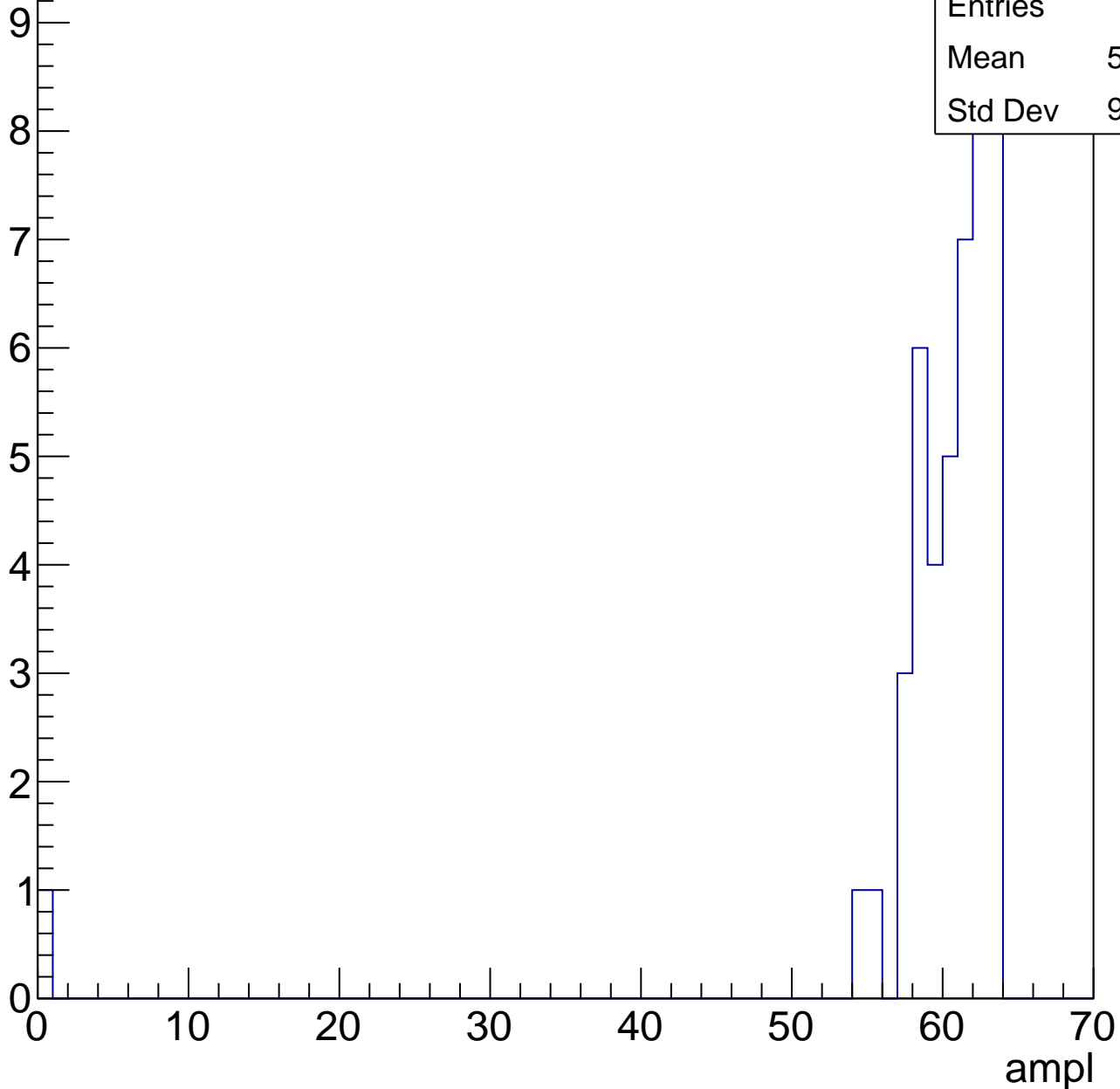


B1L100S, U12-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.04
Std Dev	9.086



B1L100S, U12-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U12-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch57, adc0

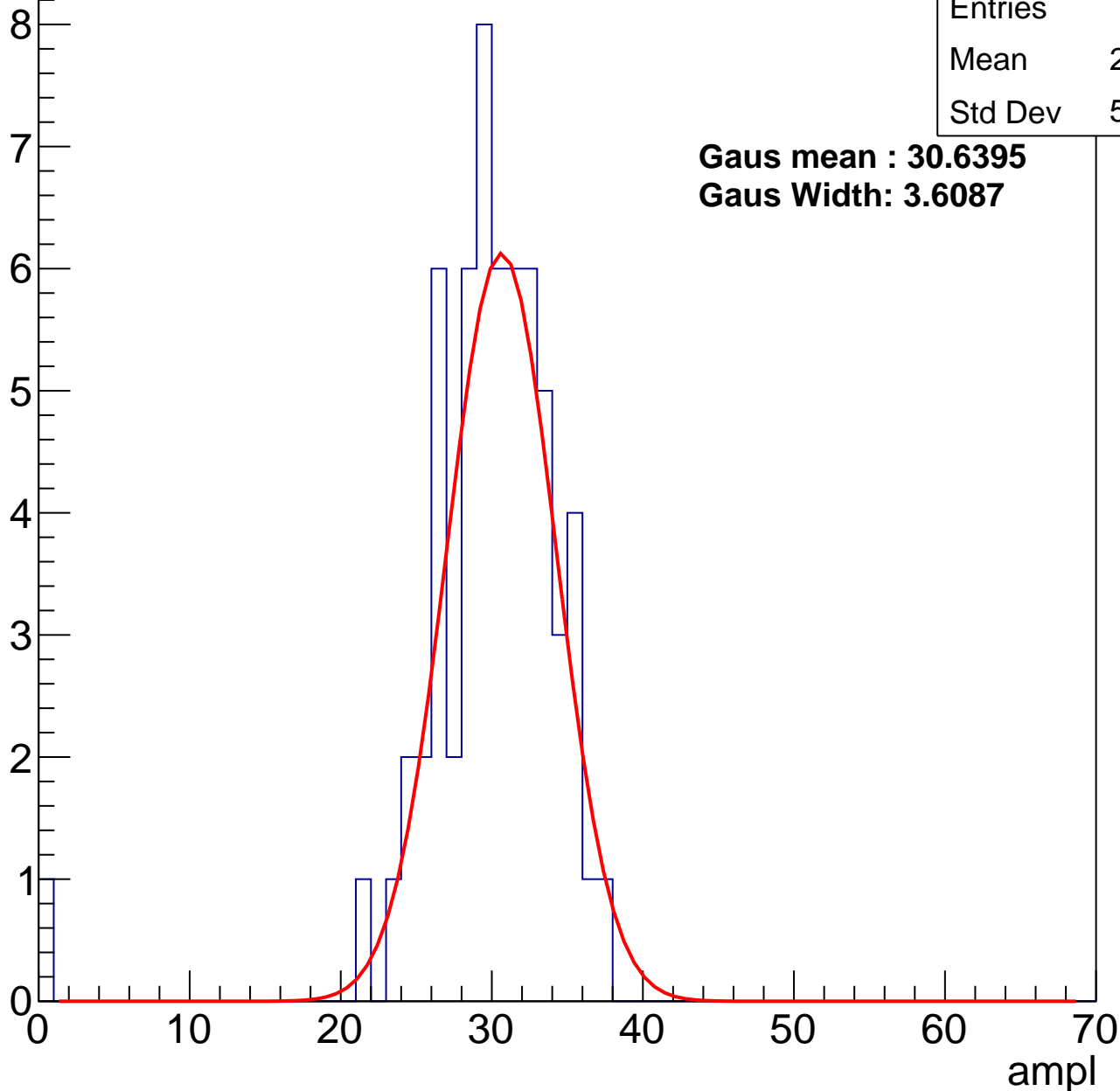
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.34
Std Dev	5.089

Gaus mean : 30.6395

Gaus Width: 3.6087



B1L100S, U12-ch57, adc1

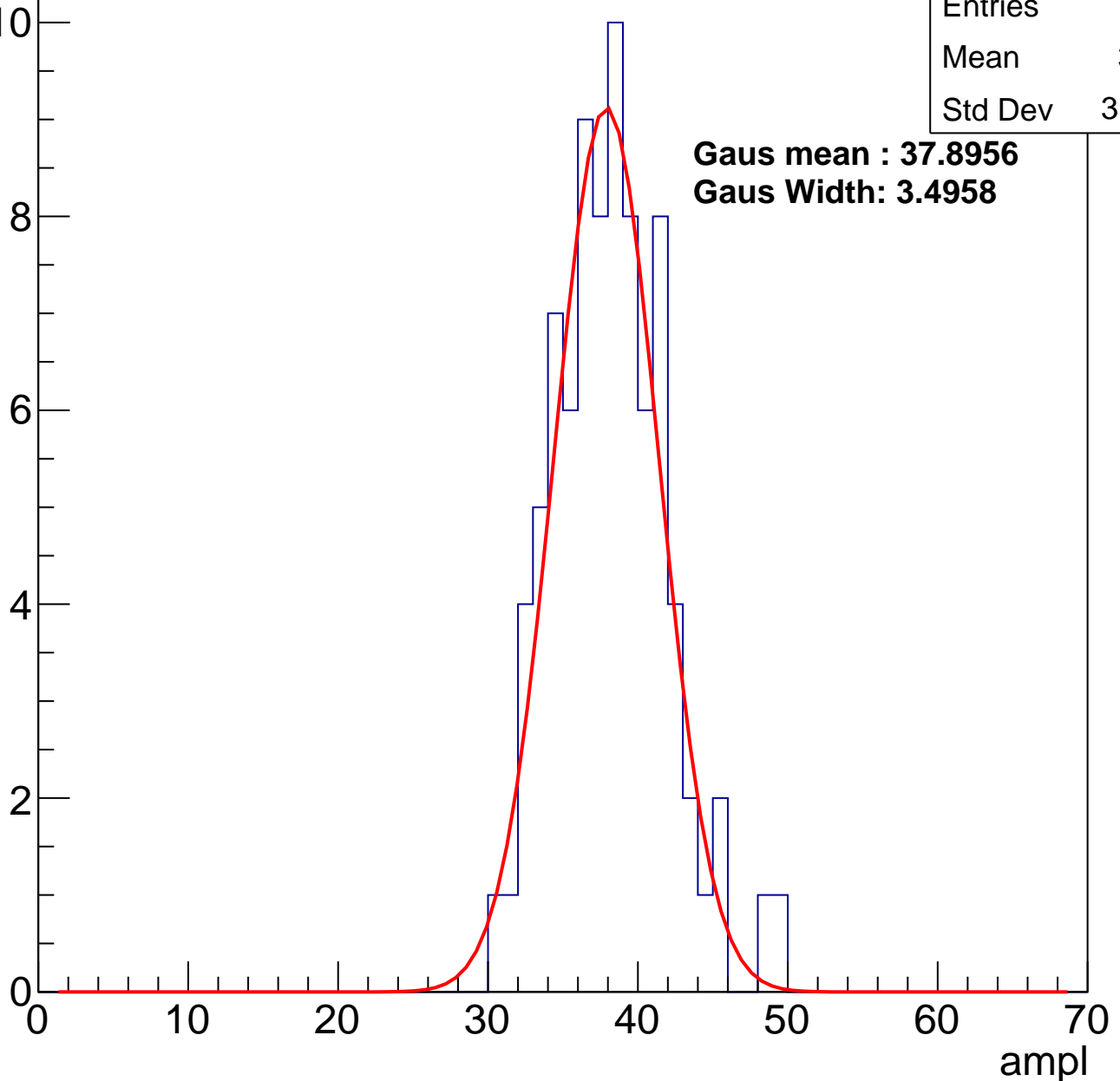
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	37.7
Std Dev	3.712

Gaus mean : 37.8956

Gaus Width: 3.4958



B1L100S, U12-ch57, adc2

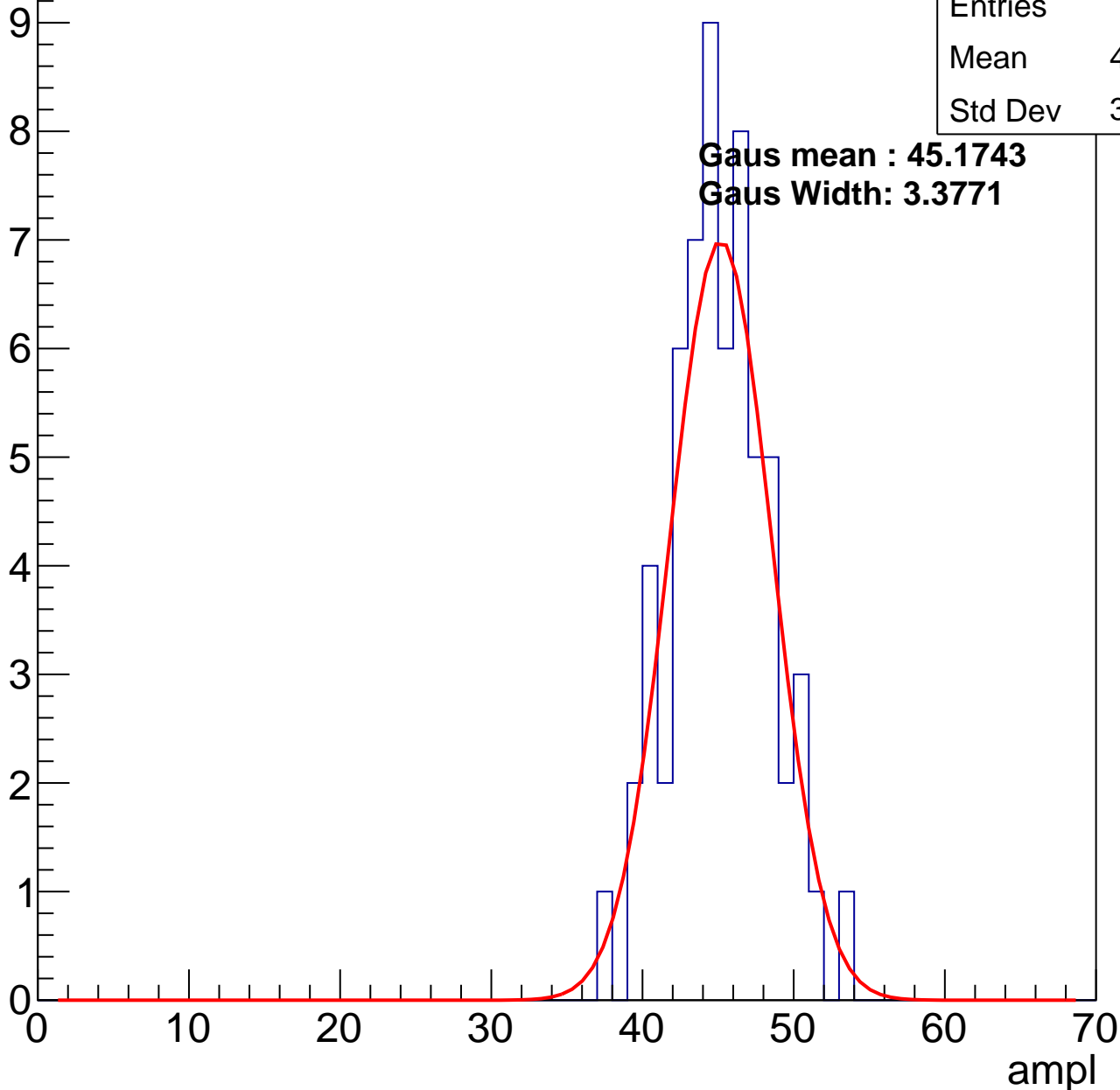
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	44.69
Std Dev	3.196

Gaus mean : 45.1743

Gaus Width: 3.3771

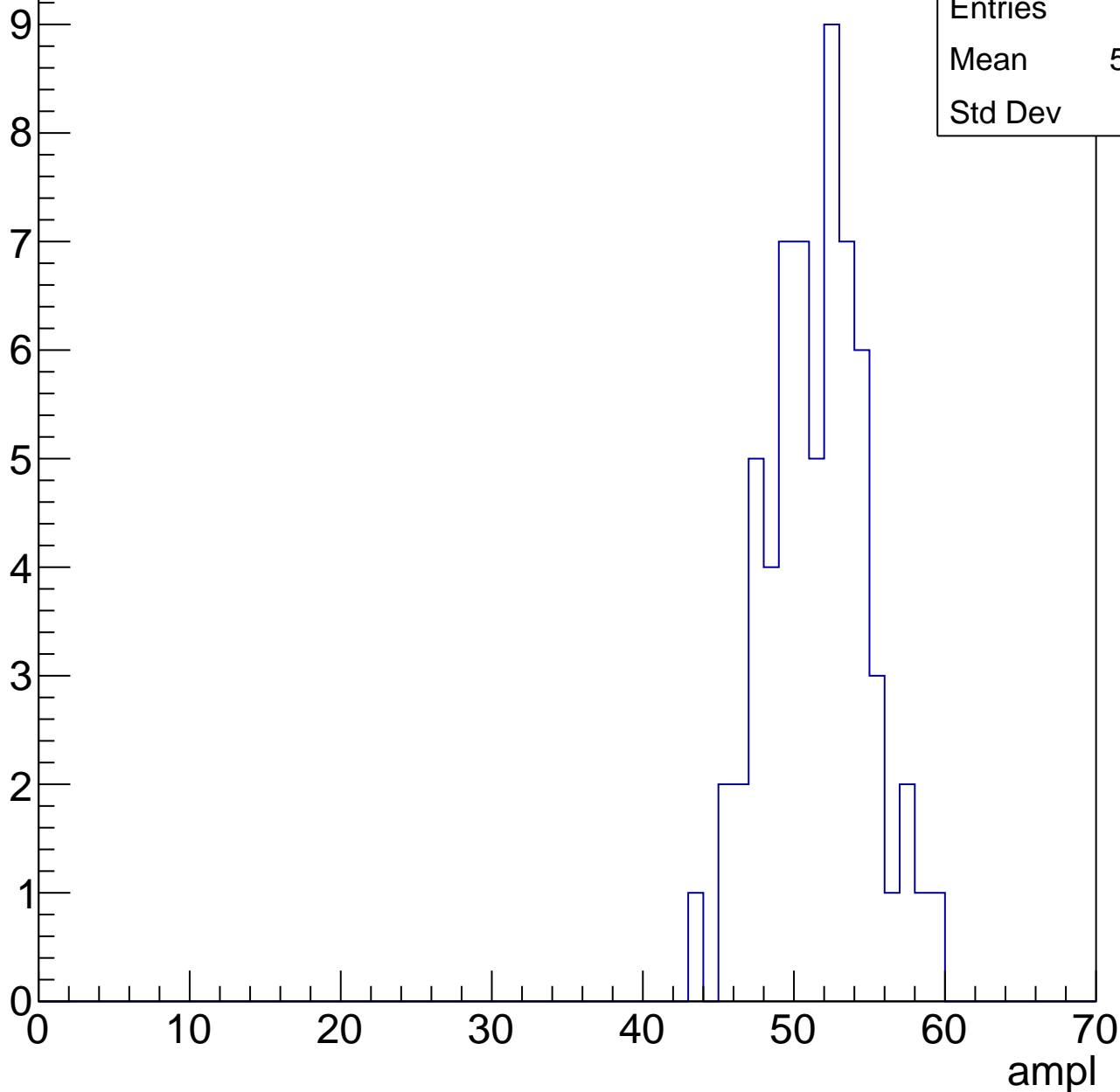


B1L100S, U12-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

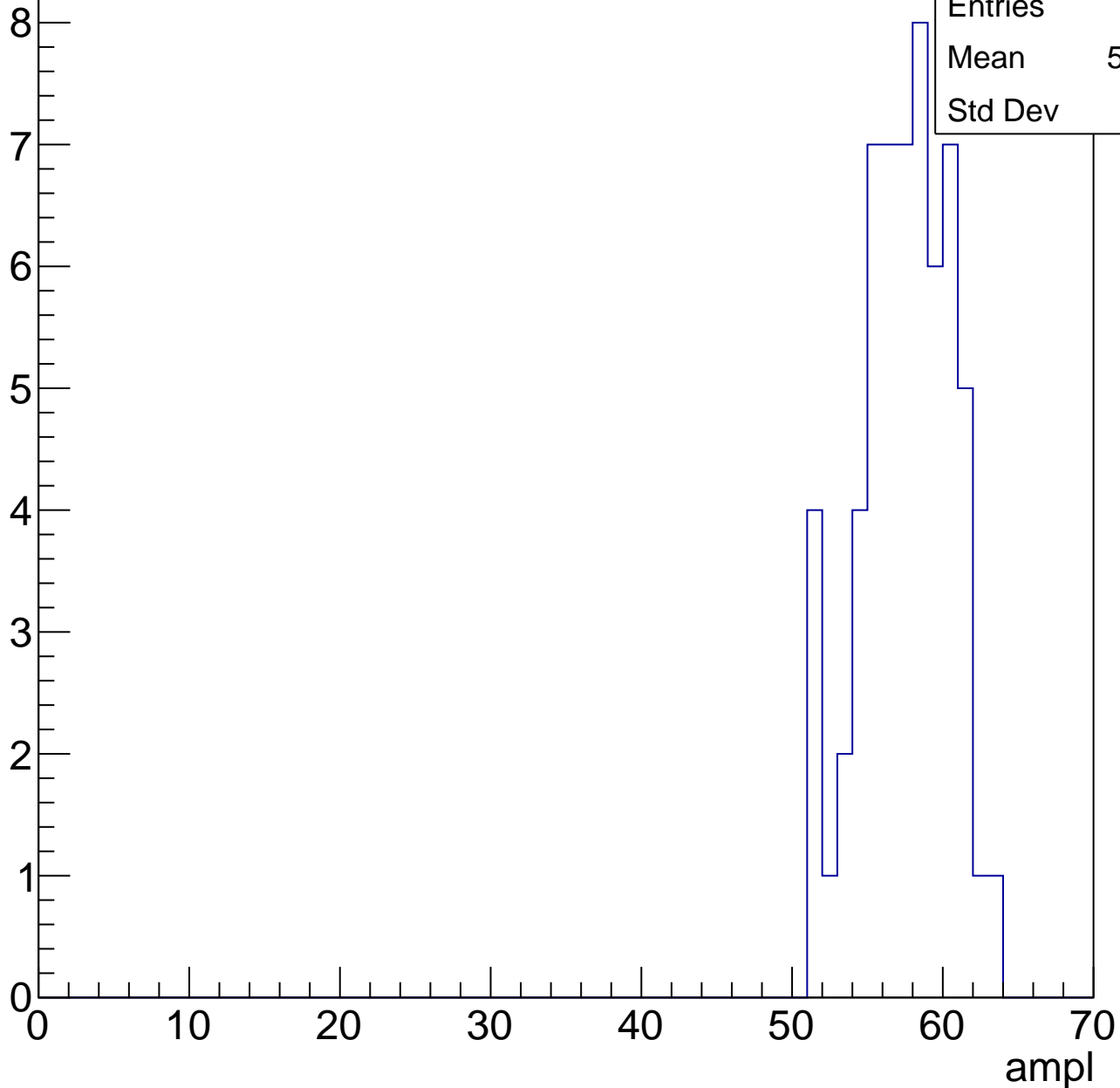
Entries	63
Mean	51.03
Std Dev	3.3



B1L100S, U12-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

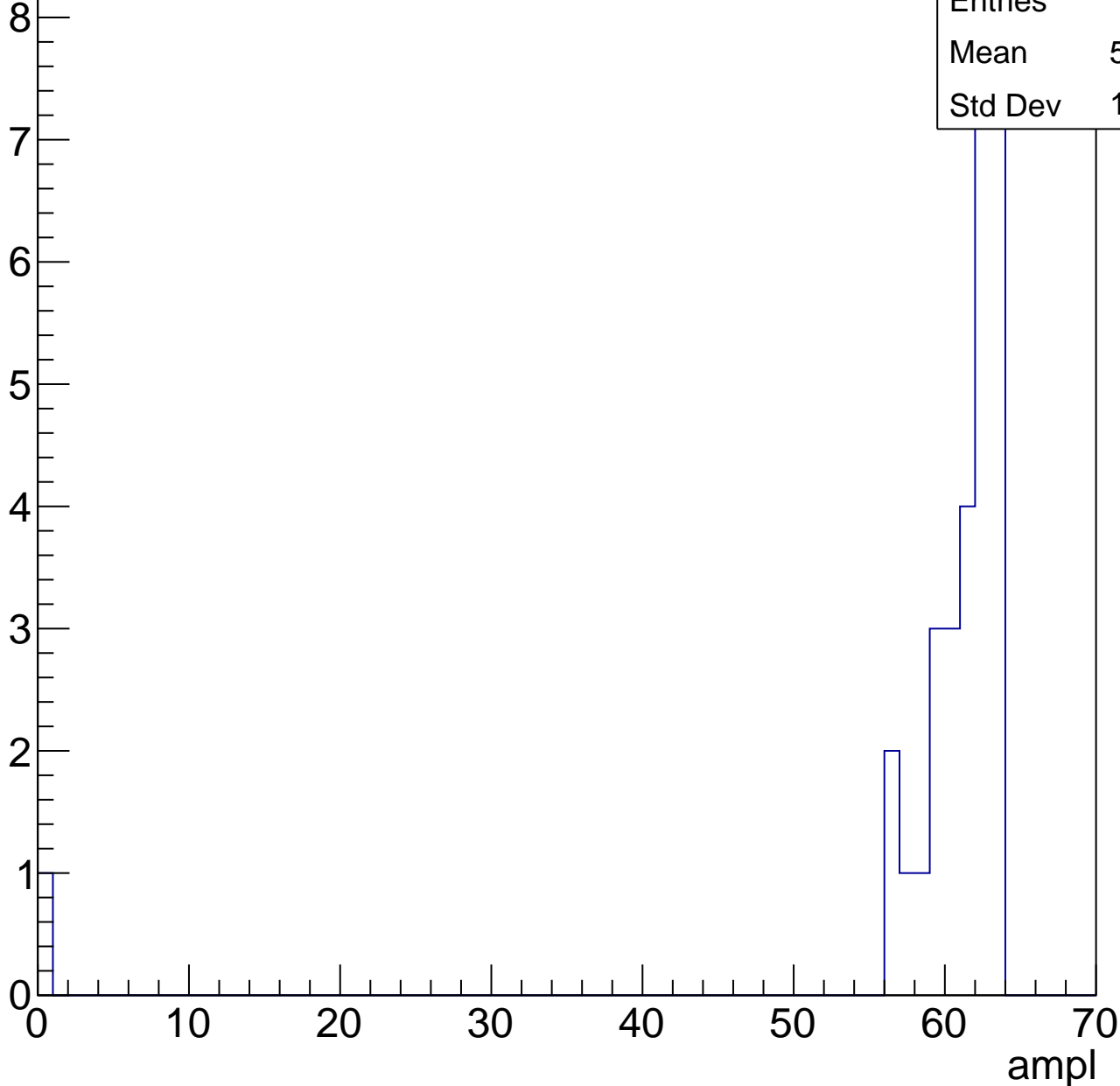


B1L100S, U12-ch57, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.97
Std Dev	10.96



B1L100S, U12-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch58, adc0

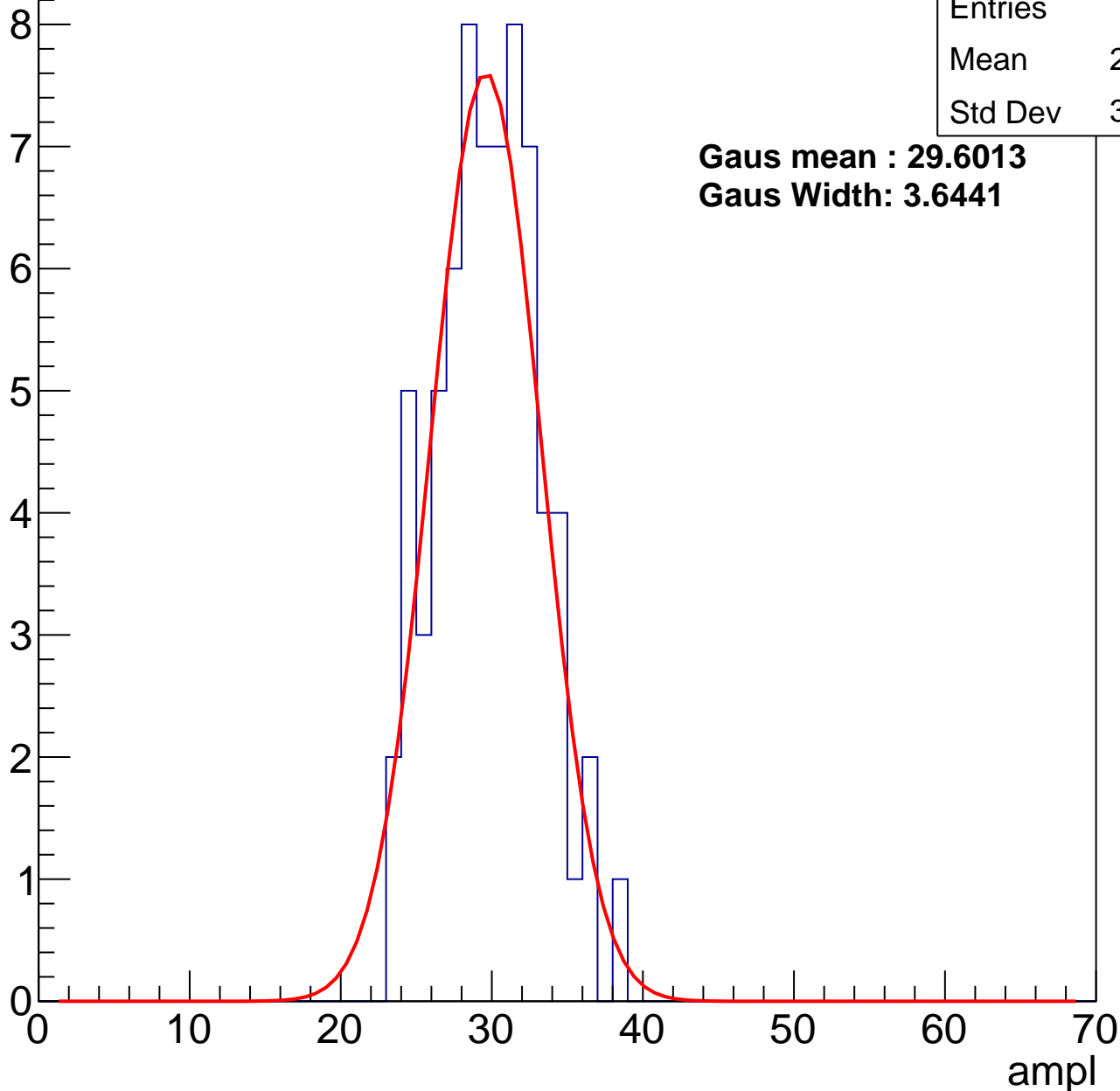
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.36
Std Dev	3.368

Gaus mean : 29.6013

Gaus Width: 3.6441



B1L100S, U12-ch58, adc1

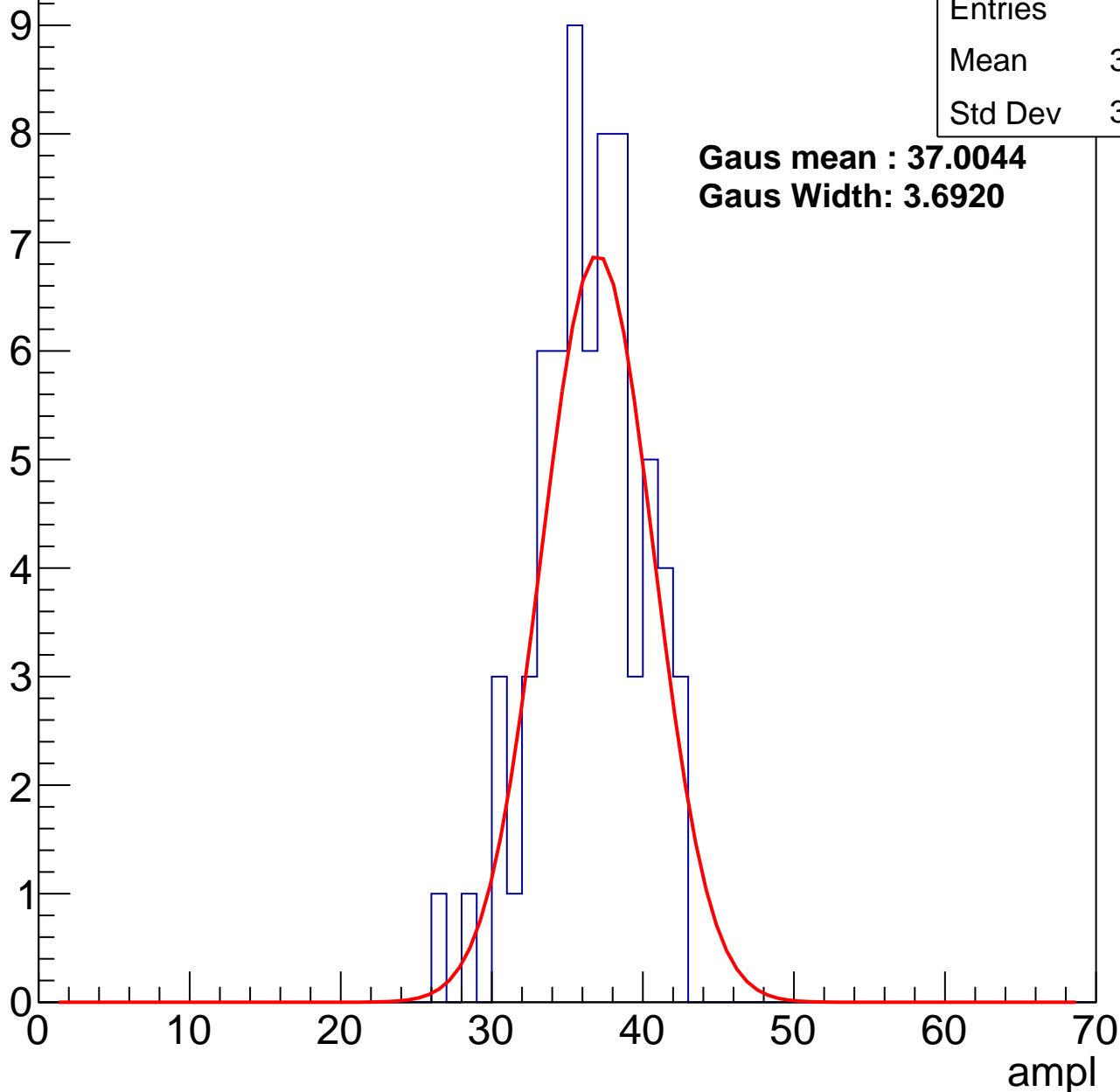
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.99
Std Dev	3.427

Gaus mean : 37.0044

Gaus Width: 3.6920



B1L100S, U12-ch58, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	42.63
Std Dev	3.408

Gaus mean : 44.3675

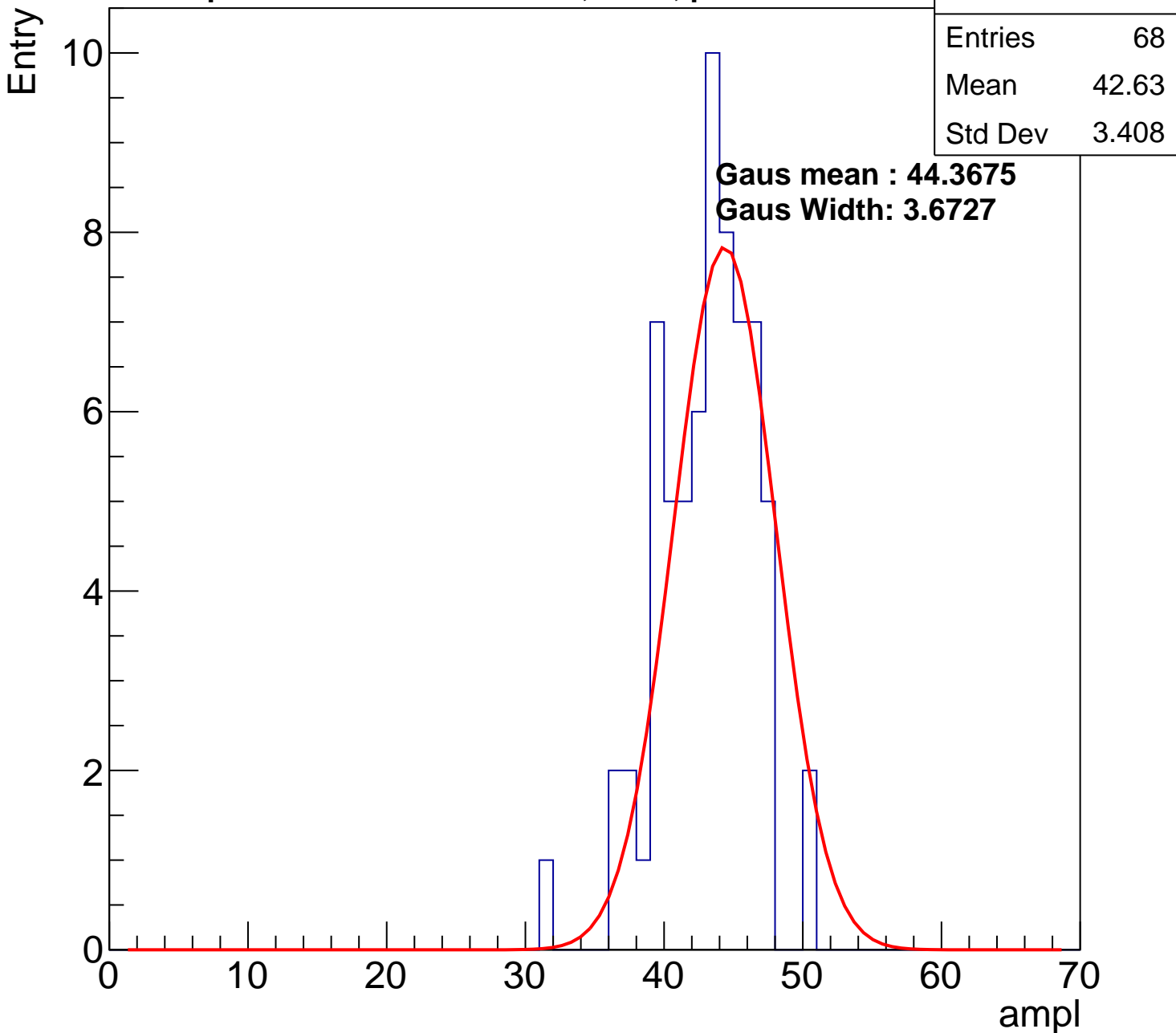
Gaus Width: 3.6727

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

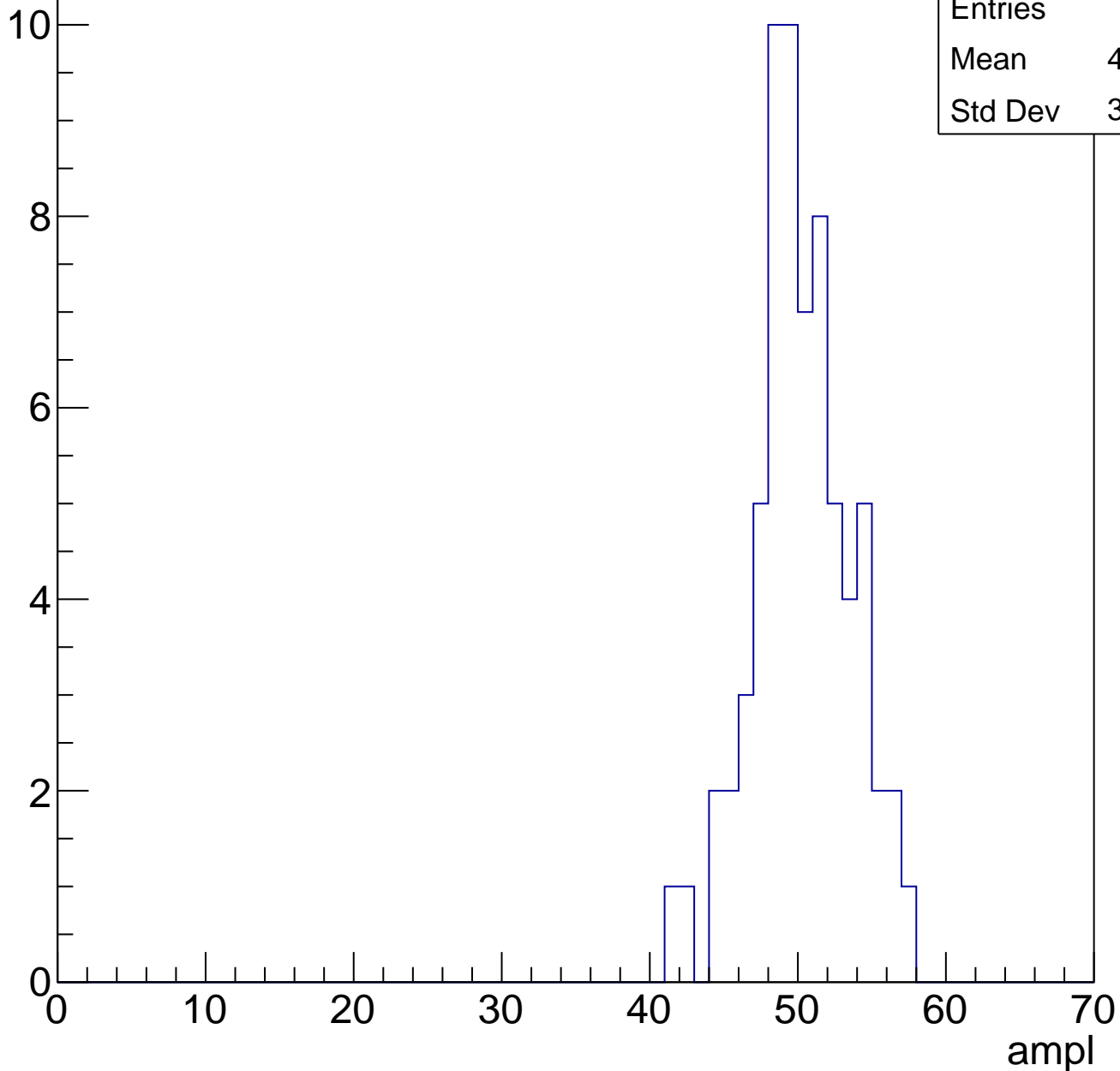


B1L100S, U12-ch58, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	49.75
Std Dev	3.256

Entry

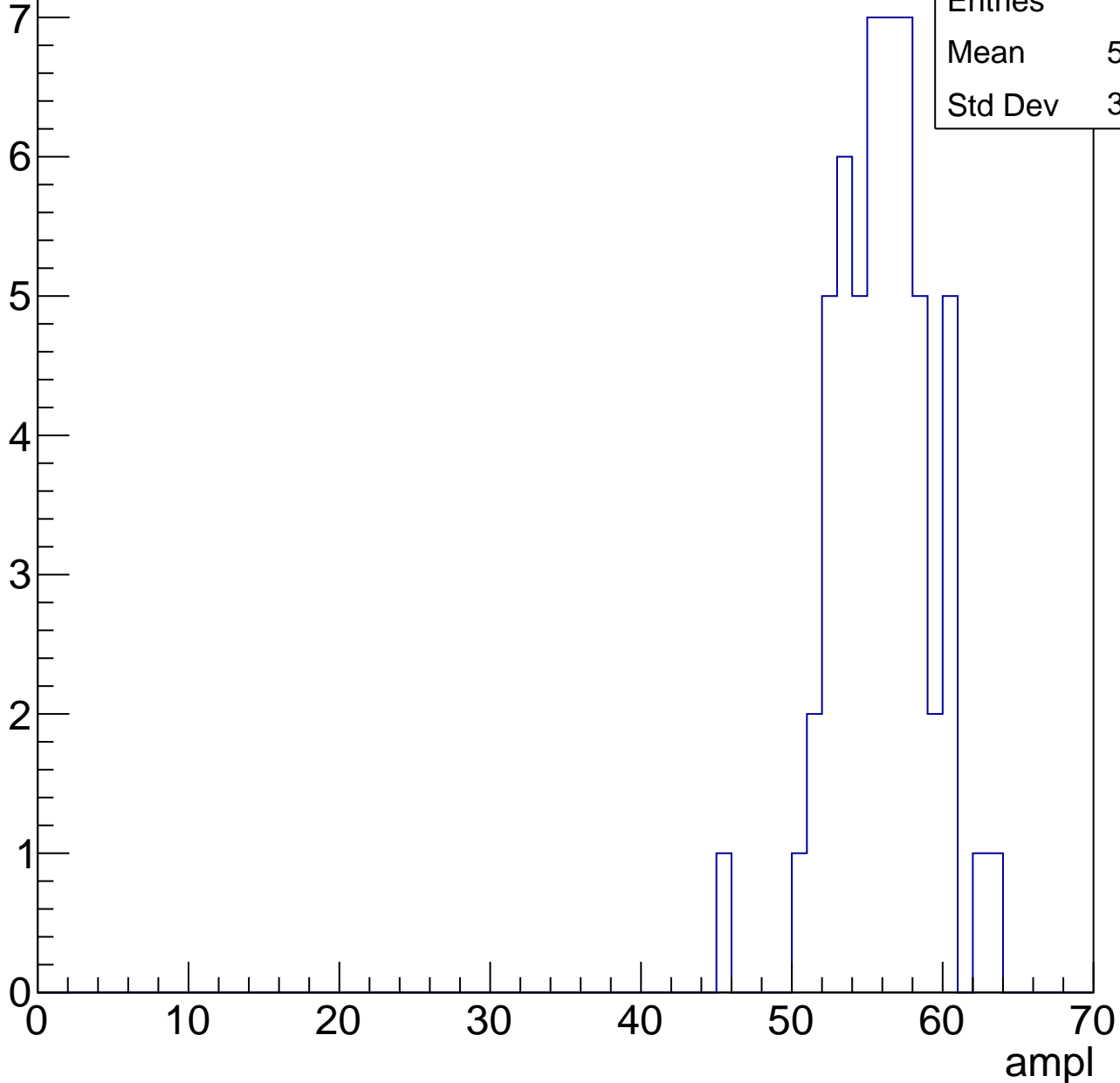


B1L100S, U12-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	55.53
Std Dev	3.207

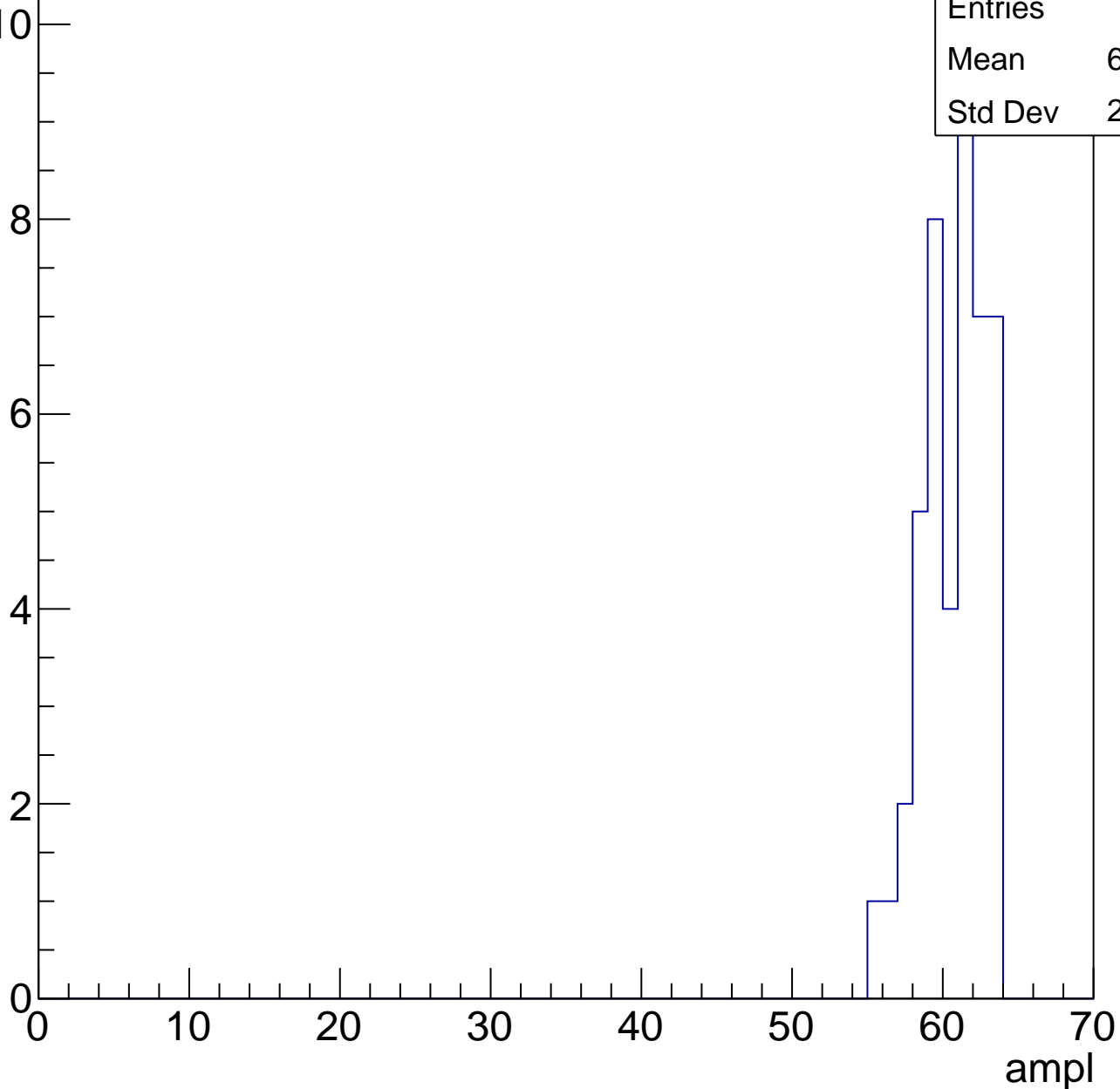


B1L100S, U12-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

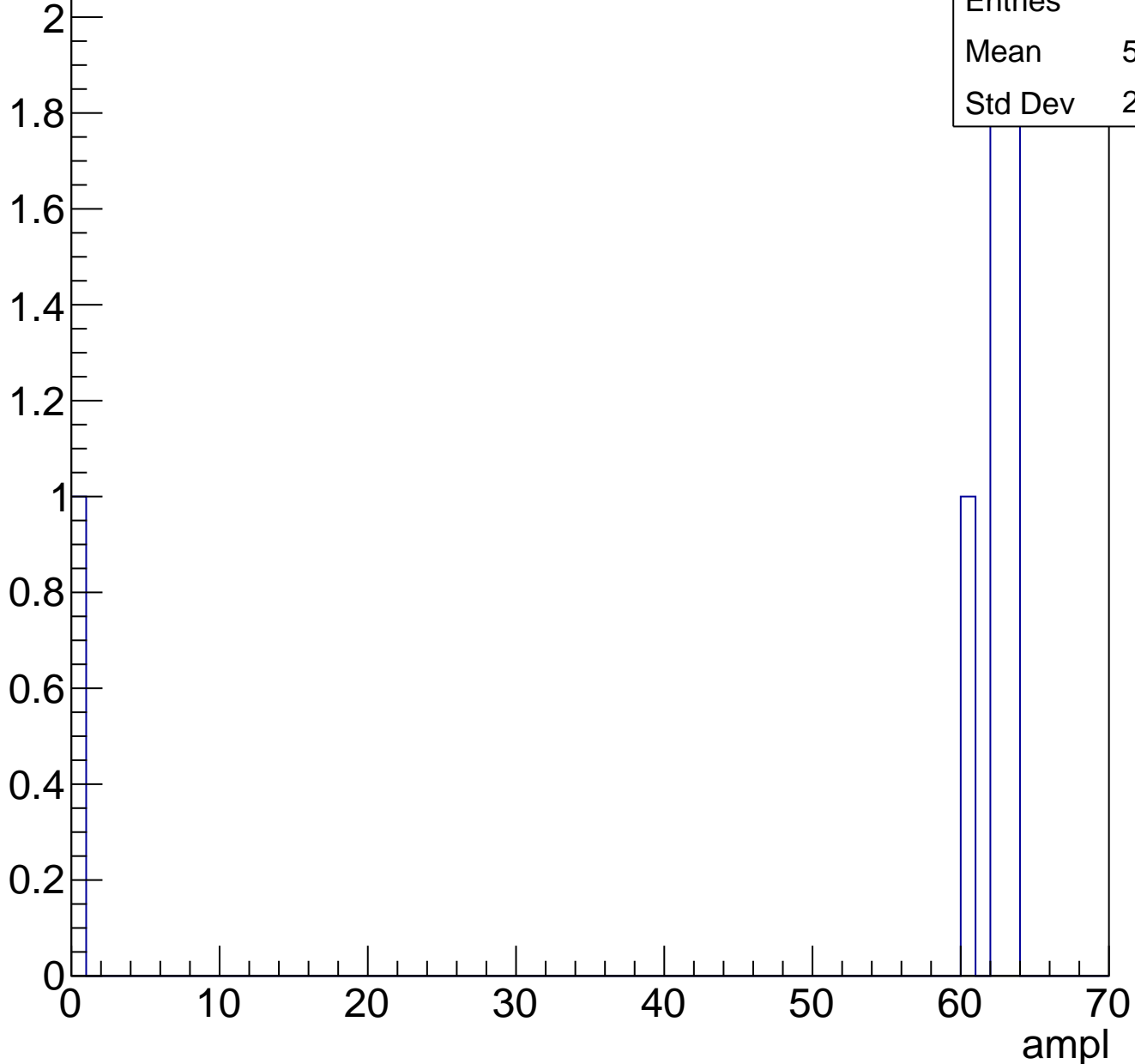
Entries	45
Mean	60.27
Std Dev	2.026



B1L100S, U12-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch59, adc0

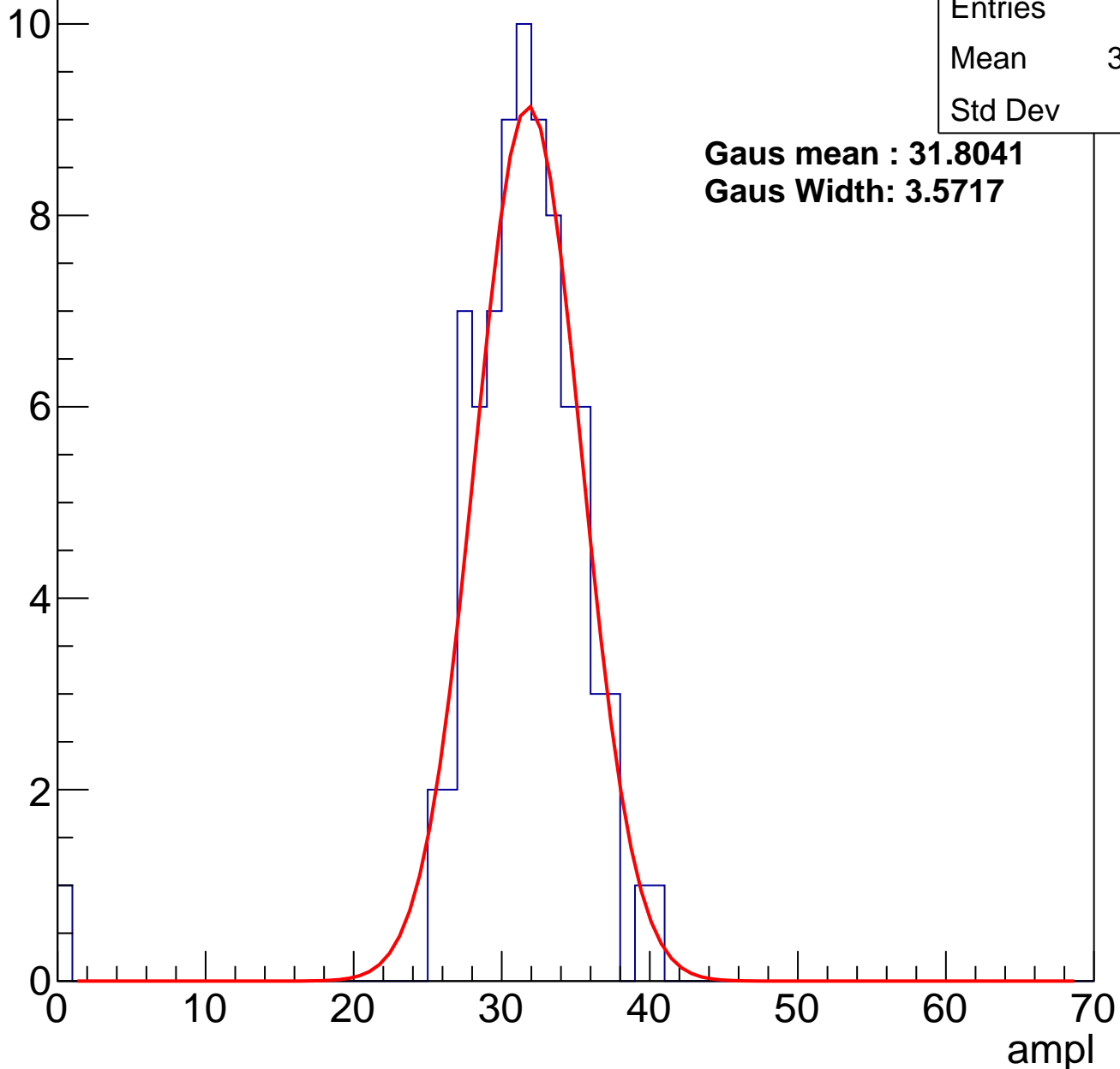
calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	30.94
Std Dev	4.72

Gaus mean : 31.8041

Gaus Width: 3.5717

Entry



B1L100S, U12-ch59, adc1

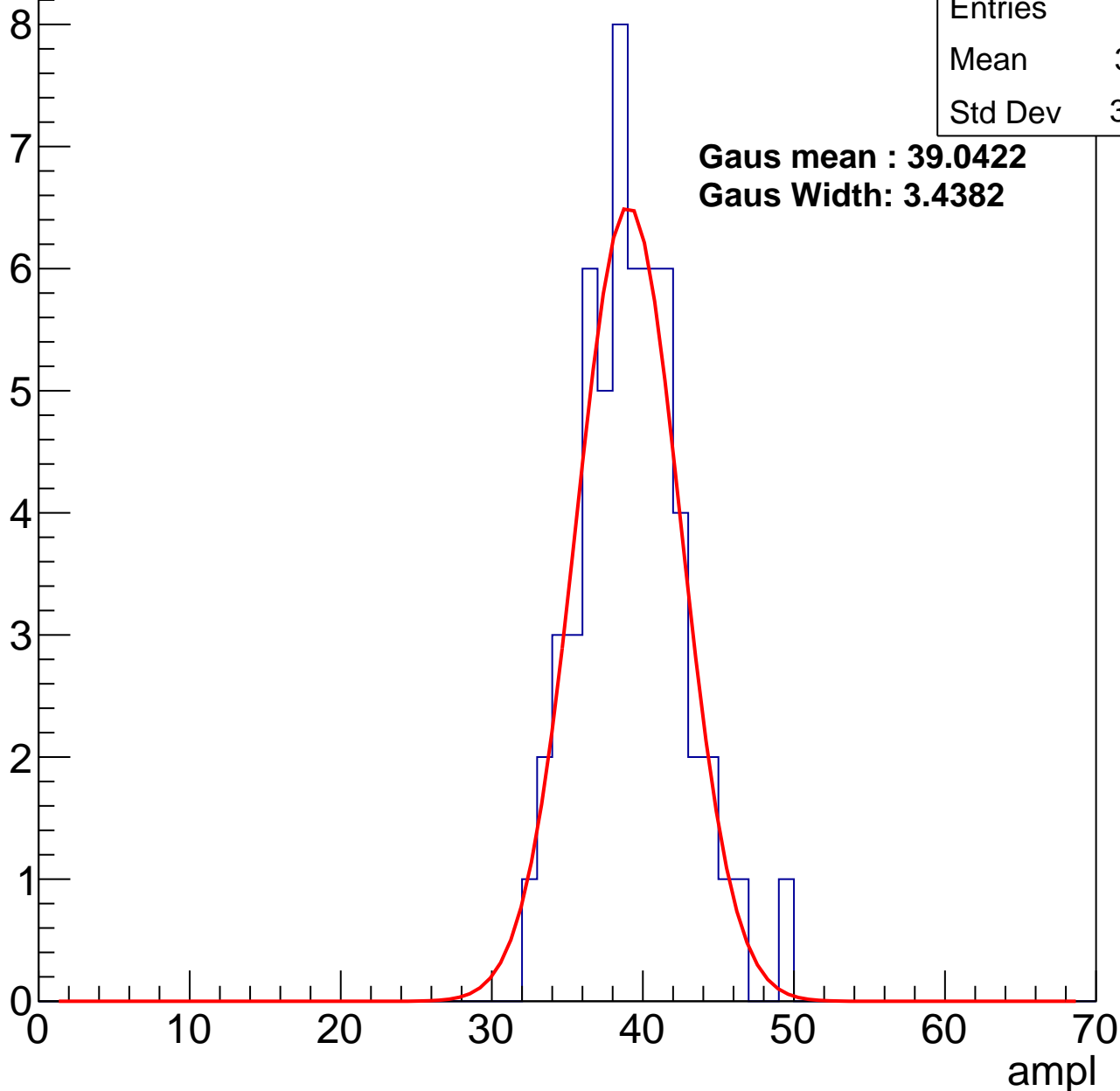
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	38.81
Std Dev	3.395

Gaus mean : 39.0422

Gaus Width: 3.4382



B1L100S, U12-ch59, adc2

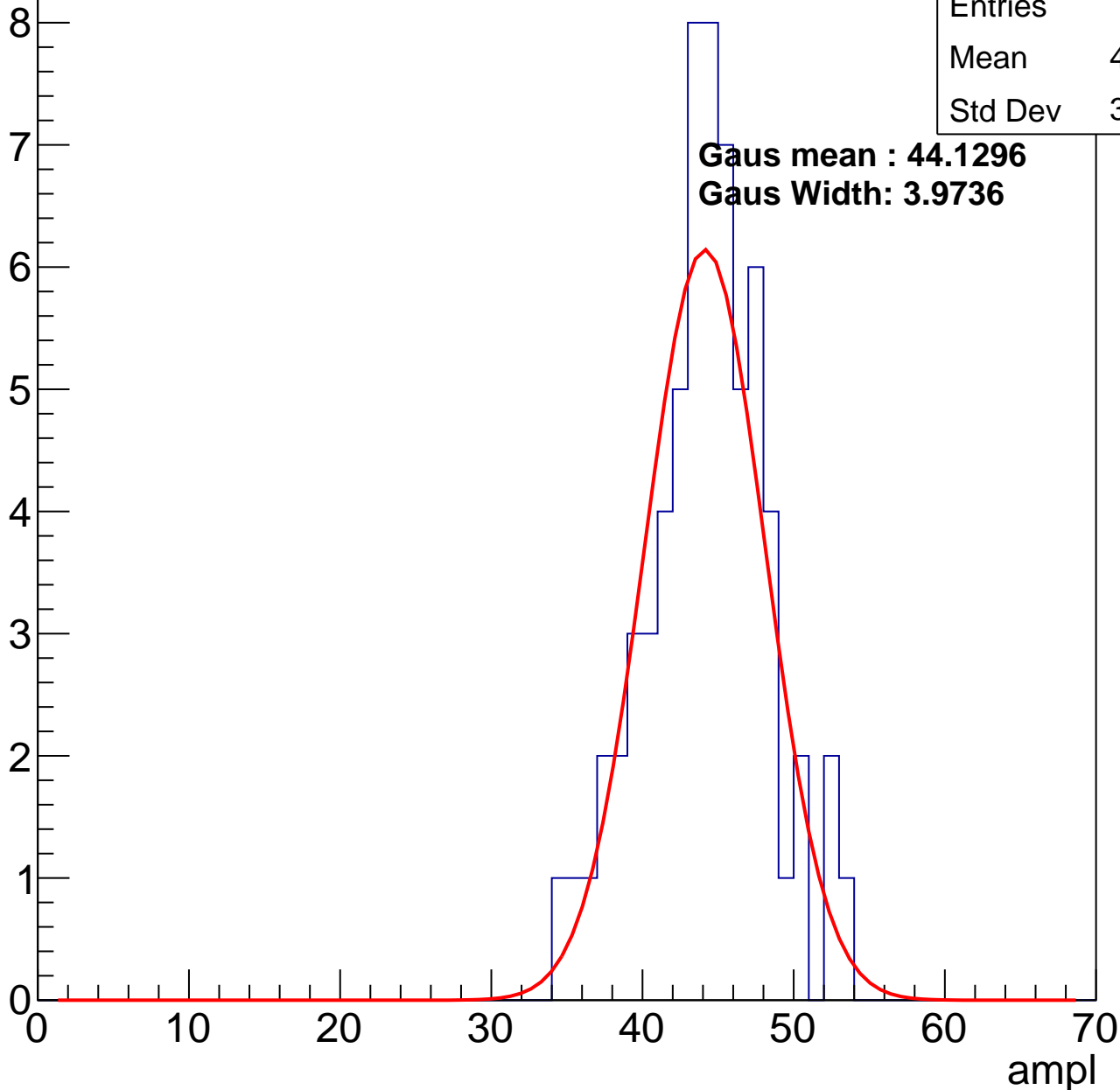
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.74
Std Dev	3.986

Gaus mean : 44.1296

Gaus Width: 3.9736

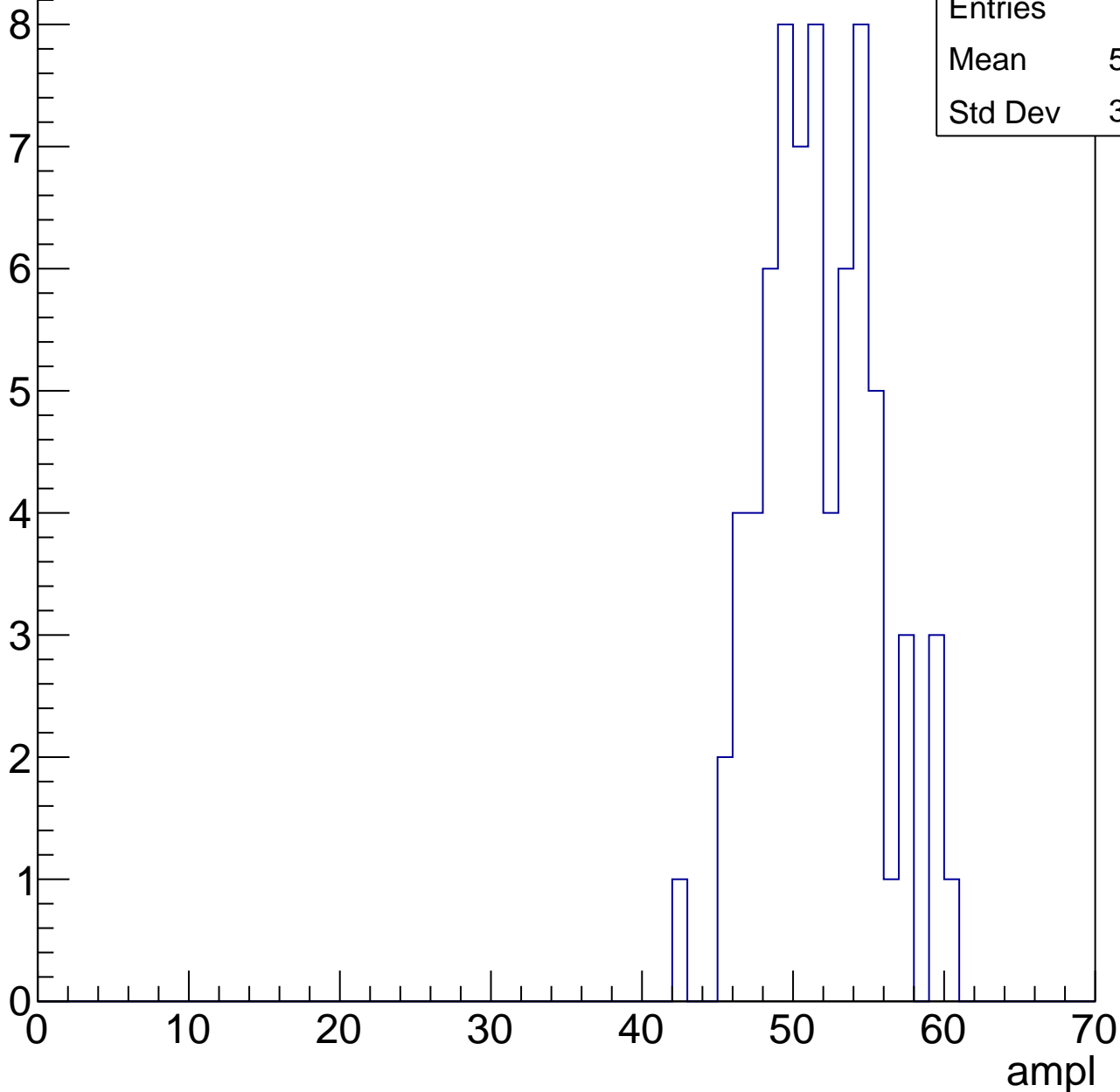


B1L100S, U12-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

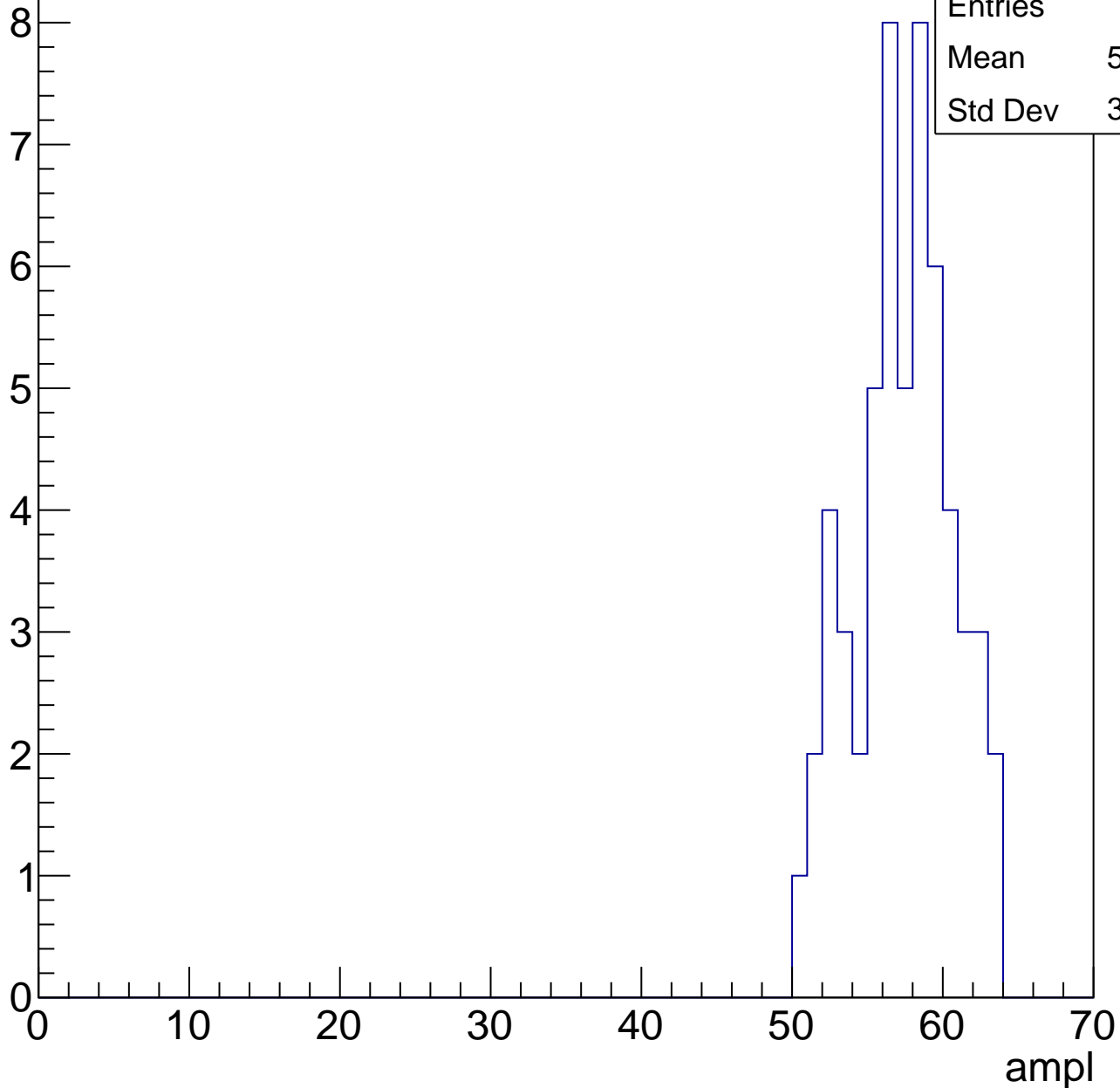
Entries	71
Mean	51.25
Std Dev	3.737



B1L100S, U12-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

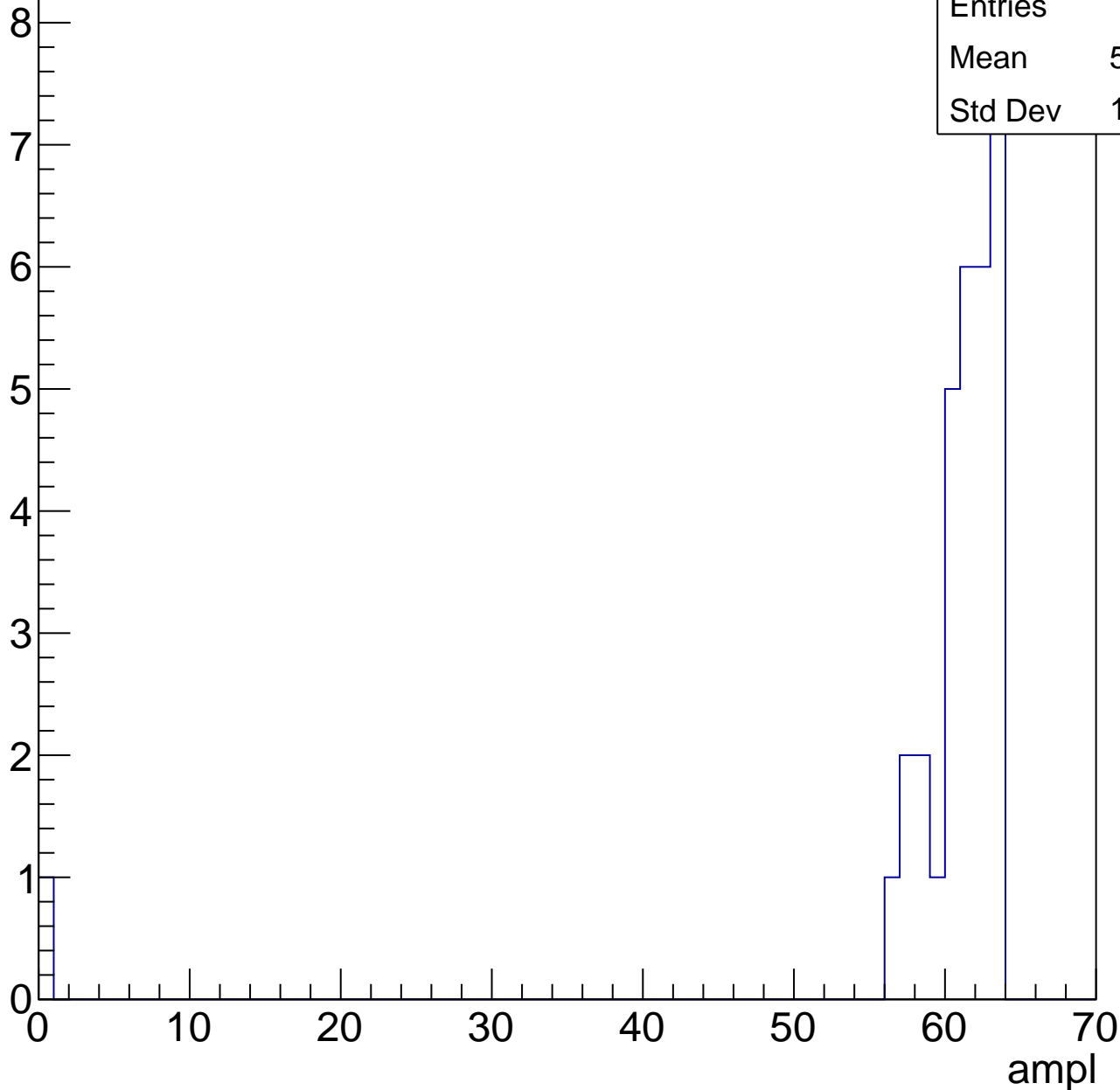


B1L100S, U12-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.97
Std Dev	10.77



B1L100S, U12-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch60, adc0

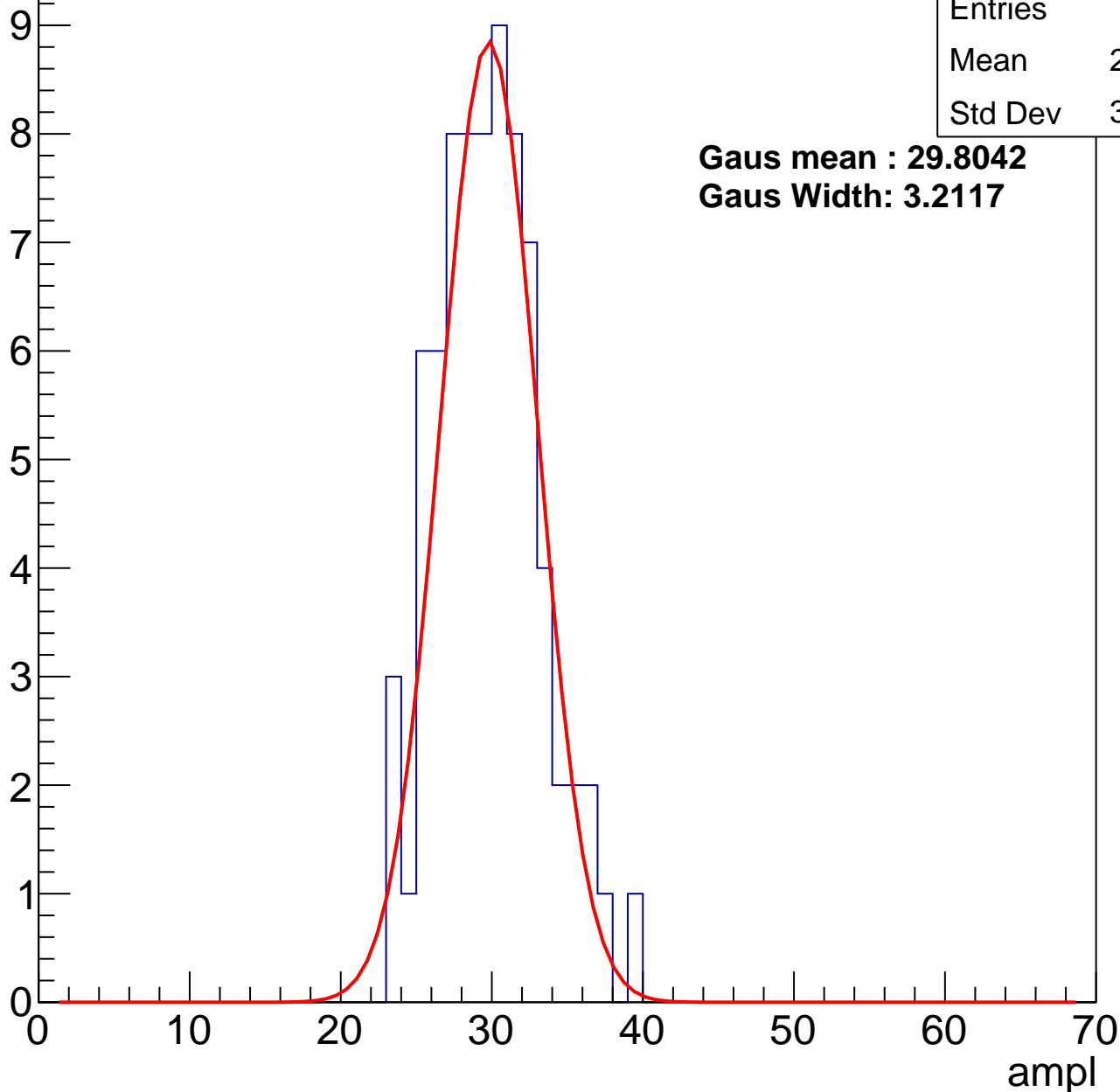
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	29.36
Std Dev	3.374

Gaus mean : 29.8042

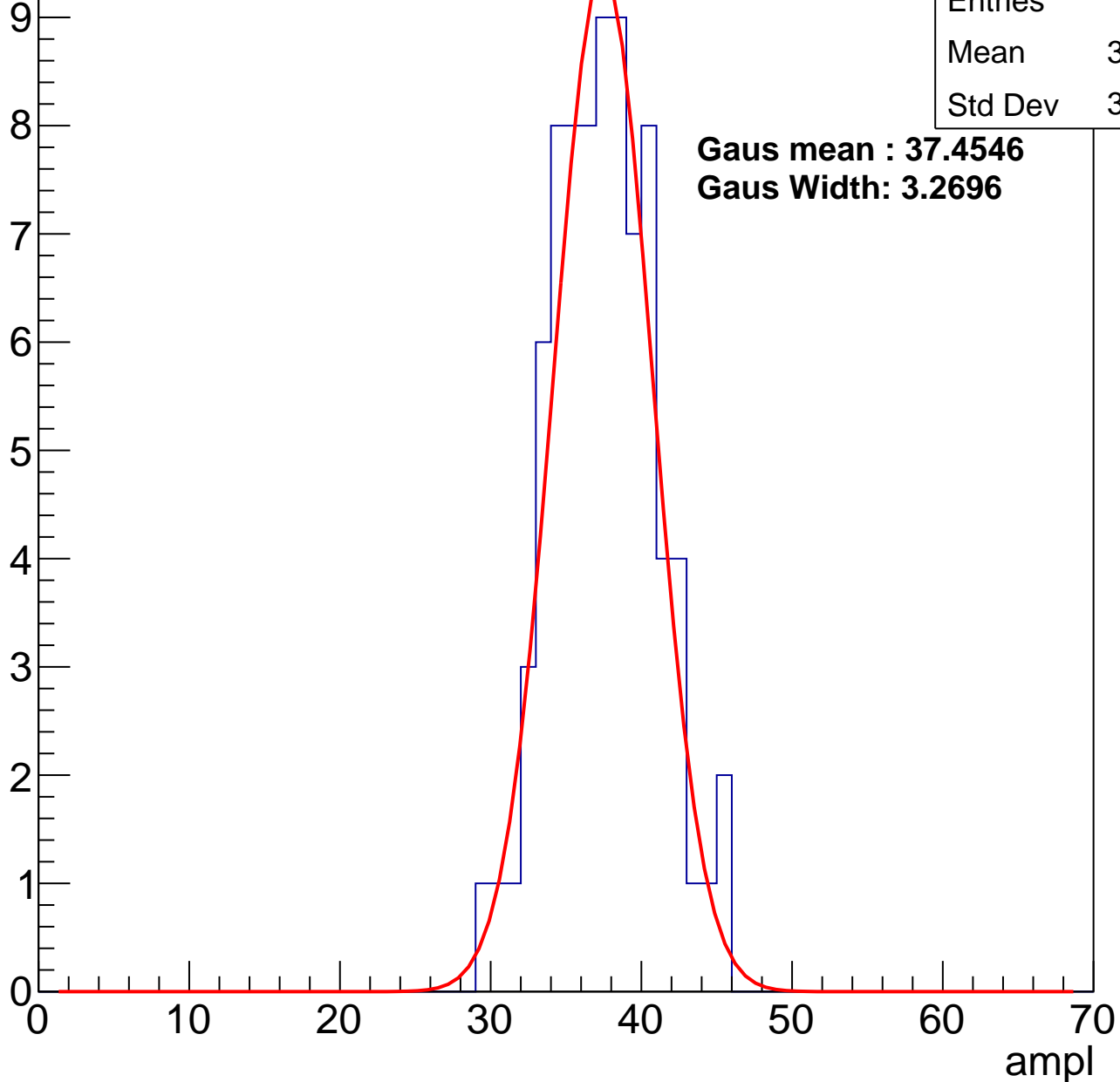
Gaus Width: 3.2117



B1L100S, U12-ch60, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch60, adc2

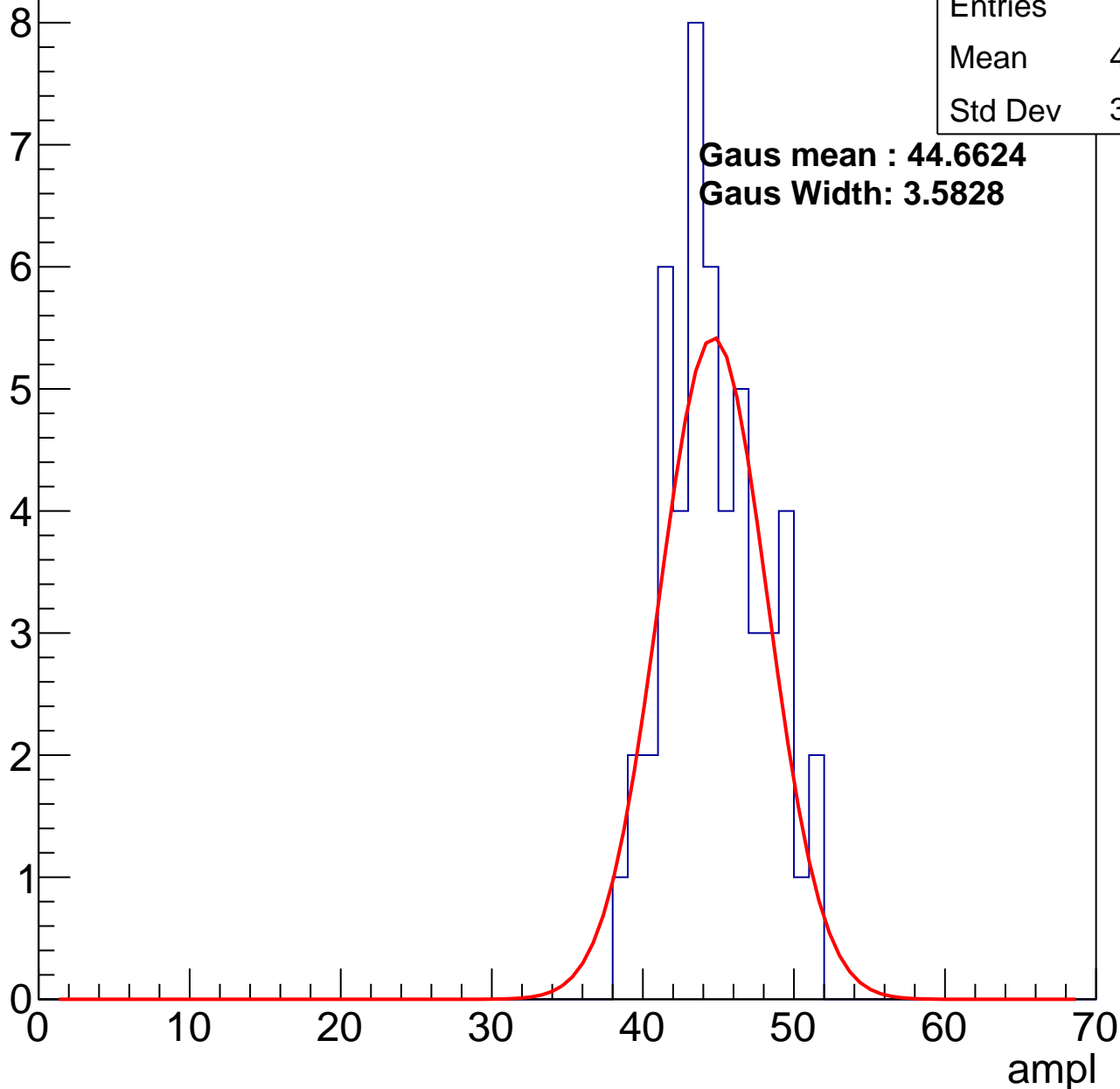
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	44.33
Std Dev	3.203

Gaus mean : 44.6624

Gaus Width: 3.5828

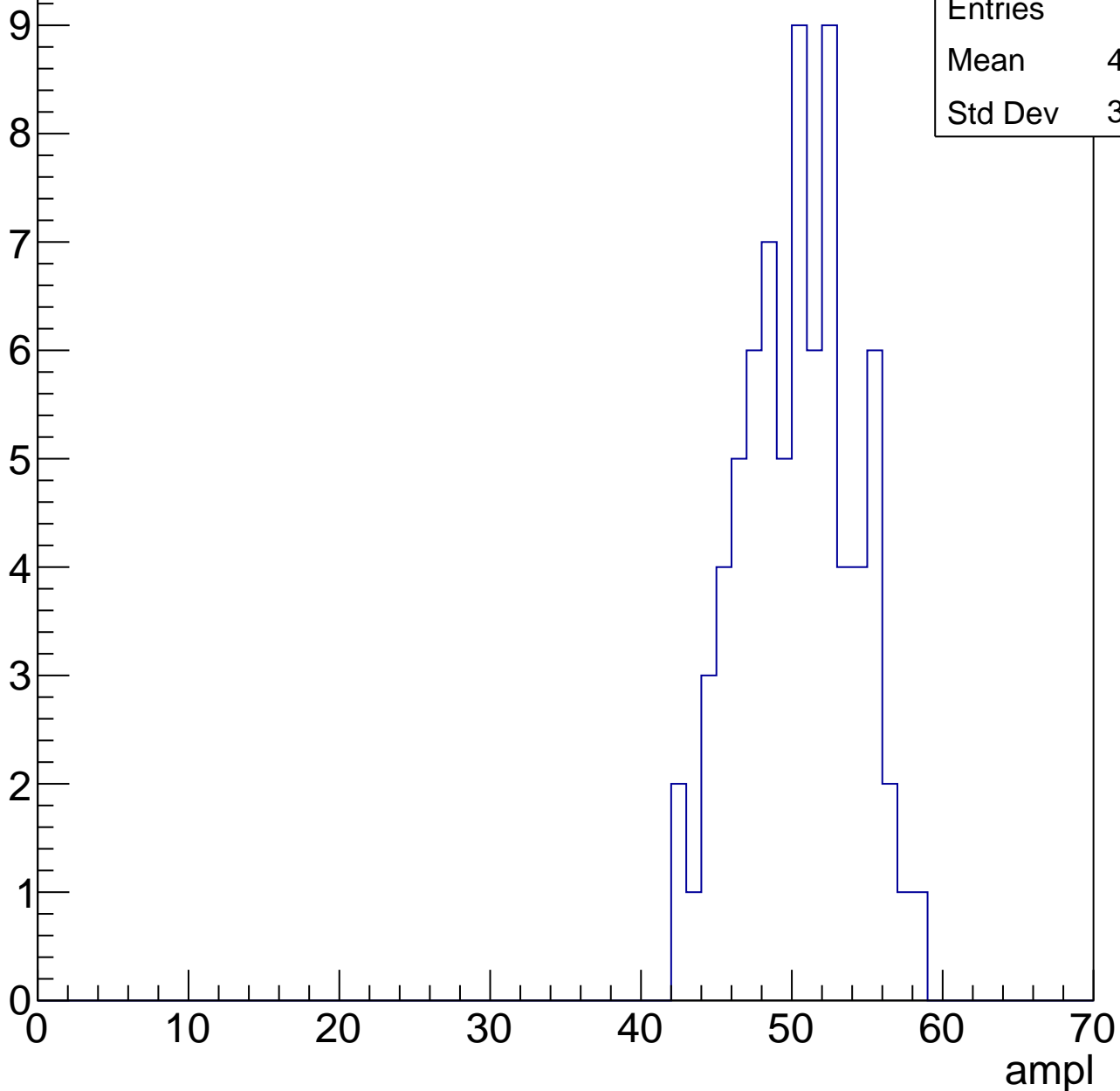


B1L100S, U12-ch60, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	49.88
Std Dev	3.702

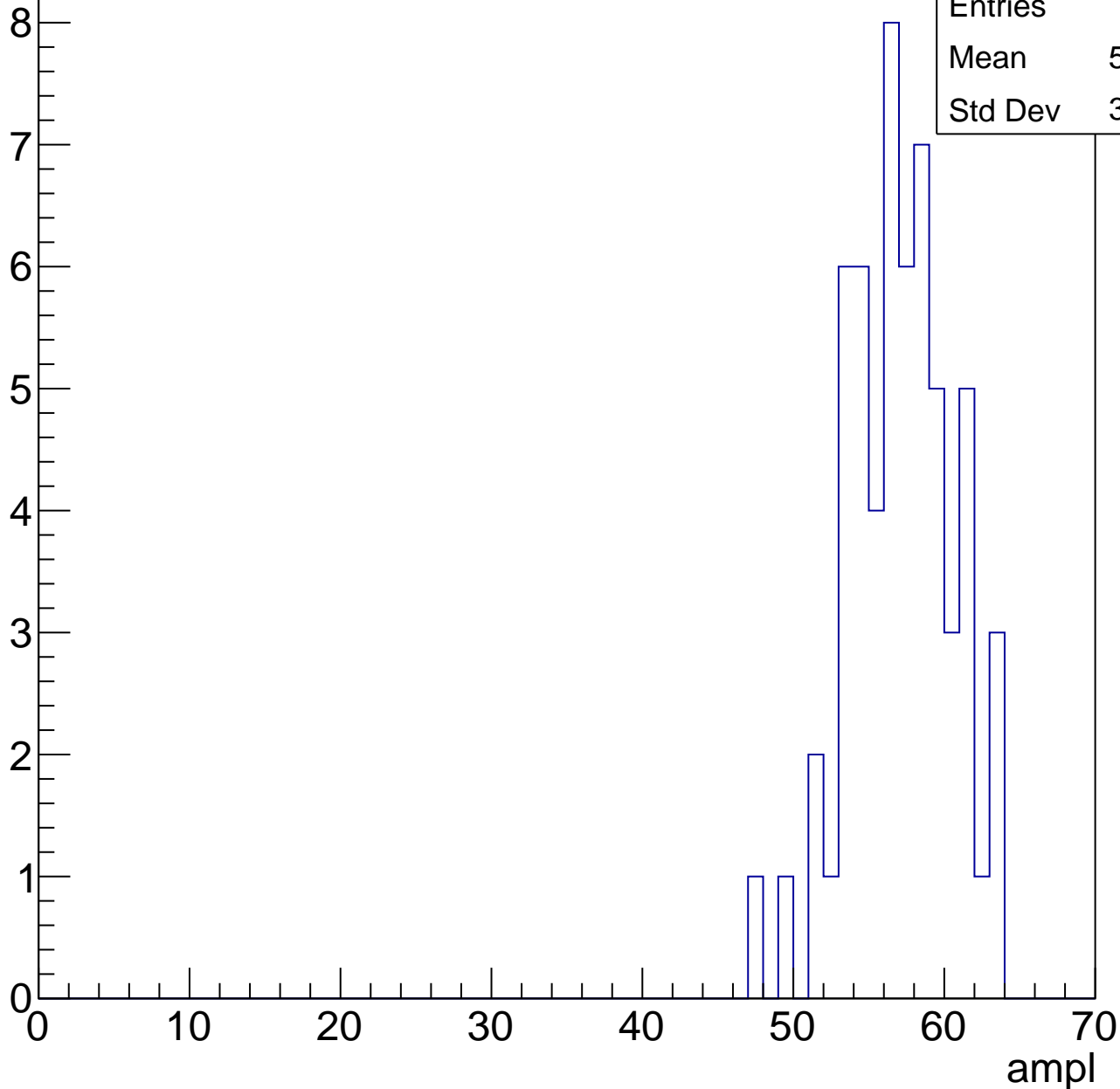


B1L100S, U12-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

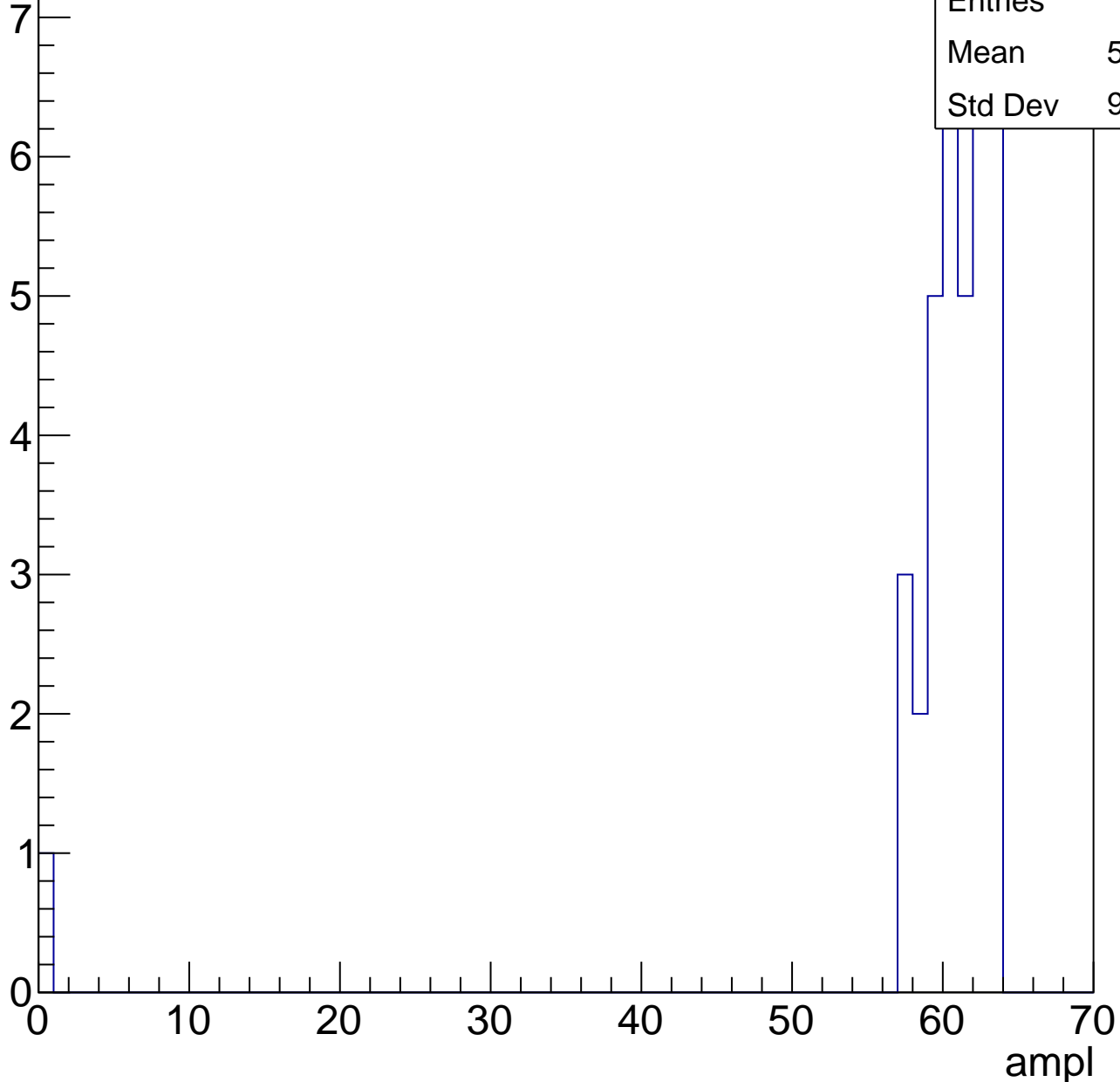
Entries	59
Mean	56.59
Std Dev	3.425



B1L100S, U12-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	37
Mean	58.97
Std Dev	9.996

B1L100S, U12-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch61, adc0

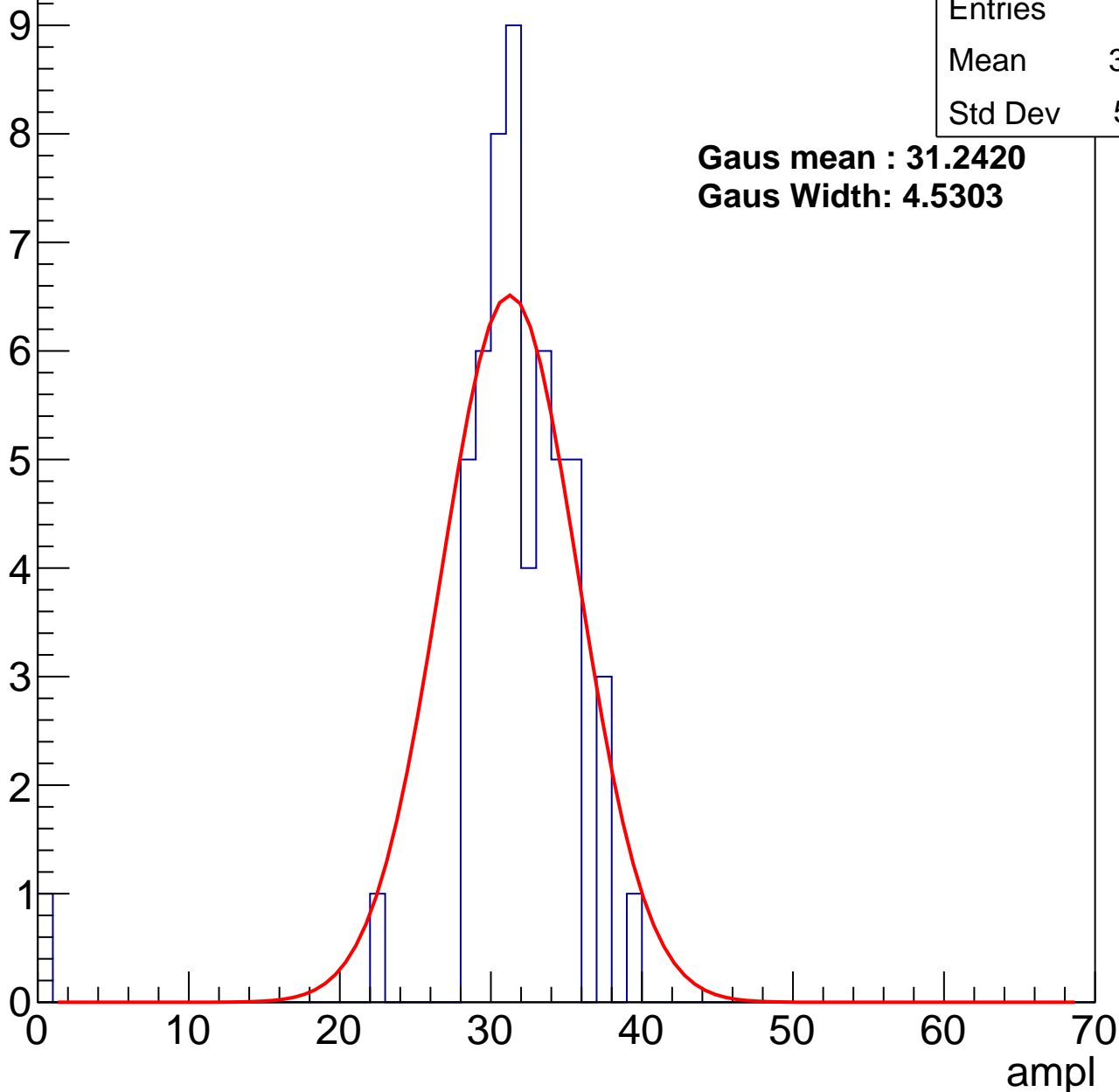
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	31.04
Std Dev	5.171

Gaus mean : 31.2420

Gaus Width: 4.5303



B1L100S, U12-ch61, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	37.88
Std Dev	3.582

Gaus mean : 38.2085

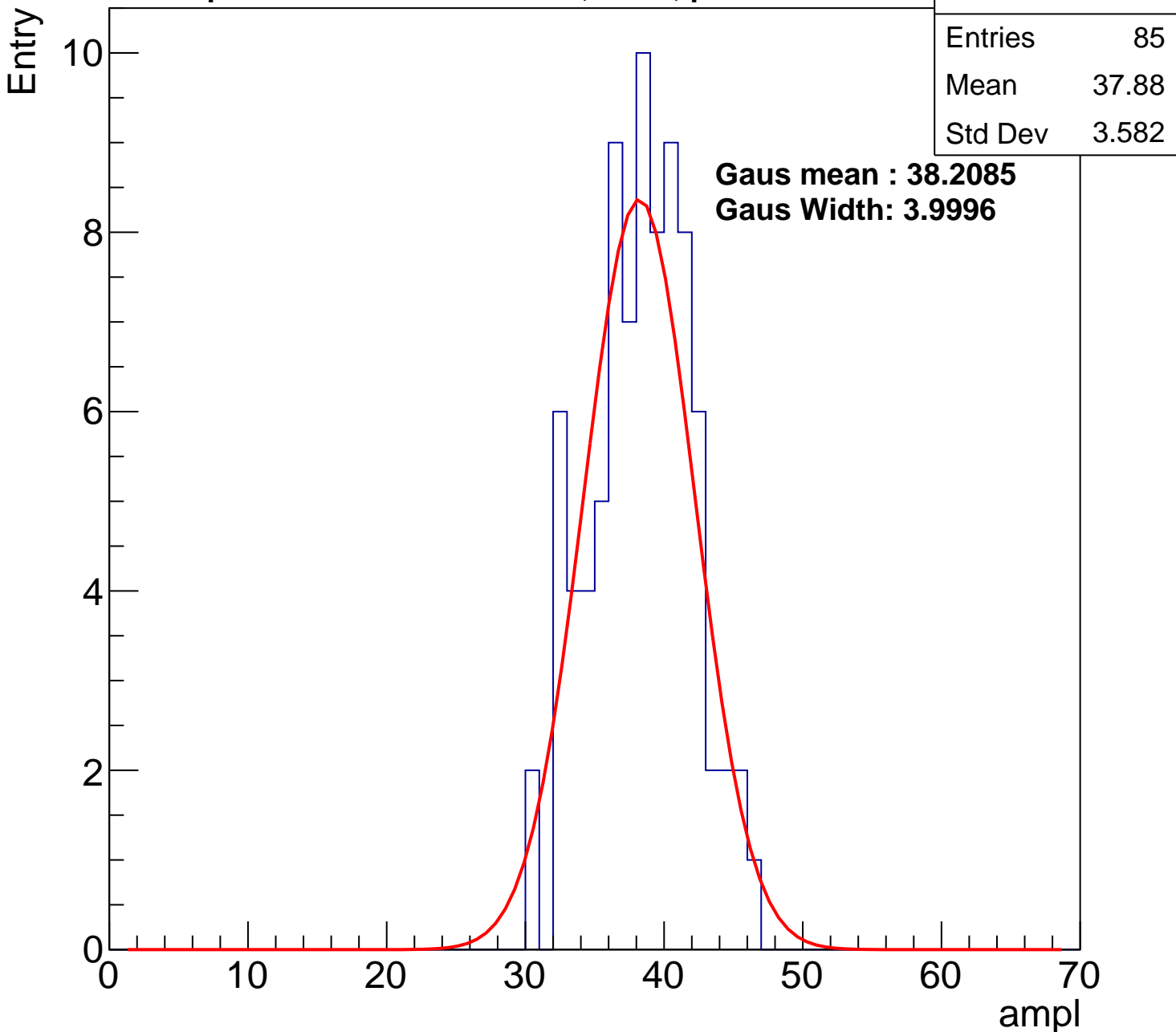
Gaus Width: 3.9996

Entry

10
8
6
4
2
0

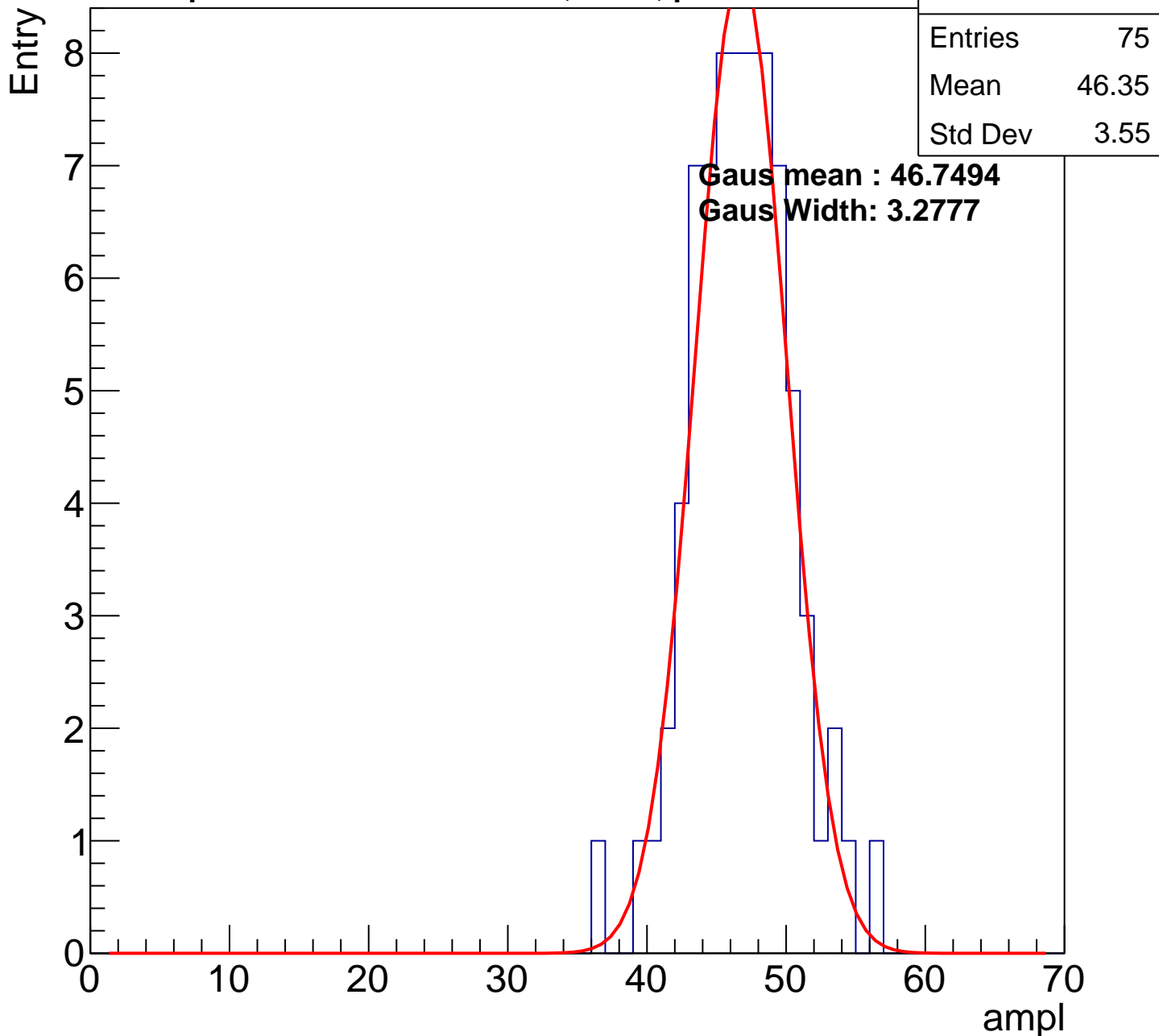
ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch61, adc2

calib_packv5_042523_0143.root, FC#4, port A2

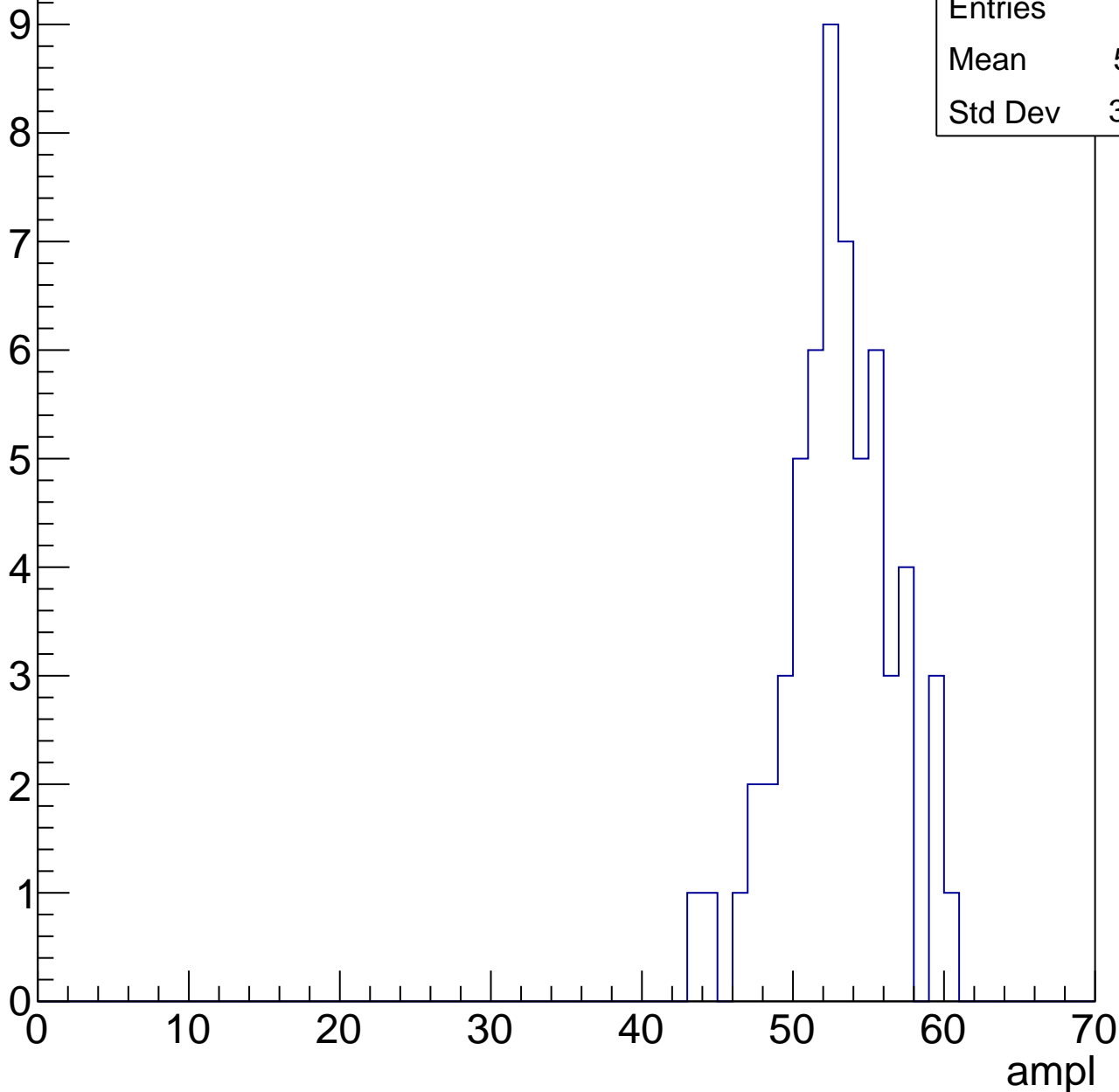


B1L100S, U12-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	52.51
Std Dev	3.543

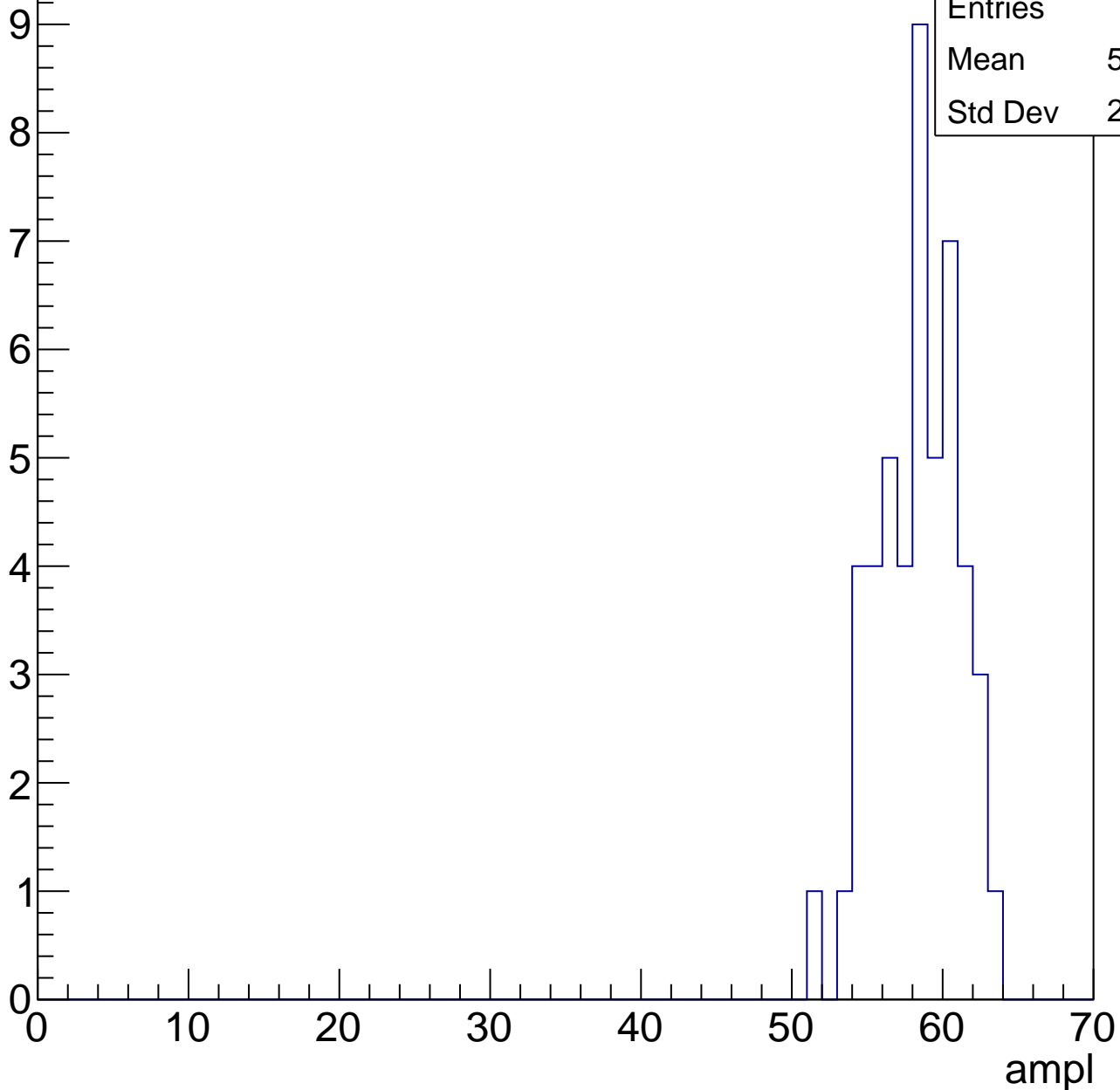


B1L100S, U12-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	57.88
Std Dev	2.659

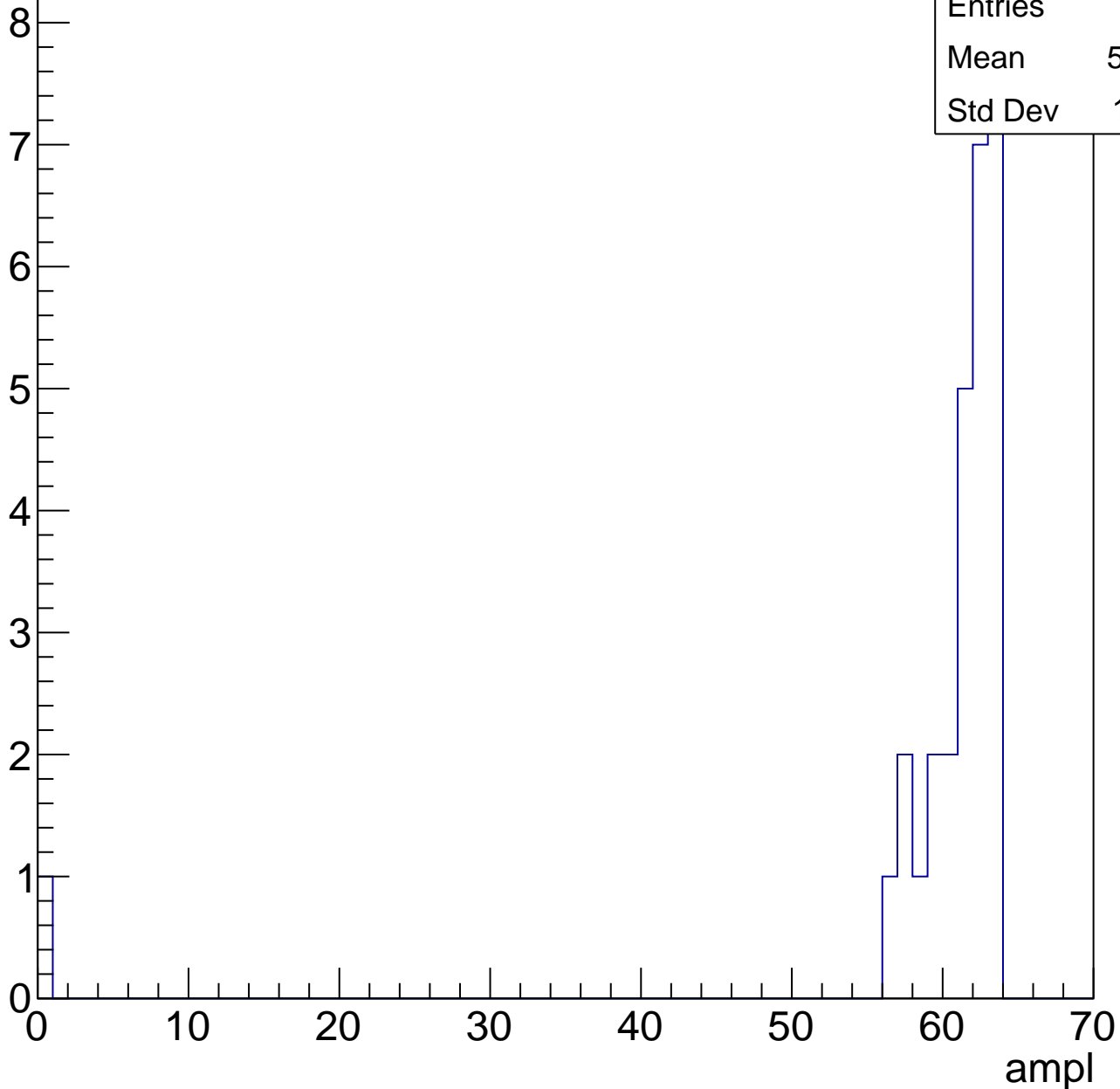


B1L100S, U12-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.93
Std Dev	11.31



B1L100S, U12-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch62, adc0

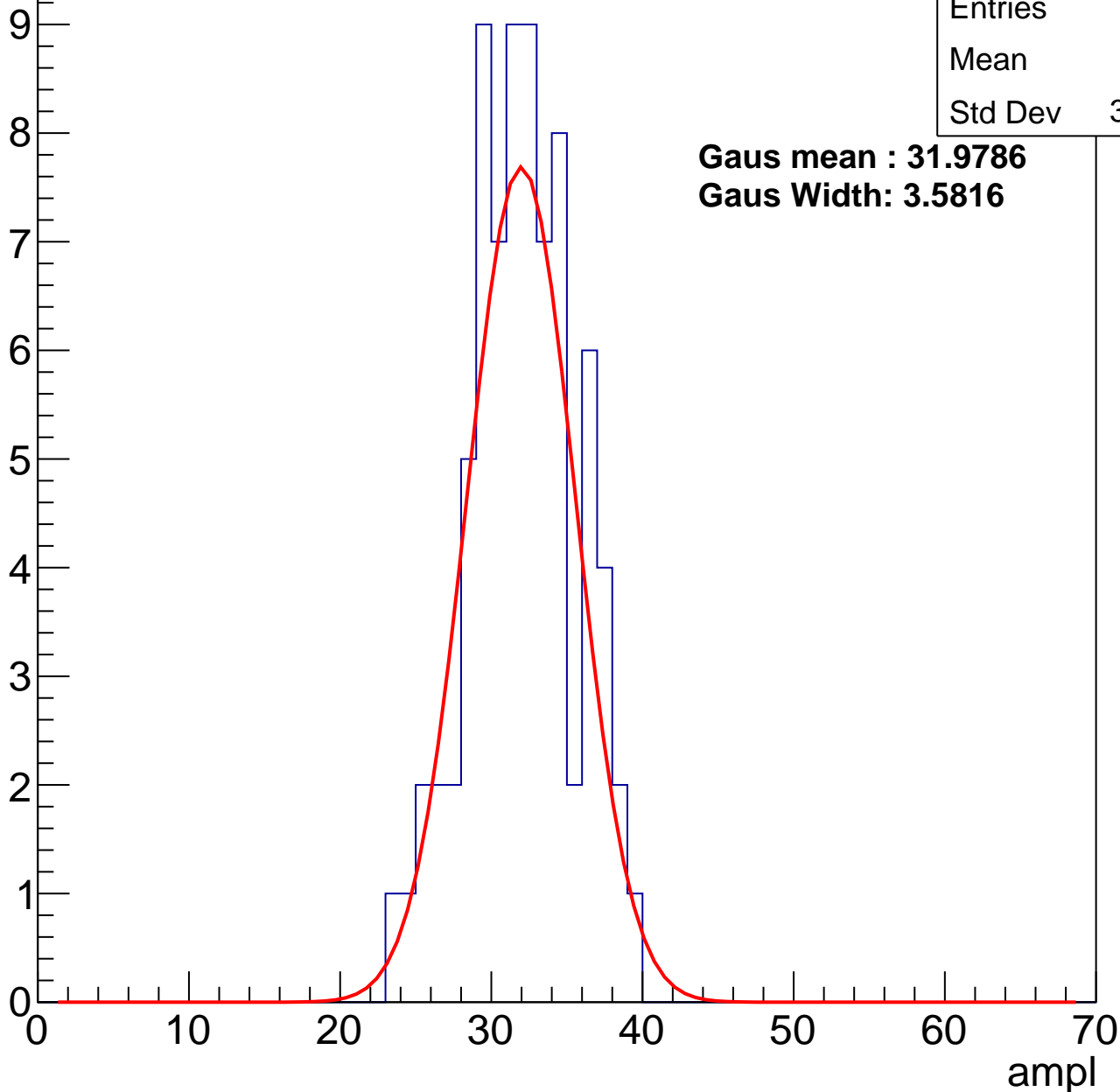
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	31.6
Std Dev	3.465

Gaus mean : 31.9786

Gaus Width: 3.5816



B1L100S, U12-ch62, adc1

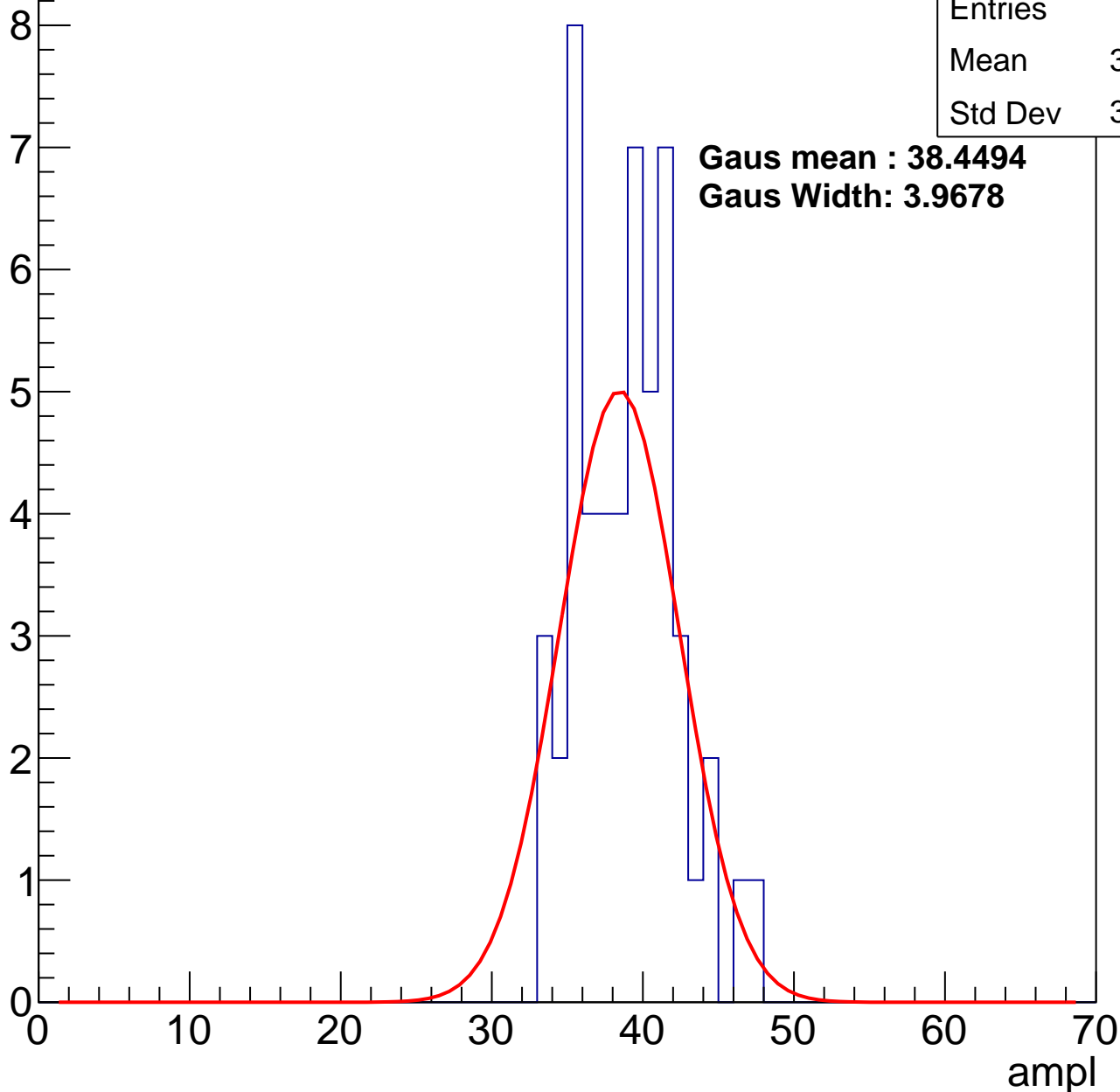
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	38.48
Std Dev	3.302

Gaus mean : 38.4494

Gaus Width: 3.9678



B1L100S, U12-ch62, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	44.29
Std Dev	3.566

Gaus mean : 44.7600

Gaus Width: 3.6463

10

8

6

4

2

0

0

10

20

30

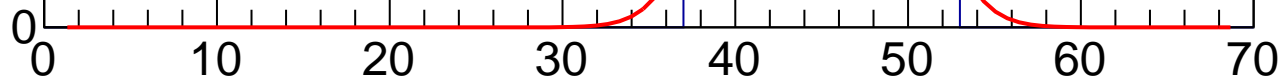
40

50

60

70

ampl

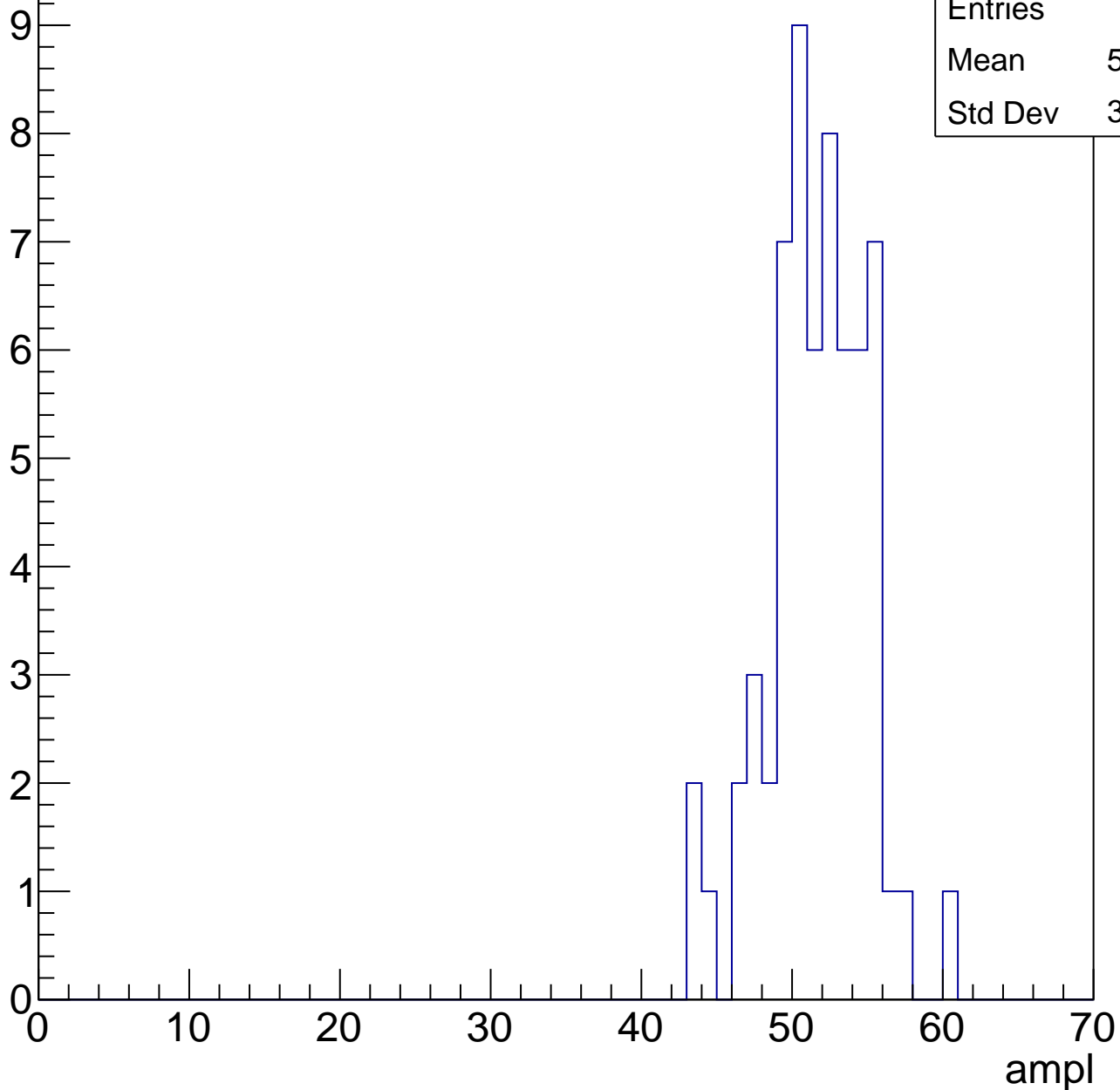


B1L100S, U12-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2

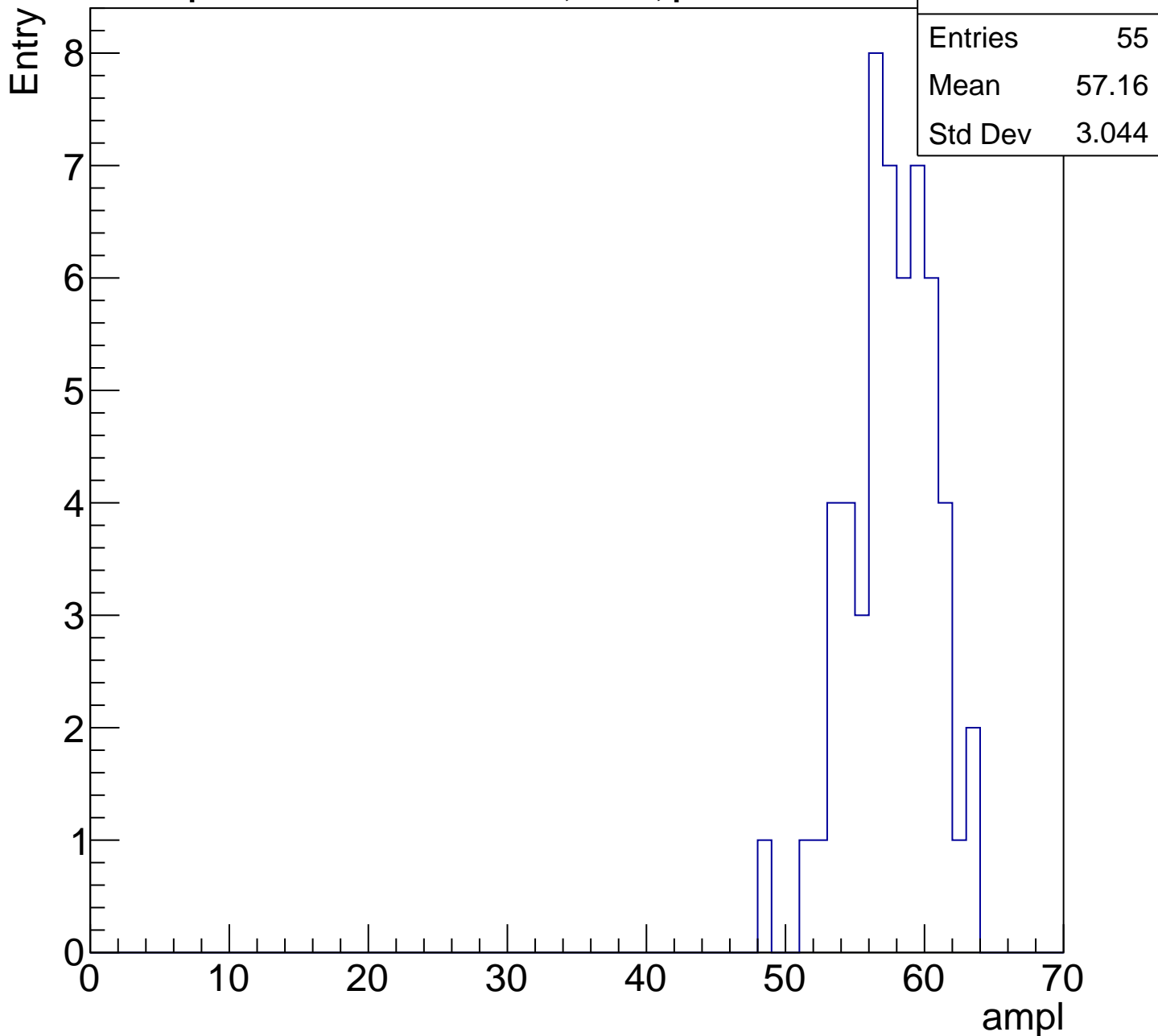
Entry

Entries	62
Mean	51.19
Std Dev	3.282



B1L100S, U12-ch62, adc4

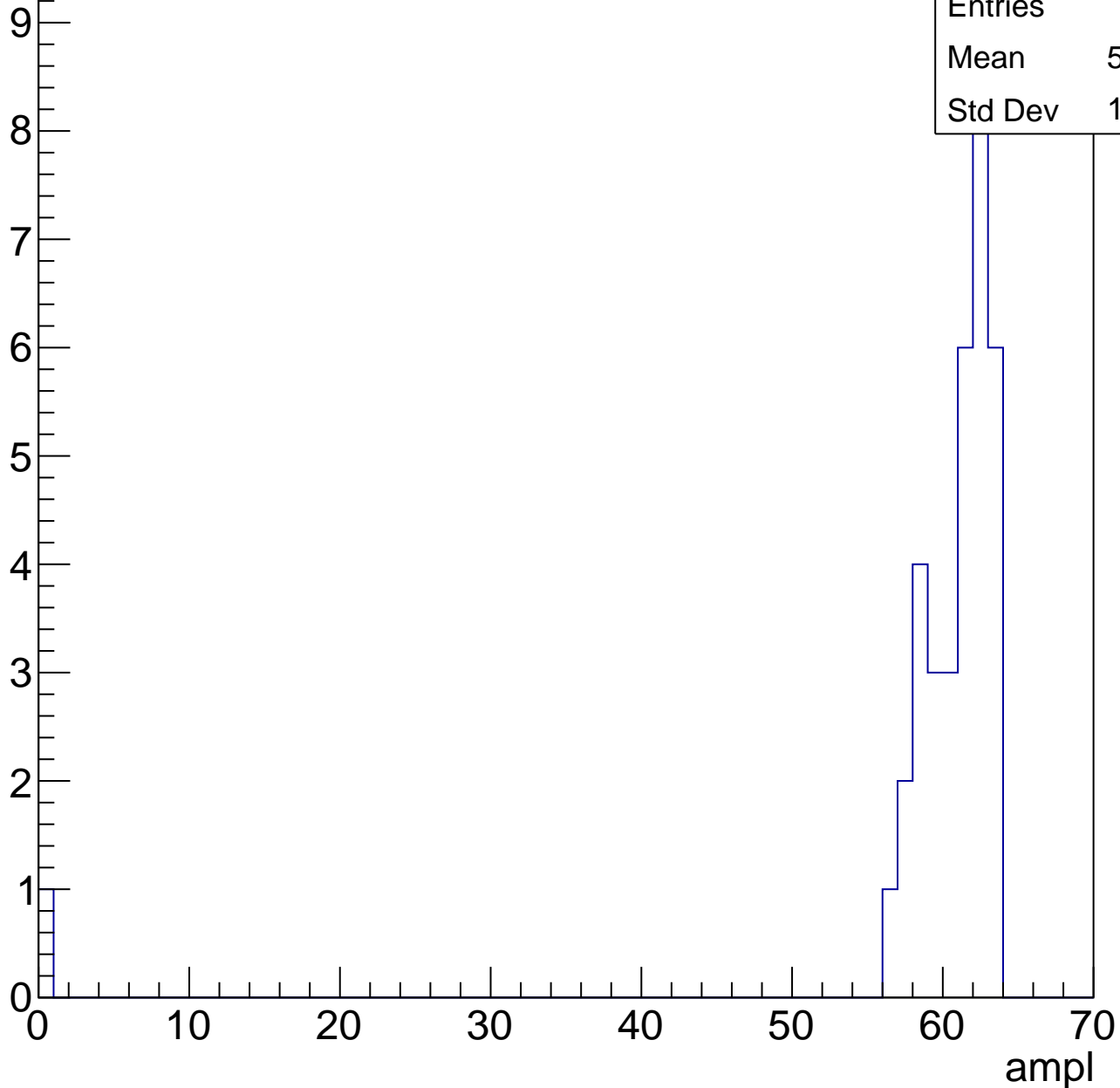
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch62, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	35
Mean	58.89
Std Dev	10.29

B1L100S, U12-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U12-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch63, adc0

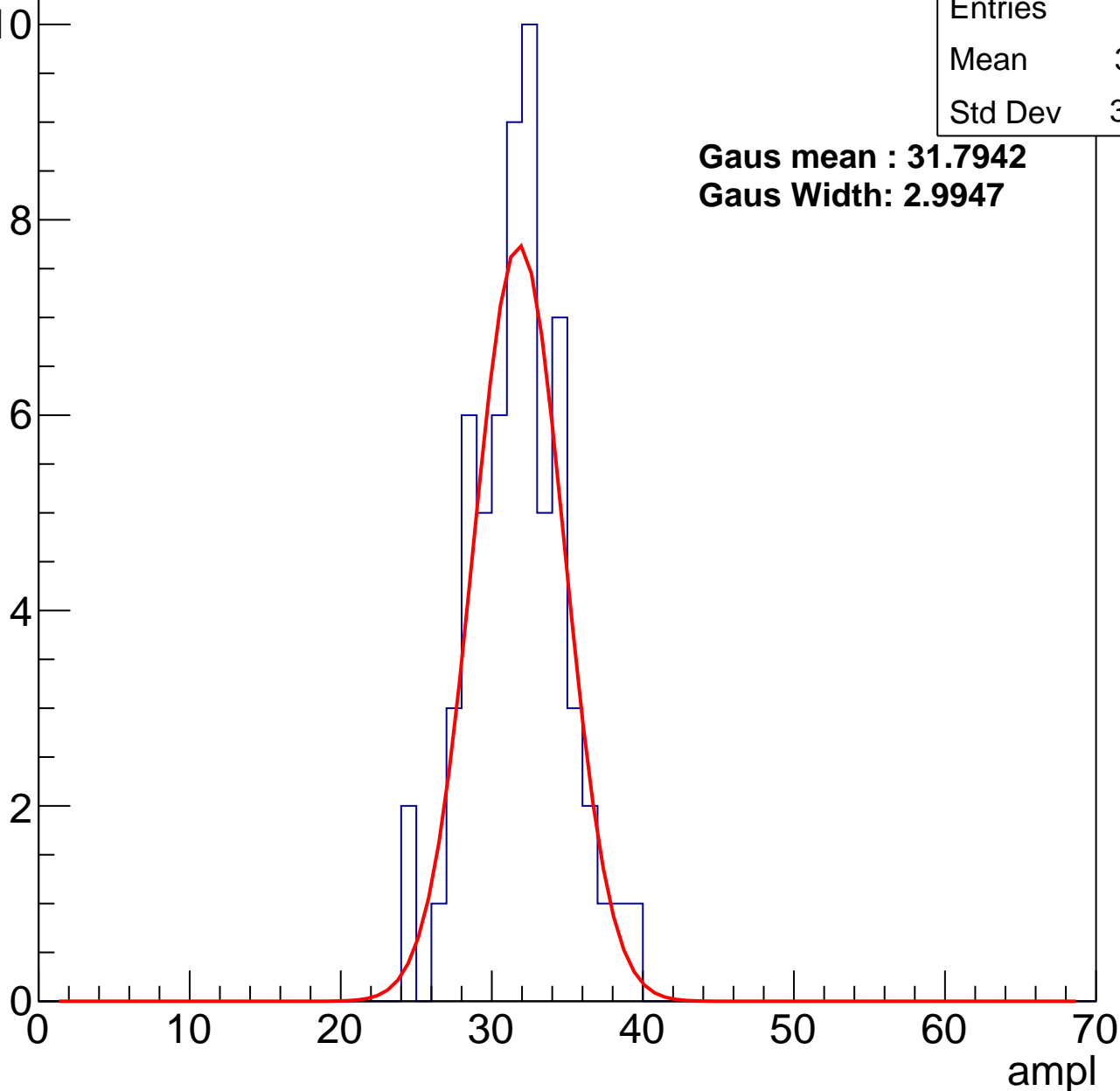
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	31.31
Std Dev	3.072

Gaus mean : 31.7942

Gaus Width: 2.9947



B1L100S, U12-ch63, adc1

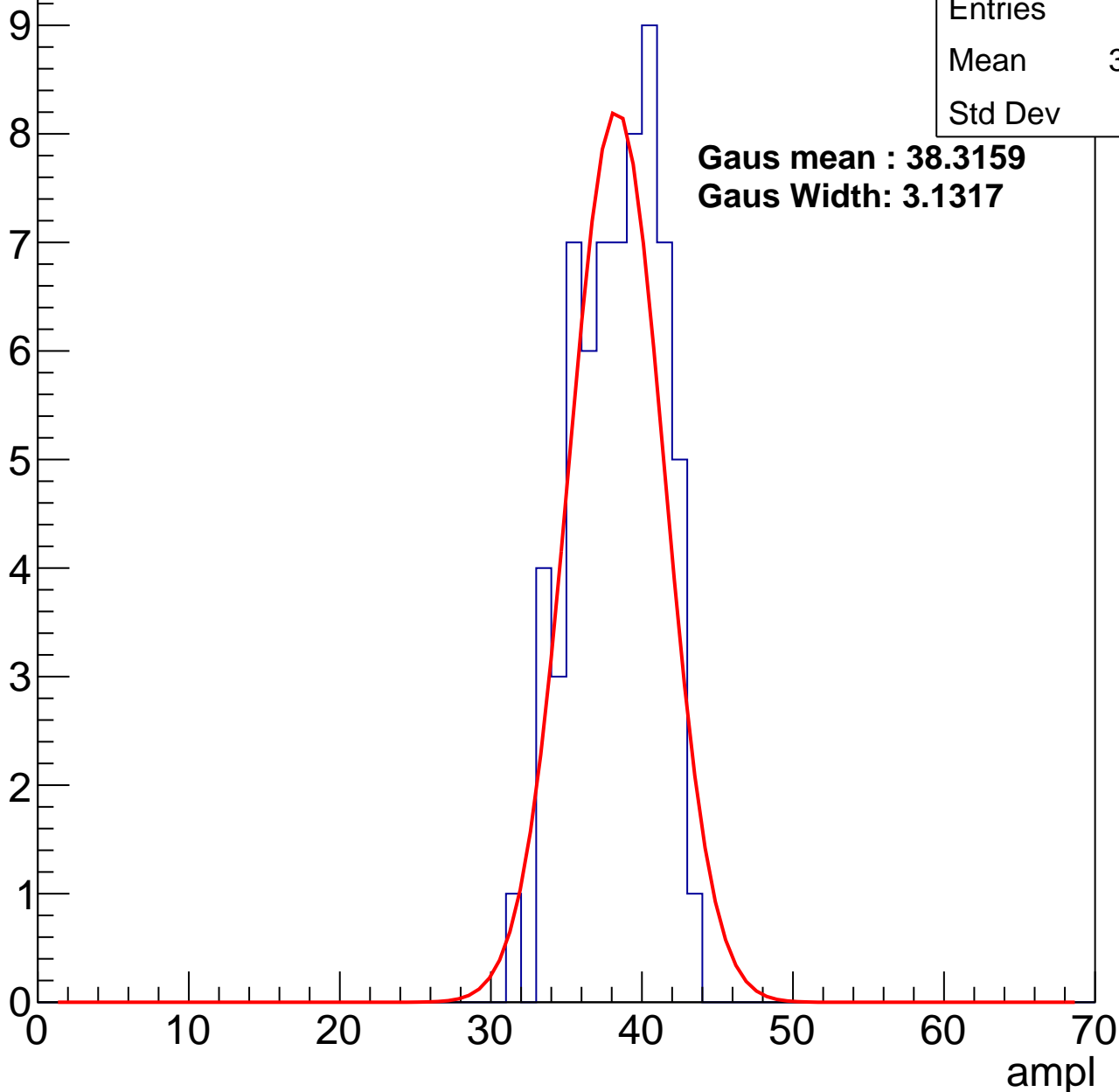
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.89
Std Dev	2.78

Gaus mean : 38.3159

Gaus Width: 3.1317



B1L100S, U12-ch63, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	45.03
Std Dev	3.853

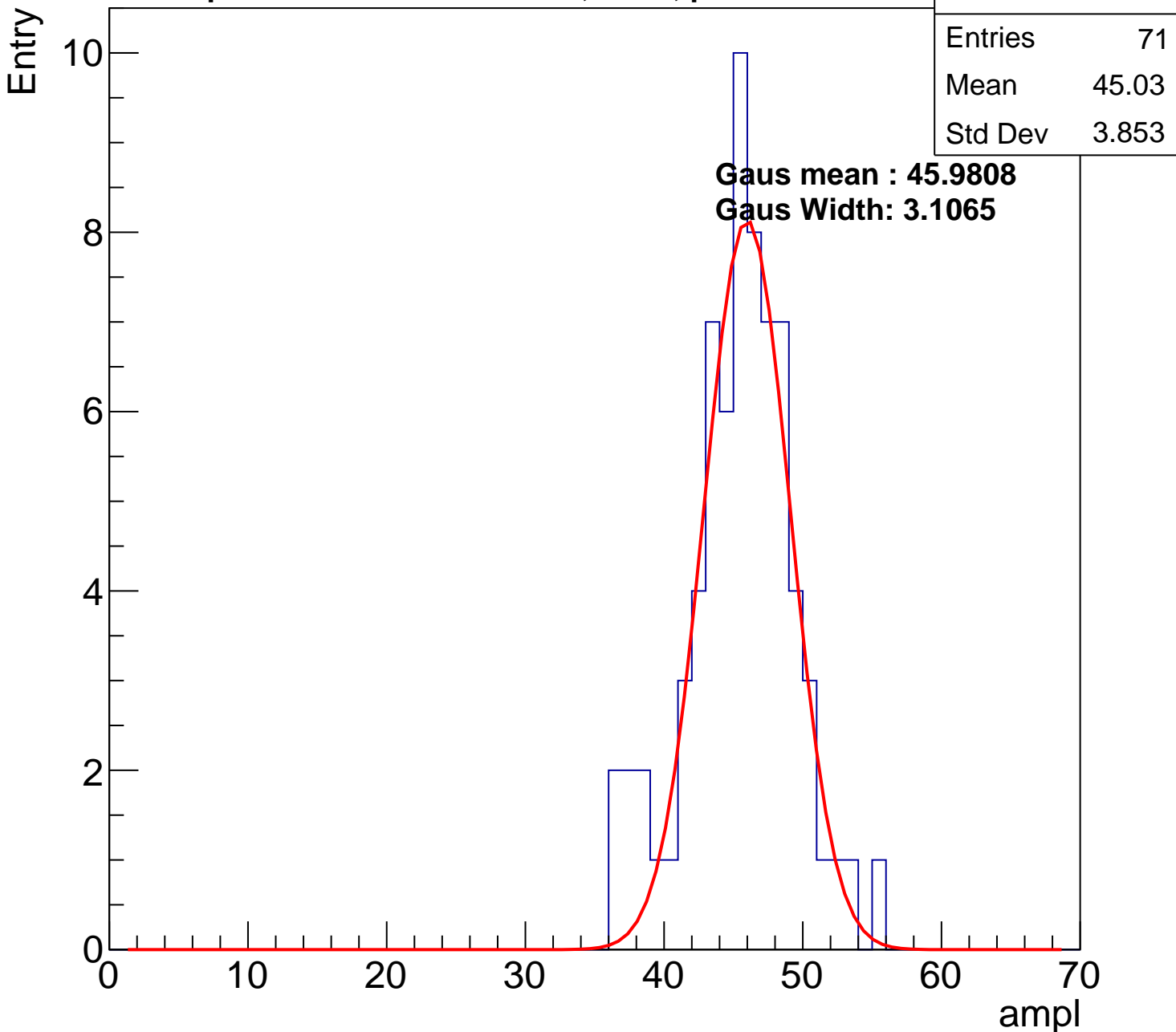
Gaus mean : 45.9808

Gaus Width: 3.1065

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

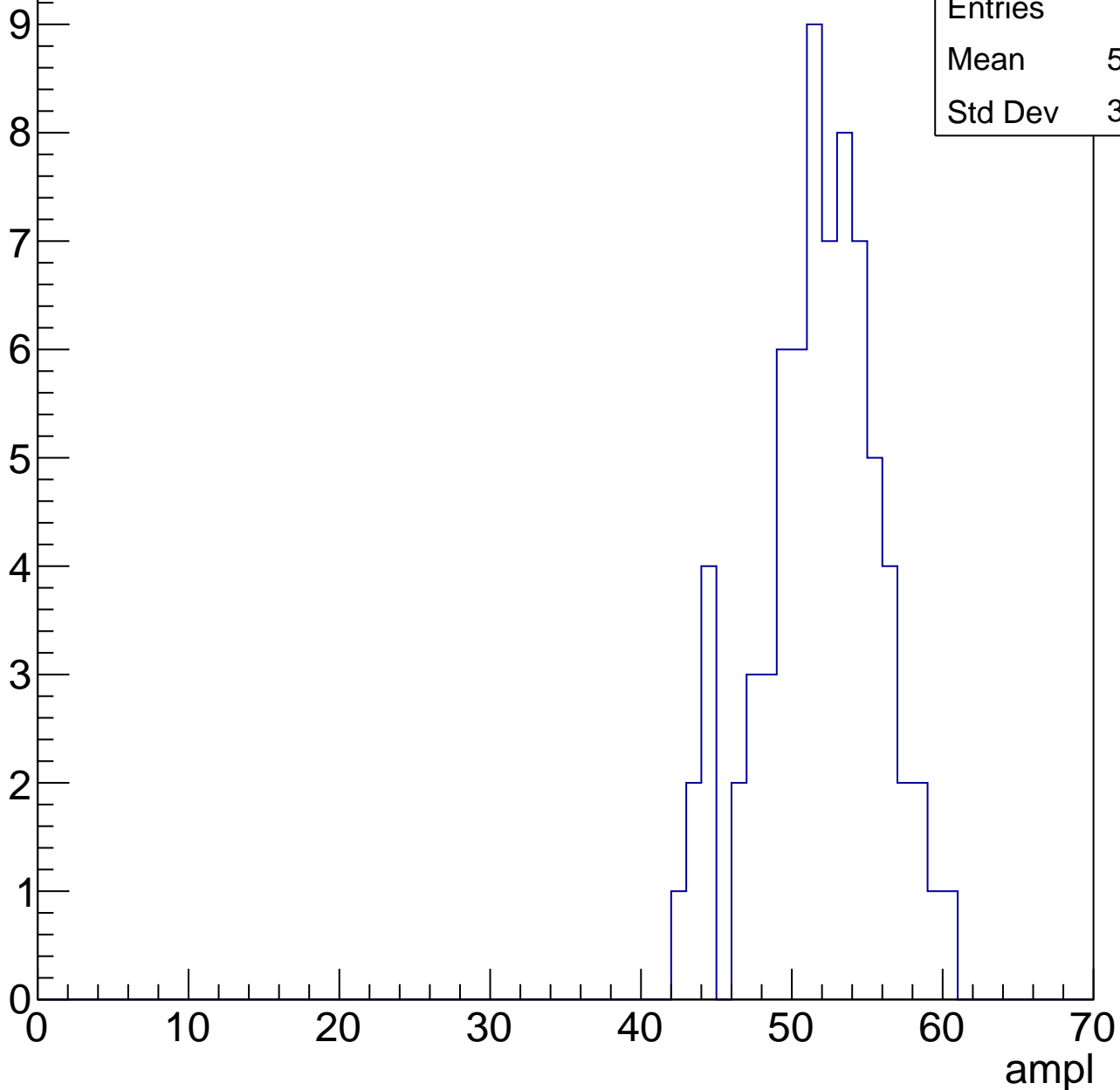


B1L100S, U12-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

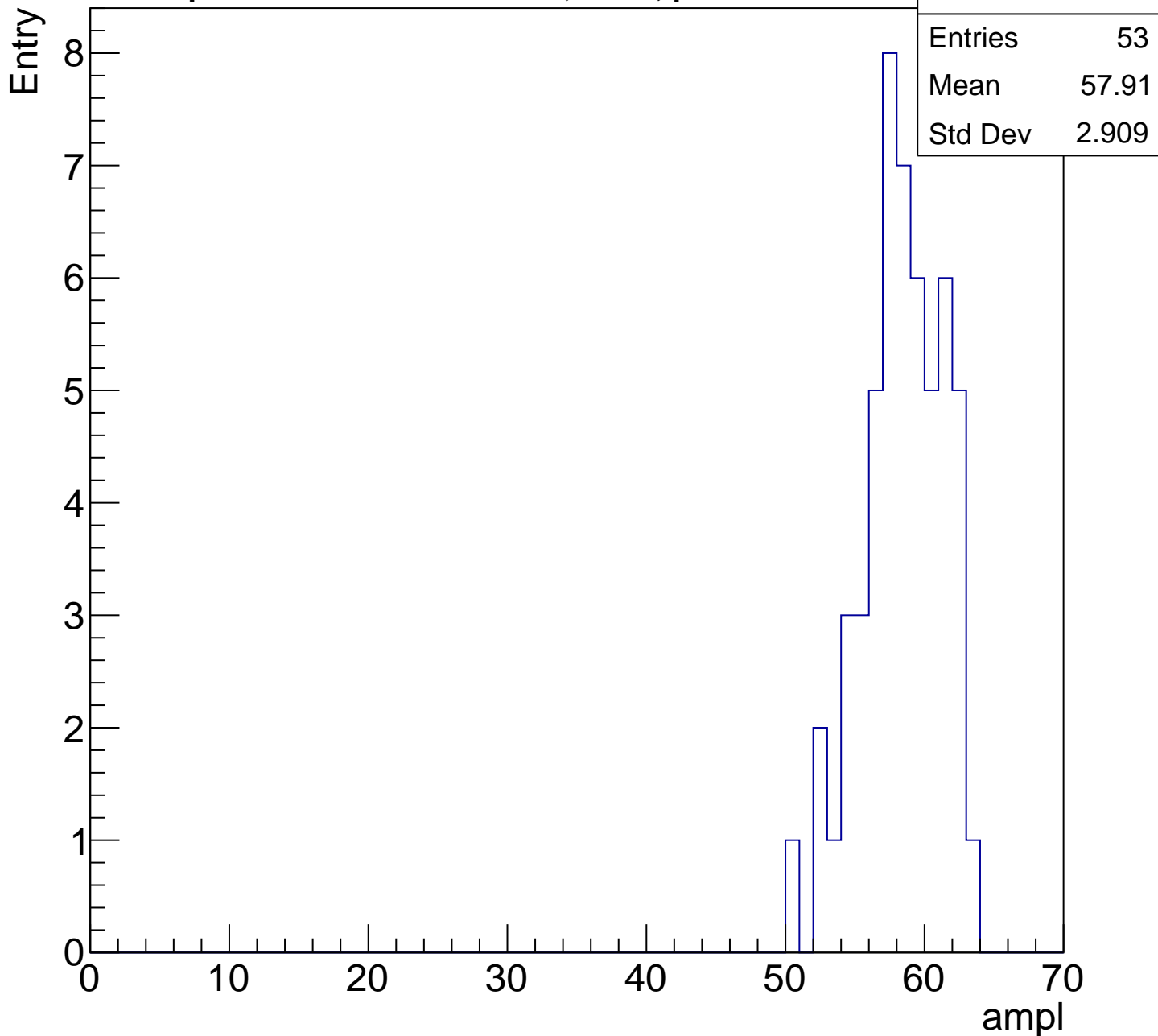
Entry

Entries	73
Mean	51.34
Std Dev	3.966



B1L100S, U12-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

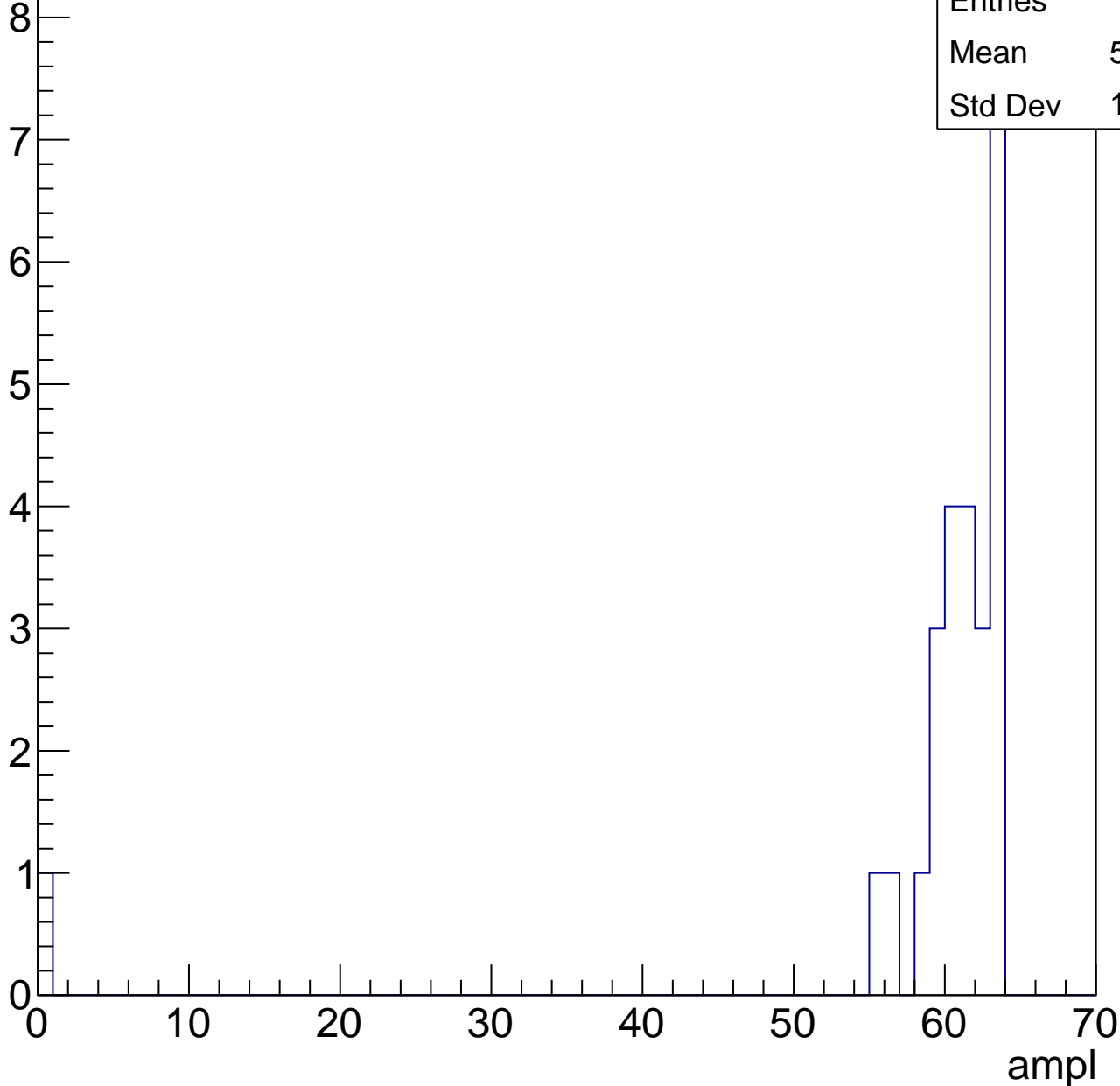


B1L100S, U12-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.46
Std Dev	11.89



B1L100S, U12-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L100S, U12-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch64, adc0

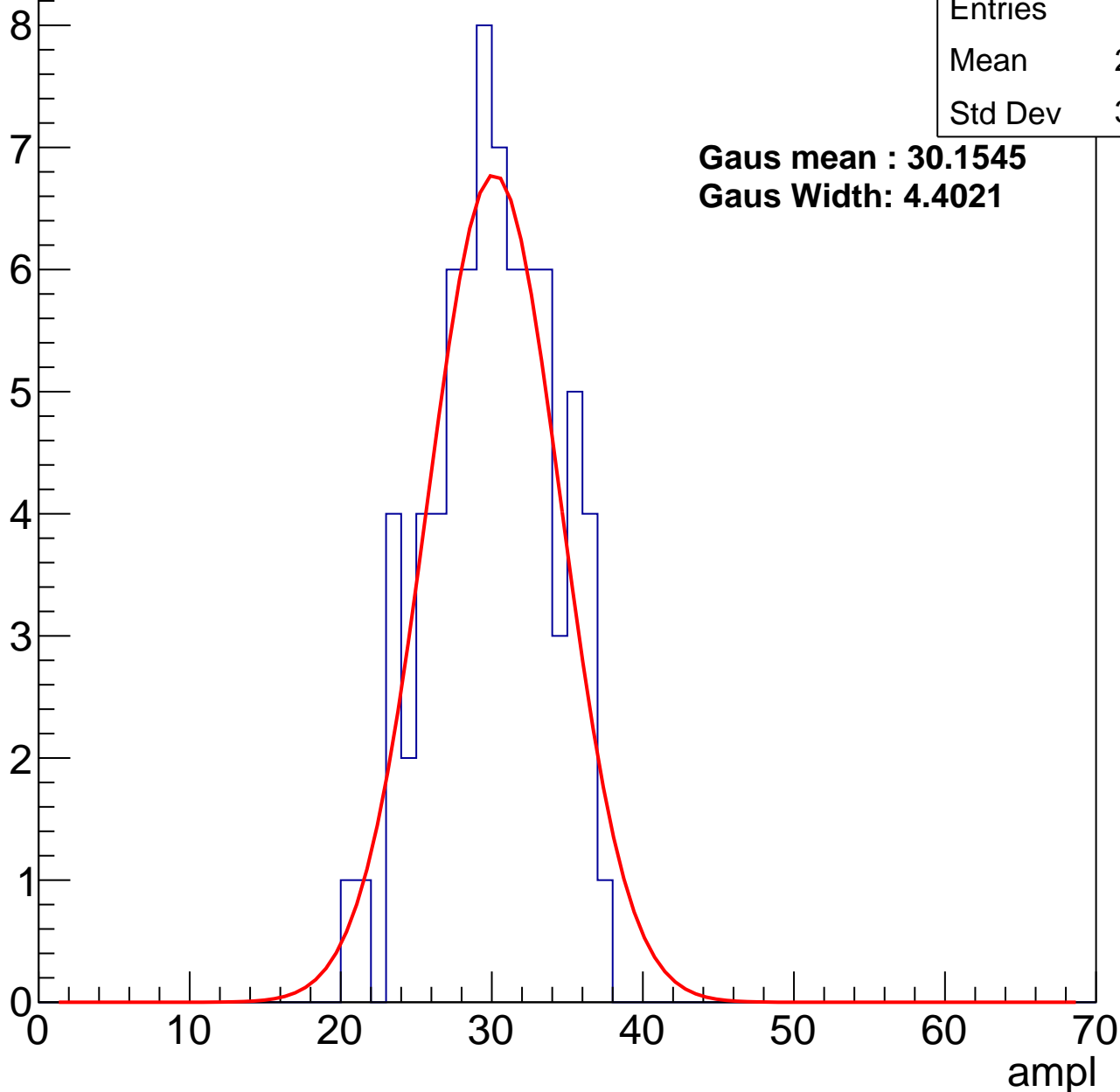
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.61
Std Dev	3.921

Gaus mean : 30.1545

Gaus Width: 4.4021



B1L100S, U12-ch64, adc1

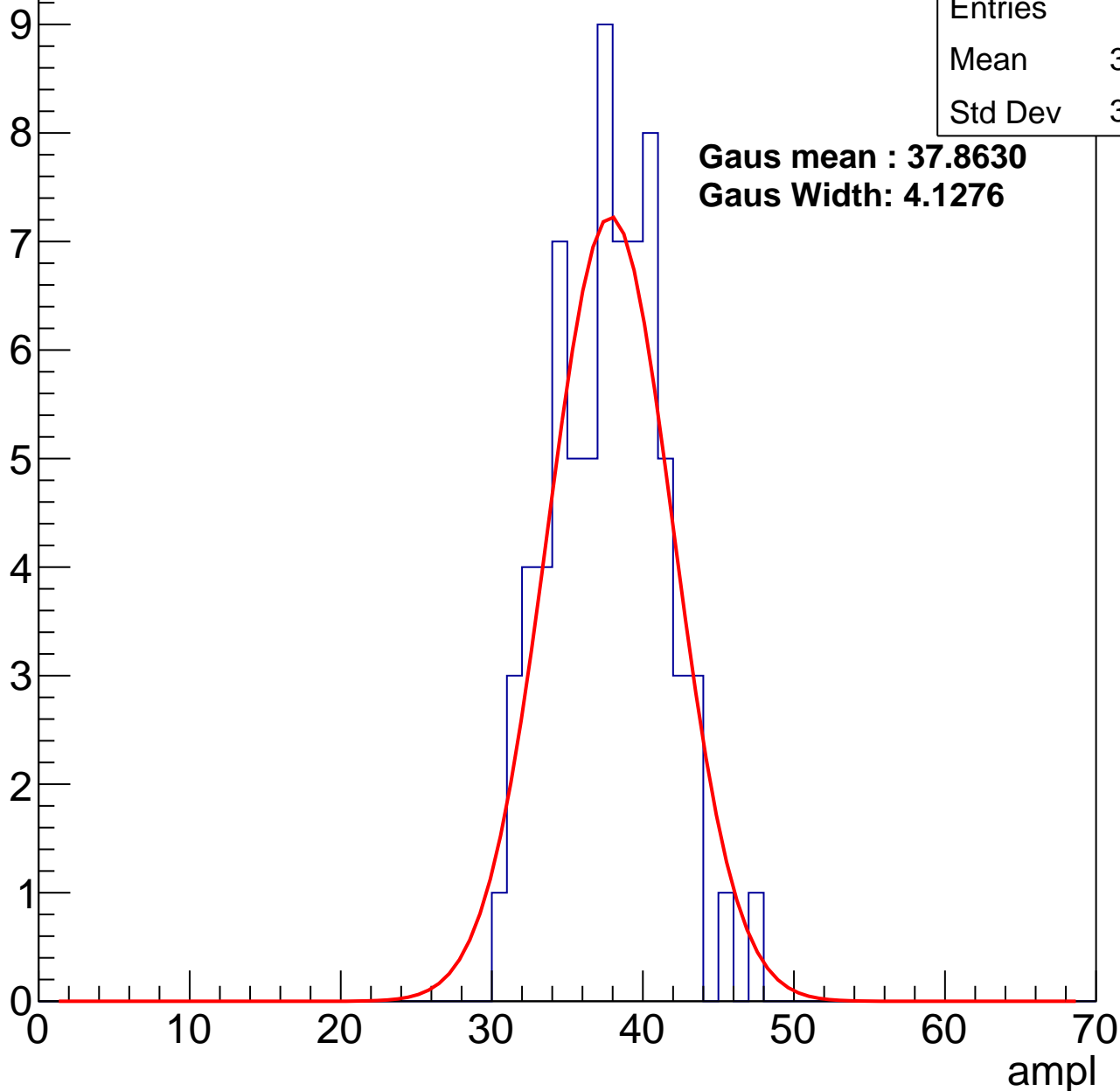
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.26
Std Dev	3.585

Gaus mean : 37.8630

Gaus Width: 4.1276



B1L100S, U12-ch64, adc2

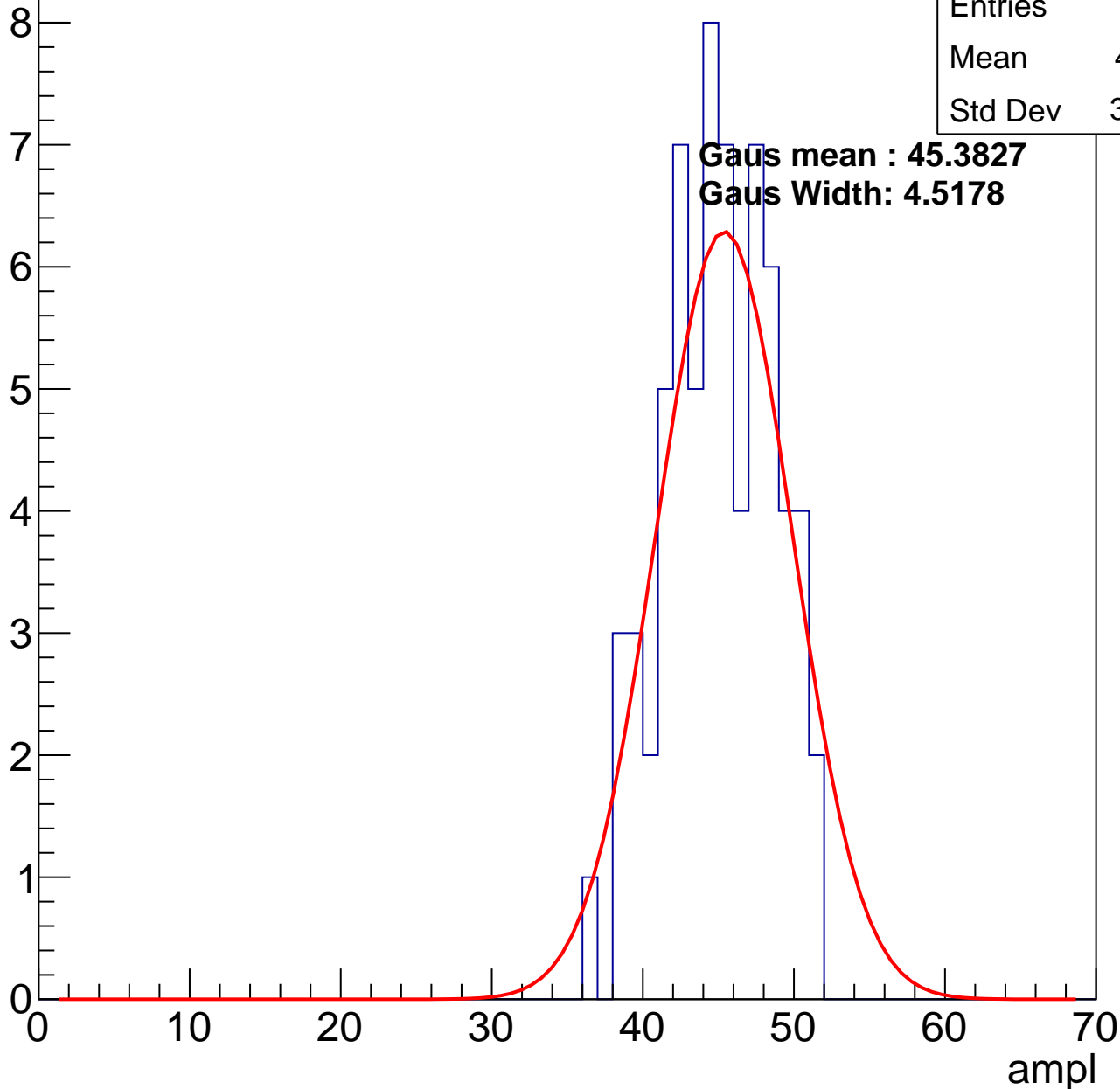
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.51
Std Dev	3.562

Gaus mean : 45.3827

Gaus Width: 4.5178

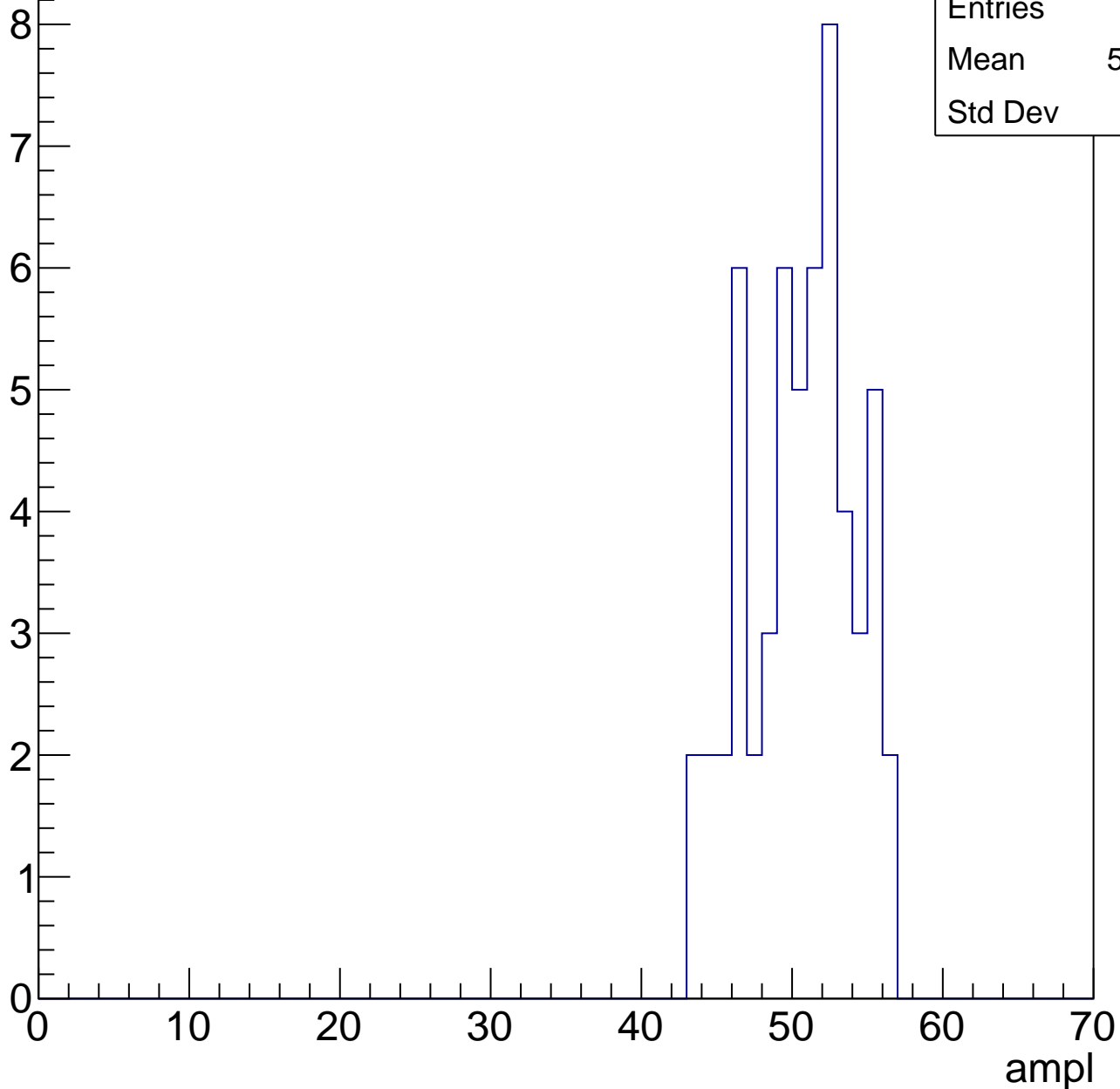


B1L100S, U12-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

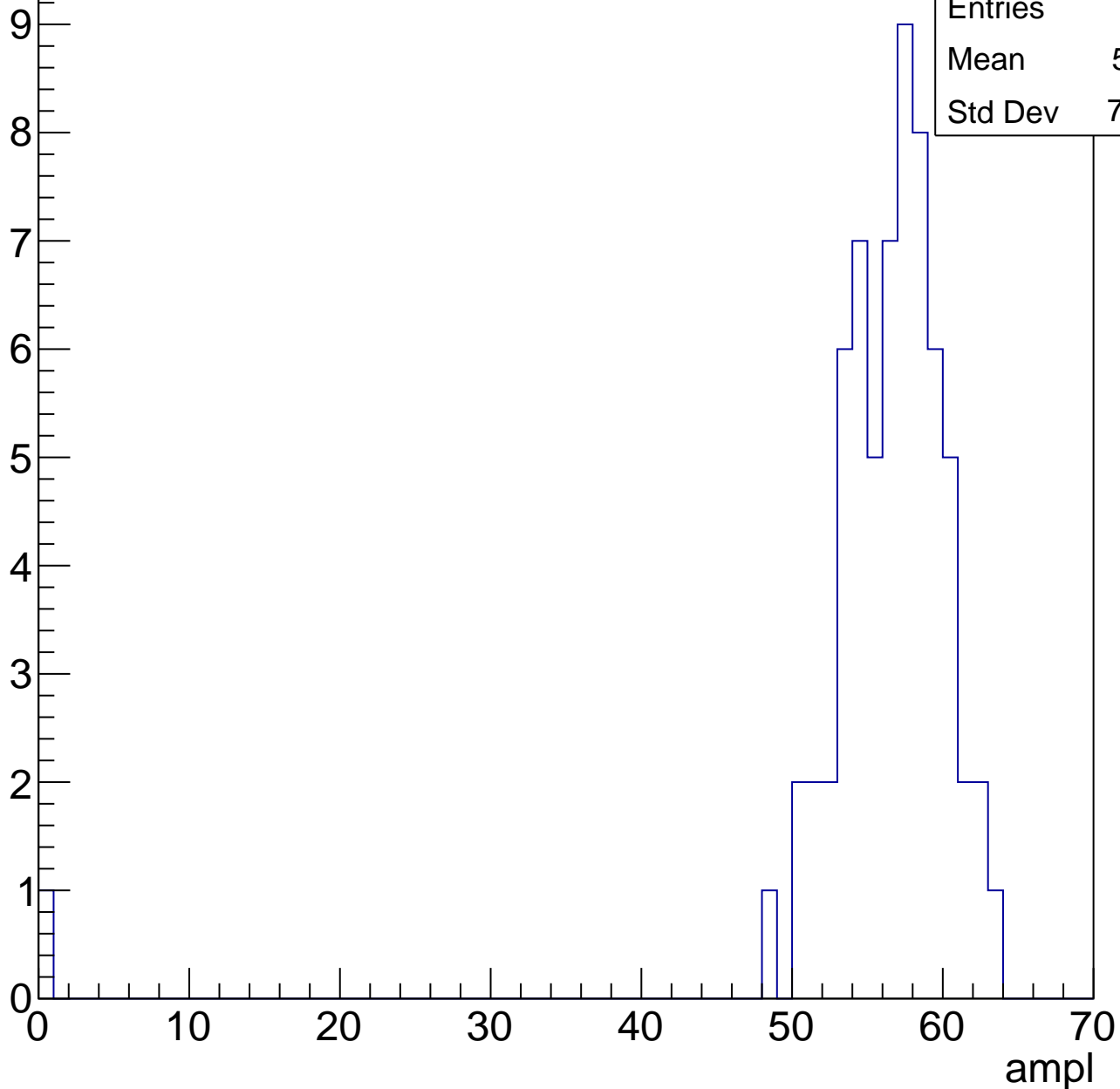
Entries	56
Mean	50.09
Std Dev	3.46



B1L100S, U12-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	66
Mean	55.41
Std Dev	7.552

B1L100S, U12-ch64, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	60.89
Std Dev	1.798

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

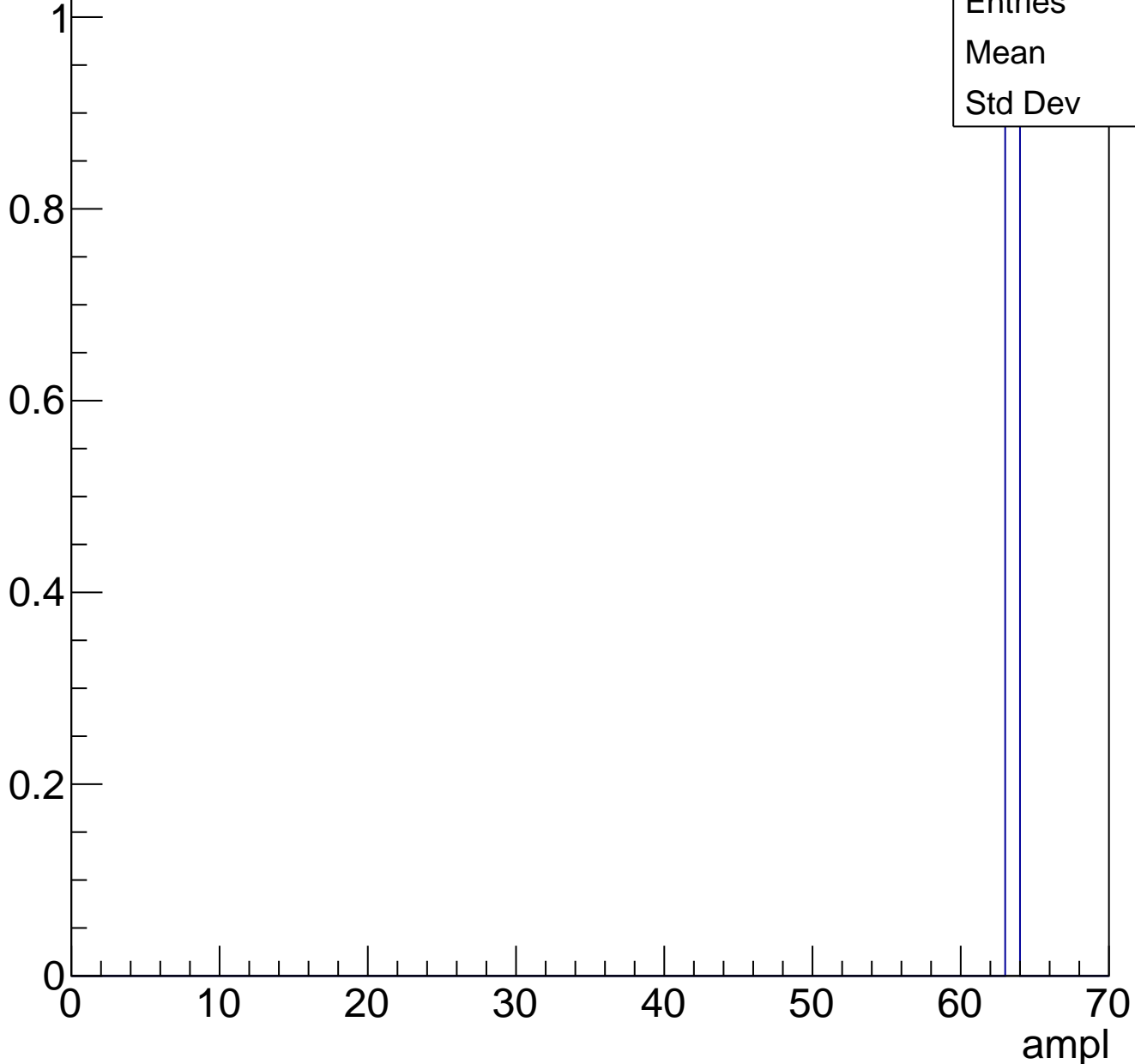
7

8

B1L100S, U12-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch65, adc0

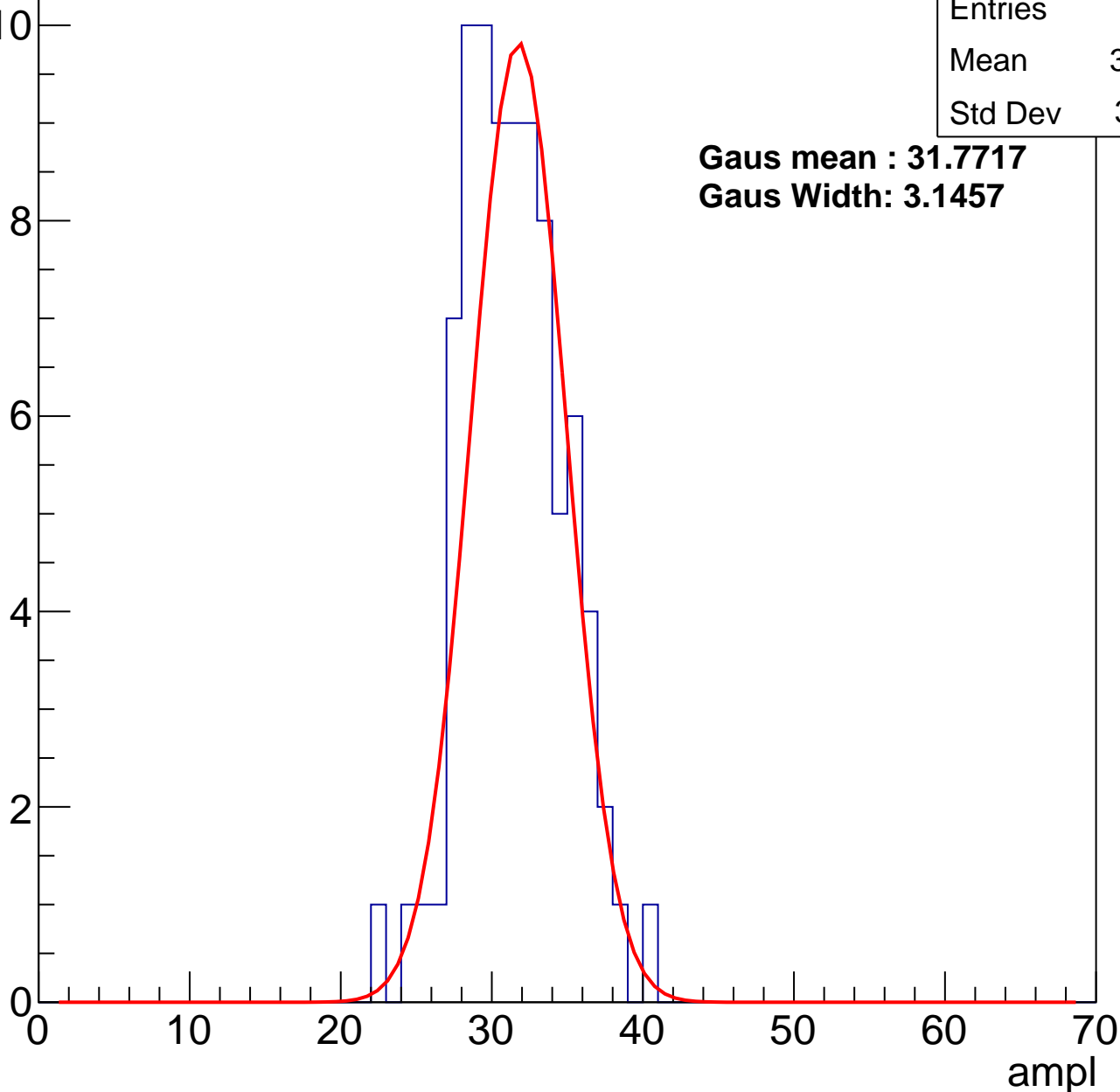
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	30.98
Std Dev	3.301

Gaus mean : 31.7717

Gaus Width: 3.1457



B1L100S, U12-ch65, adc1

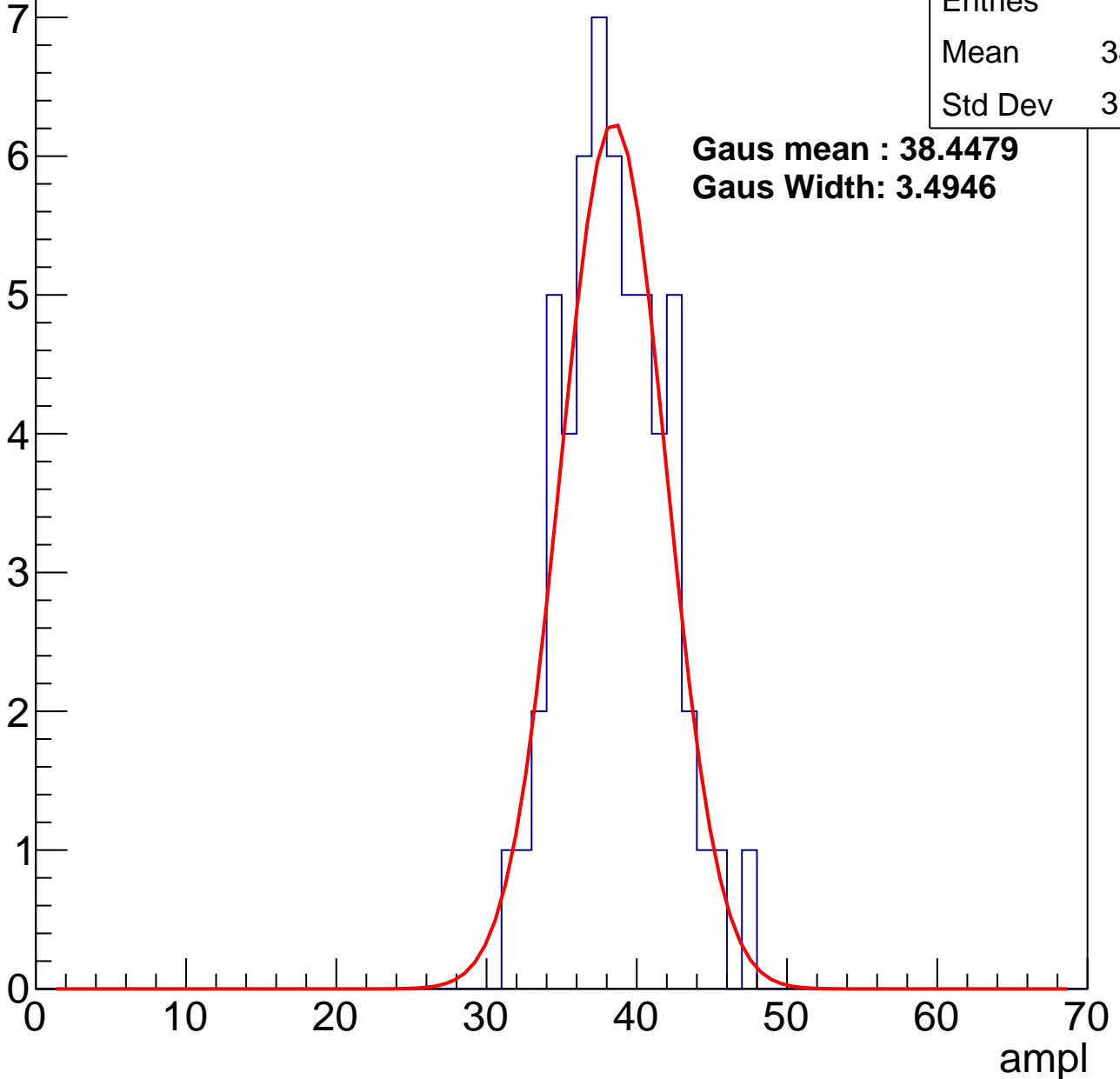
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	38.09
Std Dev	3.377

Gaus mean : 38.4479

Gaus Width: 3.4946



B1L100S, U12-ch65, adc2

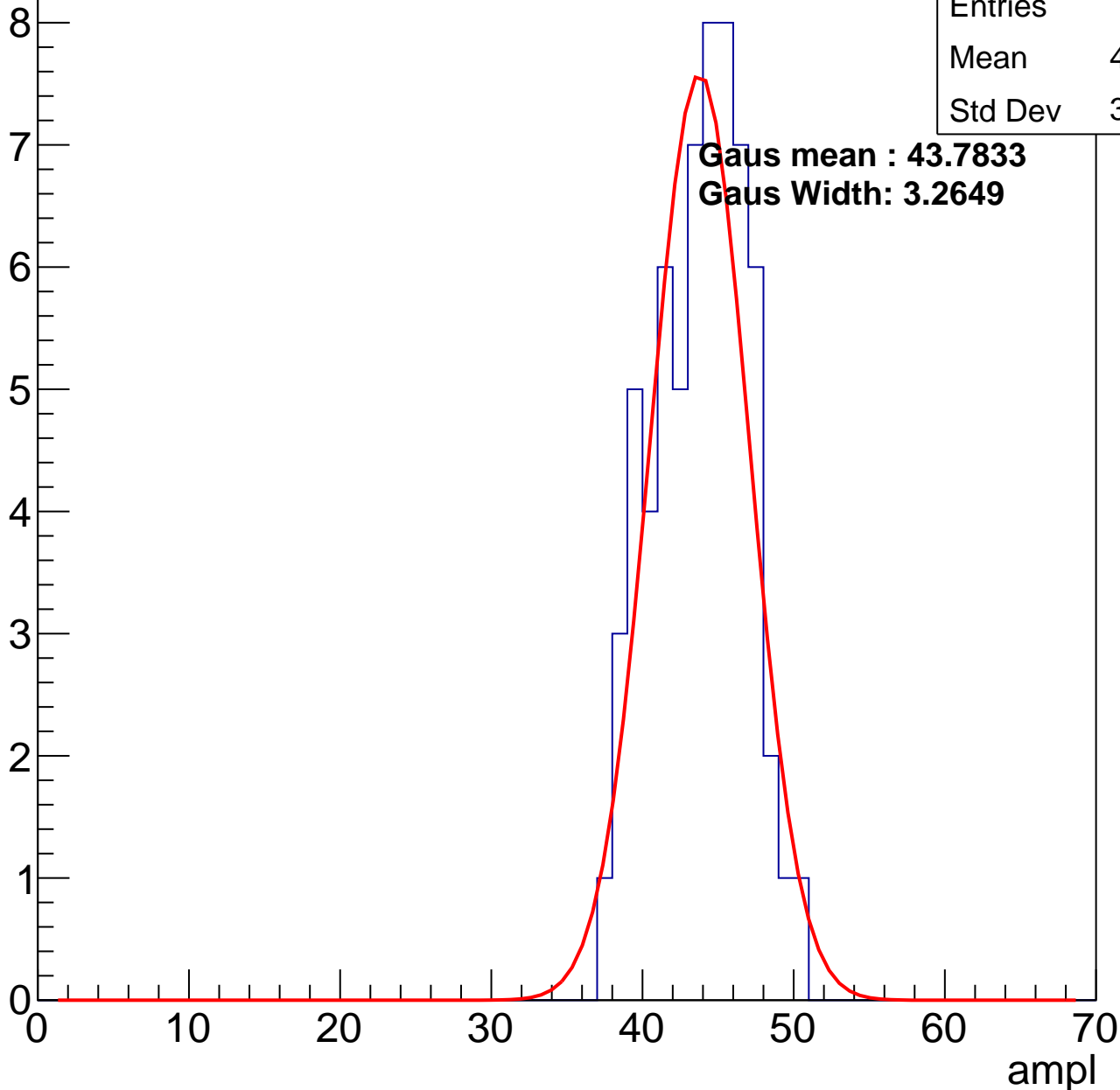
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.34
Std Dev	3.017

Gaus mean : 43.7833

Gaus Width: 3.2649



B1L100S, U12-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	51
Std Dev	3.24

Entry

10

8

6

4

2

0

0

10

20

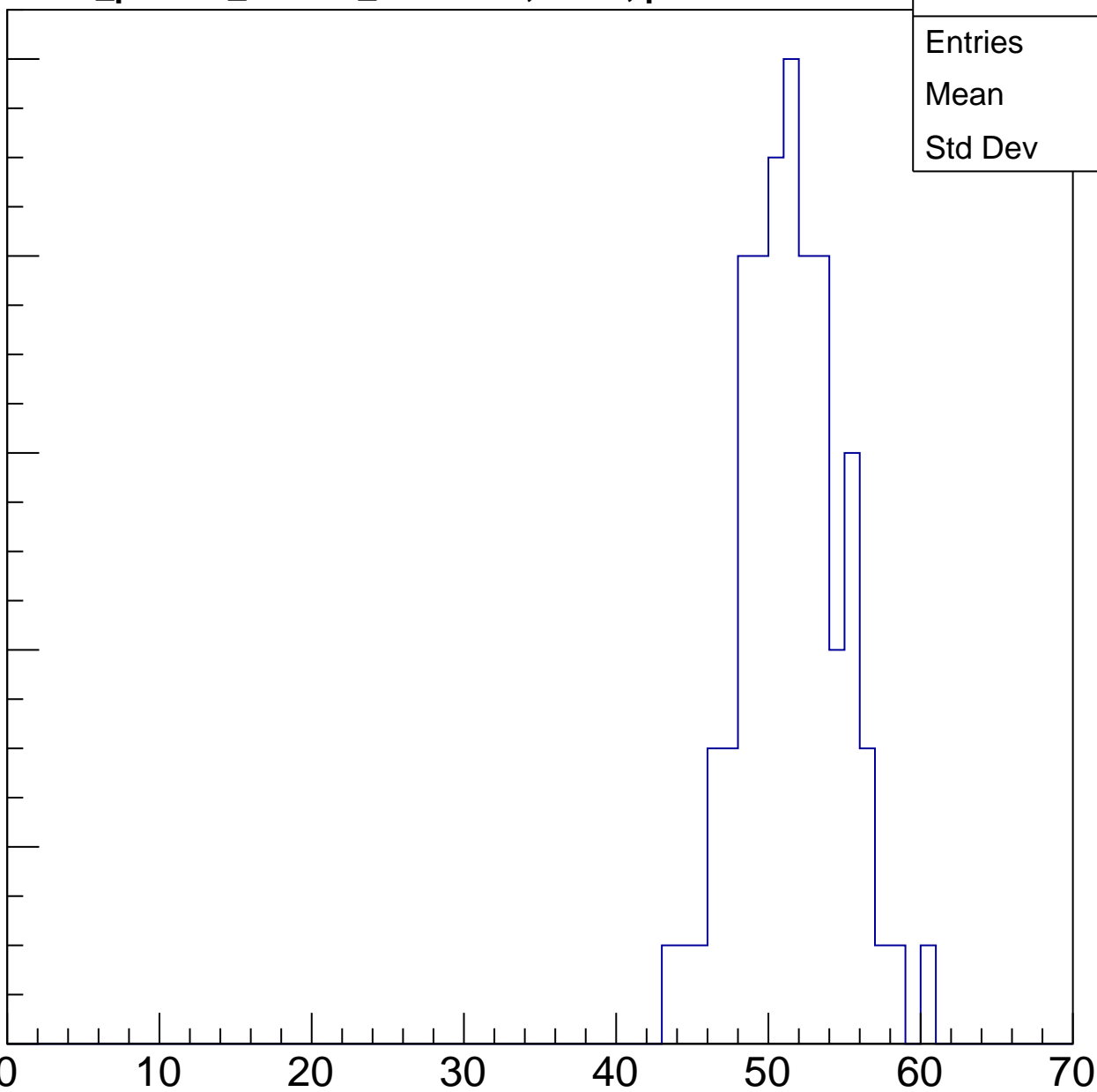
30

40

50

60

ampl

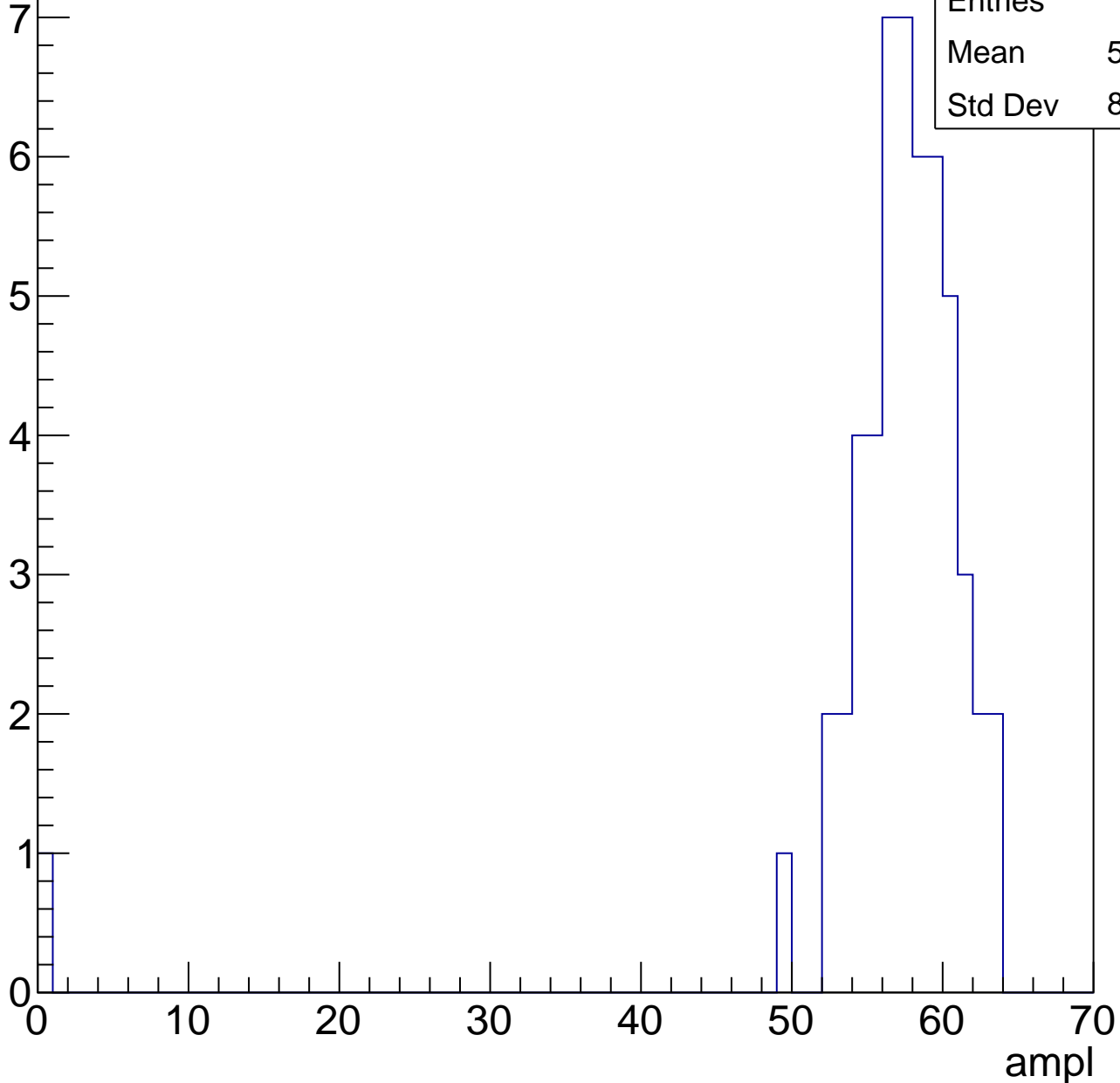


B1L100S, U12-ch65, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	56.17
Std Dev	8.393

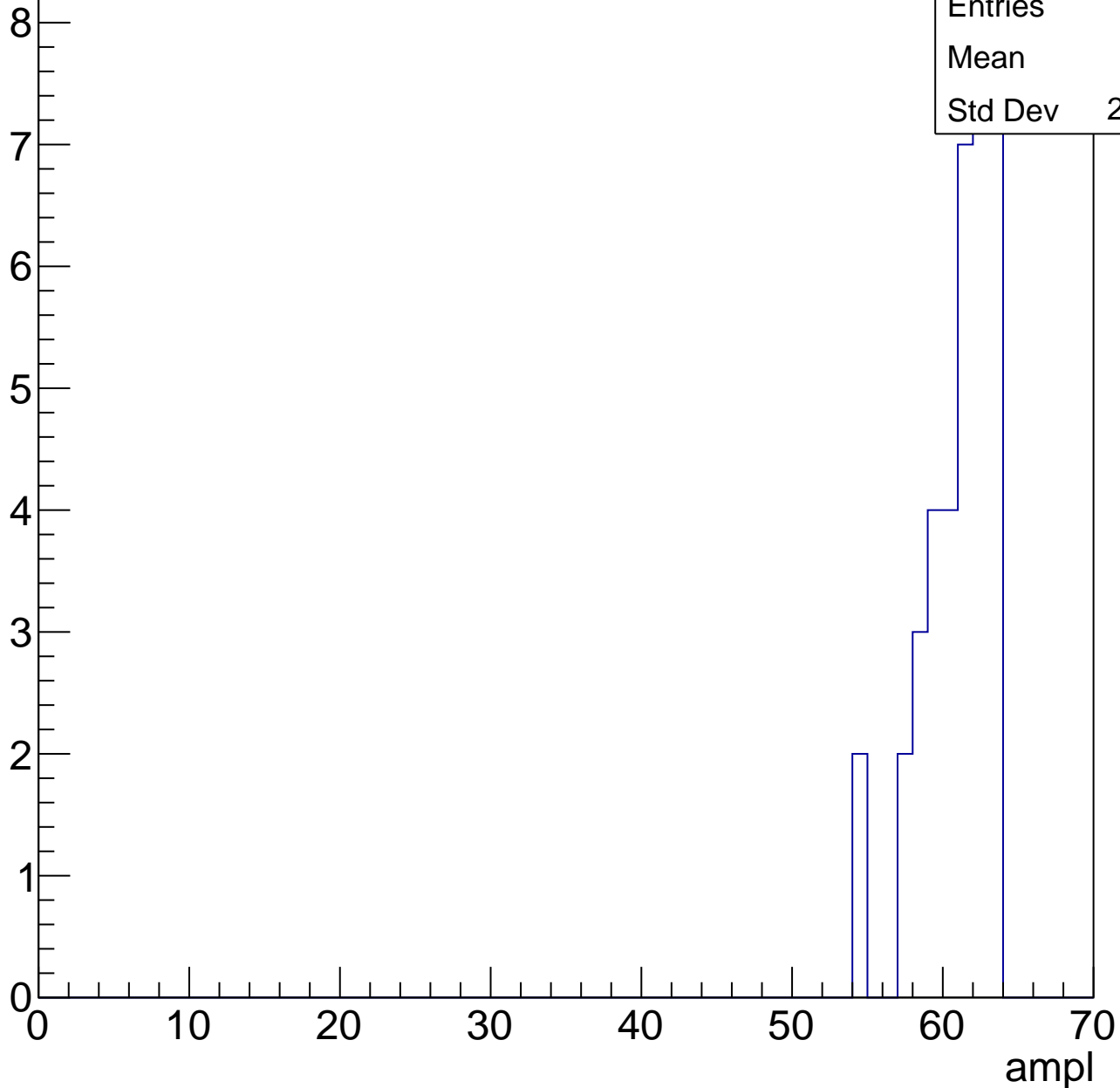


B1L100S, U12-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	60.5
Std Dev	2.337



B1L100S, U12-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

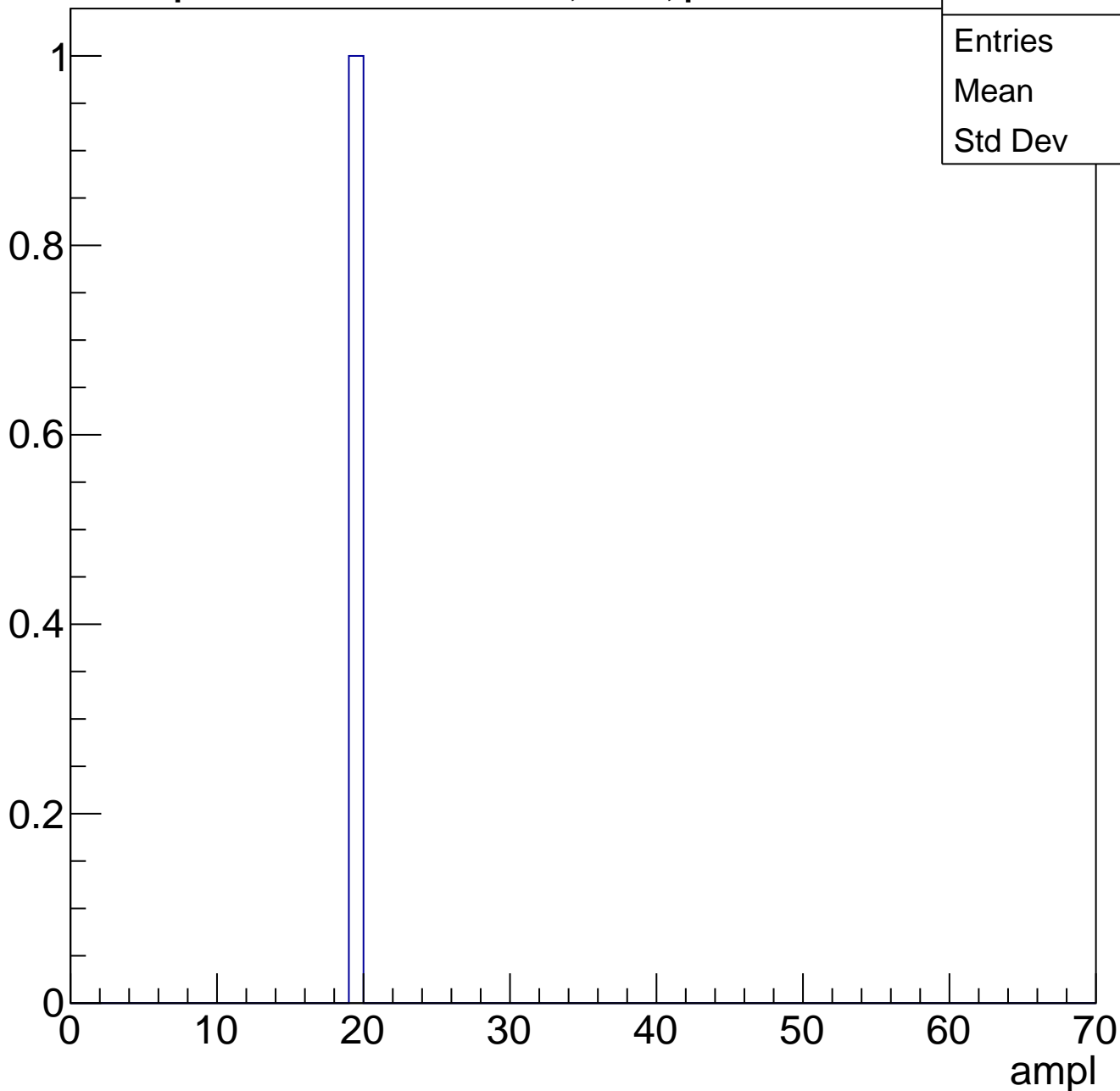


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch66, adc0

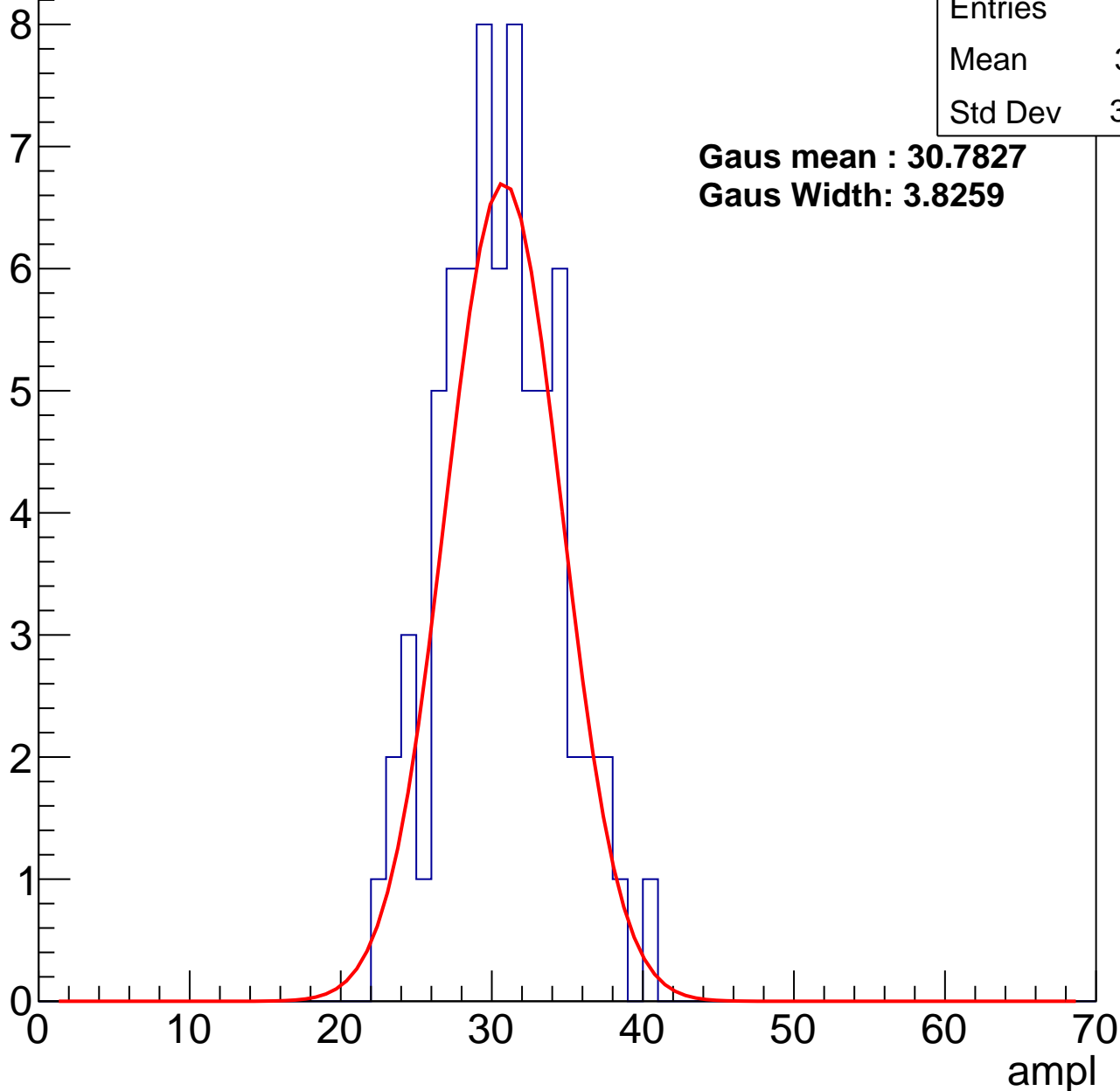
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	30.11
Std Dev	3.804

Gaus mean : 30.7827

Gaus Width: 3.8259



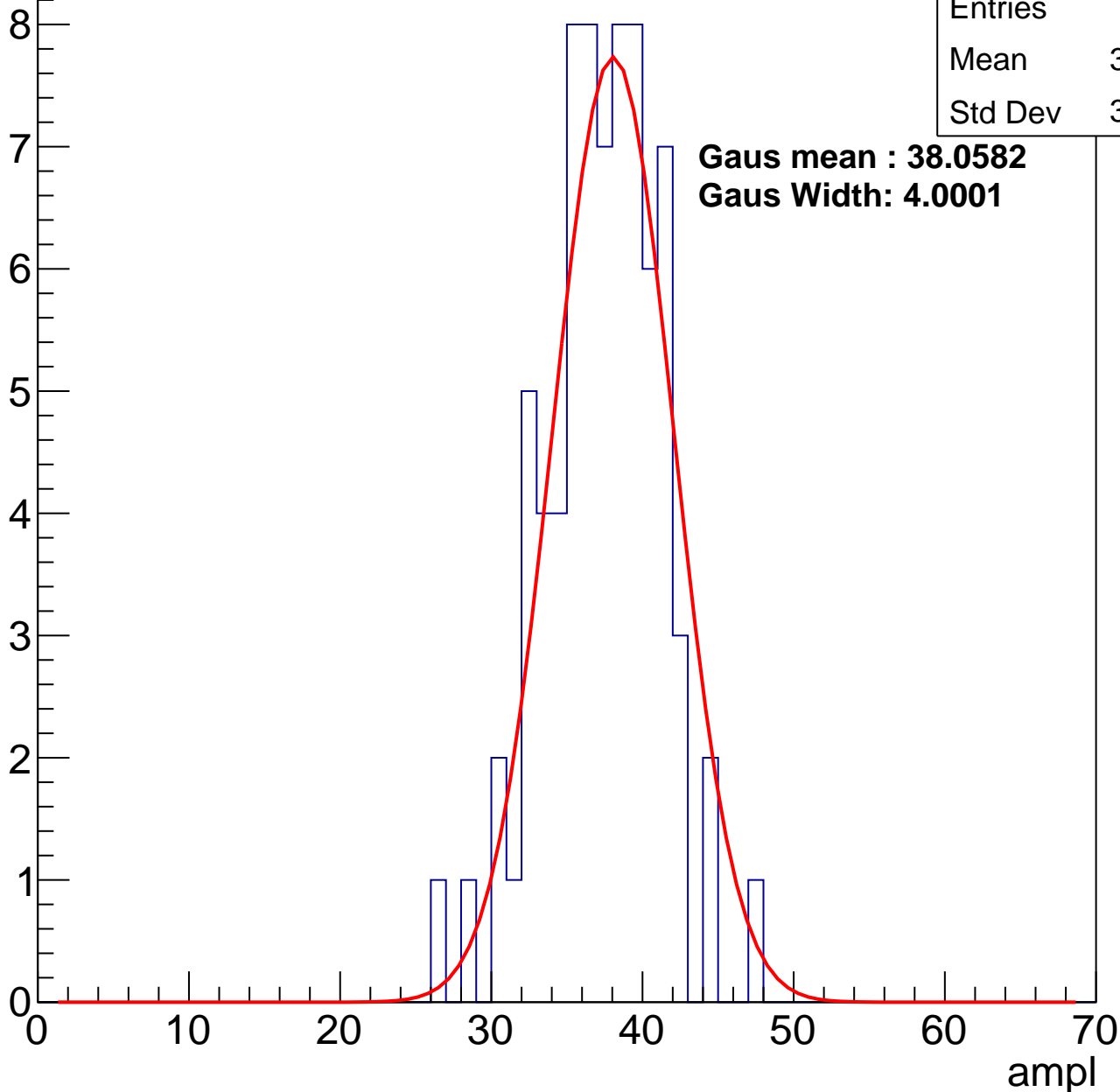
B1L100S, U12-ch66, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.89
Std Dev	3.768

Gaus mean : 38.0582
Gaus Width: 4.0001



B1L100S, U12-ch66, adc2

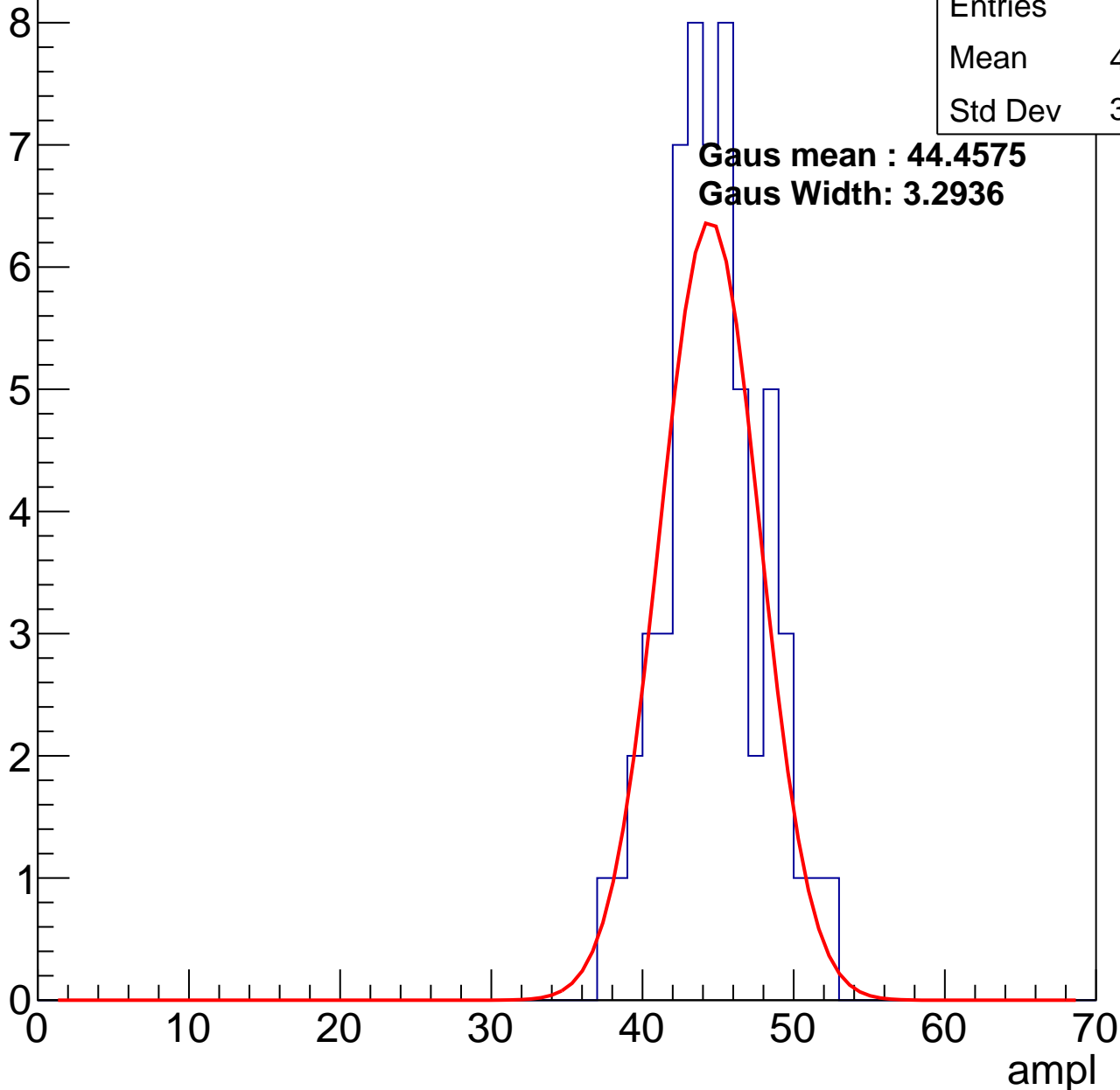
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	44.24
Std Dev	3.196

Gaus mean : 44.4575

Gaus Width: 3.2936

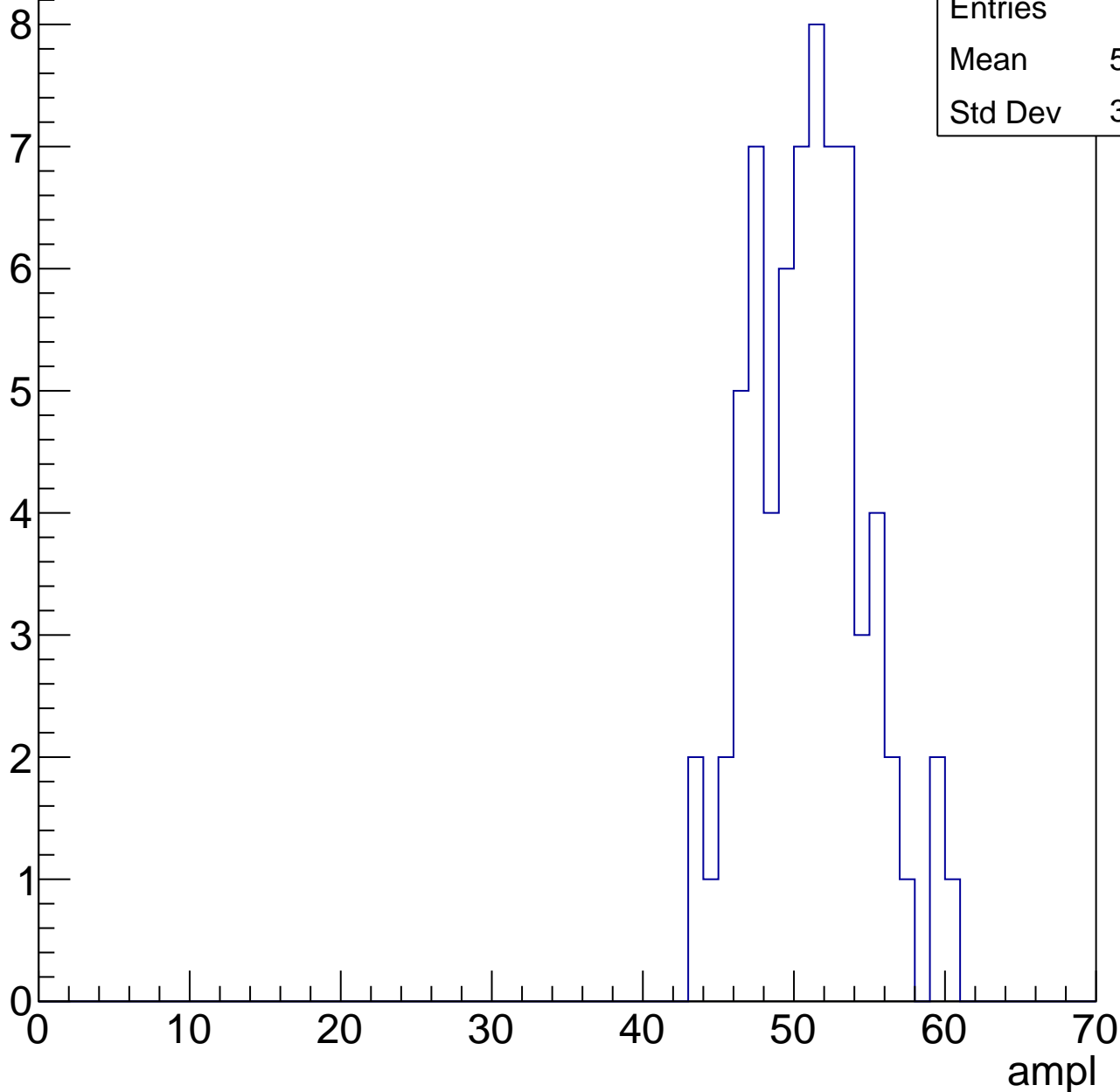


B1L100S, U12-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	50.54
Std Dev	3.728

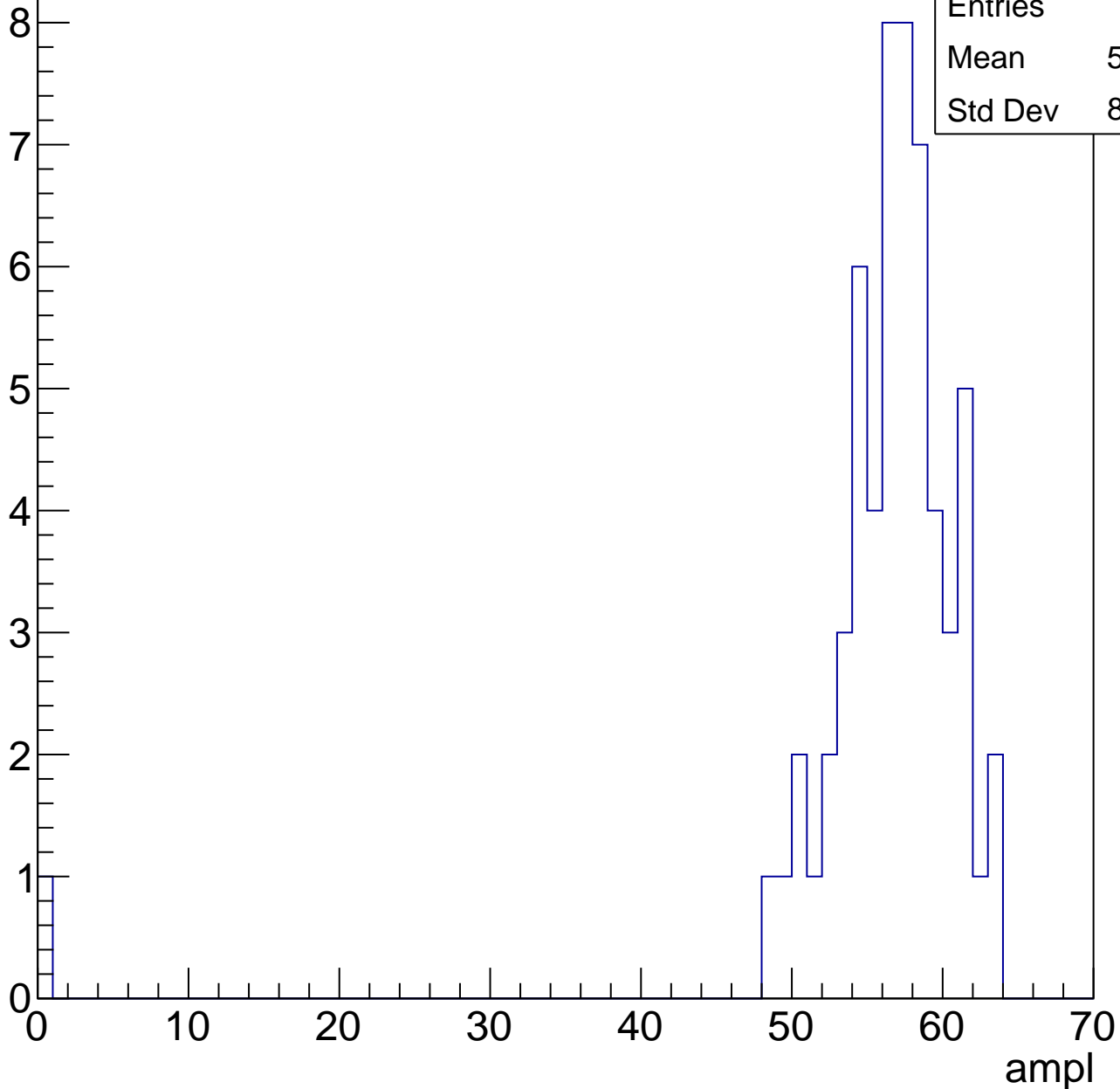


B1L100S, U12-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	55.49
Std Dev	8.022

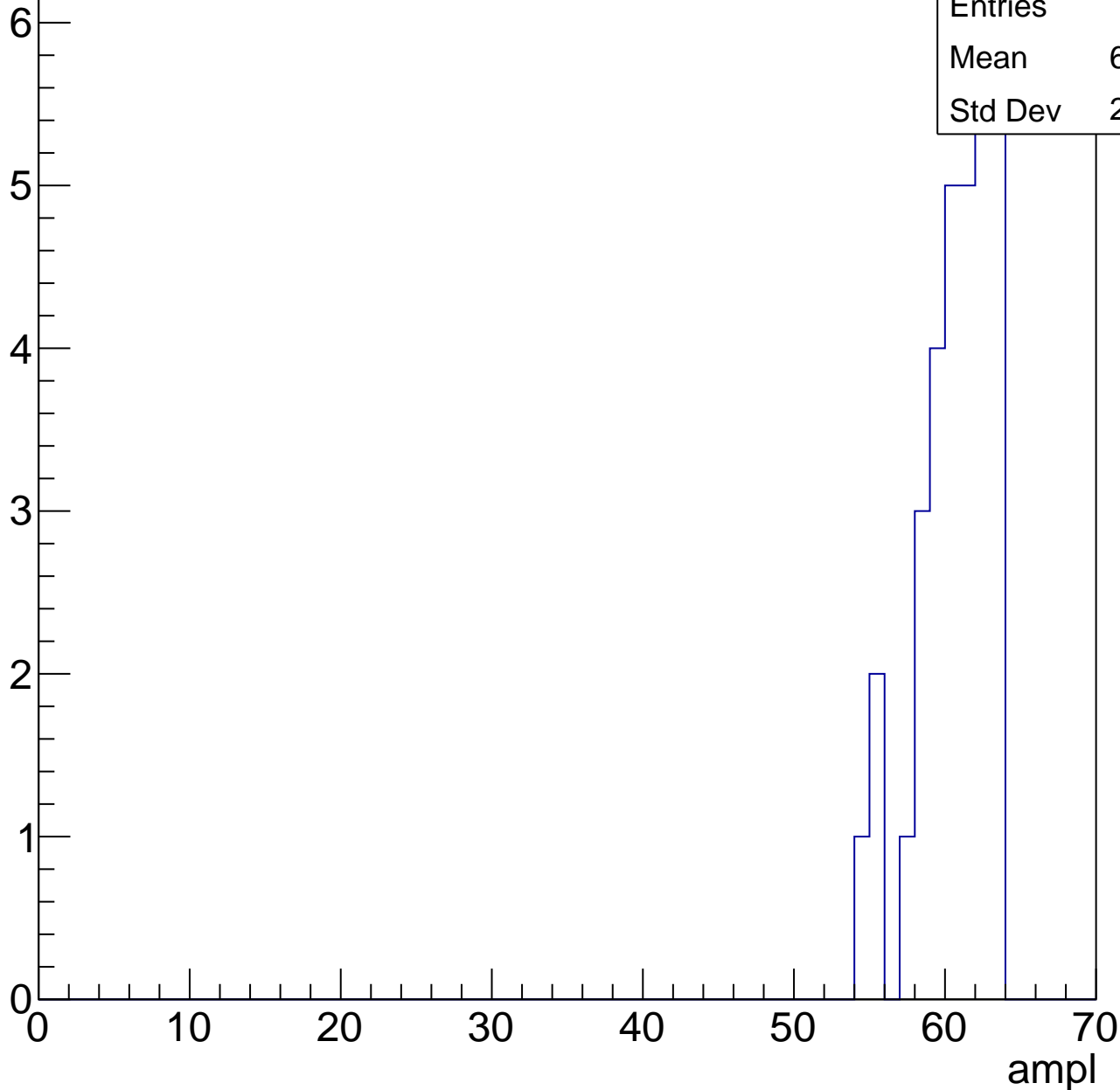


B1L100S, U12-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

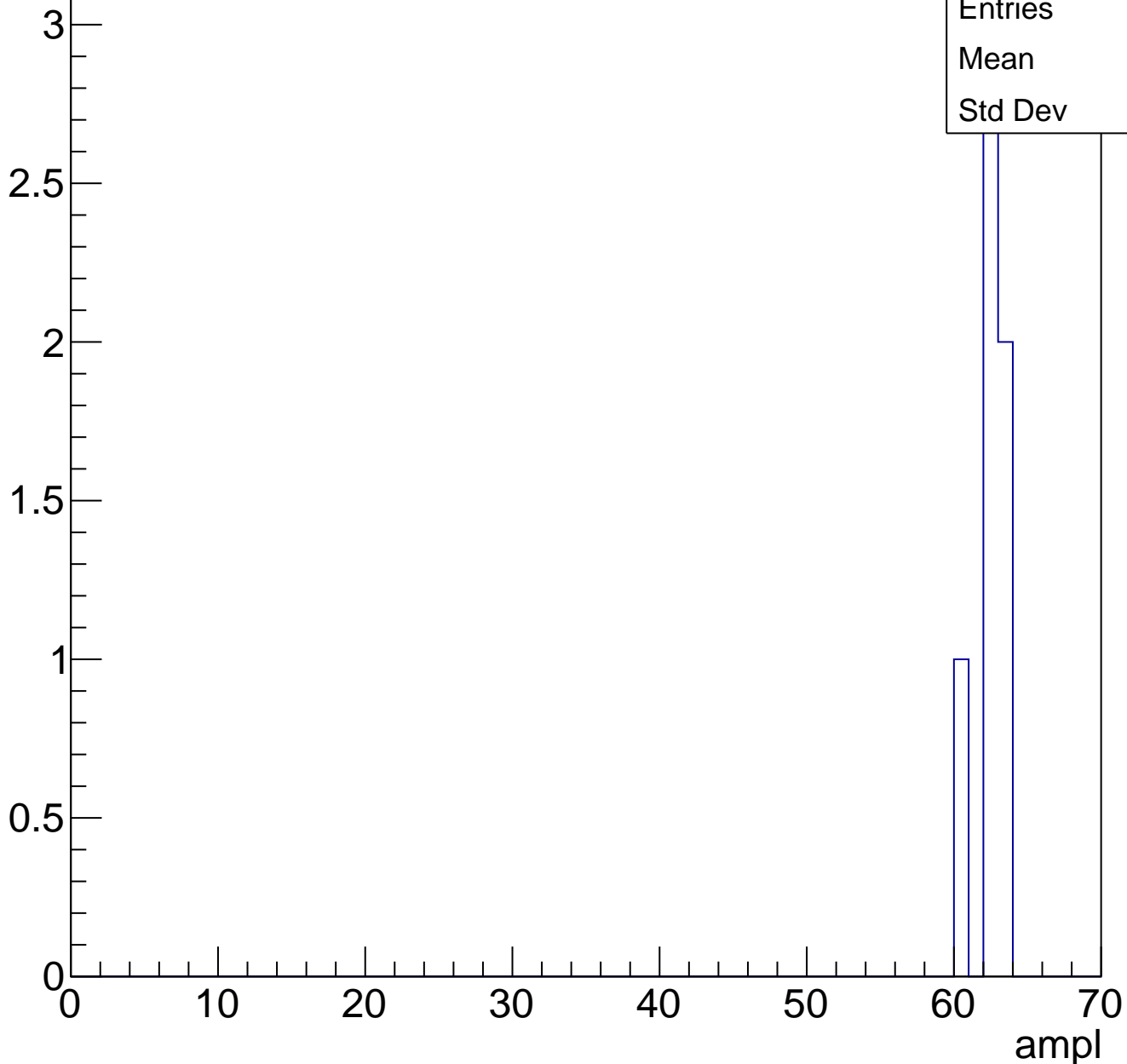
Entries	33
Mean	60.18
Std Dev	2.418



B1L100S, U12-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch67, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	30.52
Std Dev	3.058

Gaus mean : 31.1515

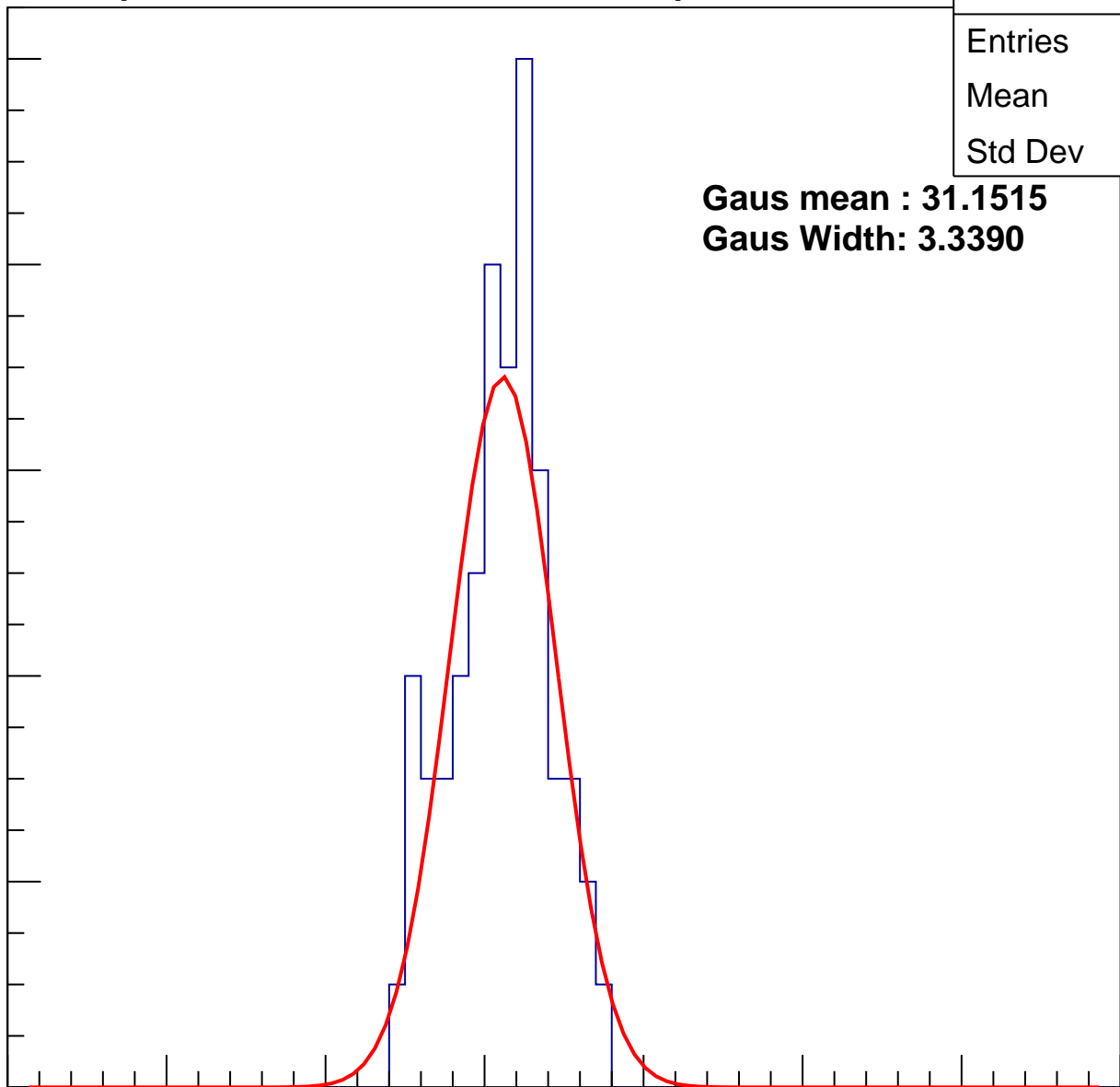
Gaus Width: 3.3390

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch67, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	37.13
Std Dev	3.976

Gaus mean : 37.0688

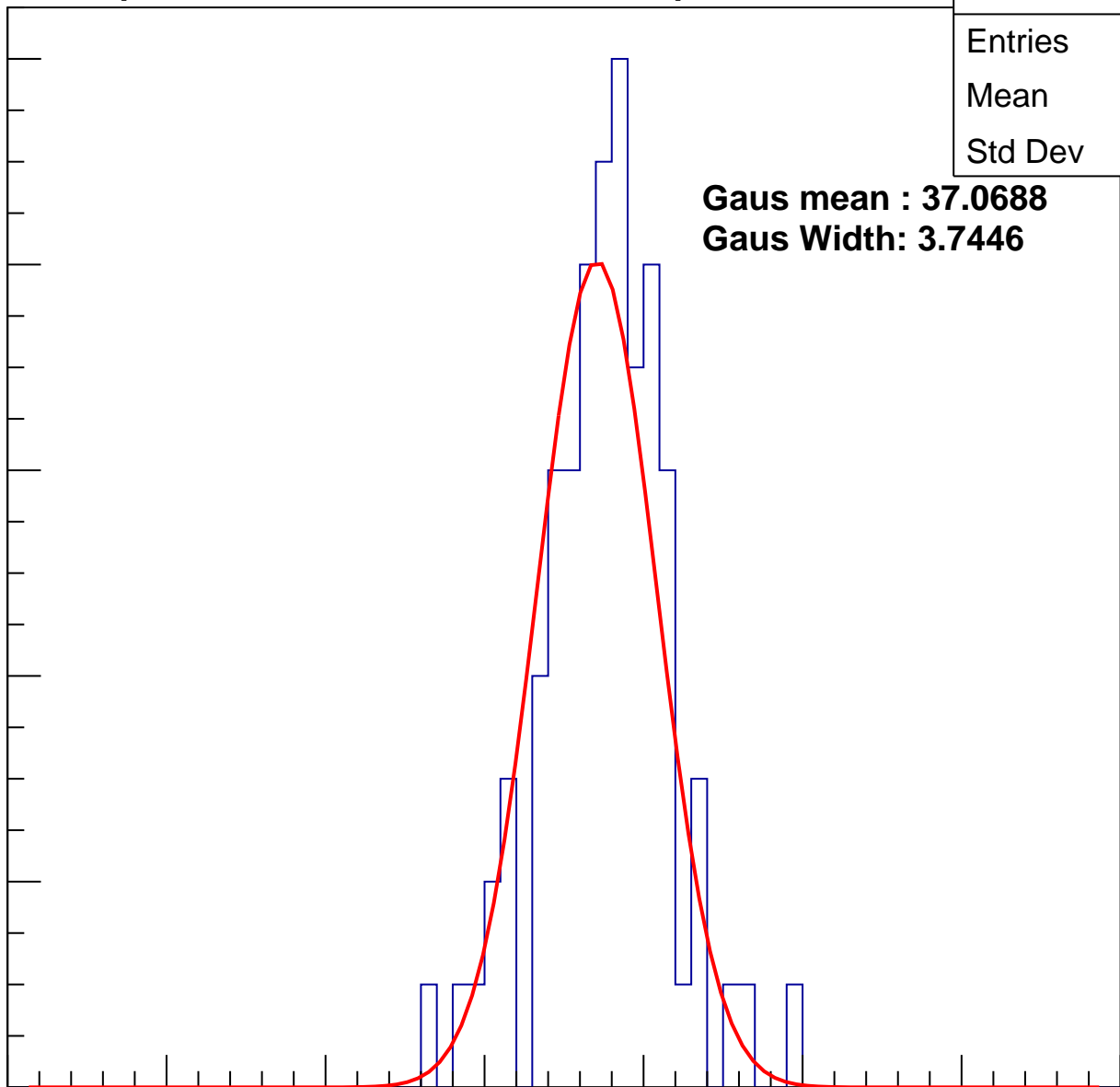
Gaus Width: 3.7446

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch67, adc2

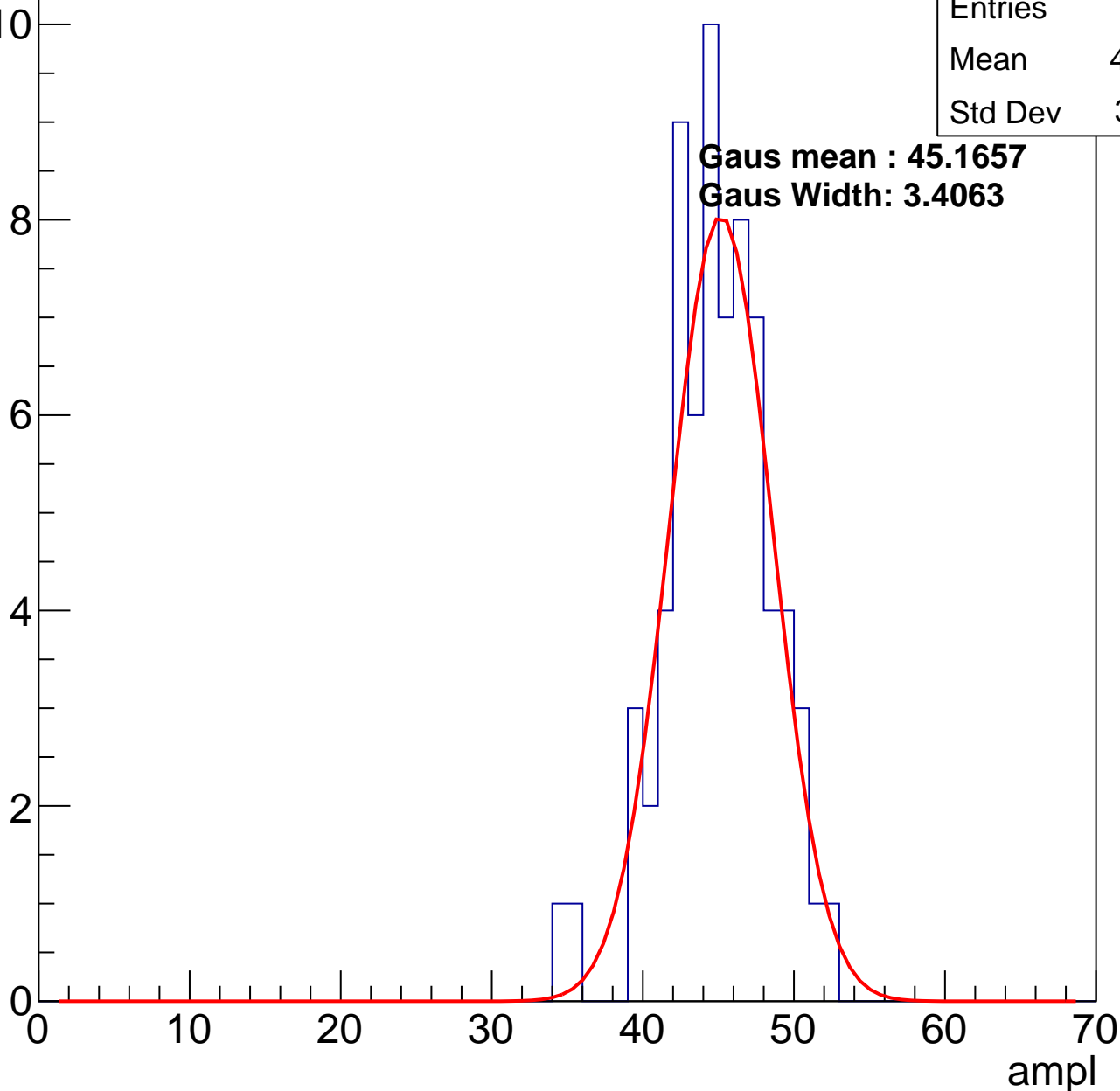
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	44.49
Std Dev	3.431

Gaus mean : 45.1657

Gaus Width: 3.4063

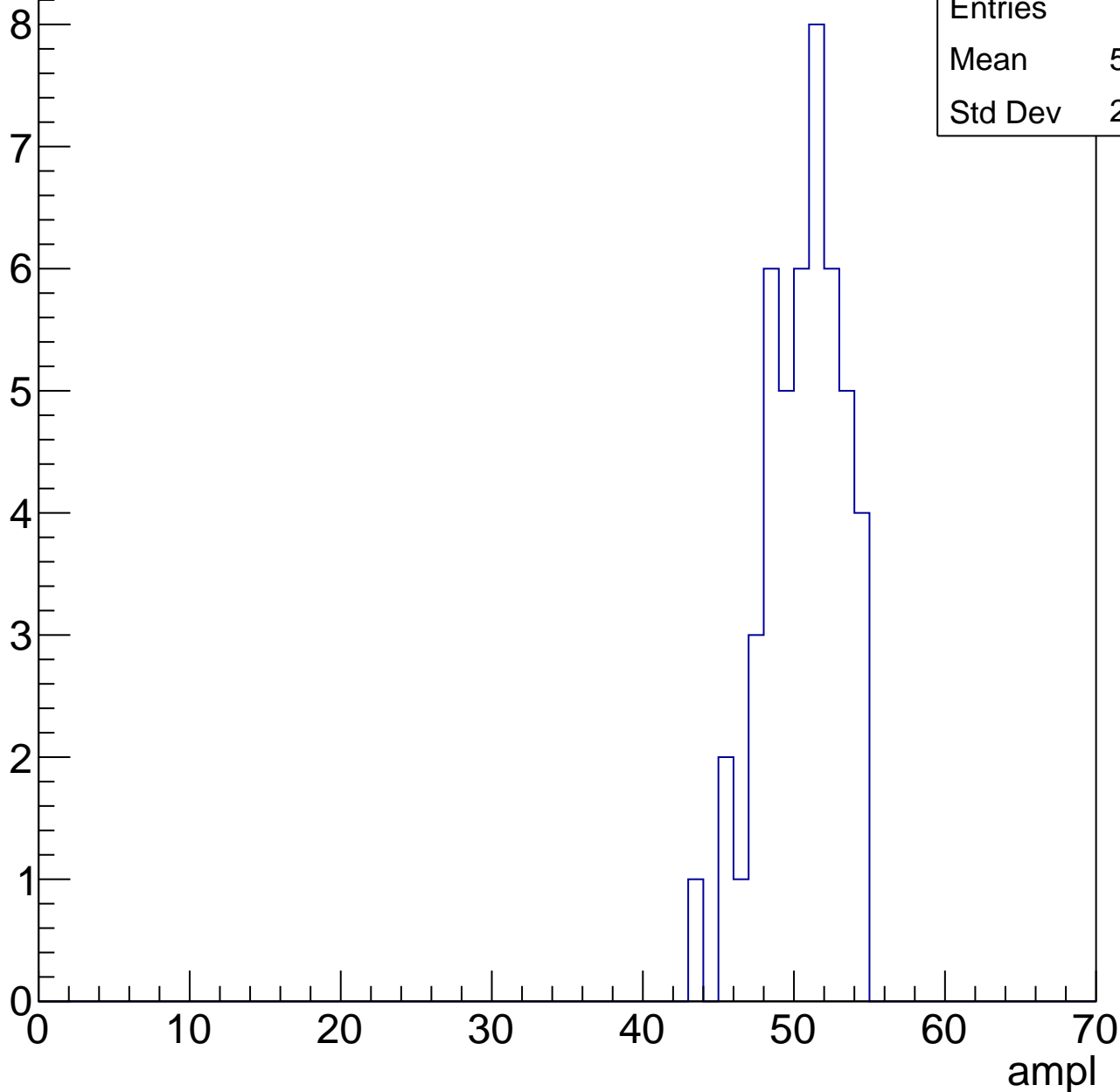


B1L100S, U12-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	50.09
Std Dev	2.575



B1L100S, U12-ch67, adc4

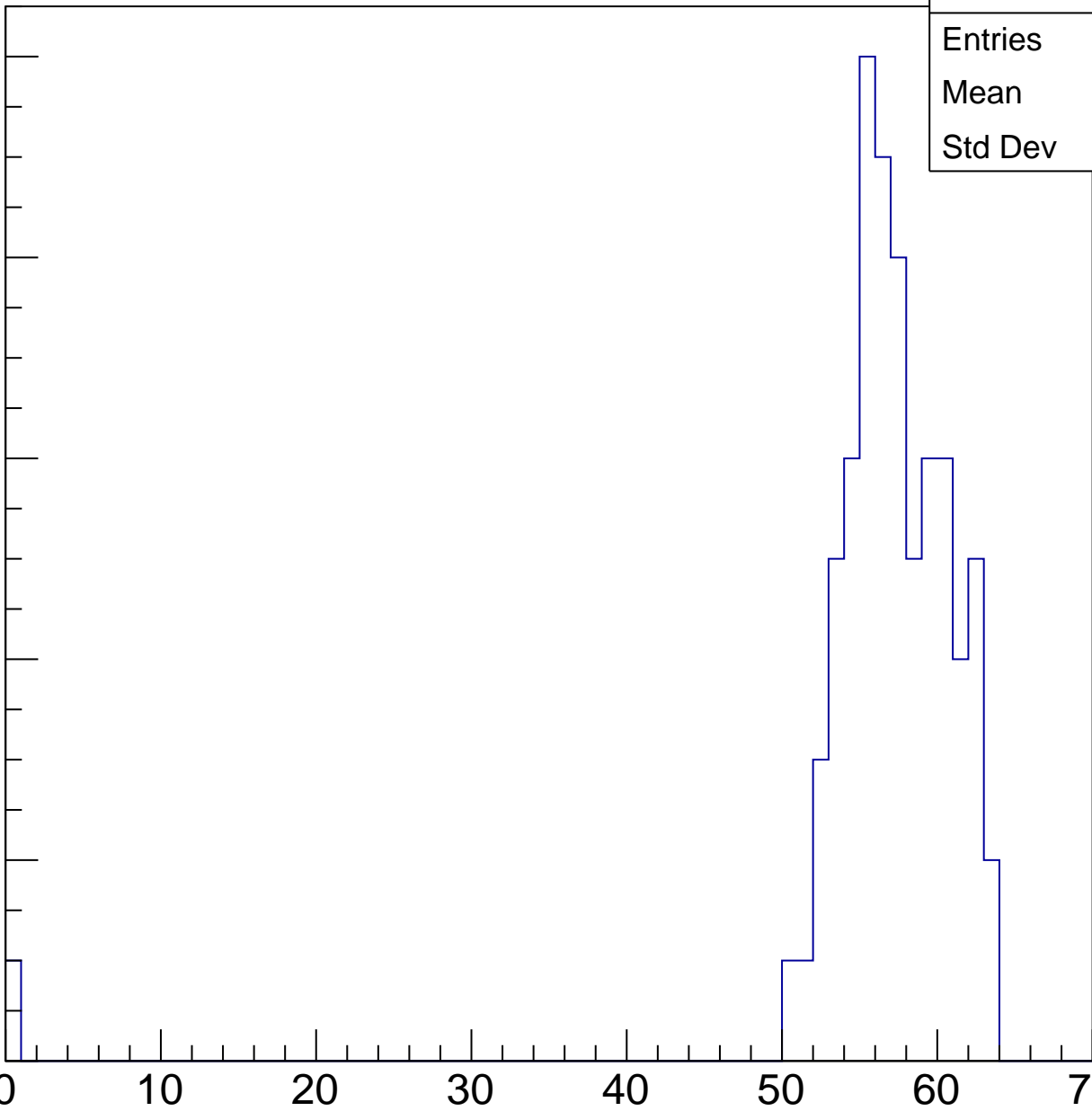
calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	56.11
Std Dev	7.34

Entry

10
8
6
4
2
0

ampl

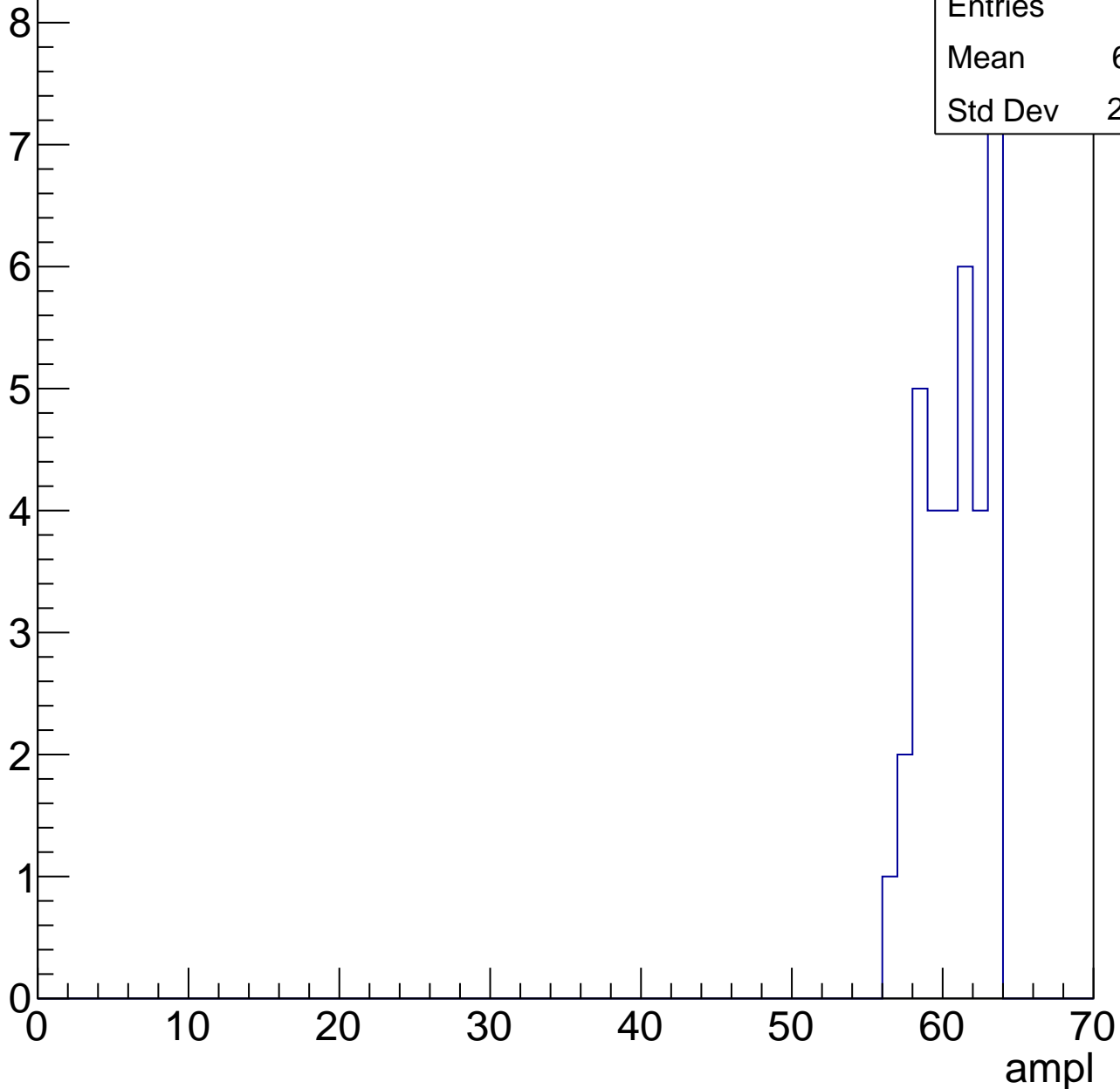


B1L100S, U12-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	60.41
Std Dev	2.074



B1L100S, U12-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L100S, U12-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch68, adc0

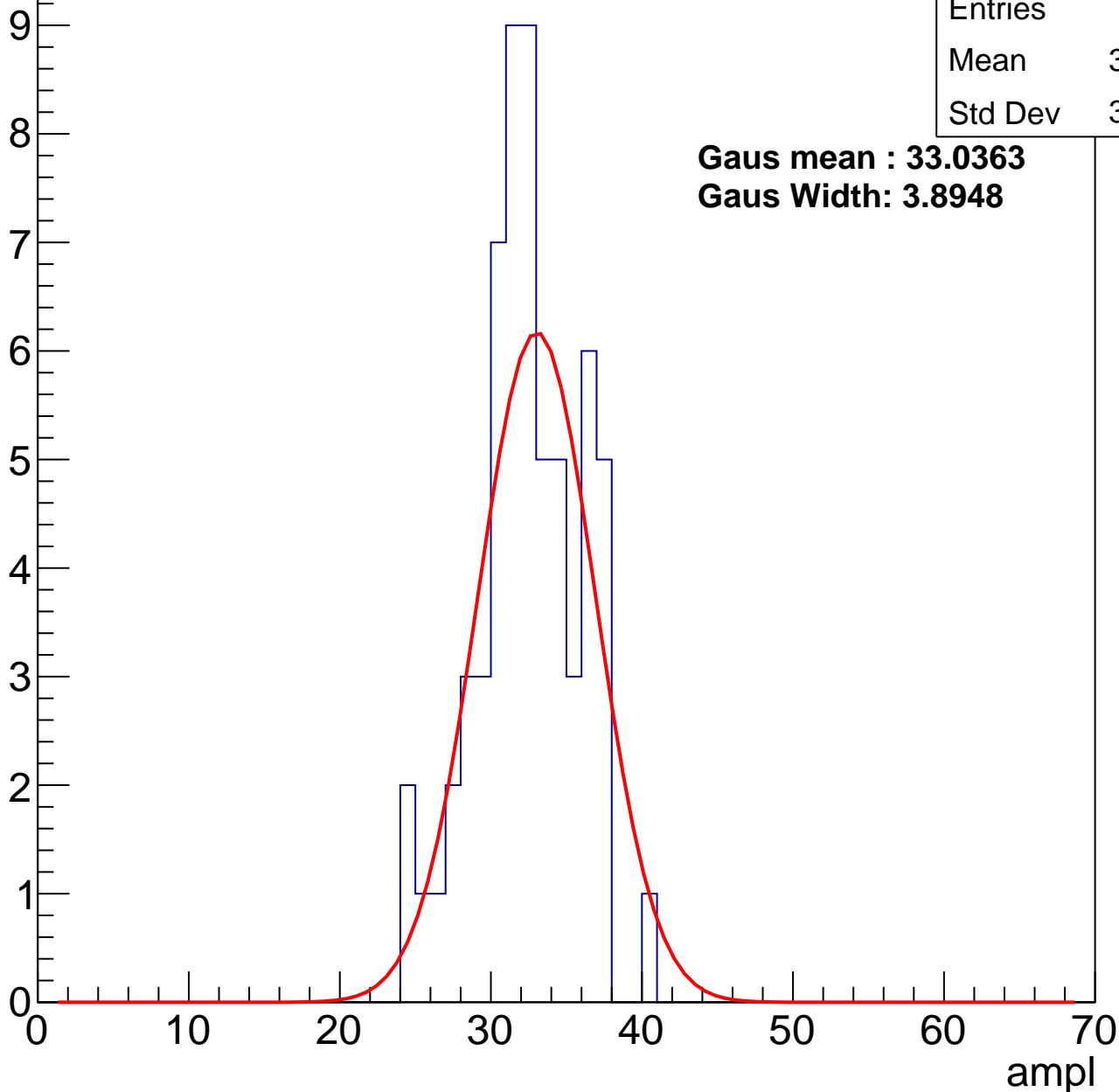
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	31.97
Std Dev	3.389

Gaus mean : 33.0363

Gaus Width: 3.8948



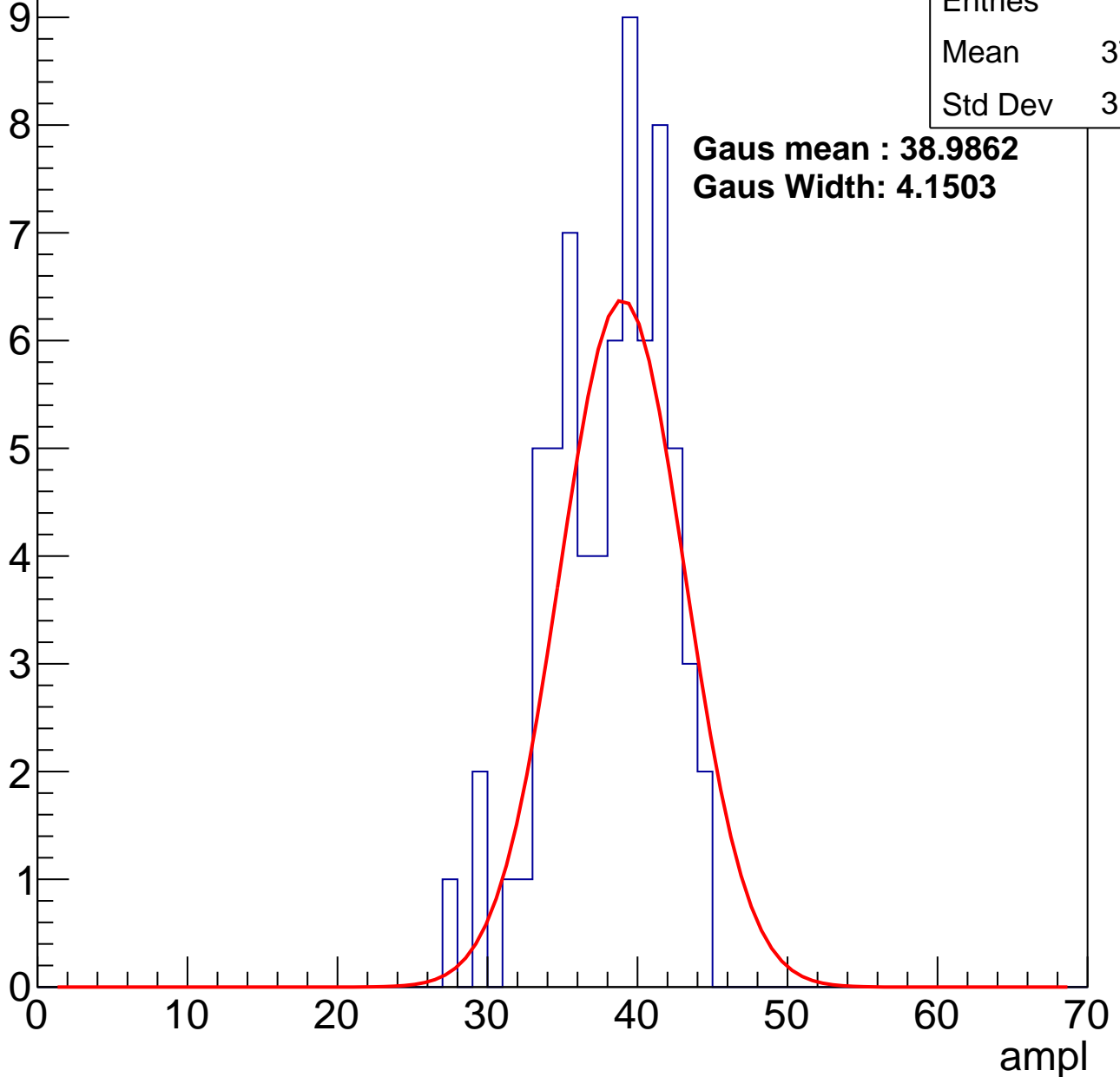
B1L100S, U12-ch68, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37.59
Std Dev	3.762

Gaus mean : 38.9862
Gaus Width: 4.1503



B1L100S, U12-ch68, adc2

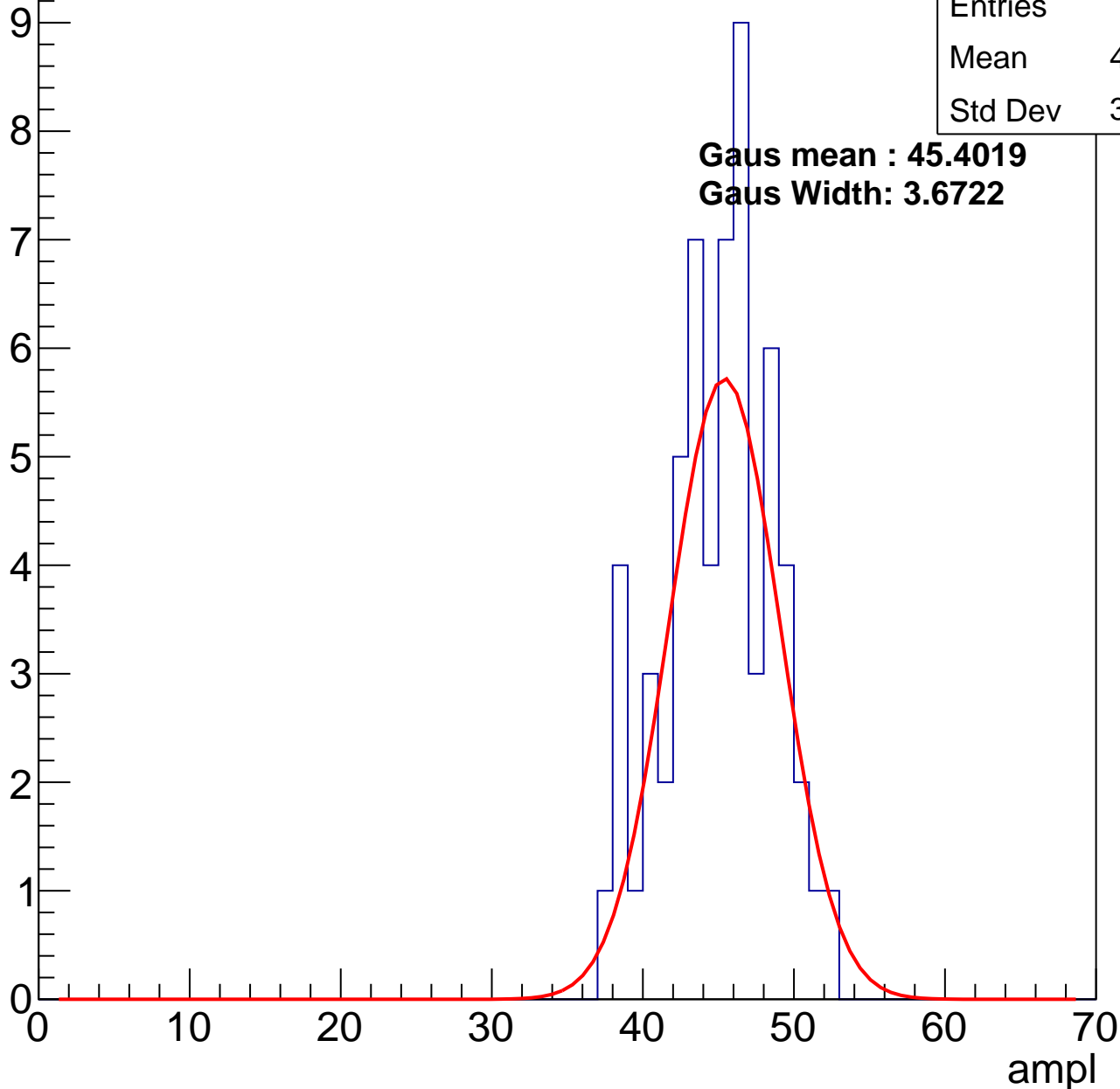
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.57
Std Dev	3.518

Gaus mean : 45.4019

Gaus Width: 3.6722

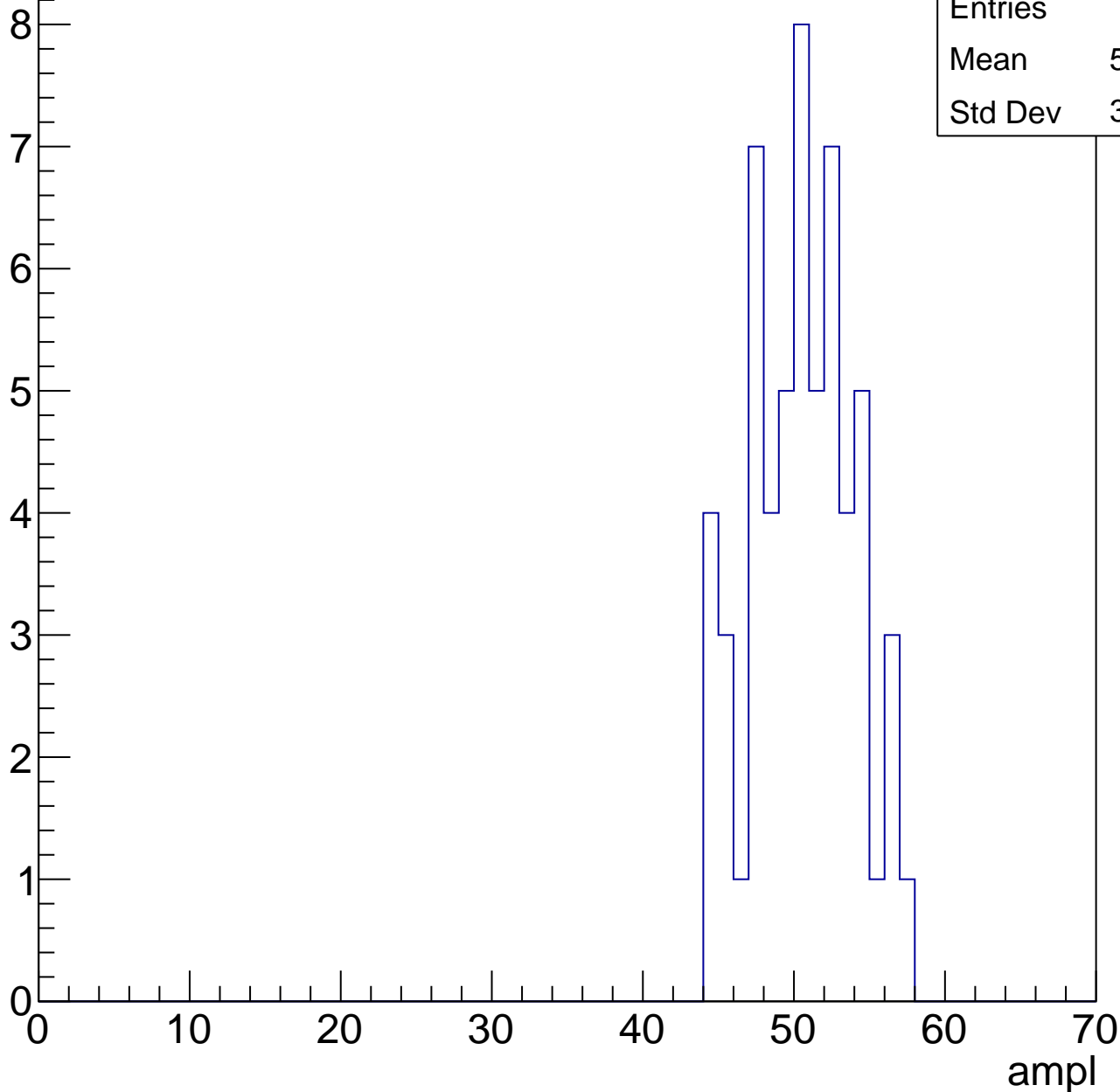


B1L100S, U12-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2

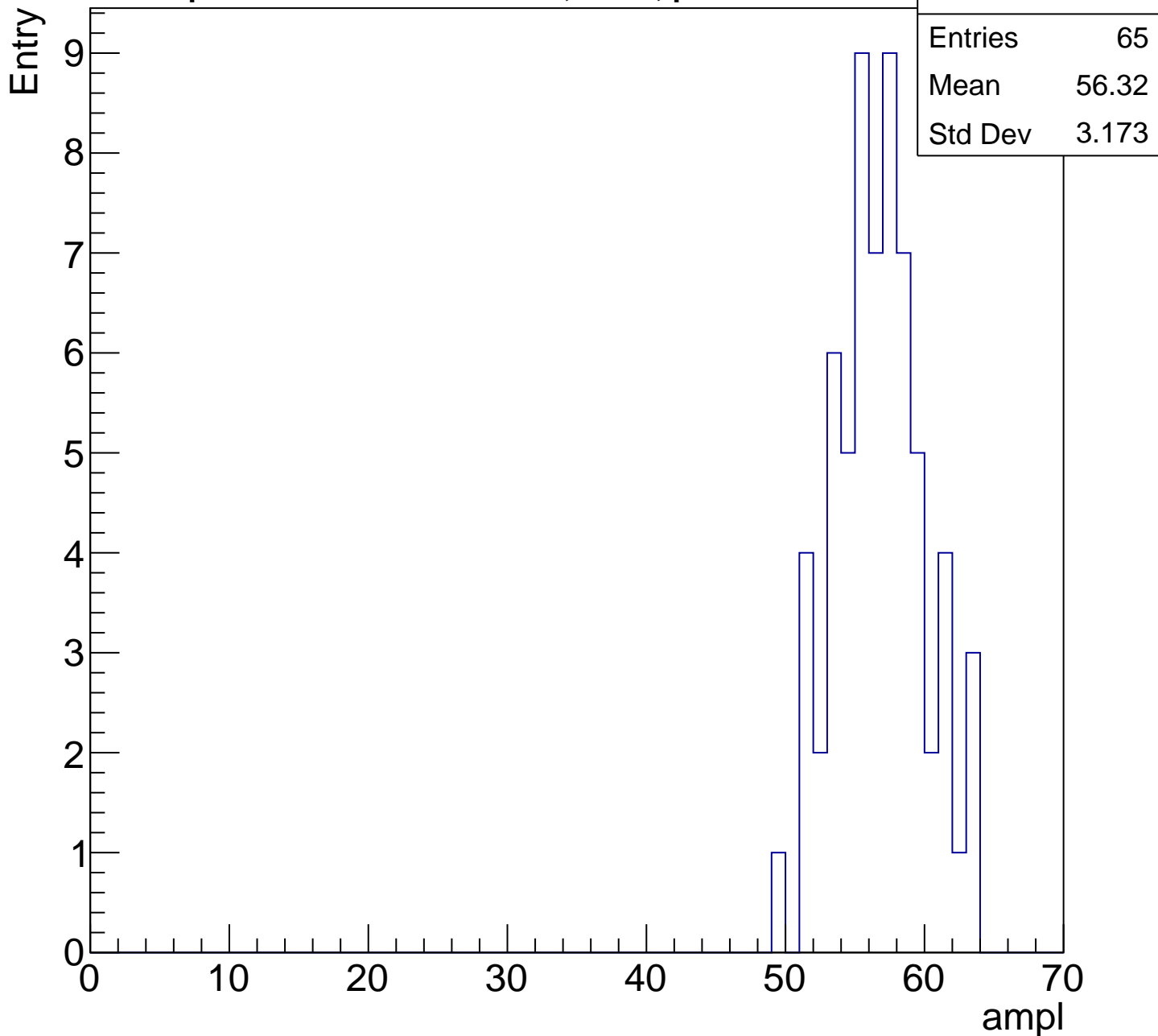
Entry

Entries	58
Mean	50.07
Std Dev	3.347



B1L100S, U12-ch68, adc4

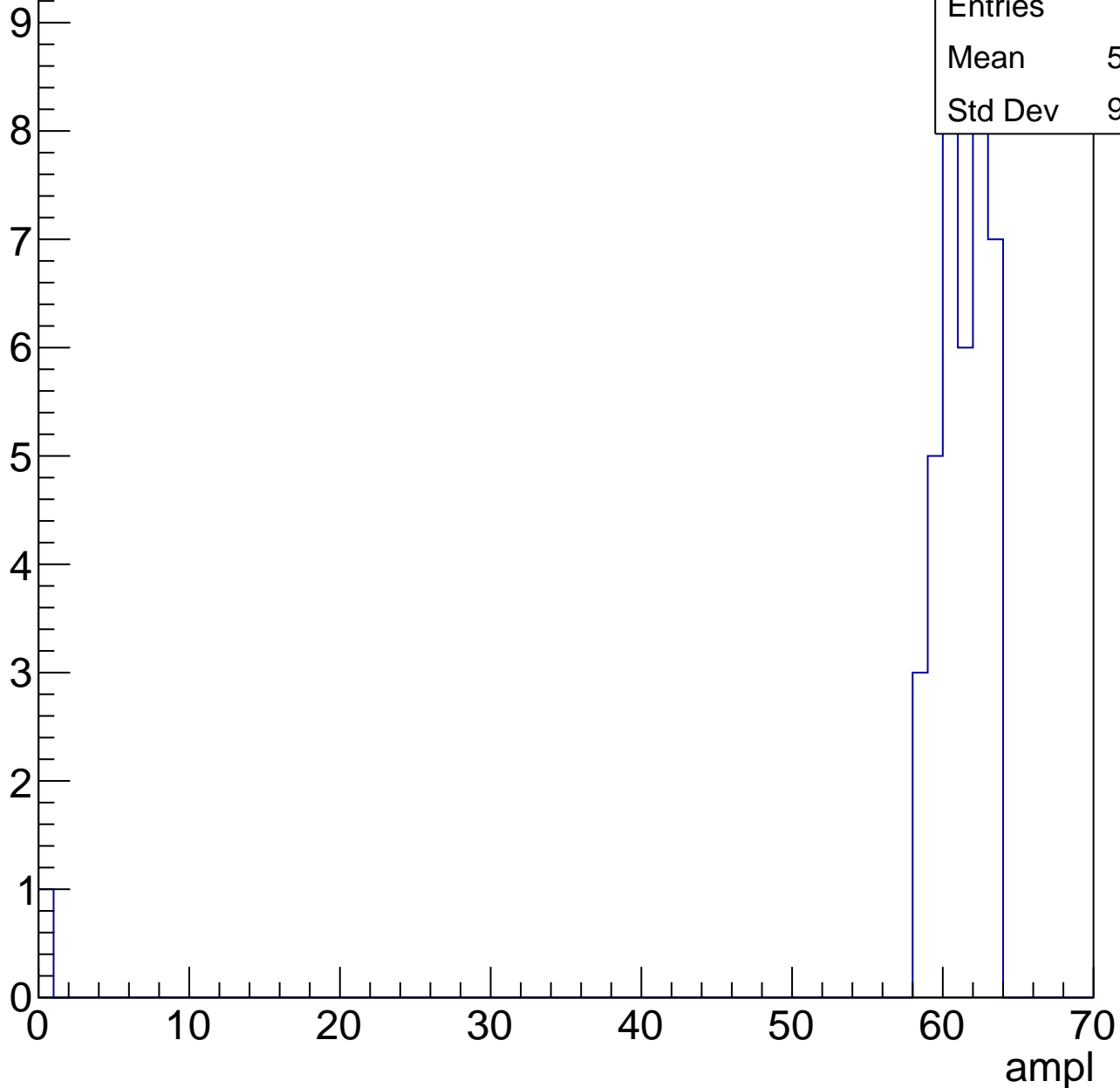
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	39
Mean	59.33
Std Dev	9.746

B1L100S, U12-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

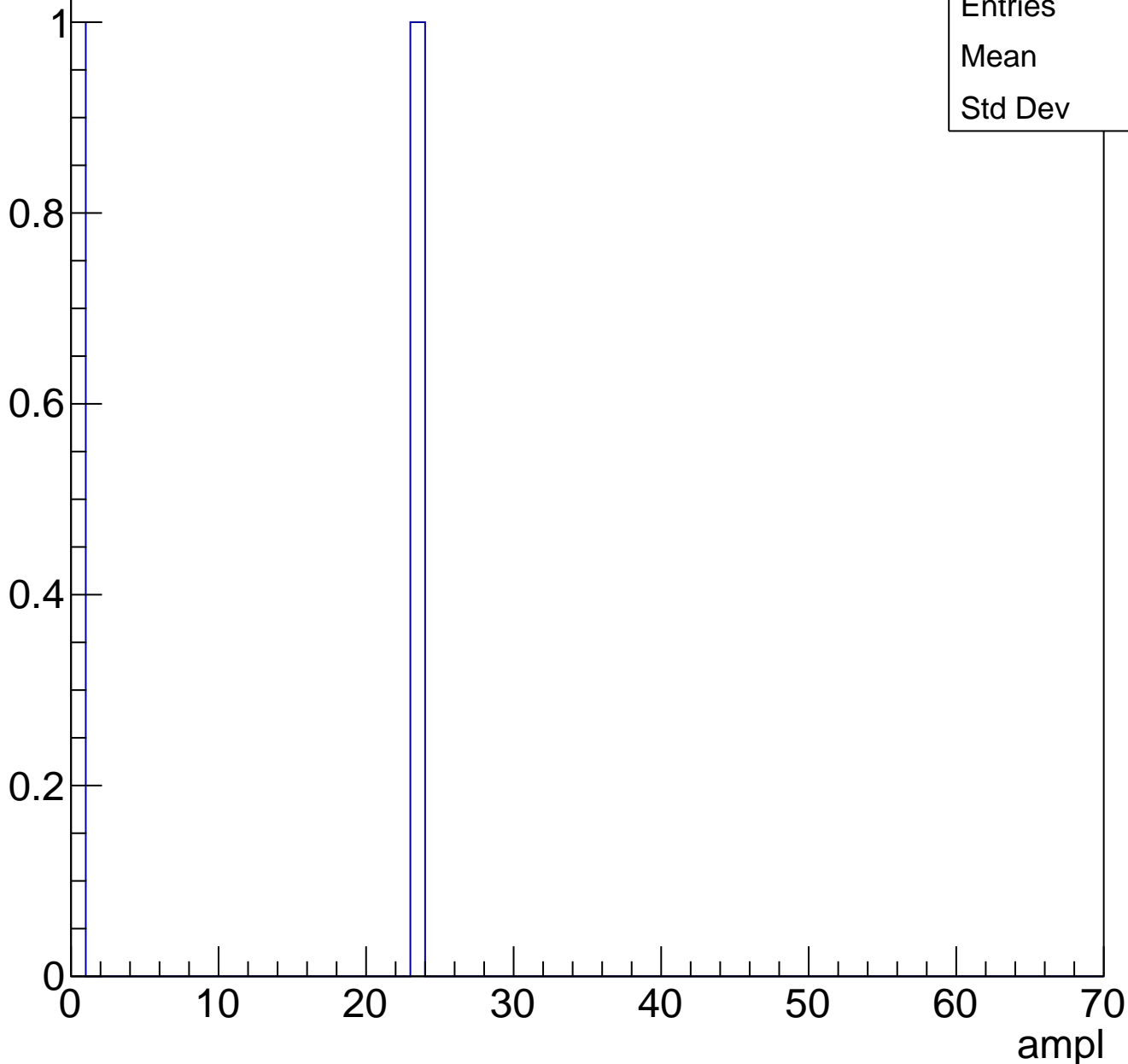


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch69, adc0

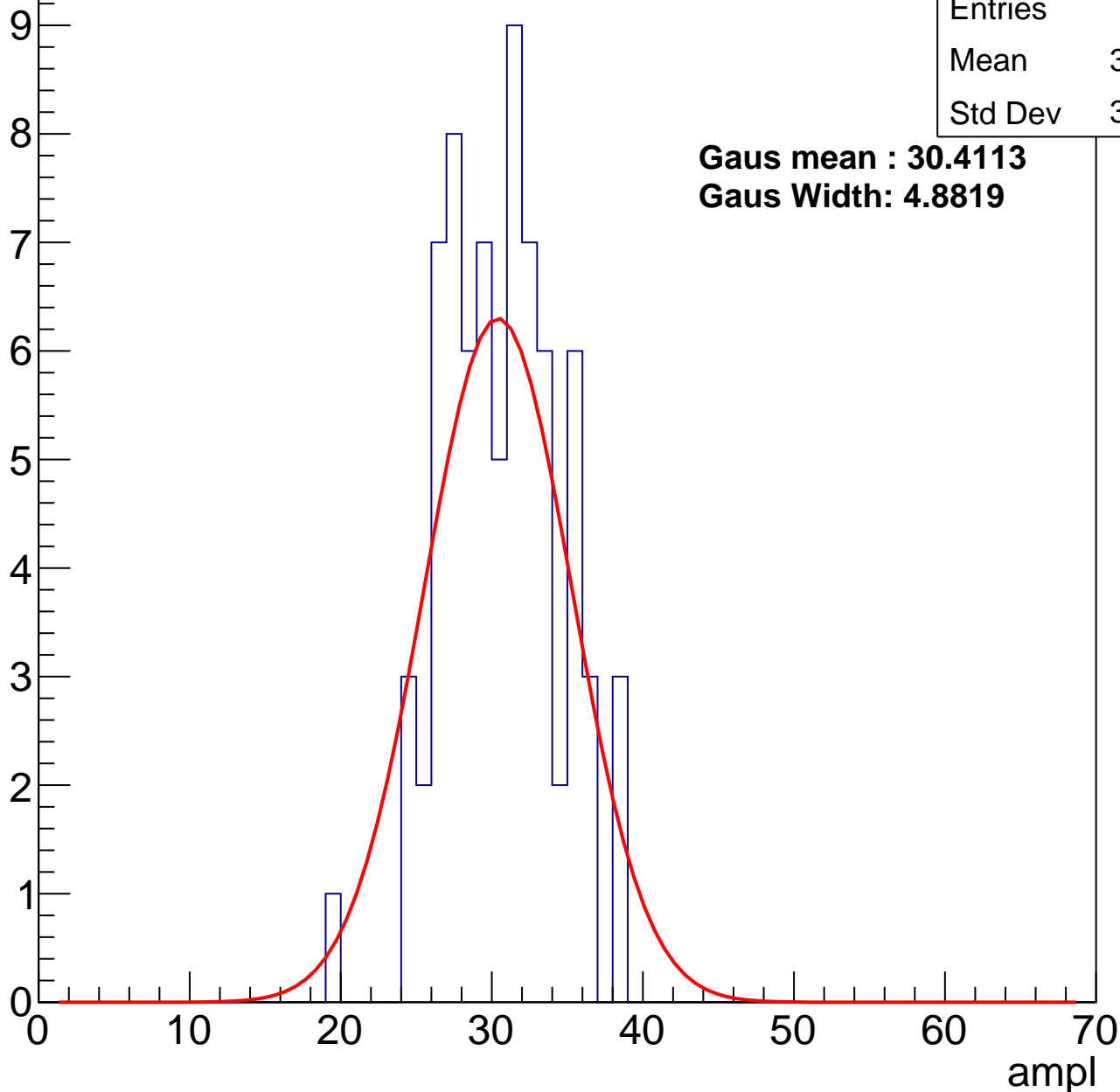
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.15
Std Dev	3.769

Gaus mean : 30.4113

Gaus Width: 4.8819



B1L100S, U12-ch69, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	37.77
Std Dev	3.626

Gaus mean : 39.1077

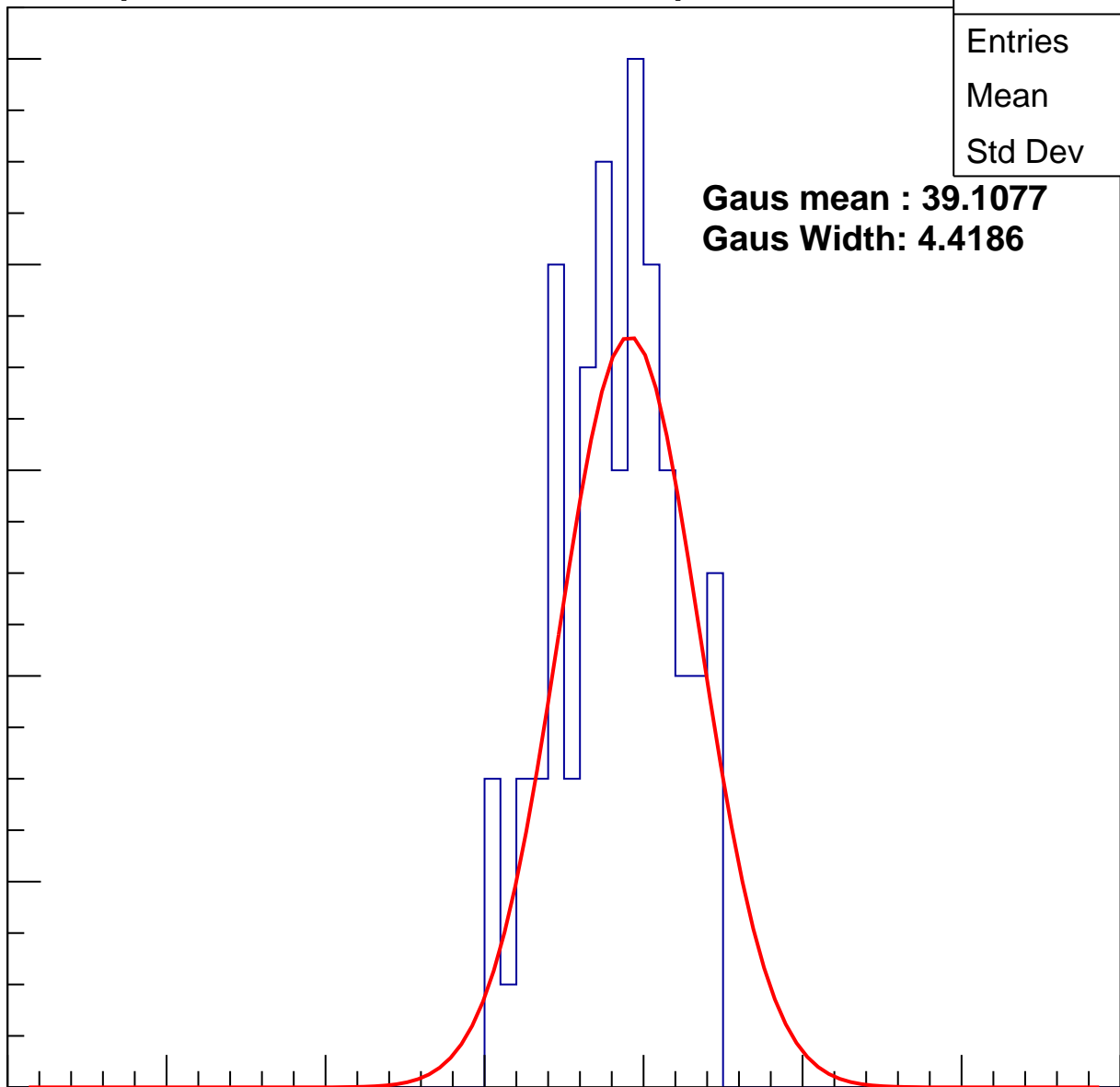
Gaus Width: 4.4186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch69, adc2

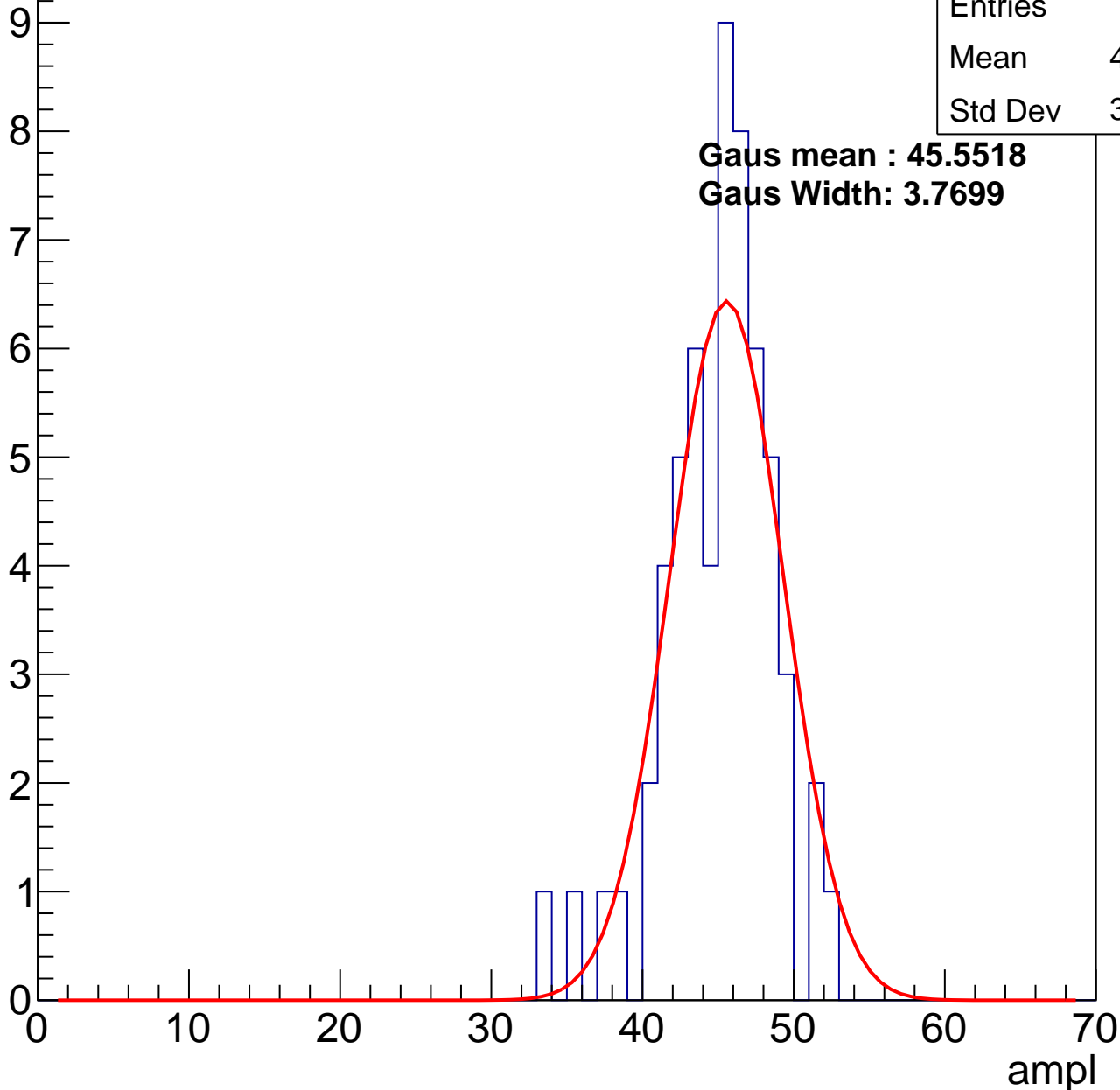
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.53
Std Dev	3.628

Gaus mean : 45.5518

Gaus Width: 3.7699

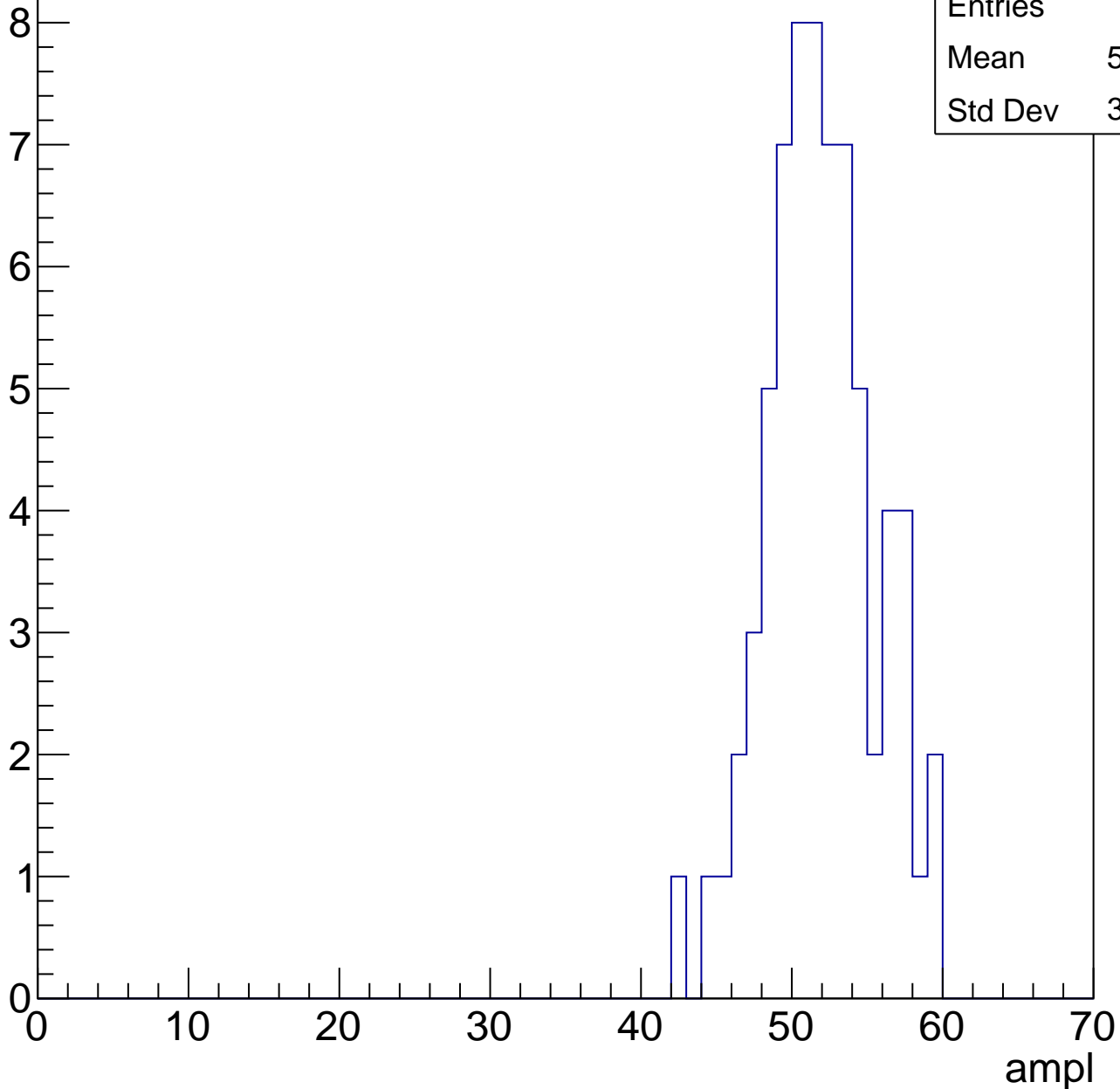


B1L100S, U12-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	51.44
Std Dev	3.566

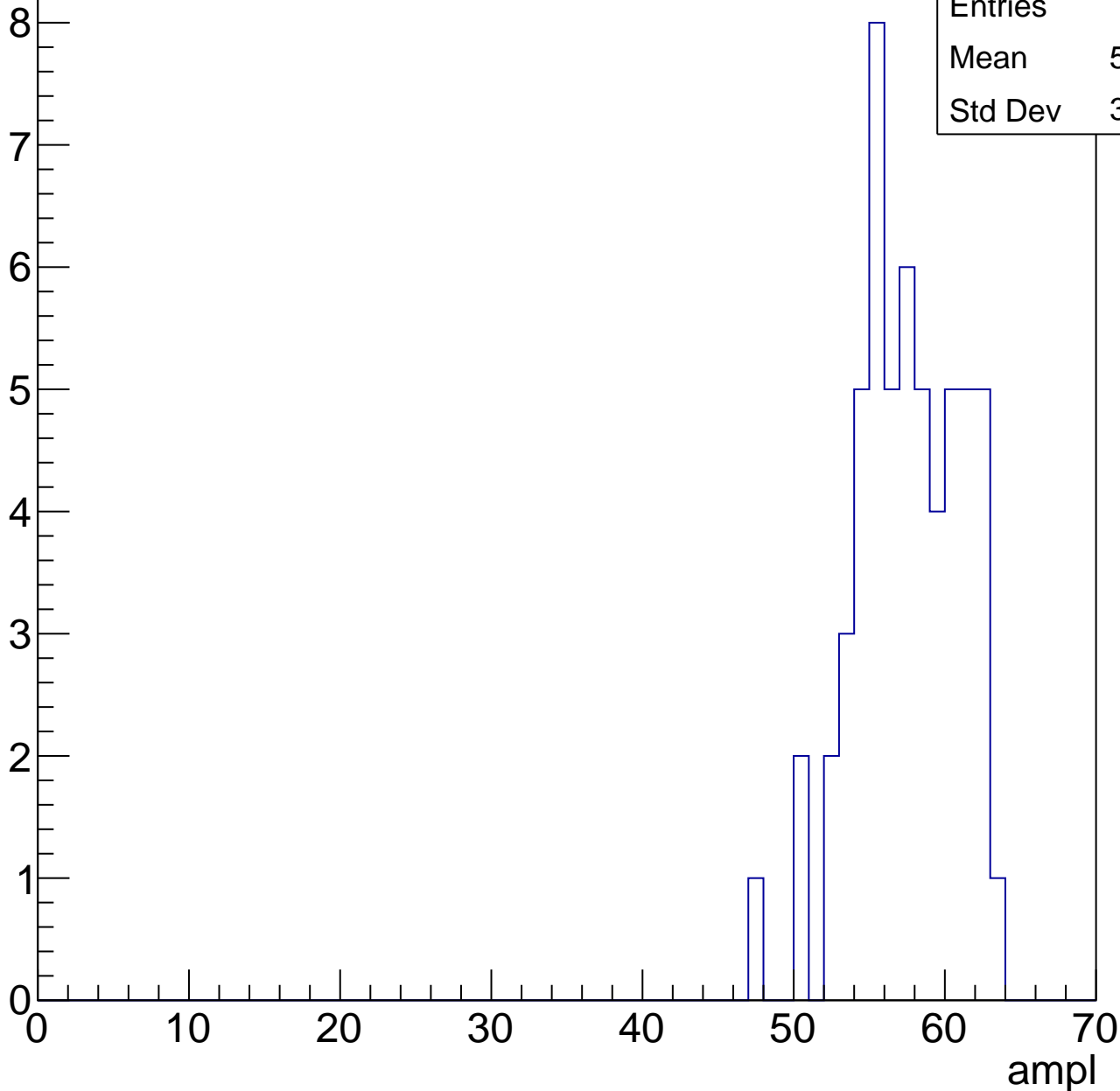


B1L100S, U12-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	56.95
Std Dev	3.466

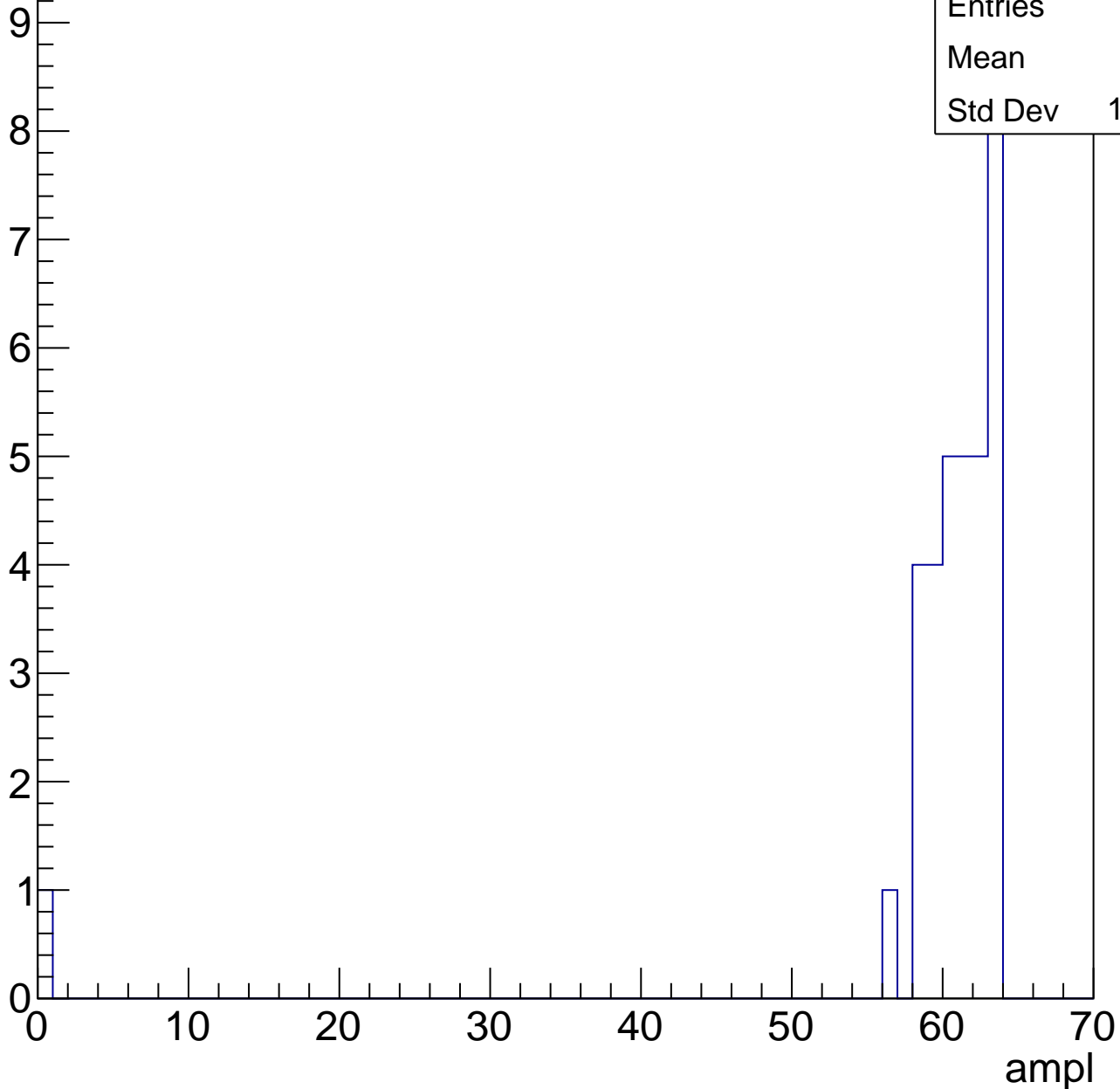


B1L100S, U12-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	59
Std Dev	10.44



B1L100S, U12-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch70, adc0

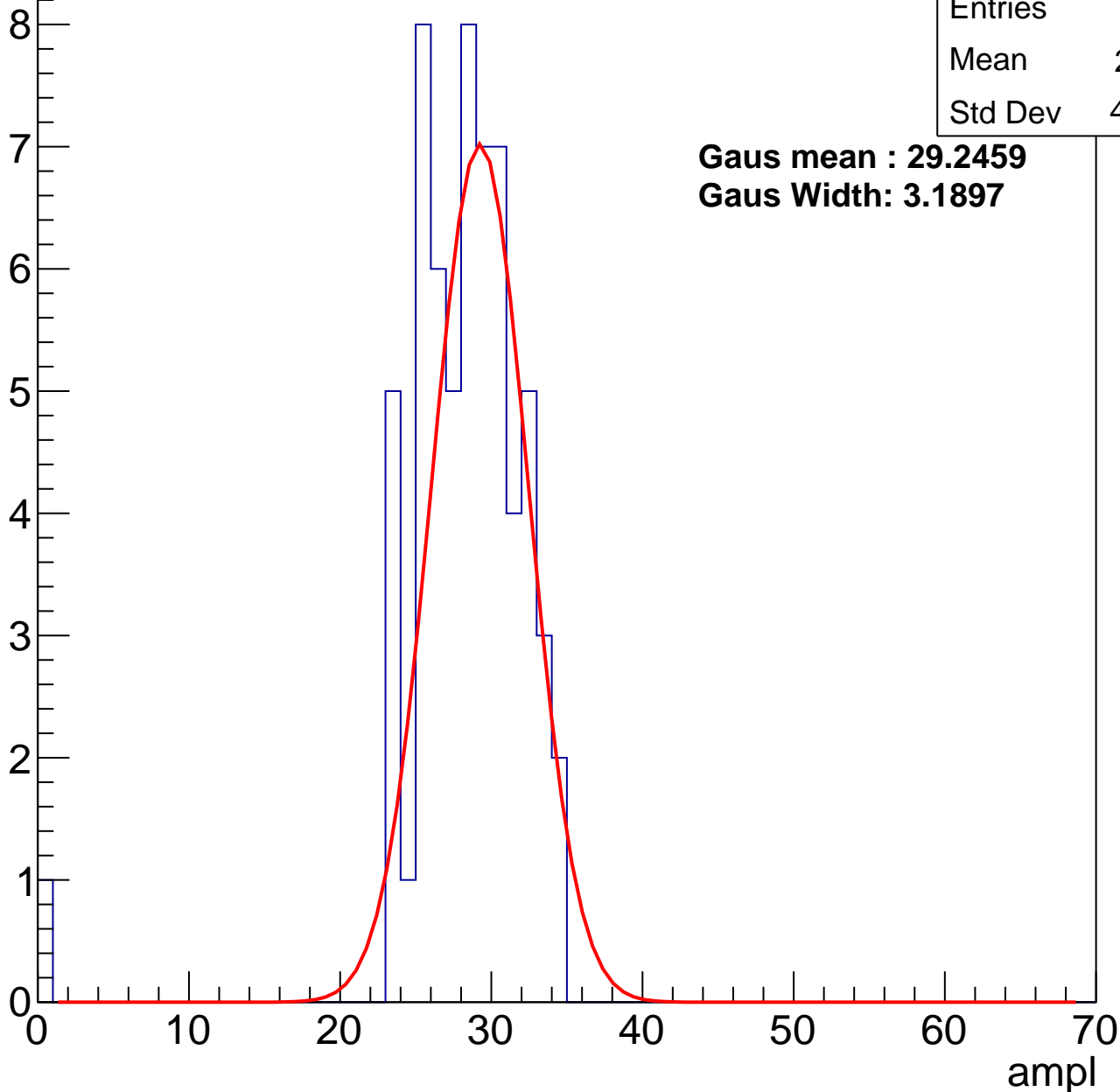
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.71
Std Dev	4.612

Gaus mean : 29.2459

Gaus Width: 3.1897



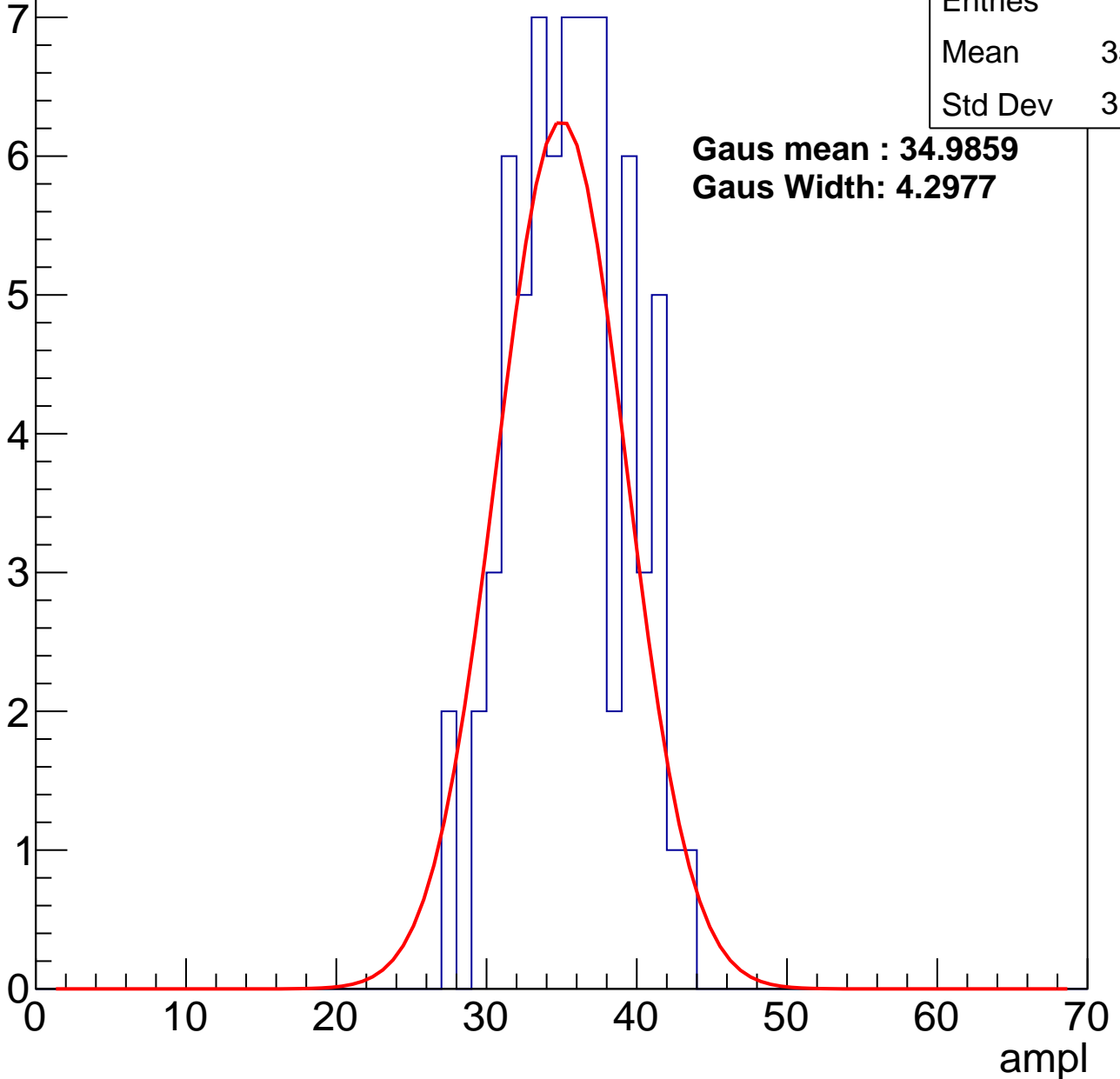
B1L100S, U12-ch70, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	35.13
Std Dev	3.715

Gaus mean : 34.9859
Gaus Width: 4.2977



B1L100S, U12-ch70, adc2

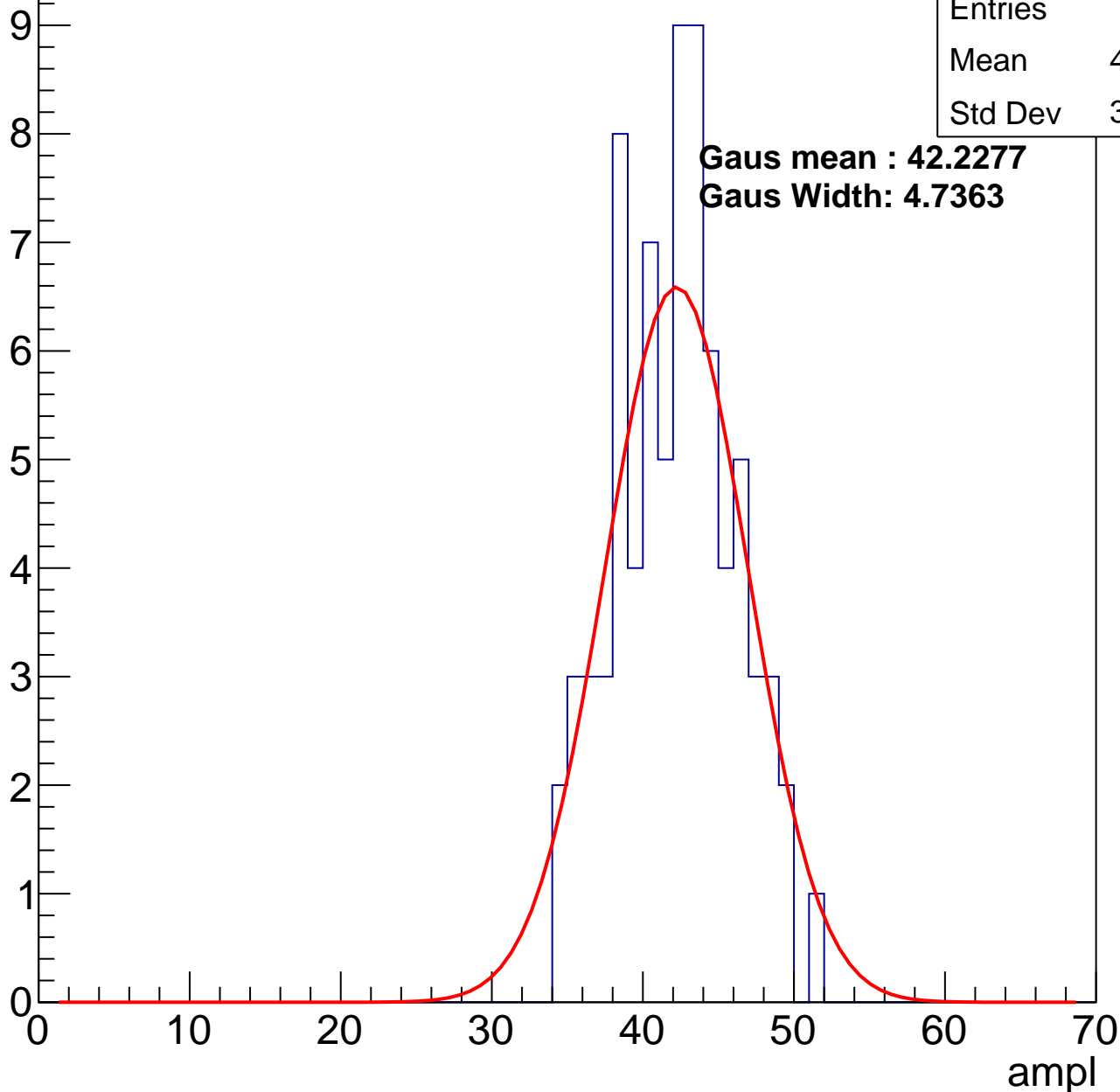
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	41.69
Std Dev	3.879

Gaus mean : 42.2277

Gaus Width: 4.7363

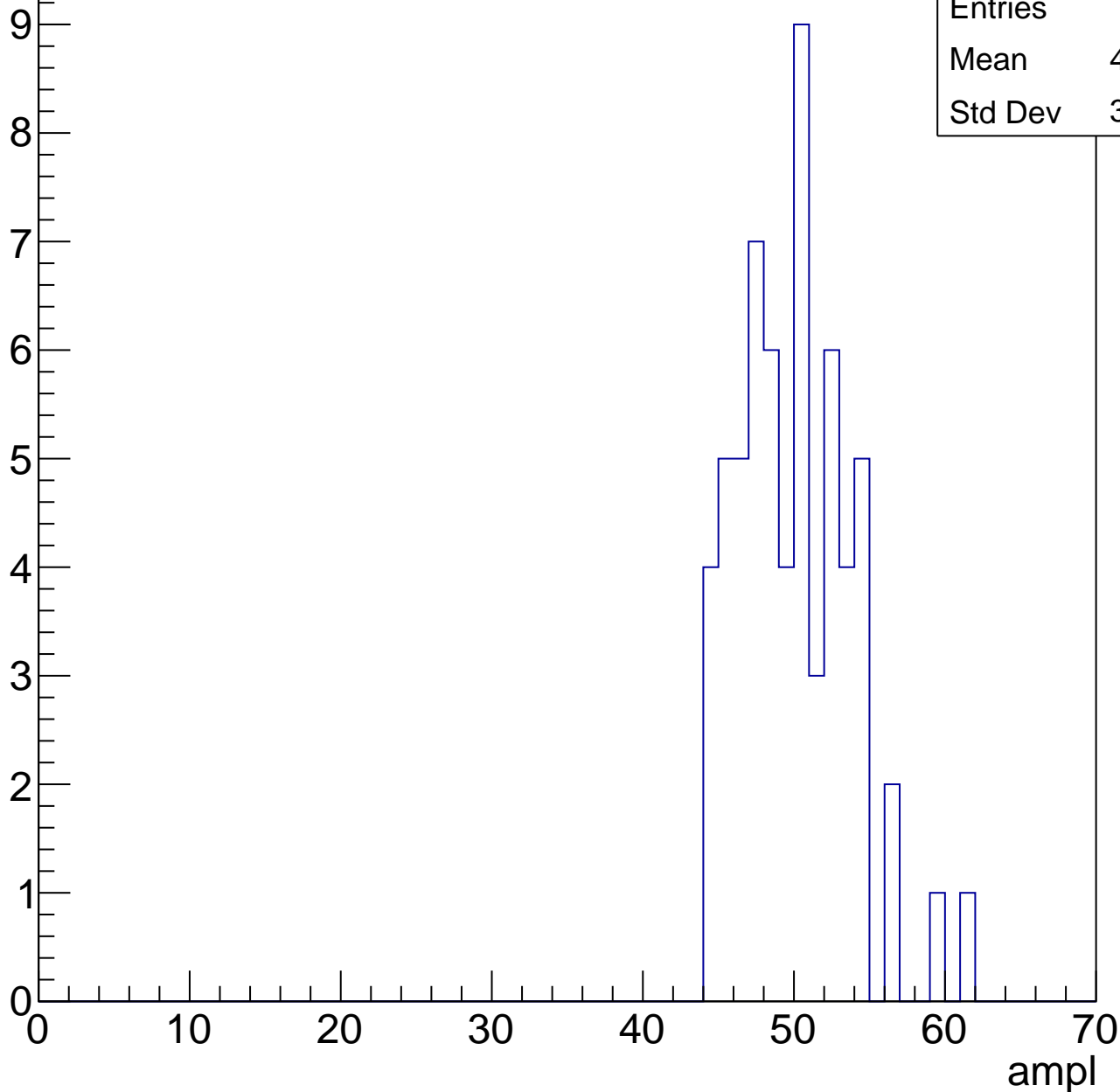


B1L100S, U12-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	49.56
Std Dev	3.692

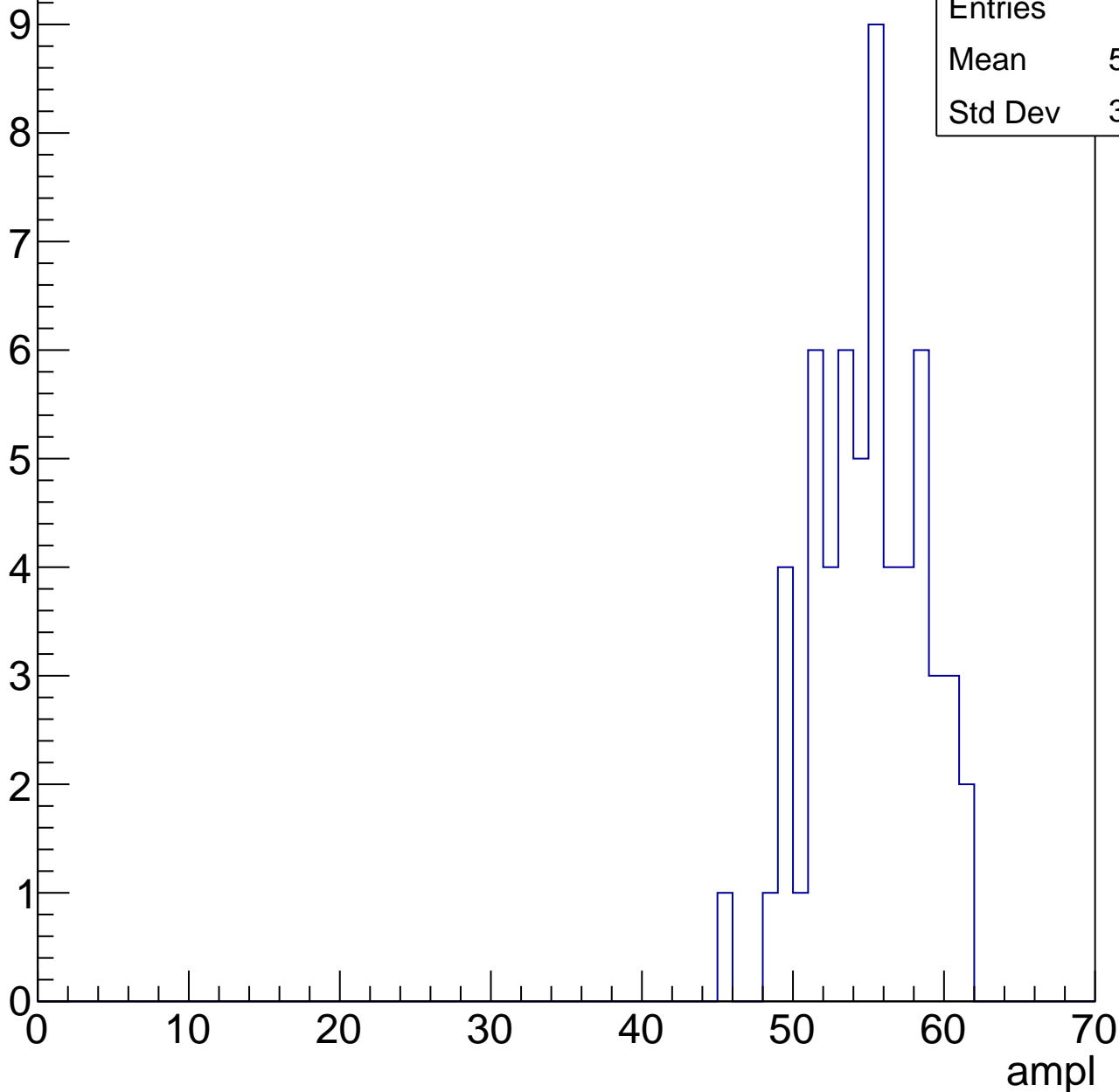


B1L100S, U12-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	54.49
Std Dev	3.524

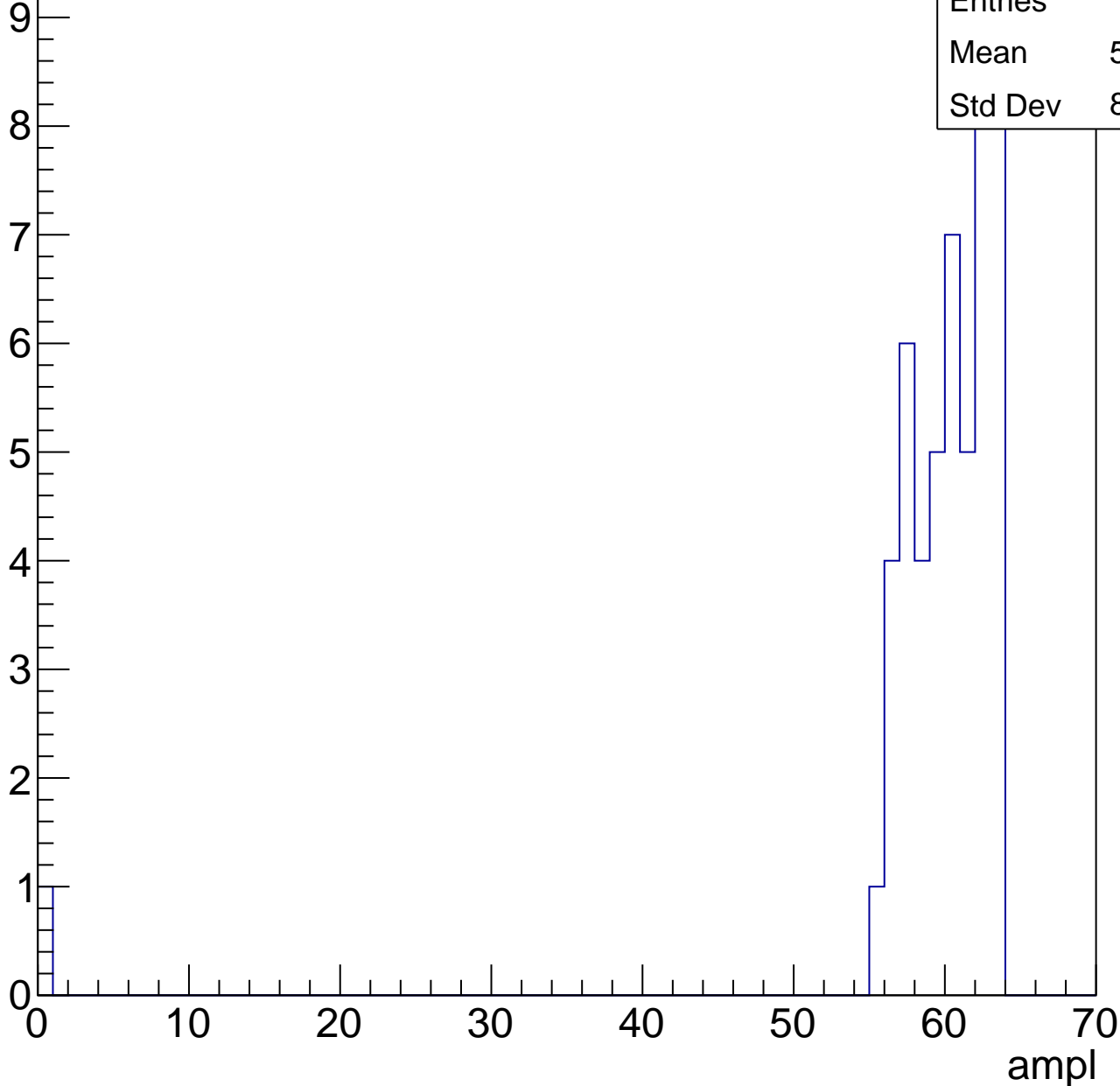


B1L100S, U12-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.72
Std Dev	8.718



B1L100S, U12-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

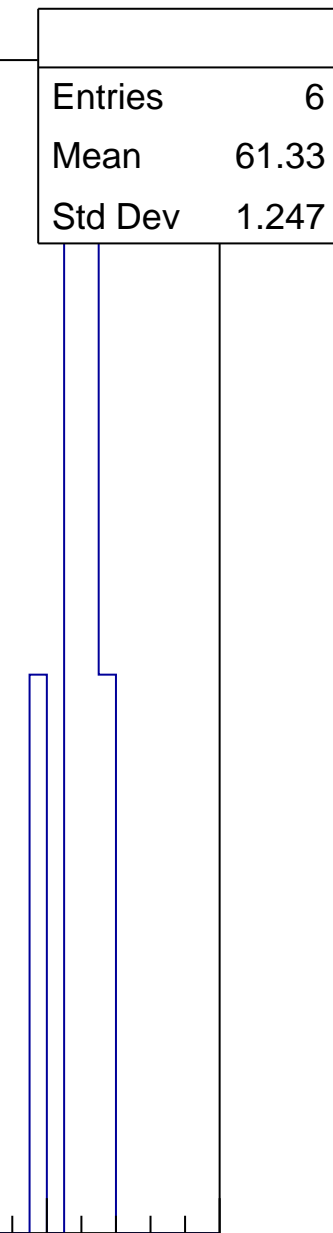
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.247

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch71, adc0

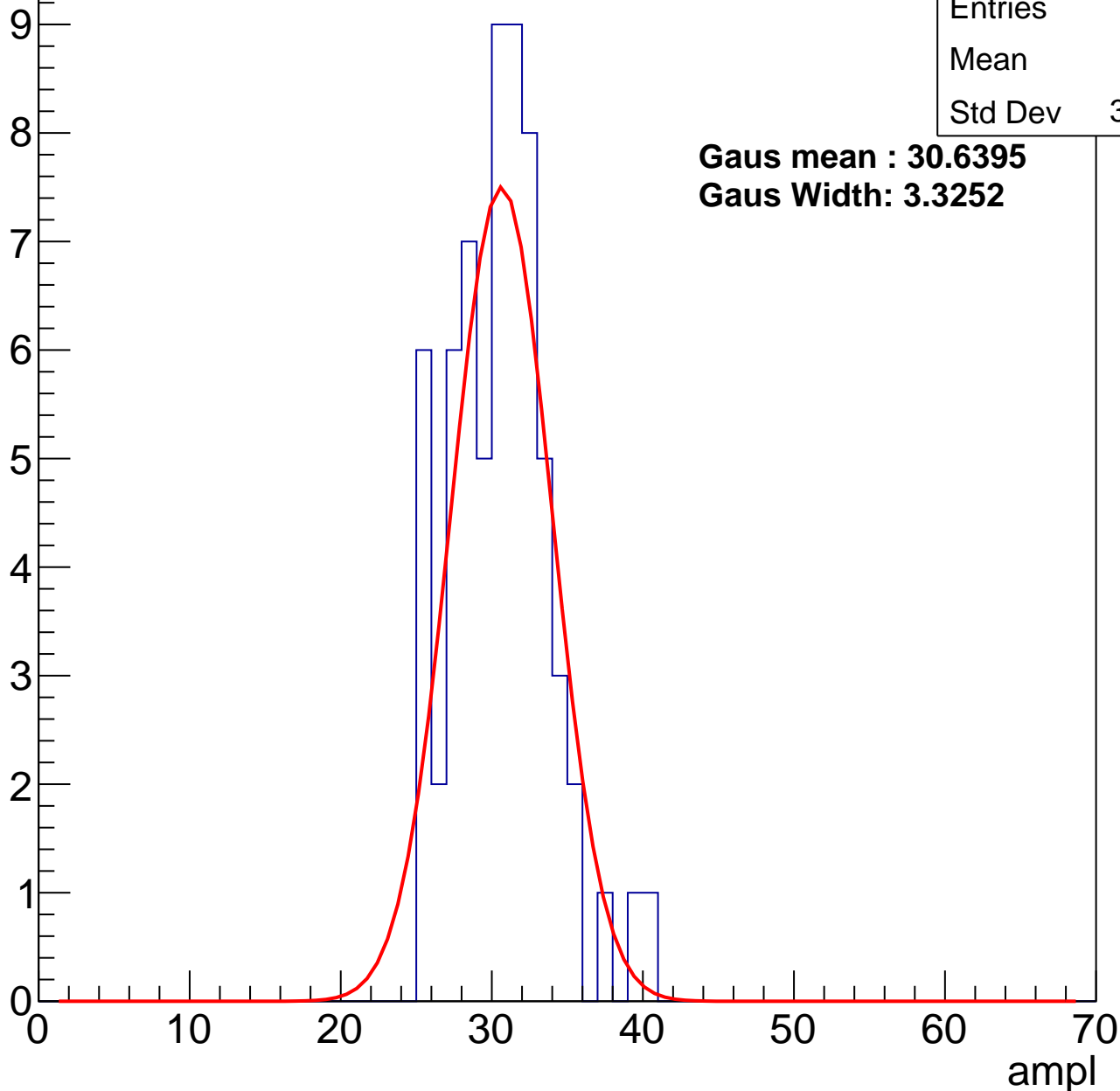
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	30.2
Std Dev	3.235

Gaus mean : 30.6395

Gaus Width: 3.3252



B1L100S, U12-ch71, adc1

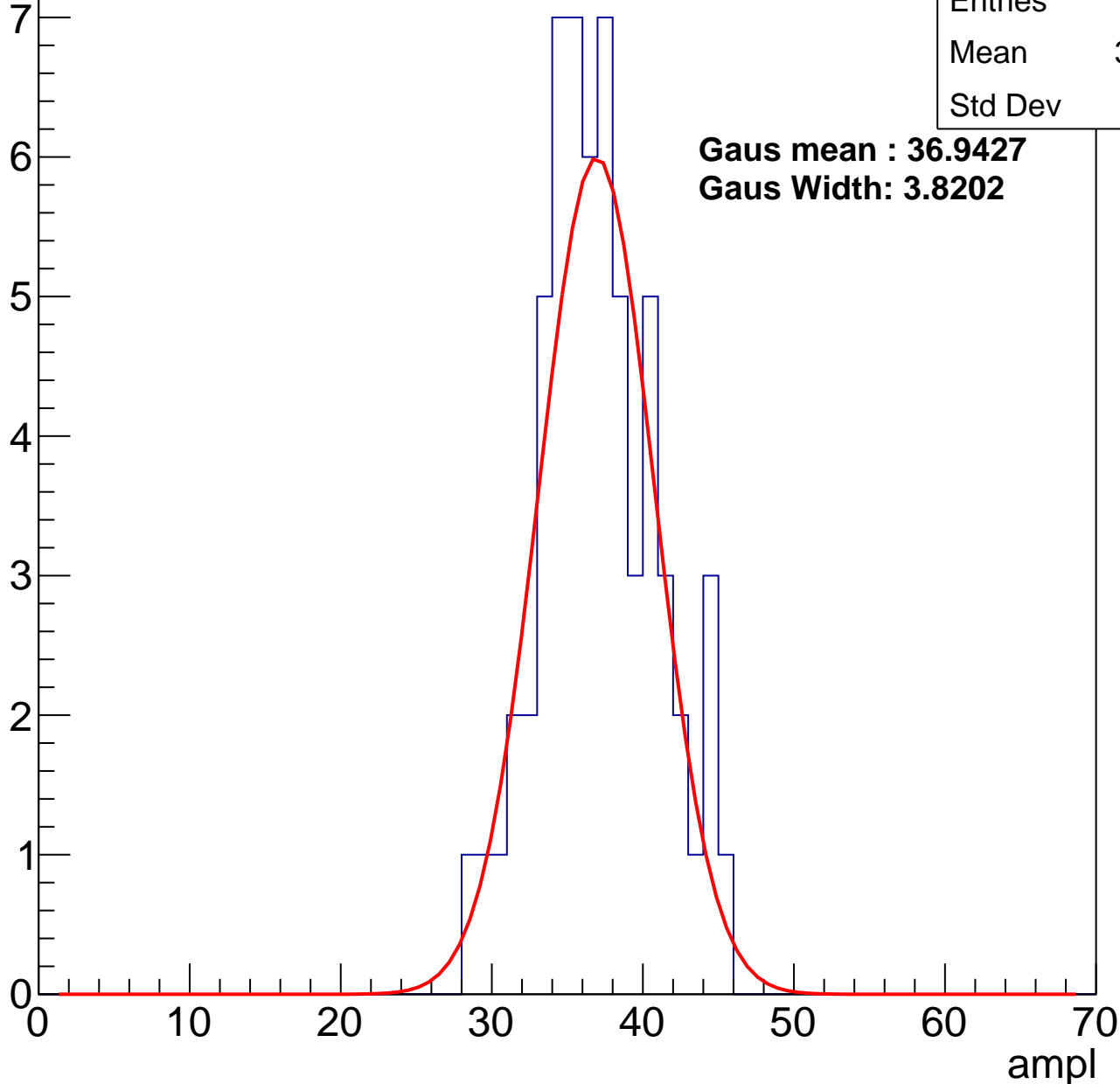
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.61
Std Dev	3.79

Gaus mean : 36.9427

Gaus Width: 3.8202



B1L100S, U12-ch71, adc2

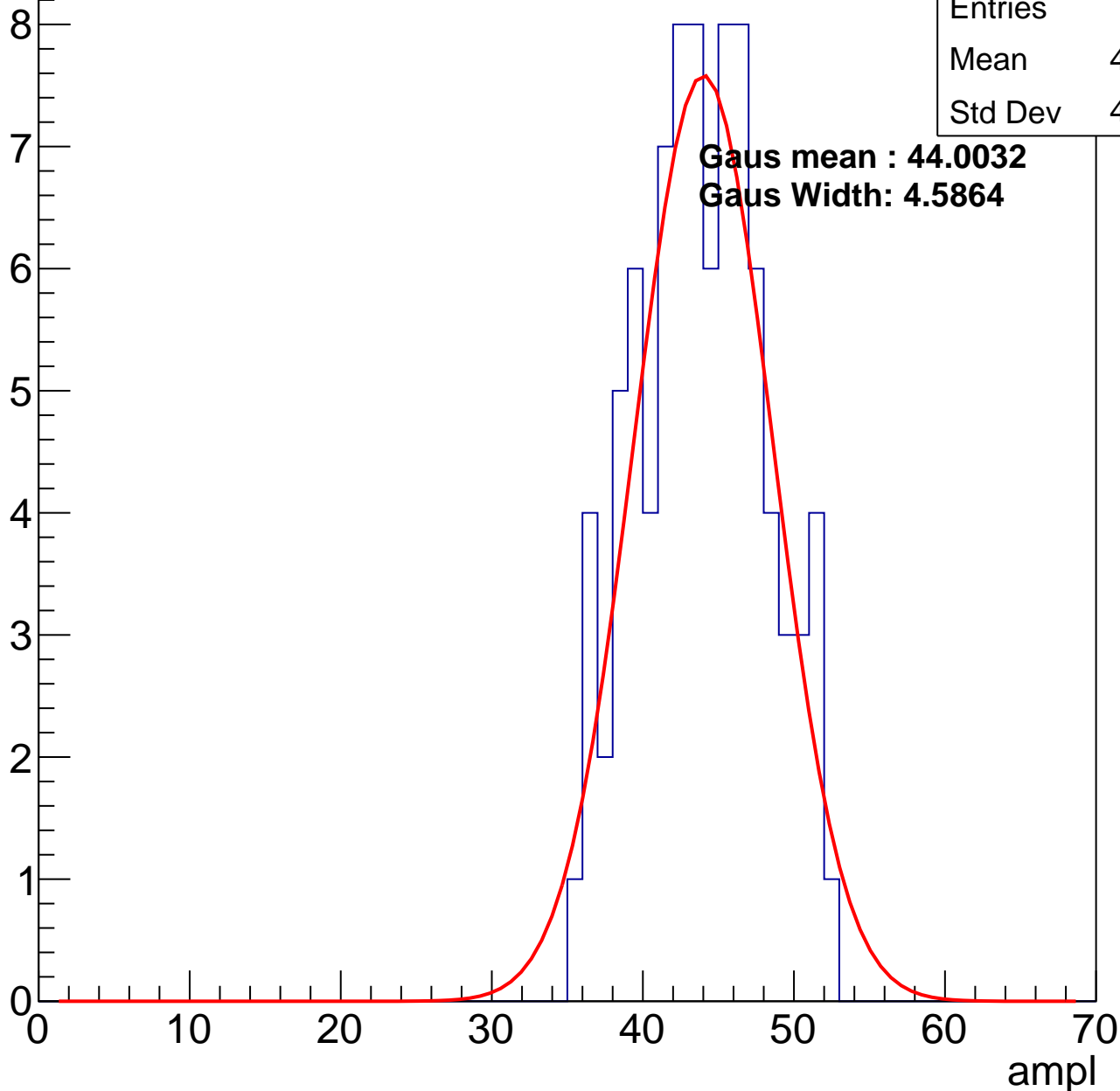
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	88
Mean	43.44
Std Dev	4.136

Gaus mean : 44.0032

Gaus Width: 4.5864

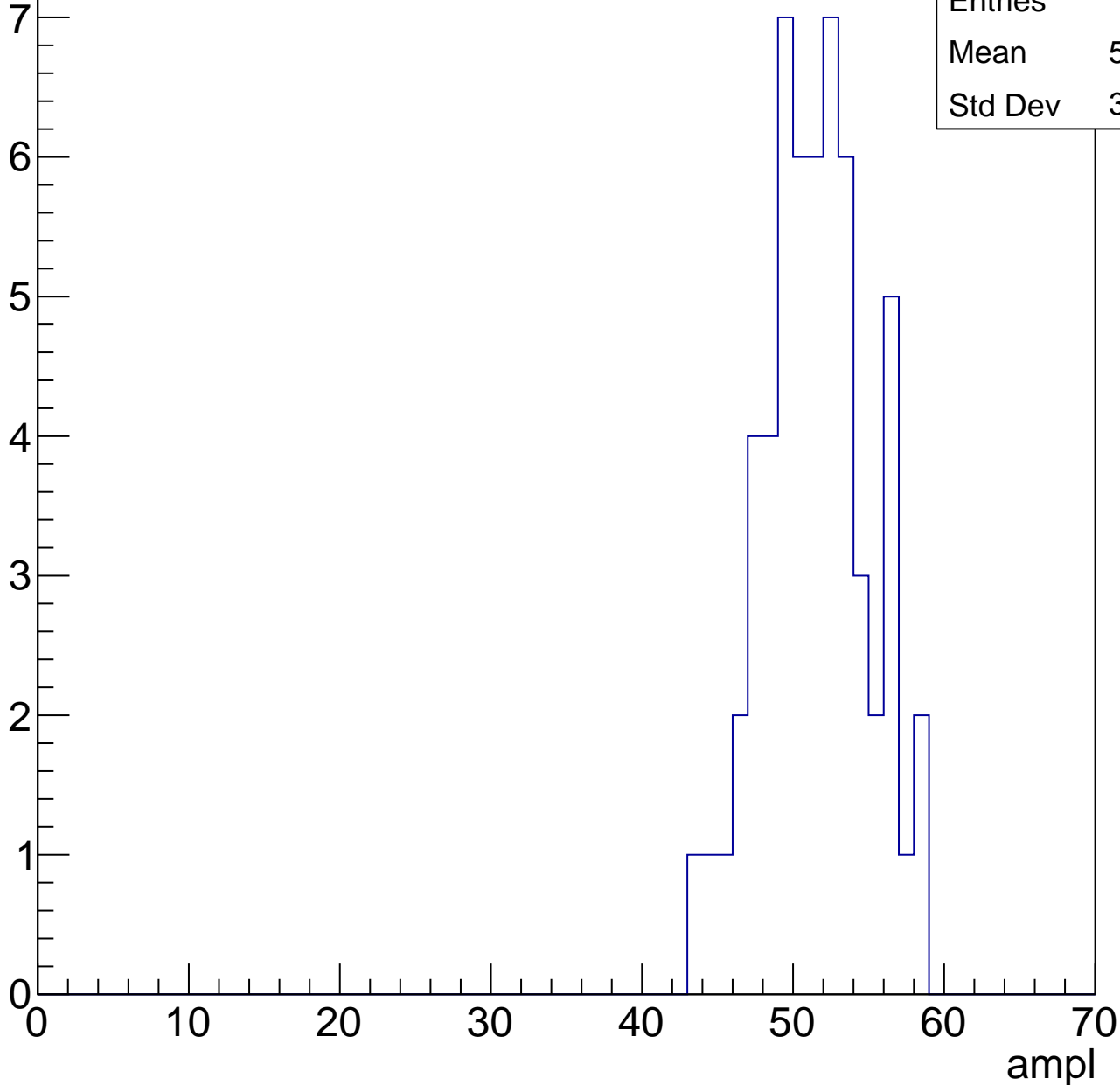


B1L100S, U12-ch71, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

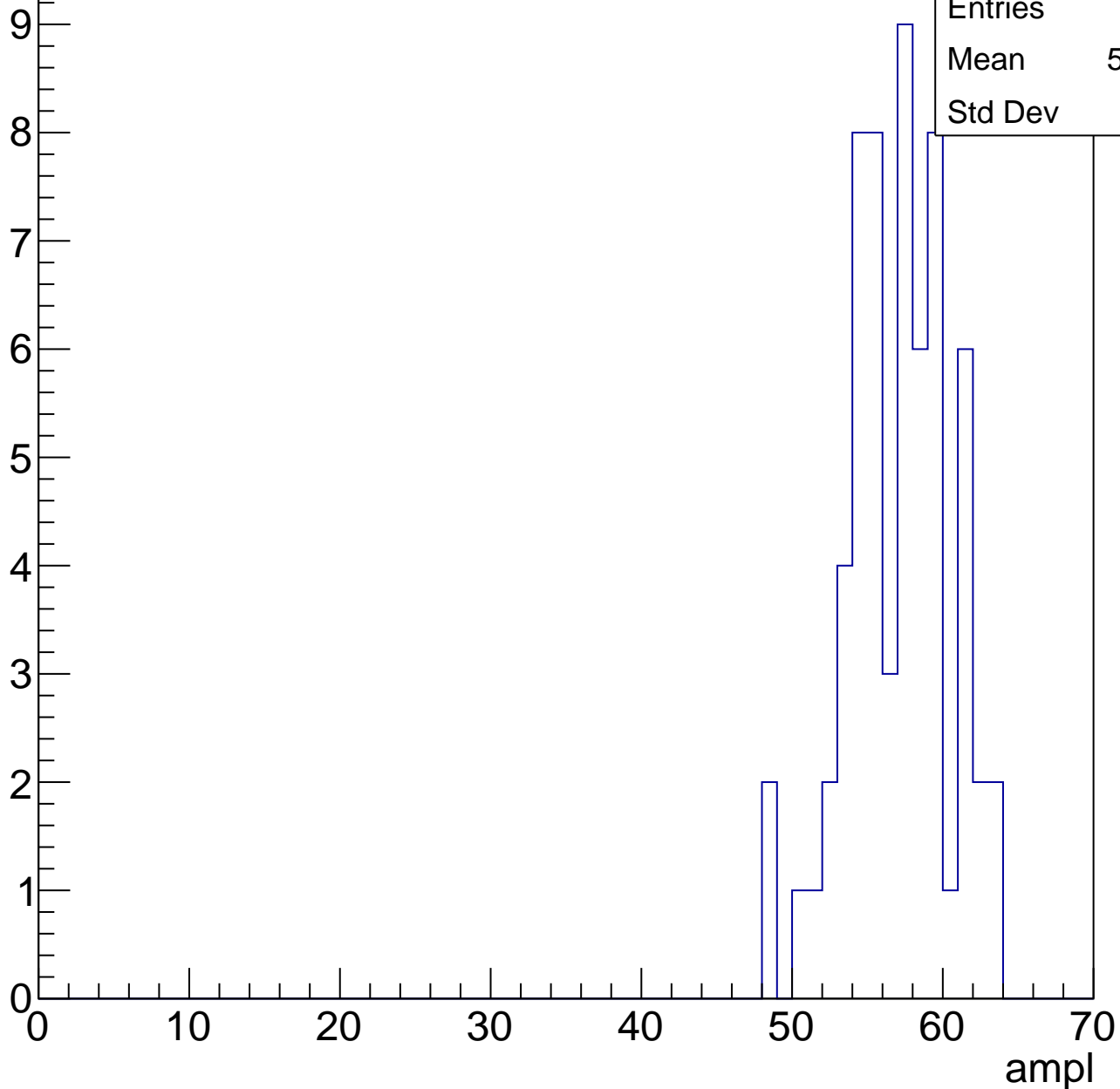
Entries	58
Mean	51.03
Std Dev	3.429



B1L100S, U12-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

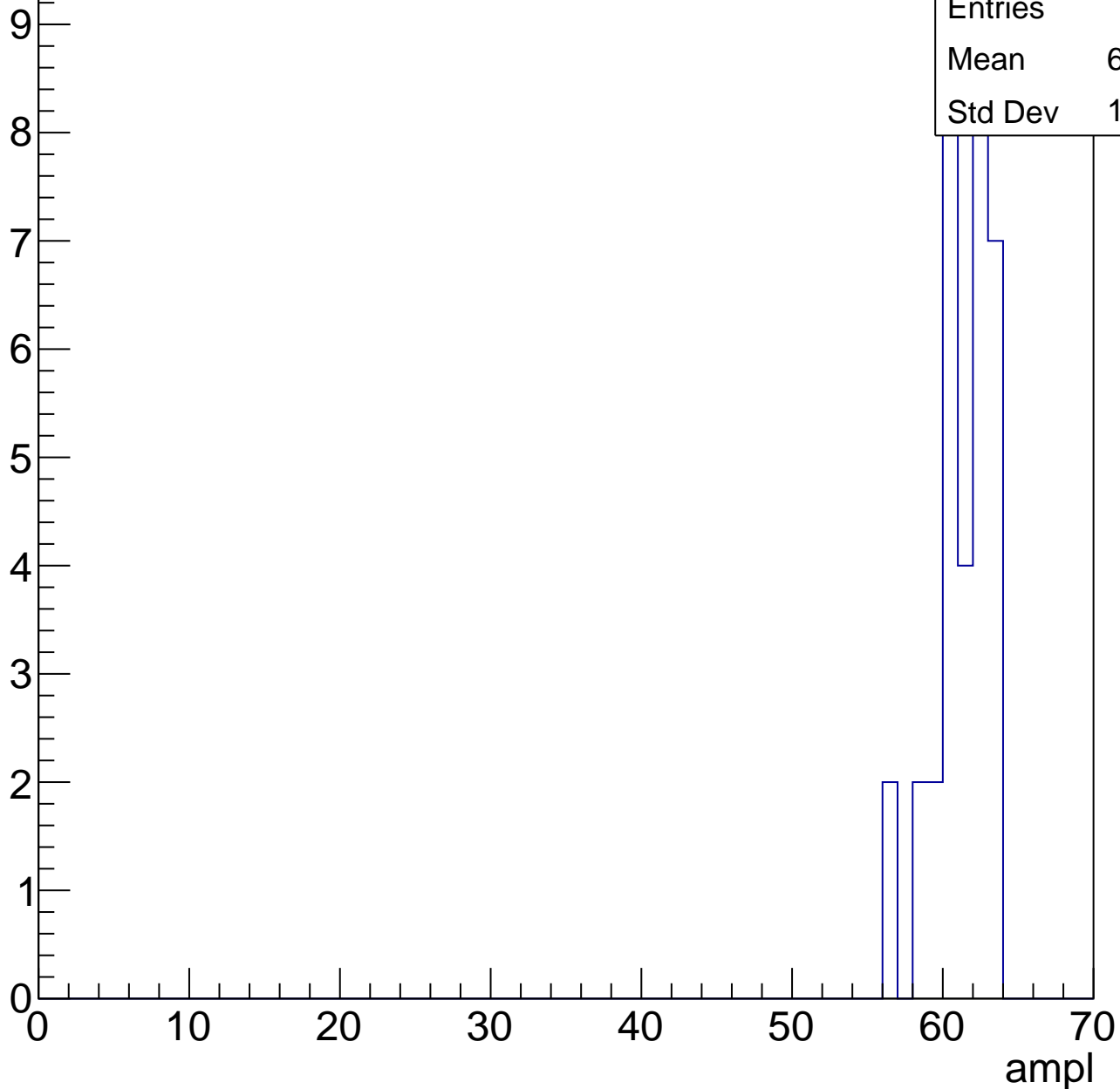


Entries	63
Mean	56.54
Std Dev	3.38

B1L100S, U12-ch71, adc5

calib_packv5_042523_0143.root, FC#4, port A2

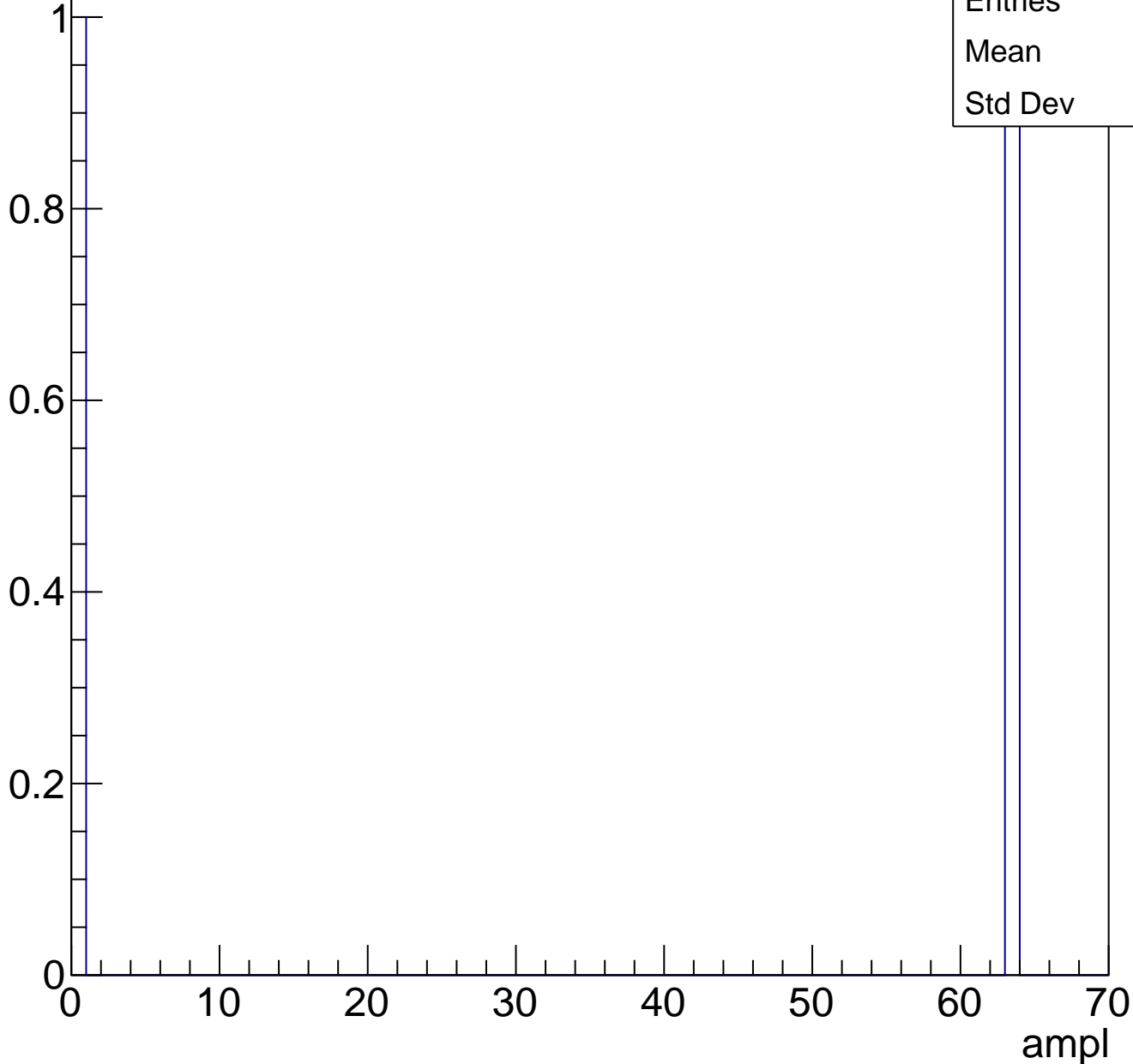
Entry



B1L100S, U12-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch72, adc0

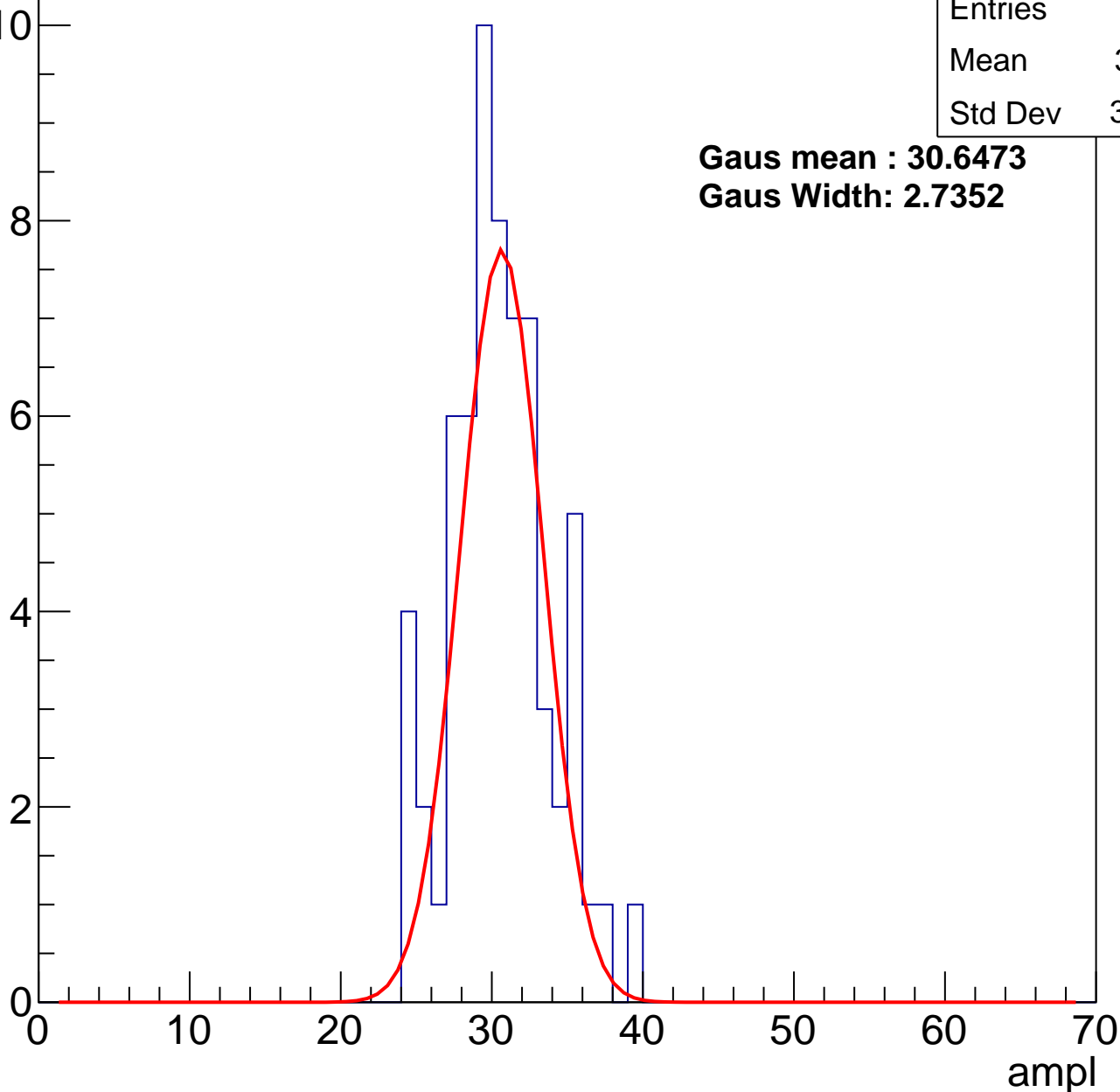
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.11
Std Dev	3.265

Gaus mean : 30.6473

Gaus Width: 2.7352



B1L100S, U12-ch72, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	37.35
Std Dev	3.514

Gaus mean : 37.5706

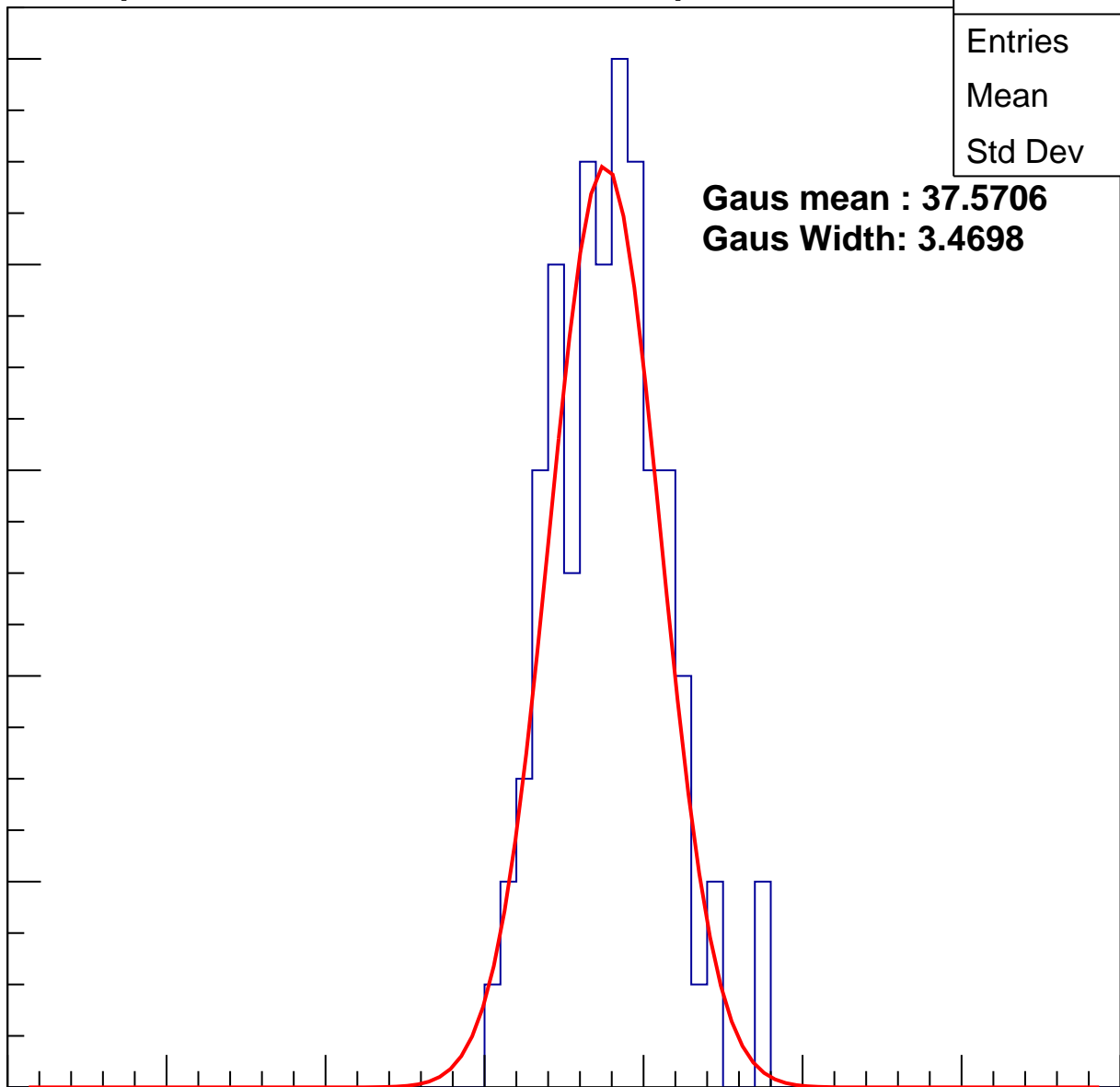
Gaus Width: 3.4698

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch72, adc2

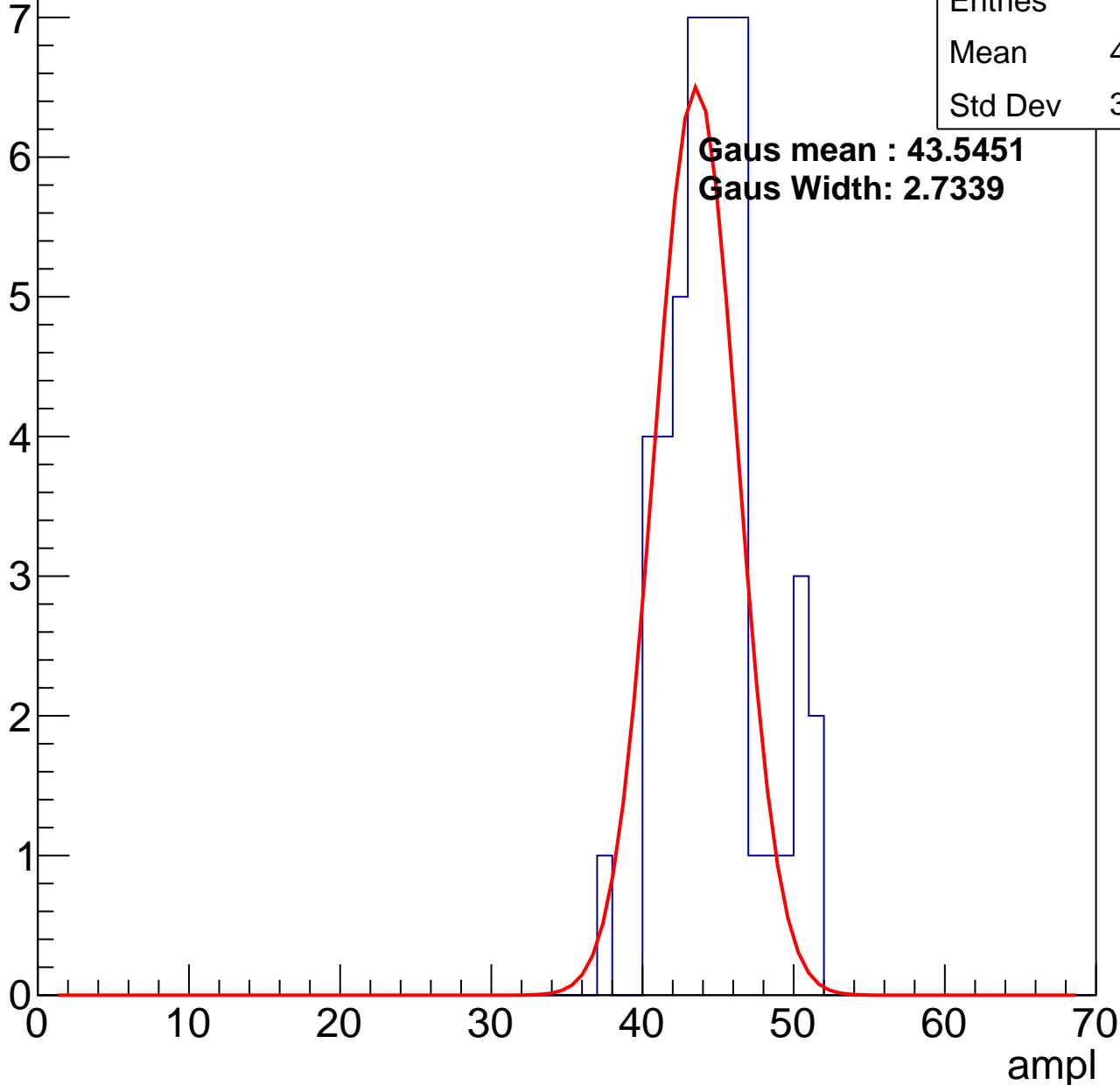
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	44.26
Std Dev	3.045

Gaus mean : 43.5451

Gaus Width: 2.7339

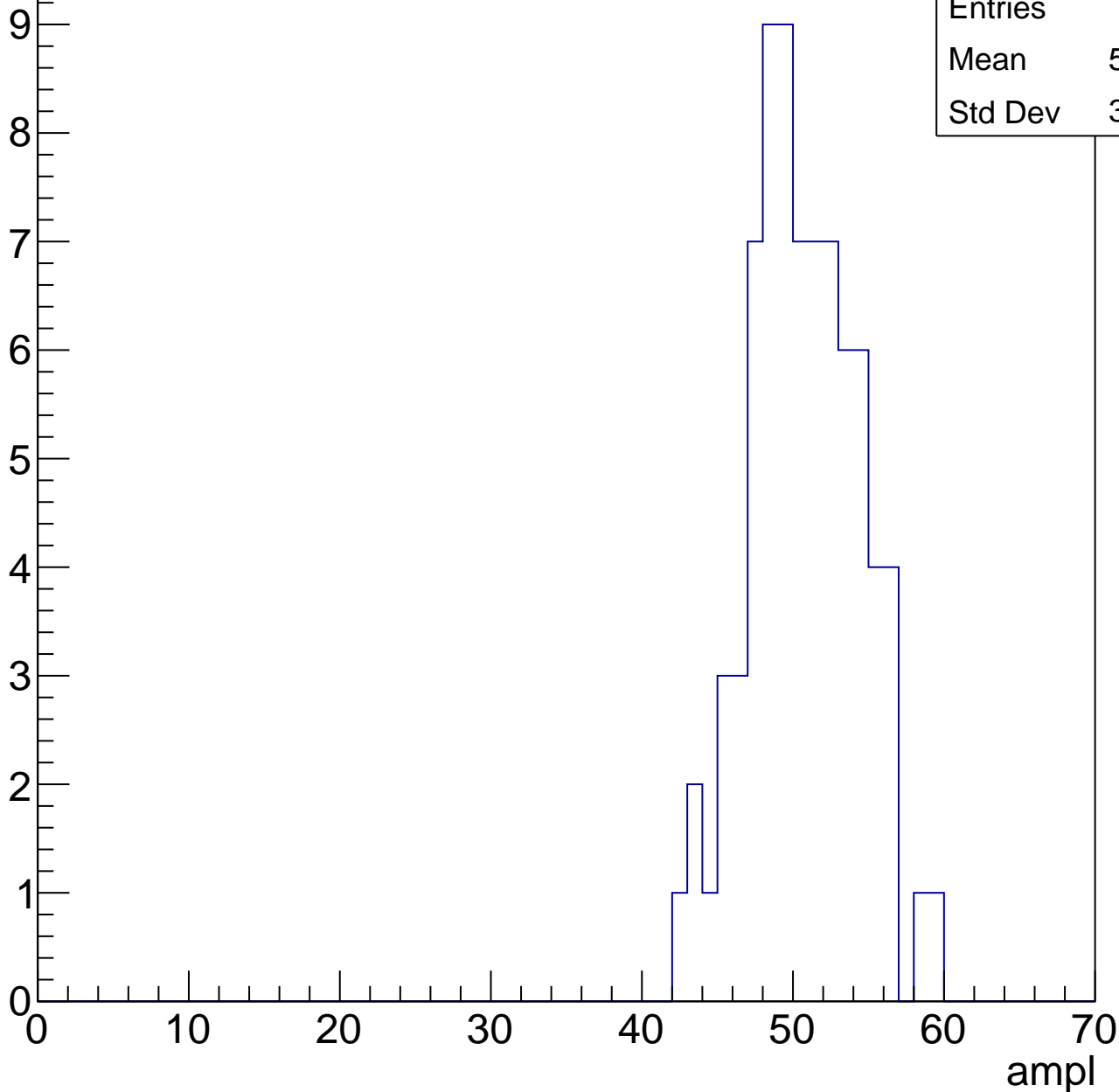


B1L100S, U12-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	50.27
Std Dev	3.576



B1L100S, U12-ch72, adc4

calib_packv5_042523_0143.root, FC#4, port A2

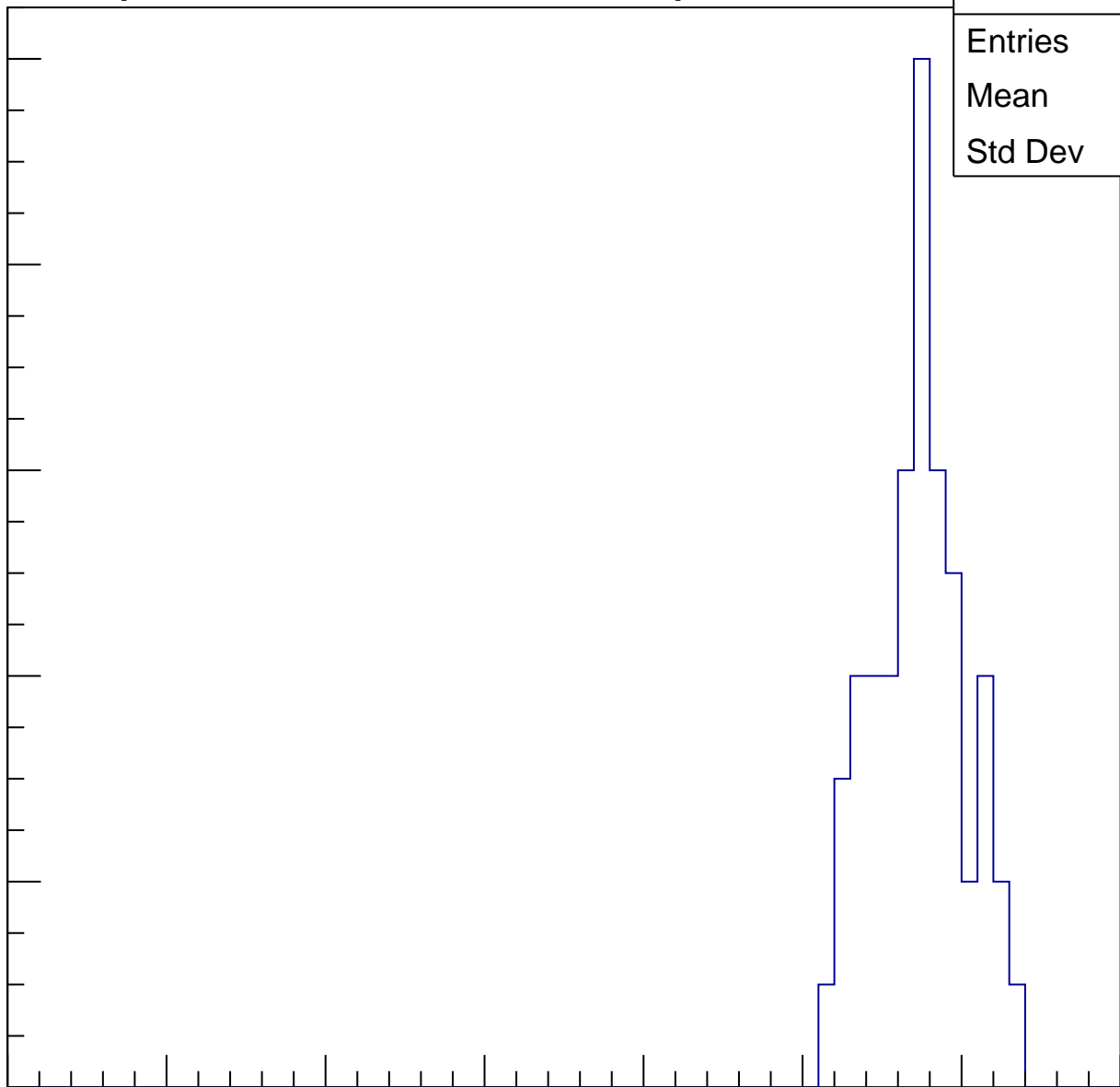
Entries	52
Mean	56.83
Std Dev	2.86

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

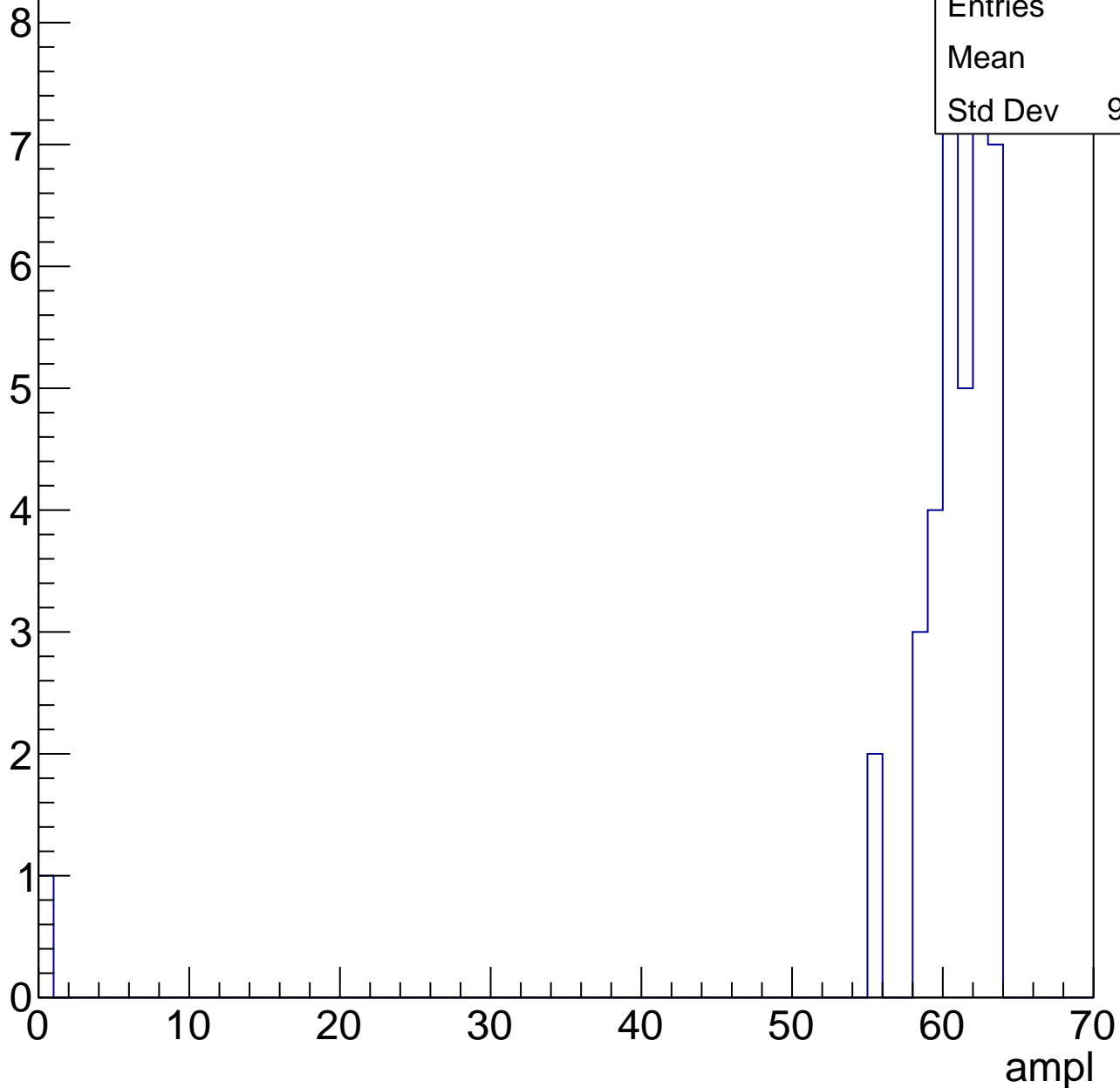


B1L100S, U12-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	59
Std Dev	9.905



B1L100S, U12-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U12-ch73, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	29.87
Std Dev	4.62

Gaus mean : 30.5598

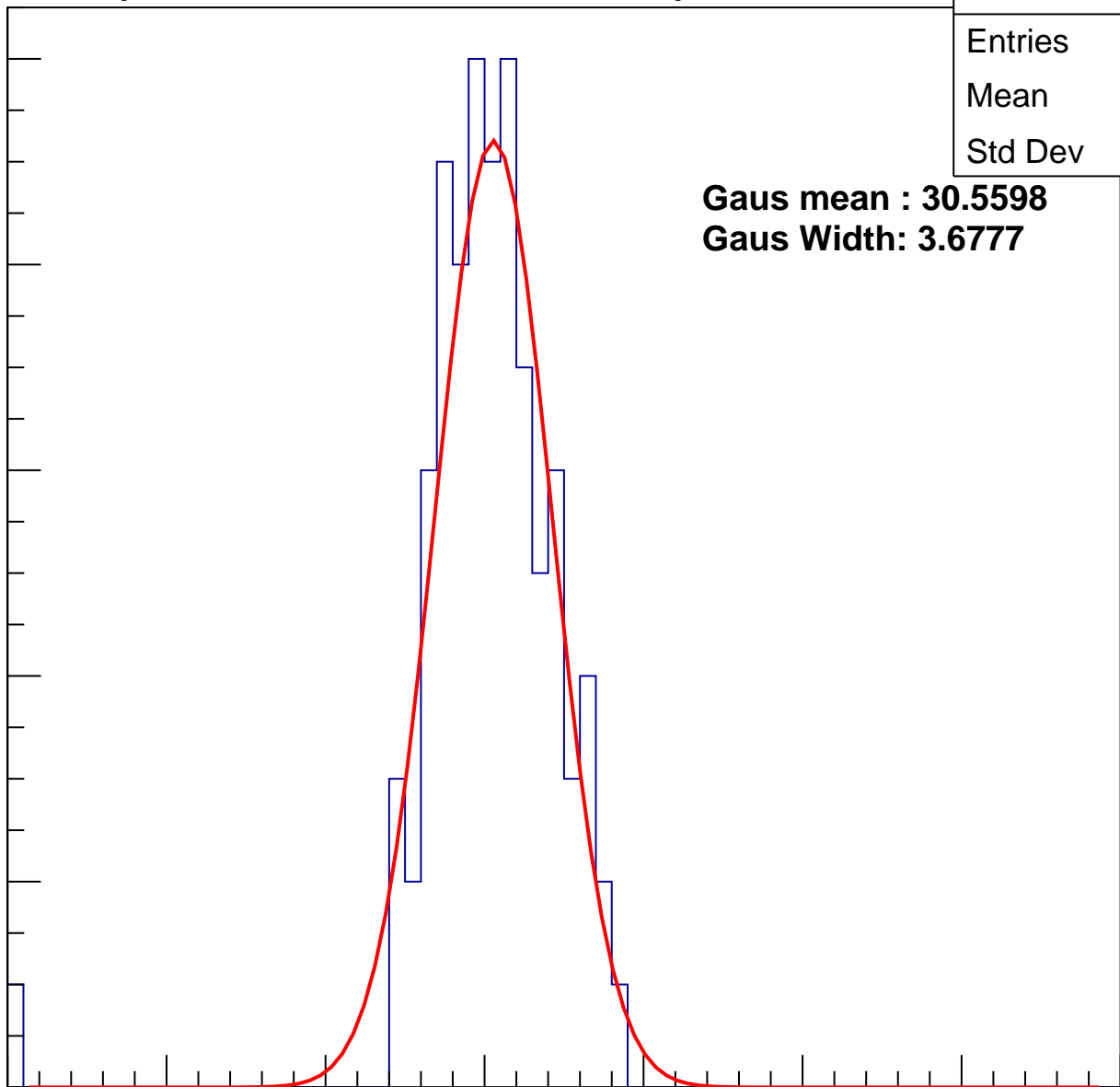
Gaus Width: 3.6777

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch73, adc1

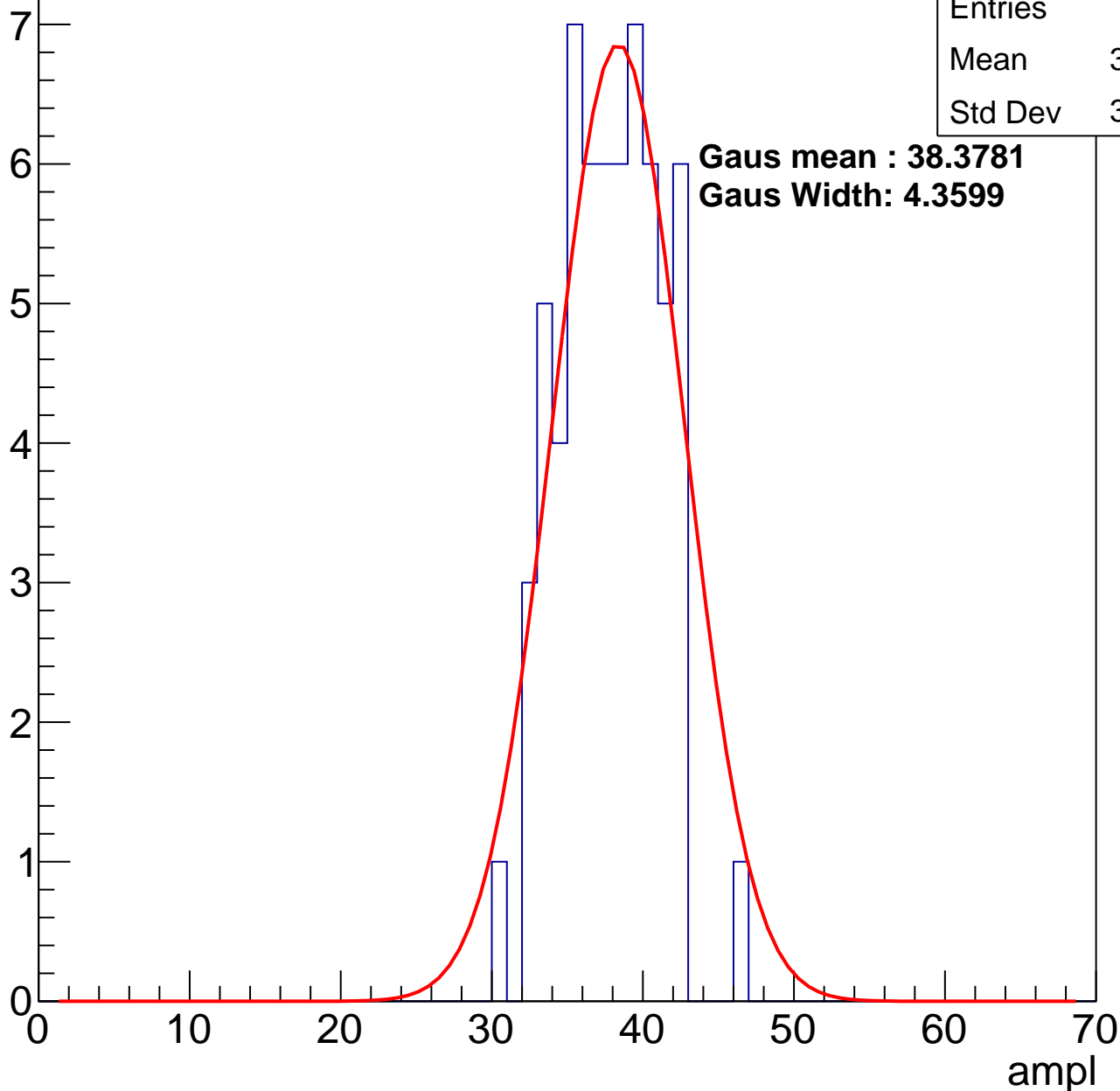
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.37
Std Dev	3.248

Gaus mean : 38.3781

Gaus Width: 4.3599



B1L100S, U12-ch73, adc2

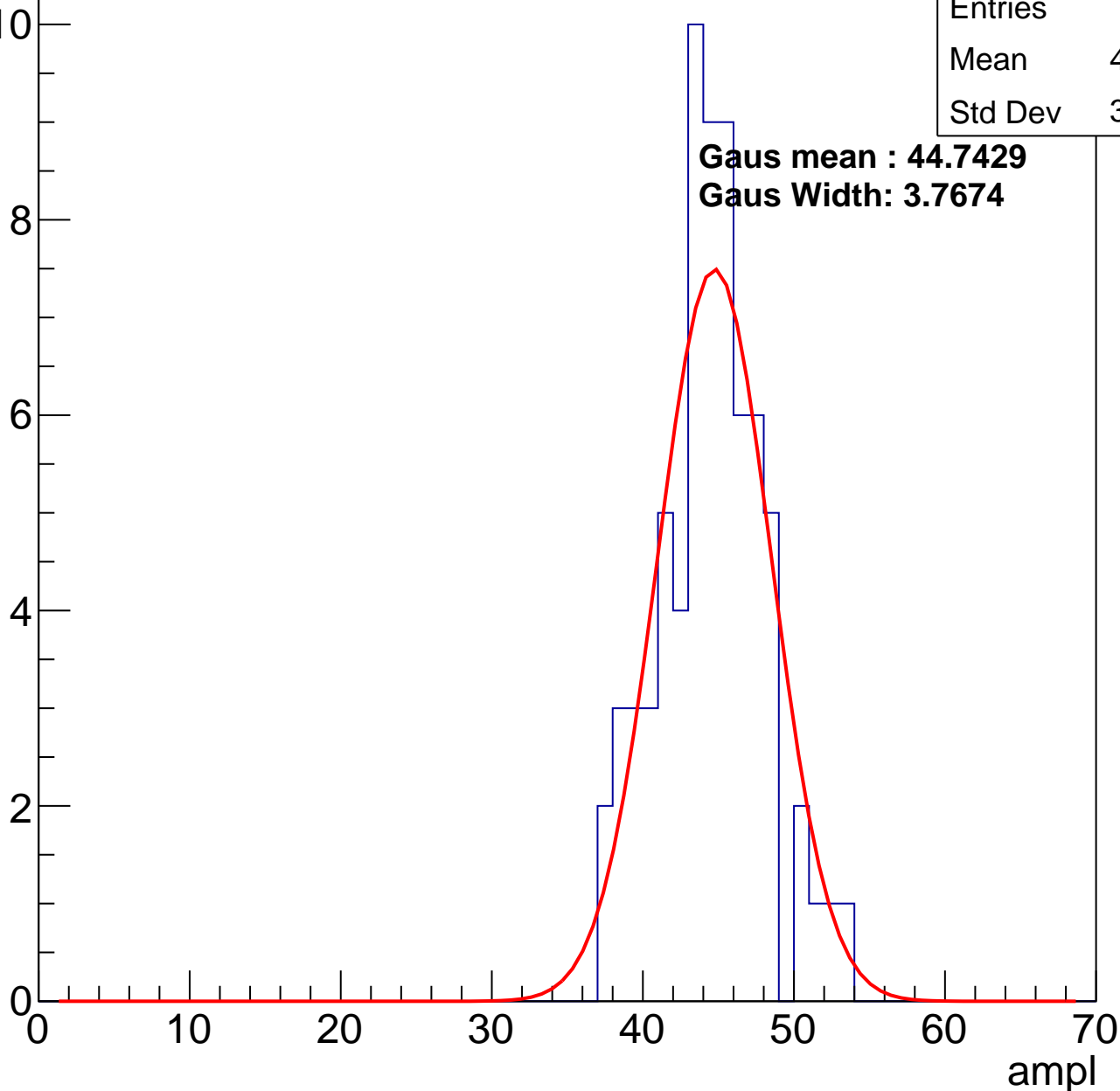
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	44.04
Std Dev	3.449

Gaus mean : 44.7429

Gaus Width: 3.7674



B1L100S, U12-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	62
Mean	50.66
Std Dev	3.587

Entry

10

8

6

4

2

0

0

10

20

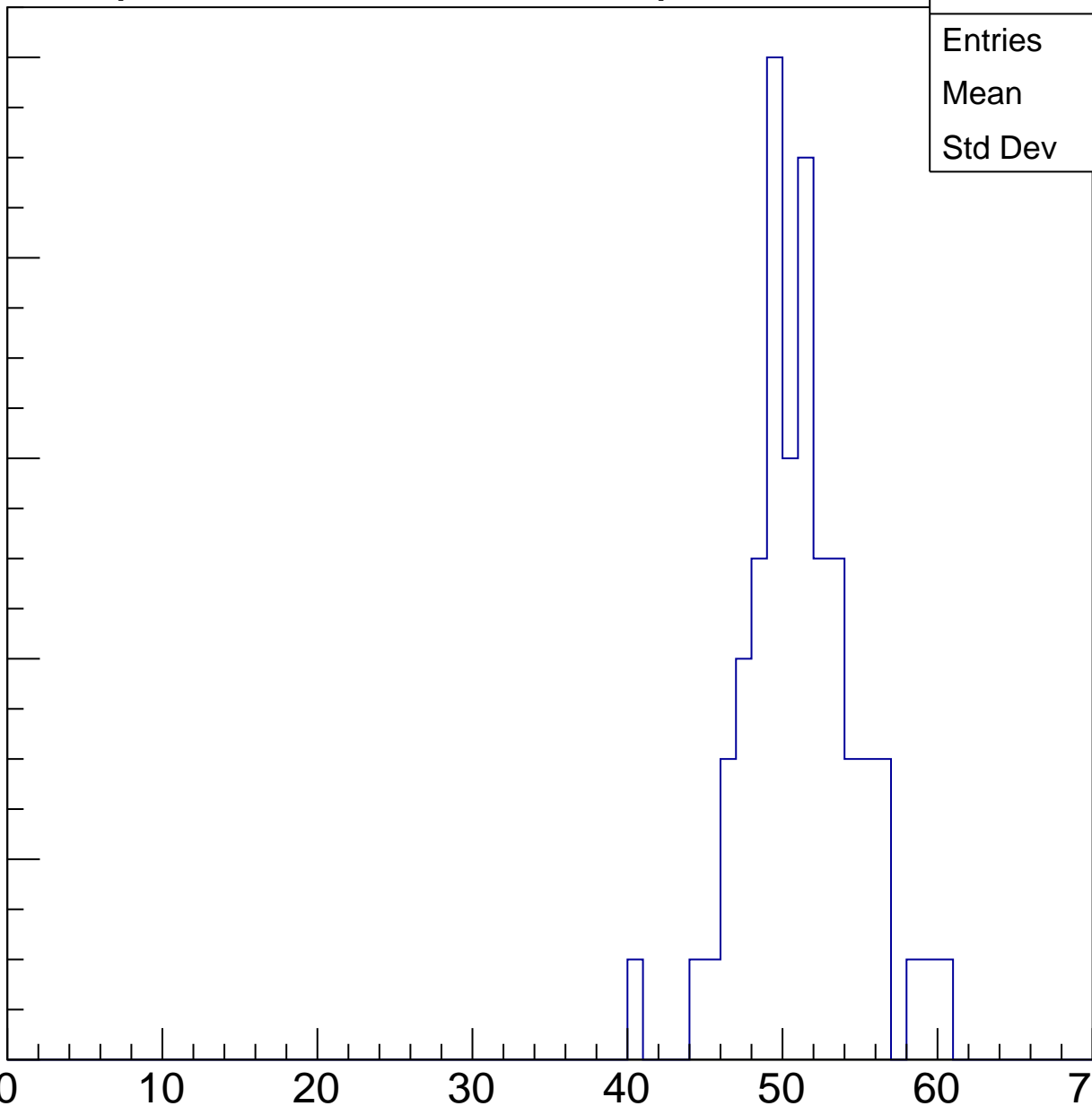
30

40

50

60

ampl

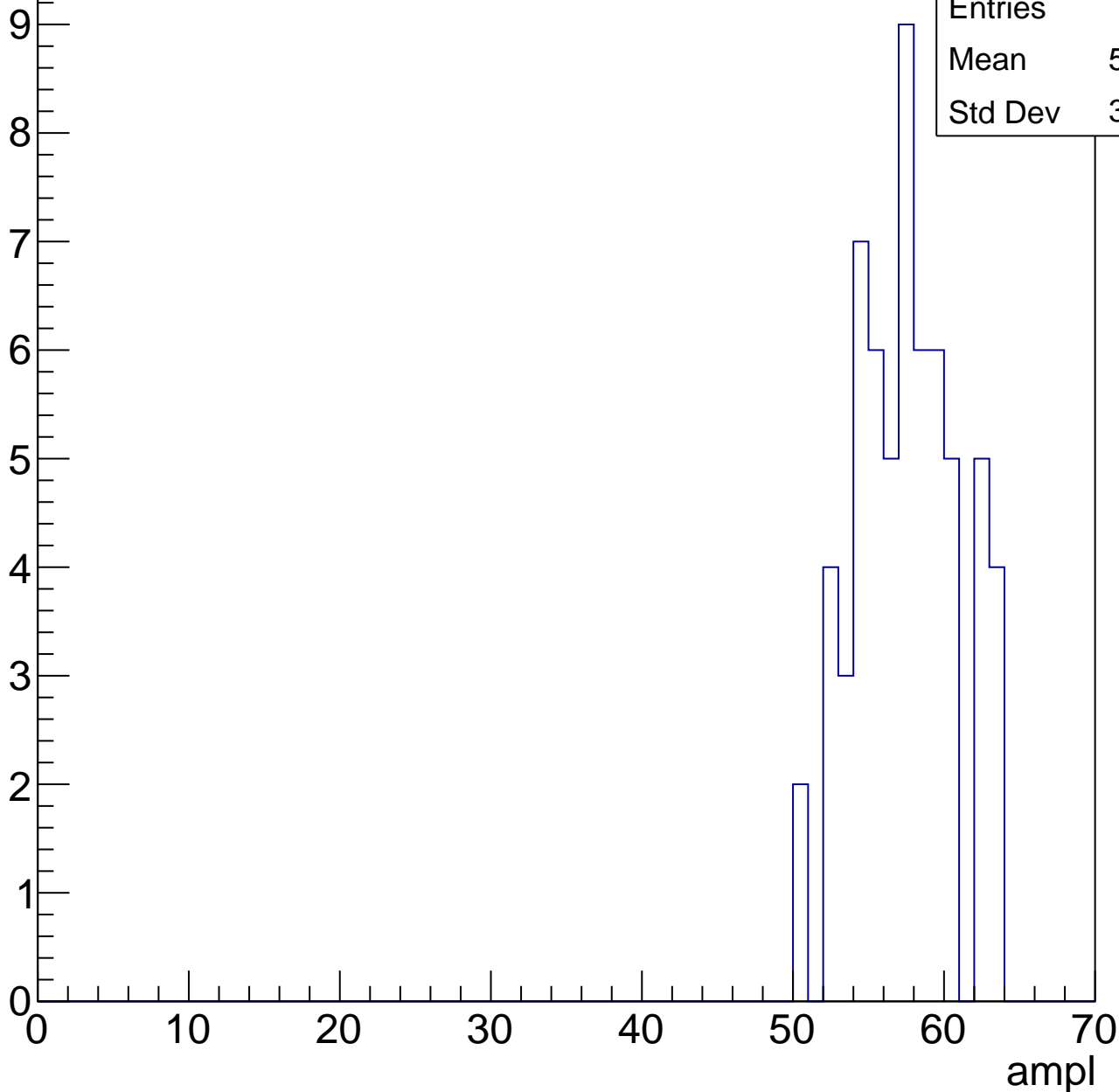


B1L100S, U12-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	56.97
Std Dev	3.316

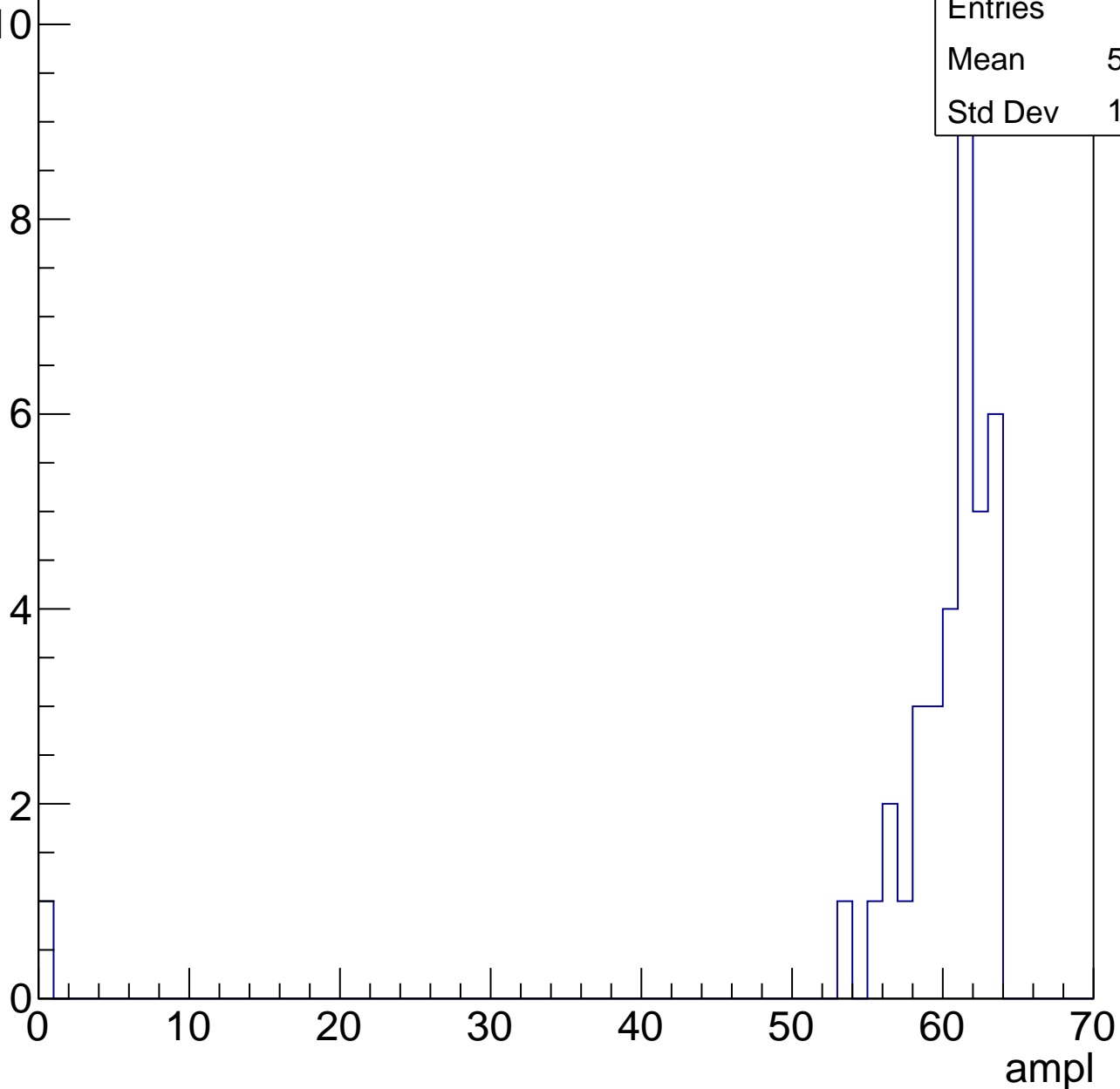


B1L100S, U12-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	58.54
Std Dev	10.05



B1L100S, U12-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch74, adc0

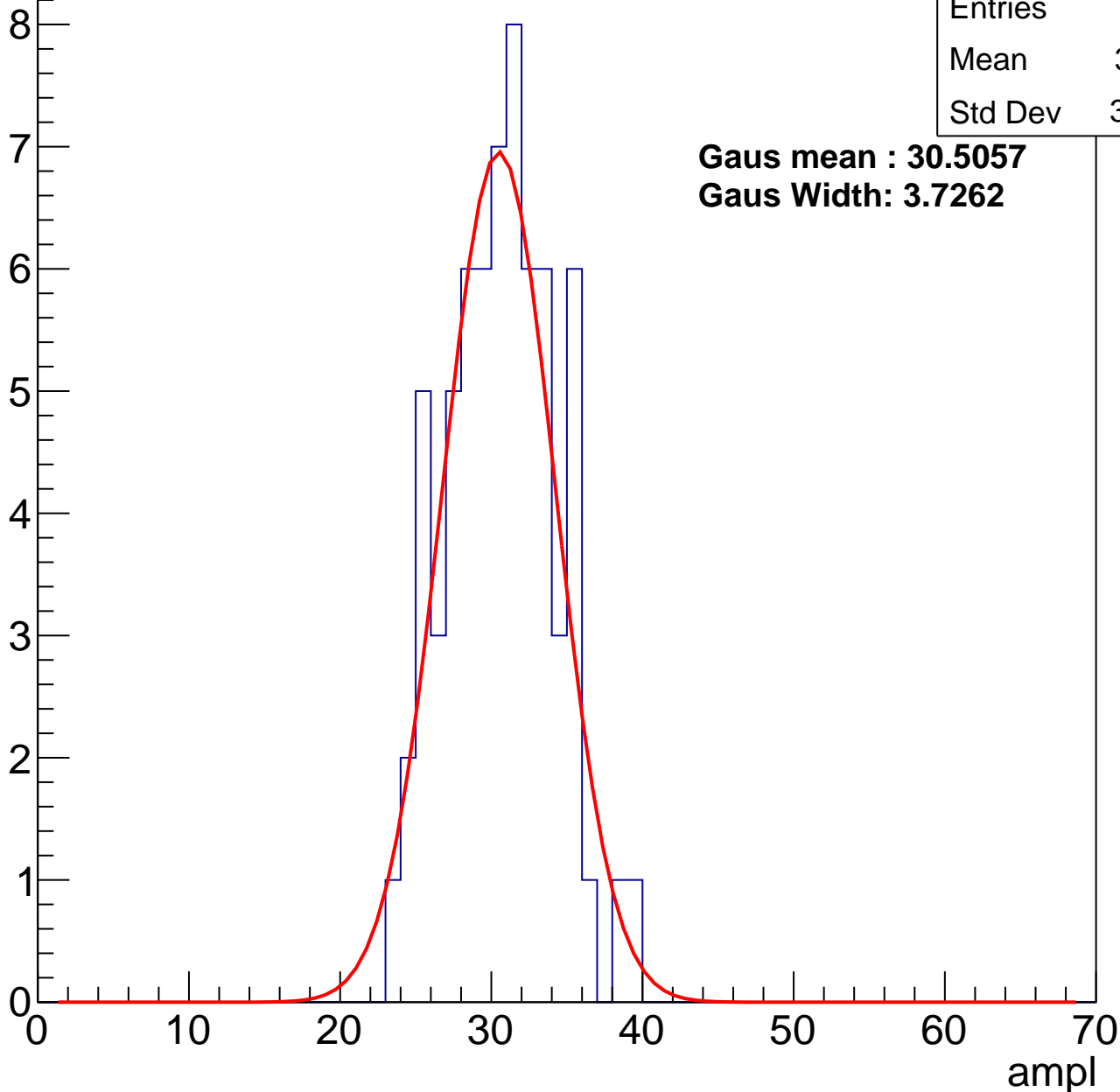
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.21
Std Dev	3.522

Gaus mean : 30.5057

Gaus Width: 3.7262



B1L100S, U12-ch74, adc1

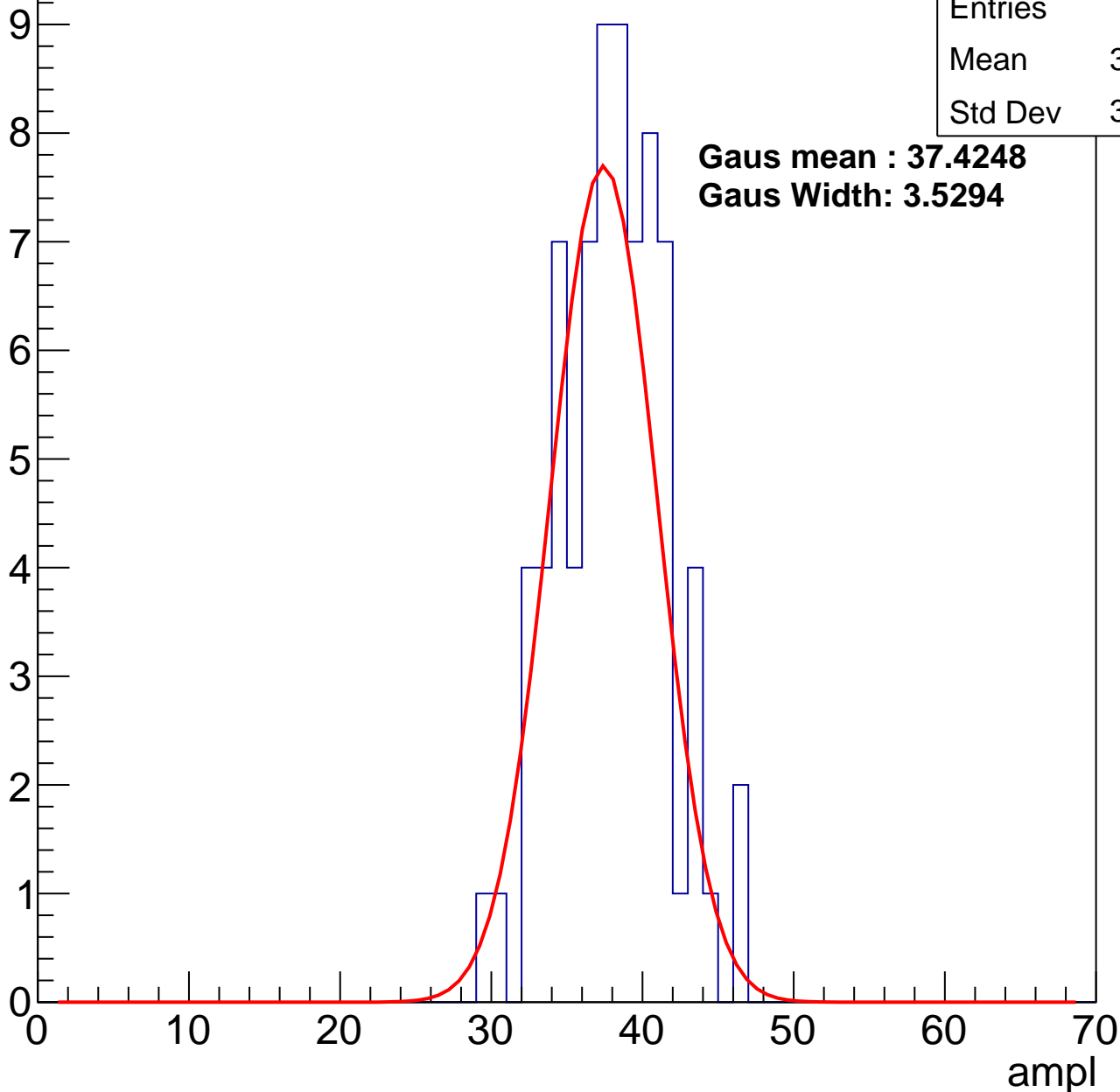
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	37.55
Std Dev	3.518

Gaus mean : 37.4248

Gaus Width: 3.5294



B1L100S, U12-ch74, adc2

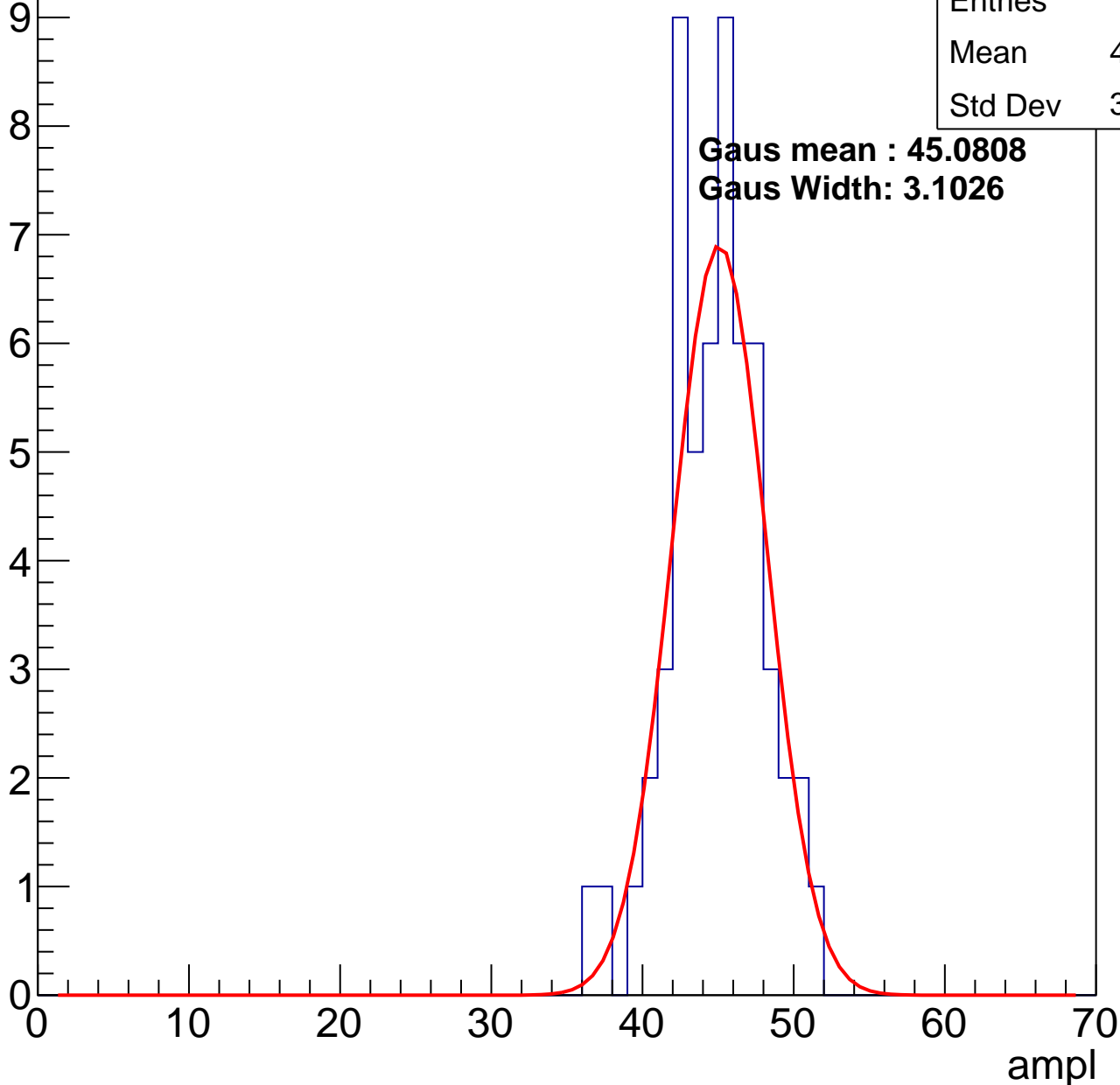
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	44.35
Std Dev	3.069

Gaus mean : 45.0808

Gaus Width: 3.1026

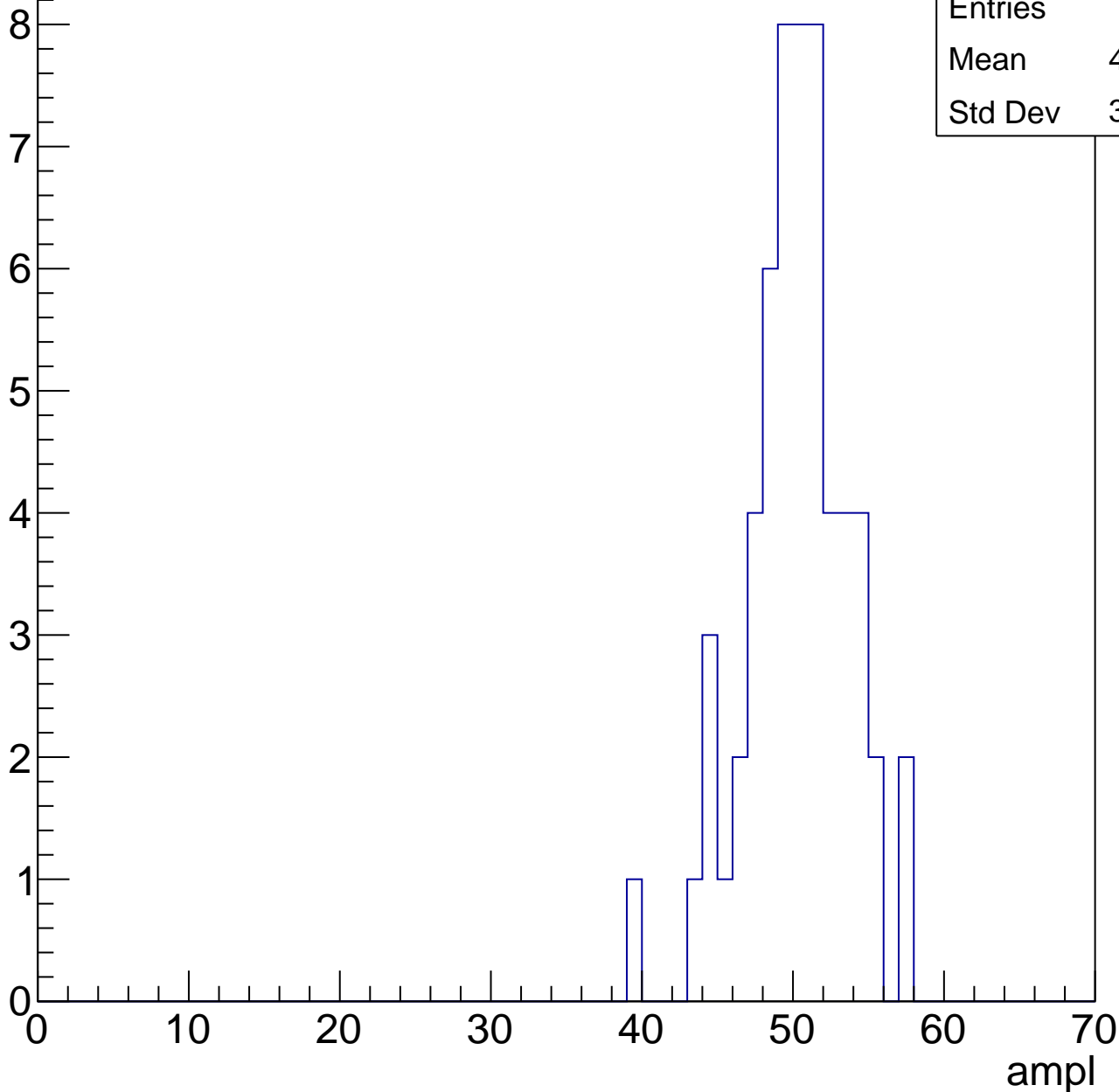


B1L100S, U12-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	49.78
Std Dev	3.404

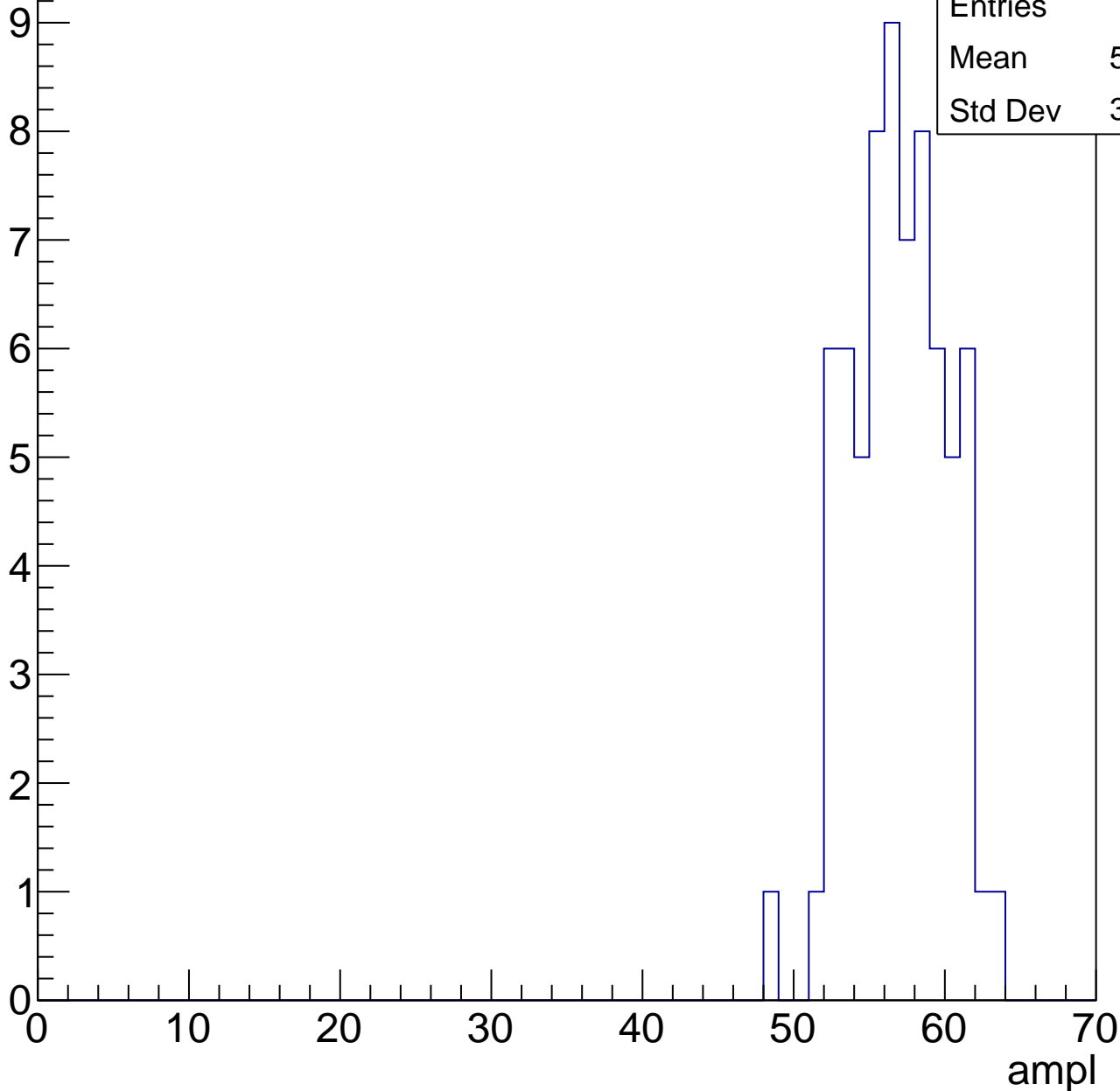


B1L100S, U12-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	56.44
Std Dev	3.074

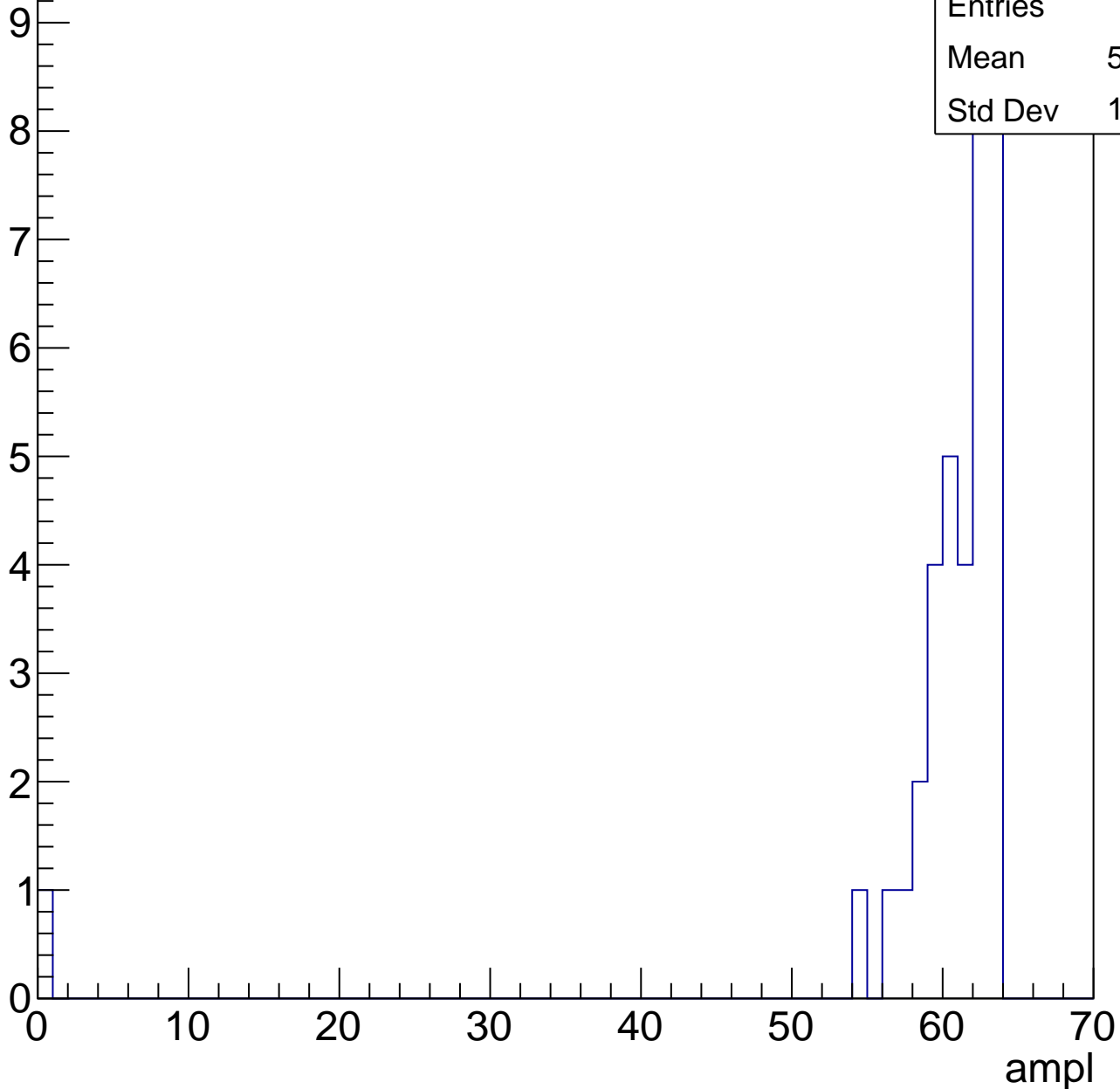


B1L100S, U12-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59.14
Std Dev	10.09



B1L100S, U12-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch75, adc0

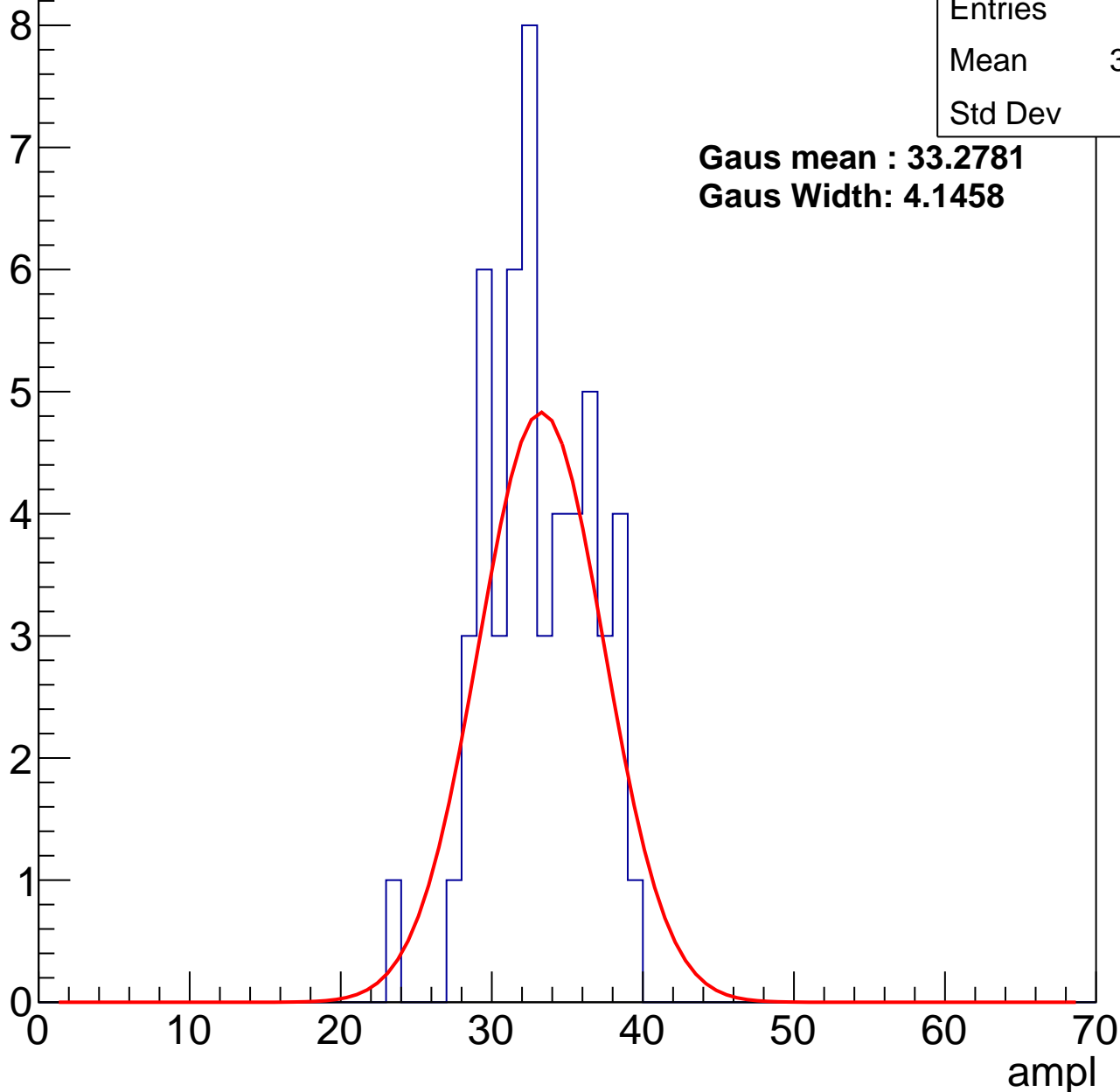
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	32.63
Std Dev	3.42

Gaus mean : 33.2781

Gaus Width: 4.1458



B1L100S, U12-ch75, adc1

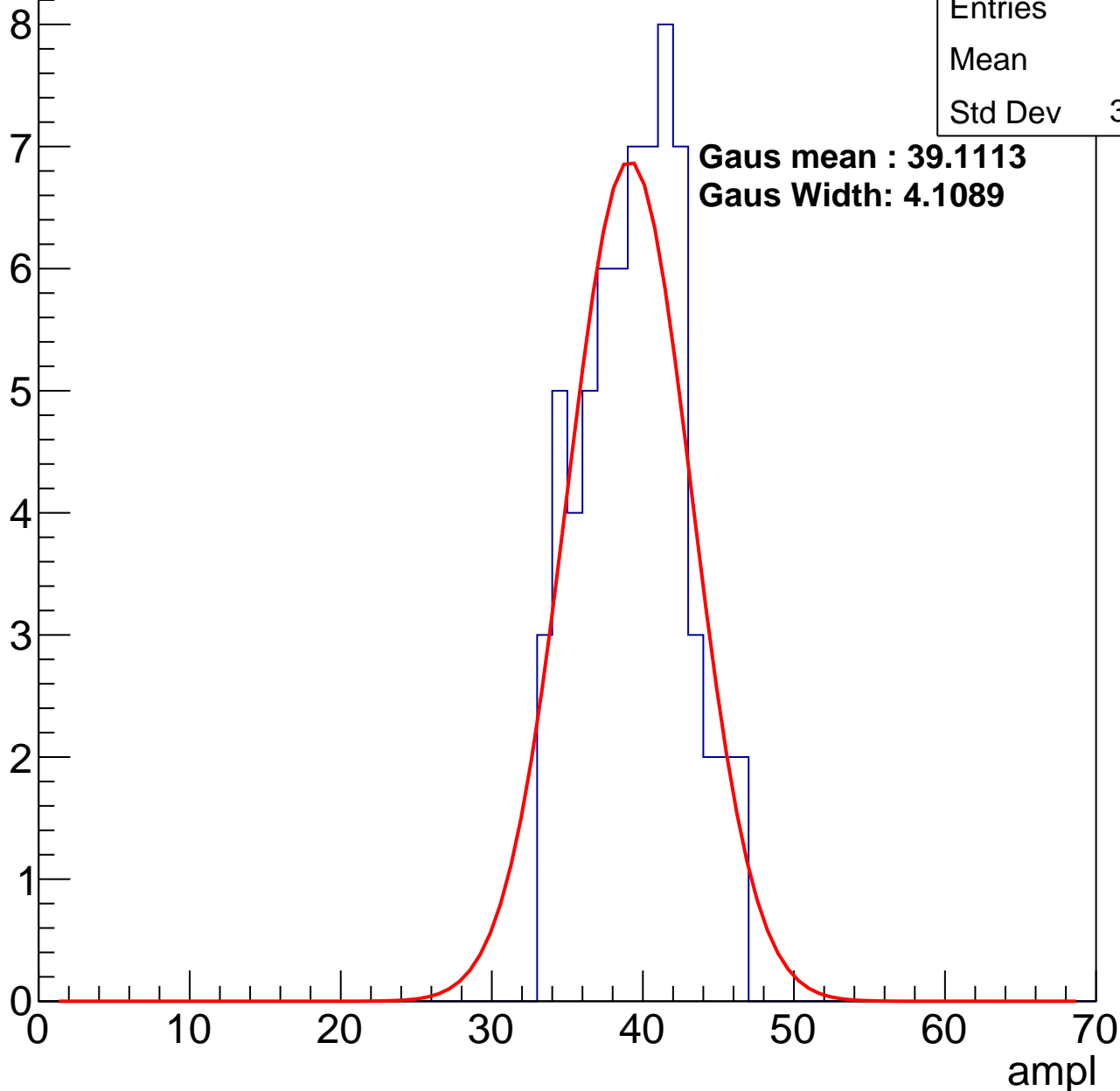
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	39
Std Dev	3.328

Gaus mean : 39.1113

Gaus Width: 4.1089



B1L100S, U12-ch75, adc2

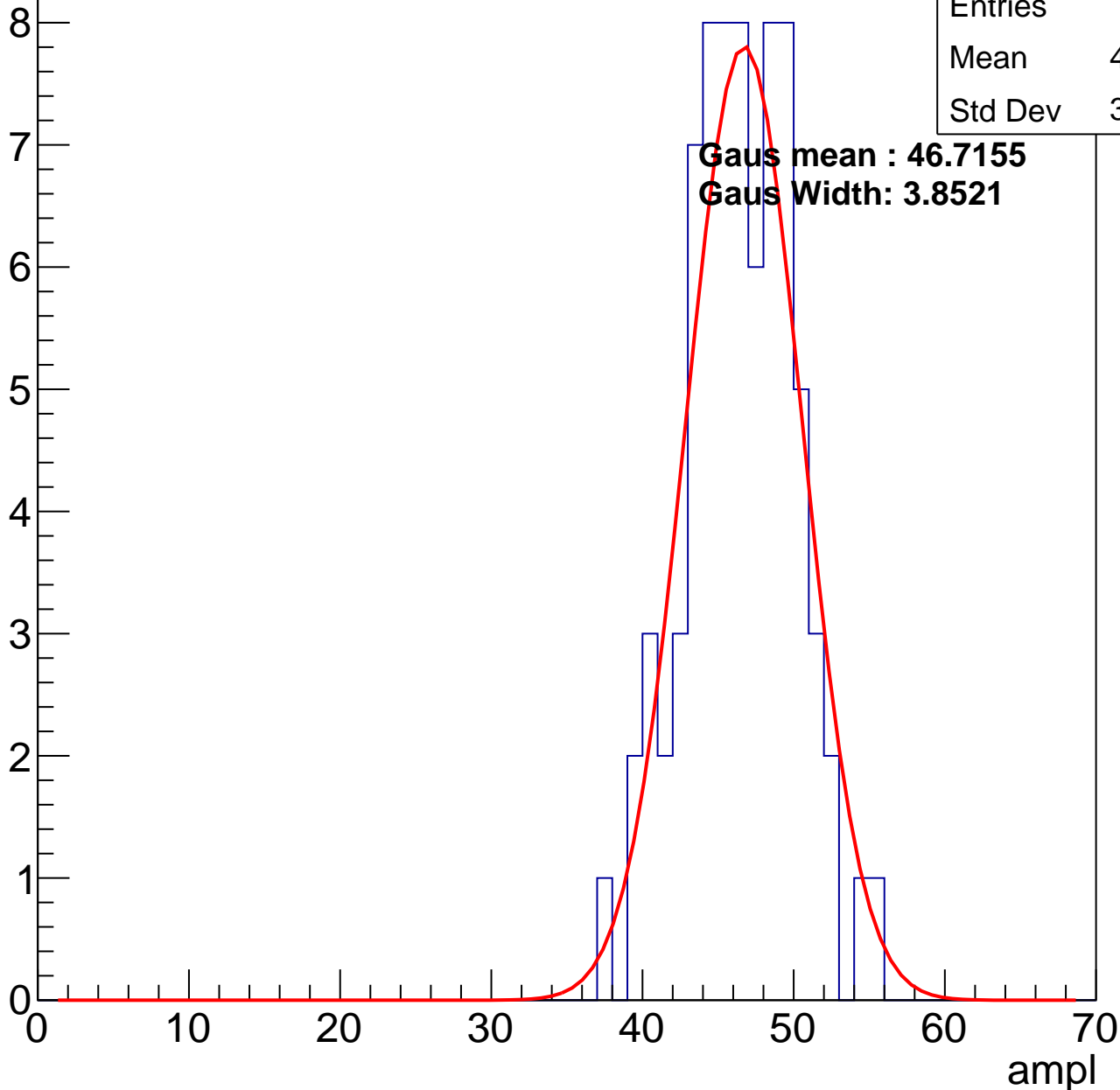
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	46.03
Std Dev	3.572

Gaus mean : 46.7155

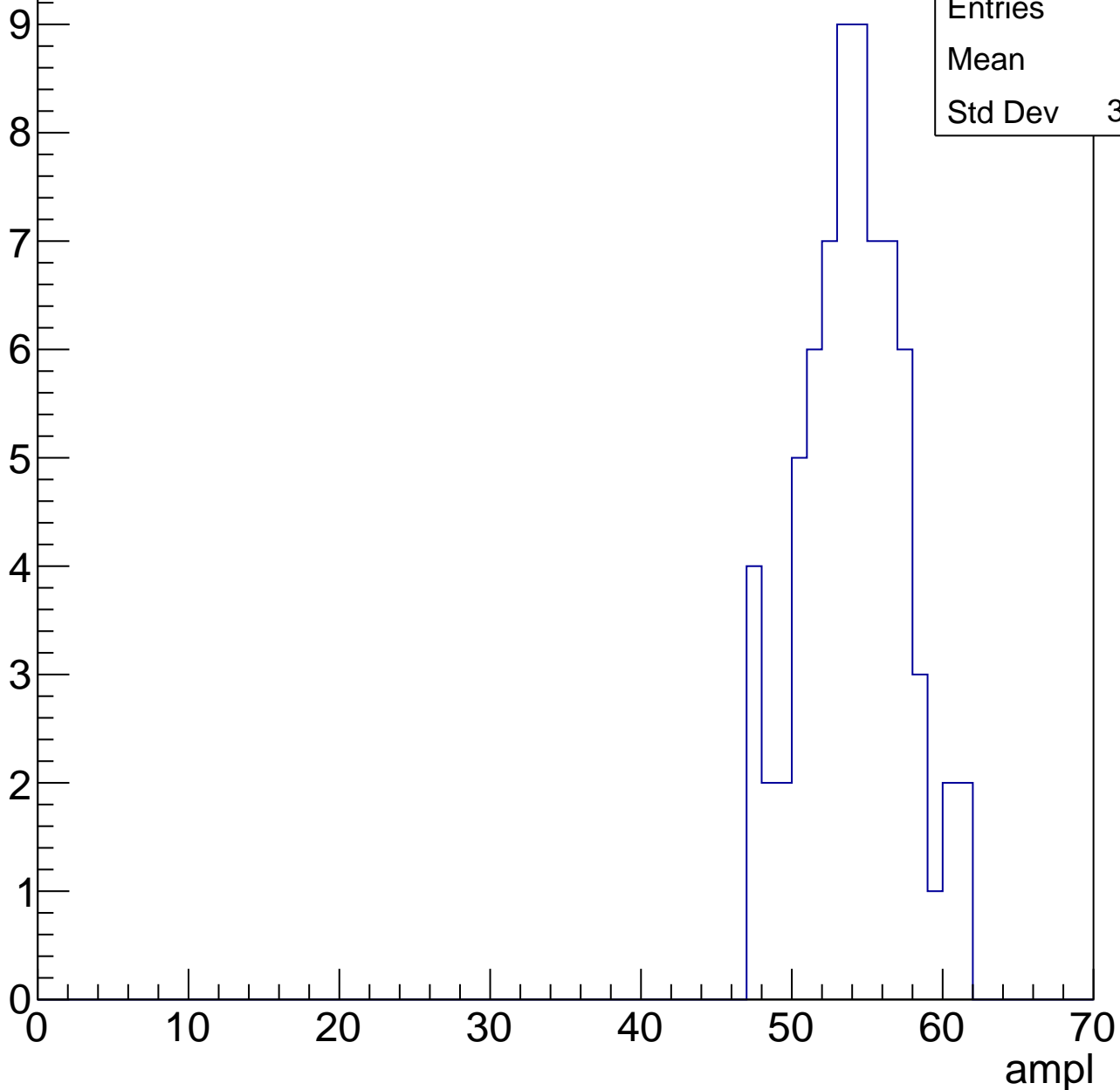
Gaus Width: 3.8521



B1L100S, U12-ch75, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

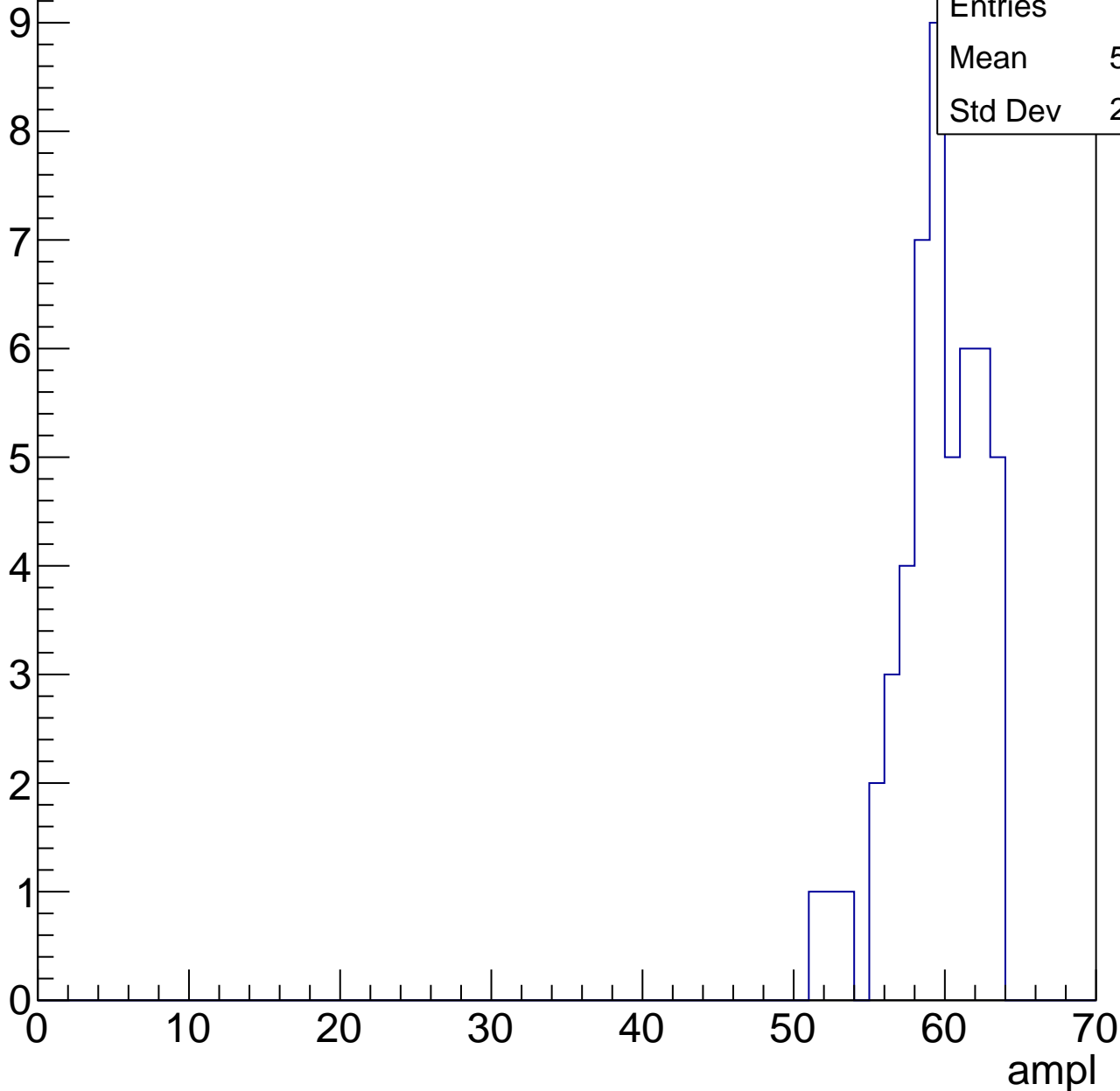


B1L100S, U12-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

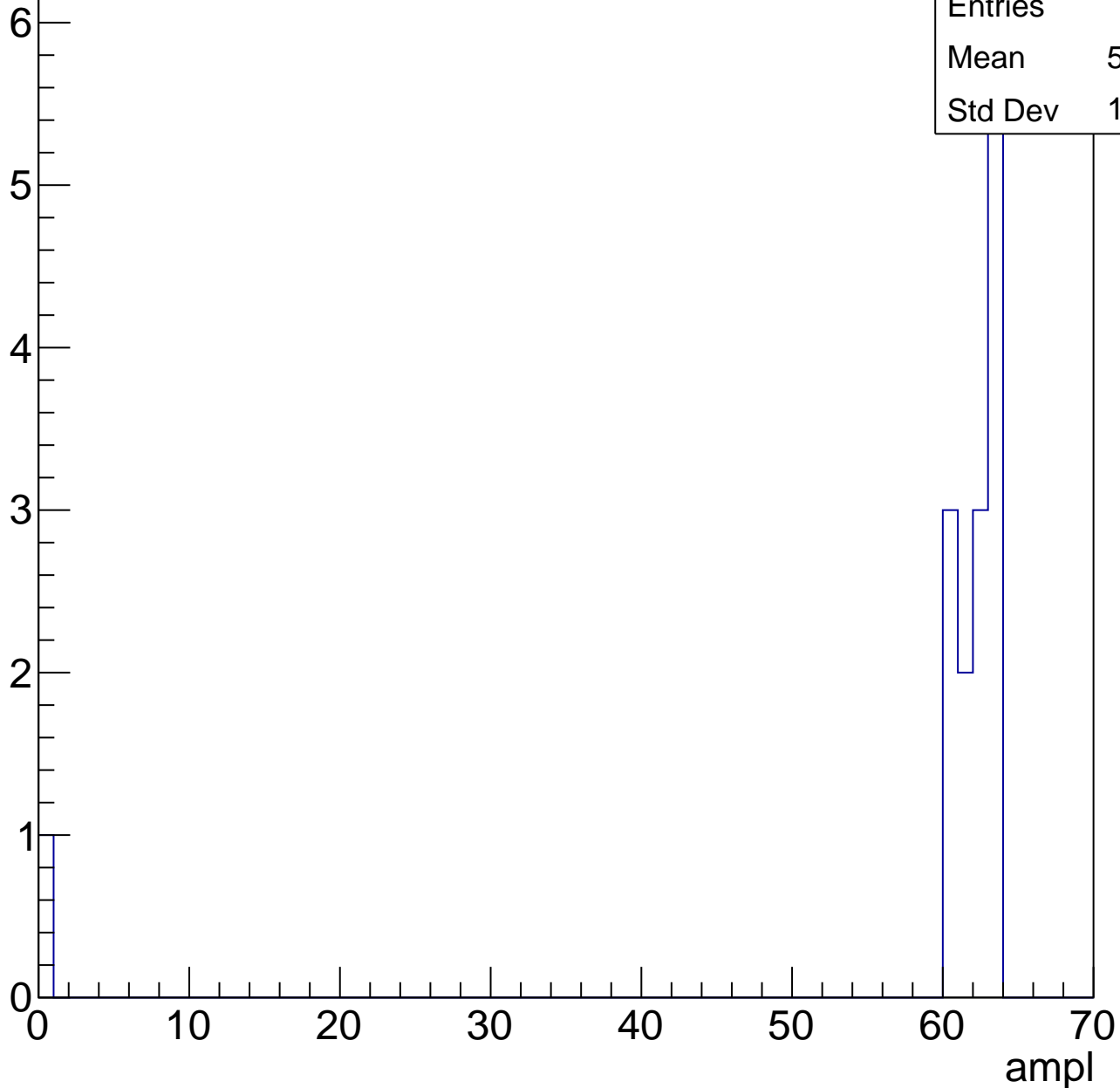
Entries	50
Mean	59.04
Std Dev	2.807



B1L100S, U12-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch76, adc0

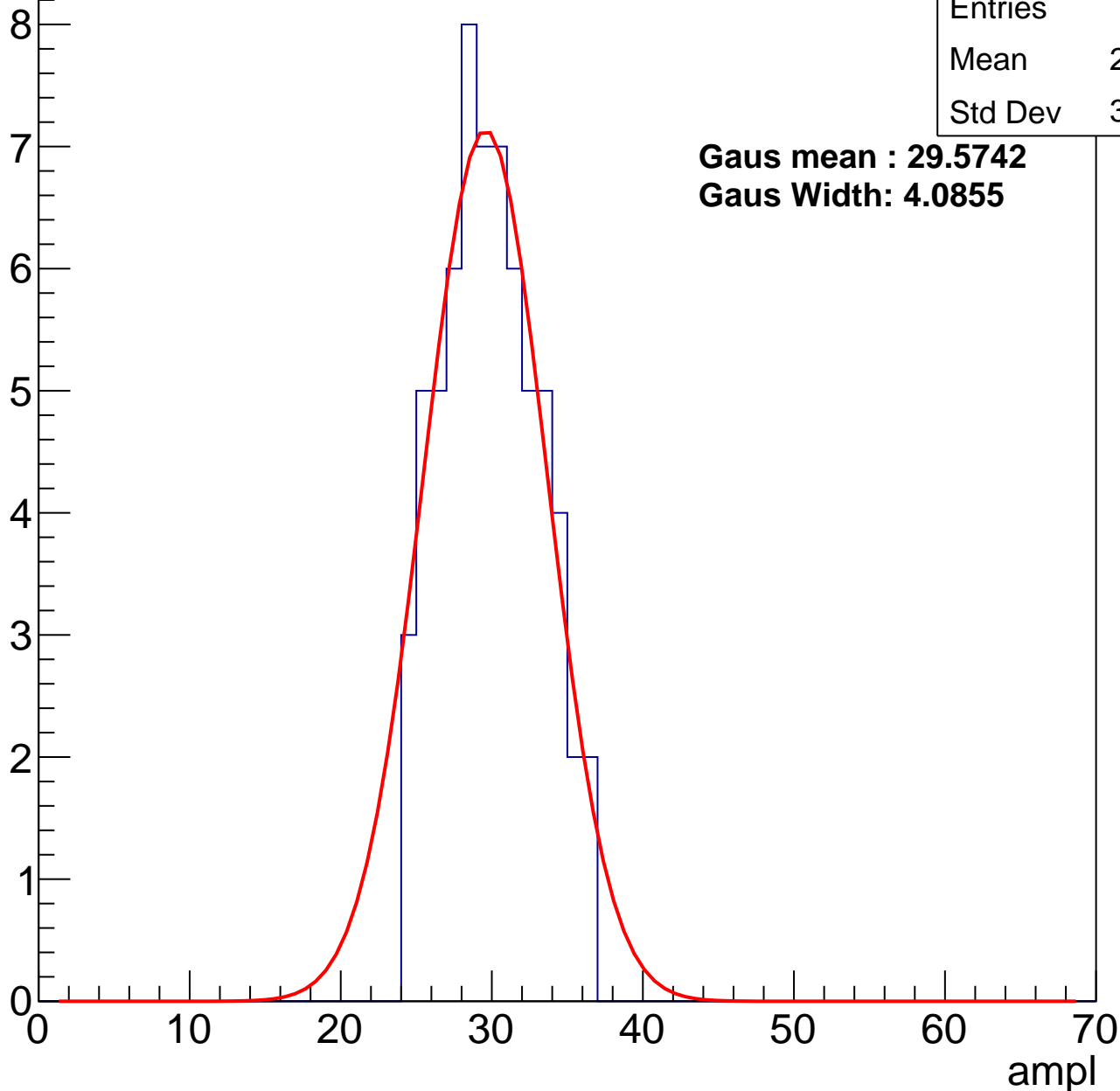
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.46
Std Dev	3.148

Gaus mean : 29.5742

Gaus Width: 4.0855



B1L100S, U12-ch76, adc1

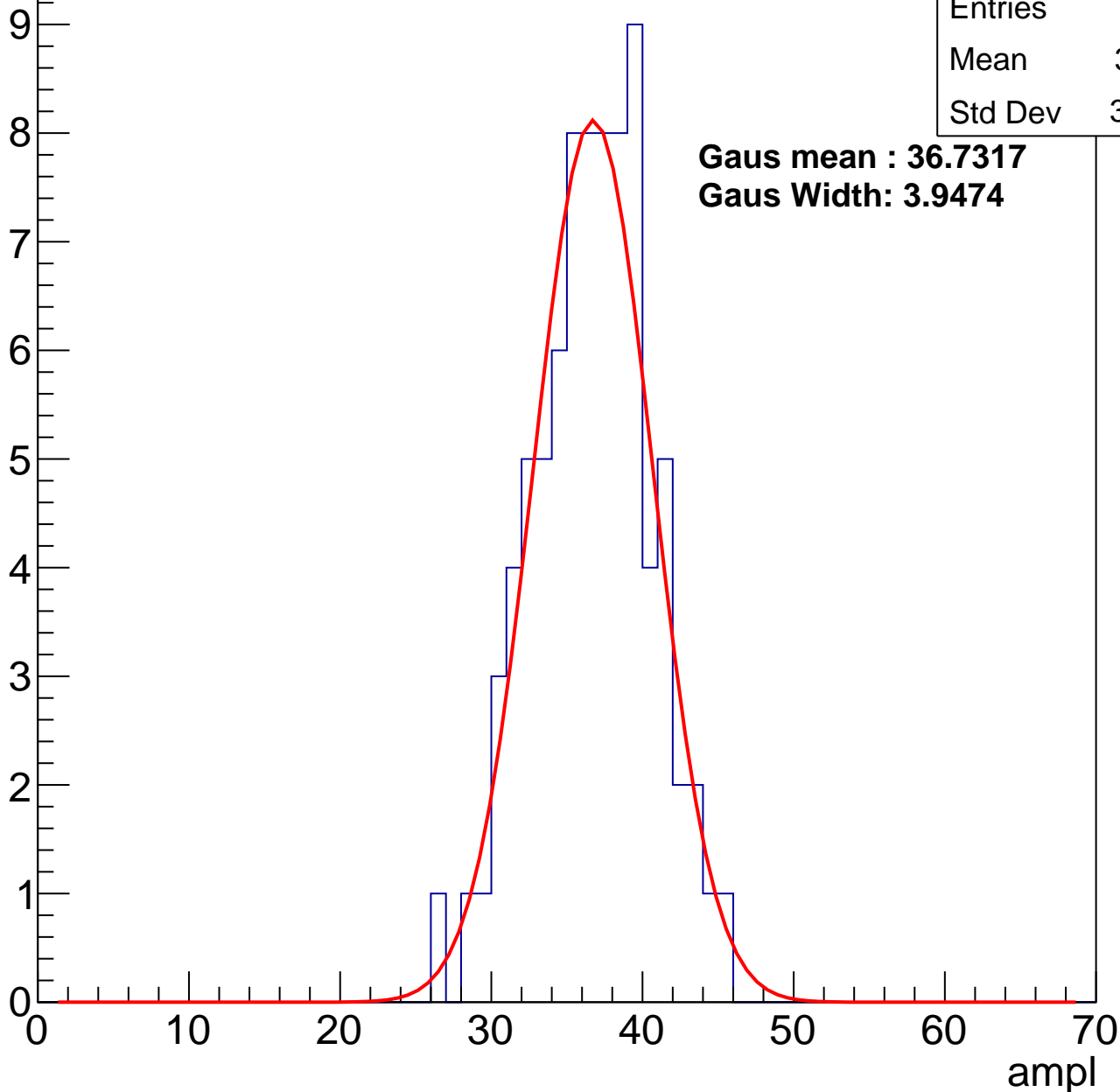
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	36.21
Std Dev	3.825

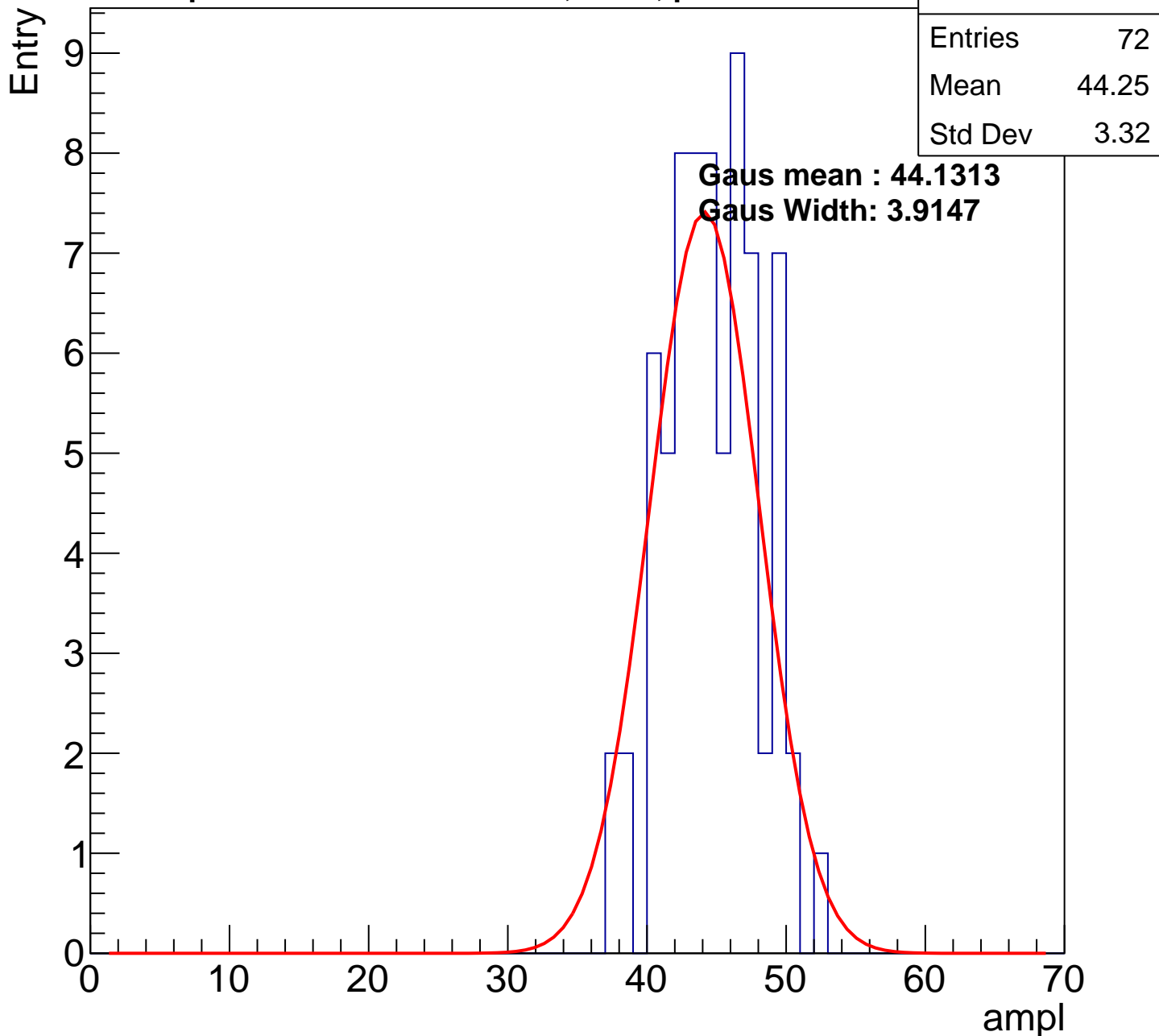
Gaus mean : 36.7317

Gaus Width: 3.9474



B1L100S, U12-ch76, adc2

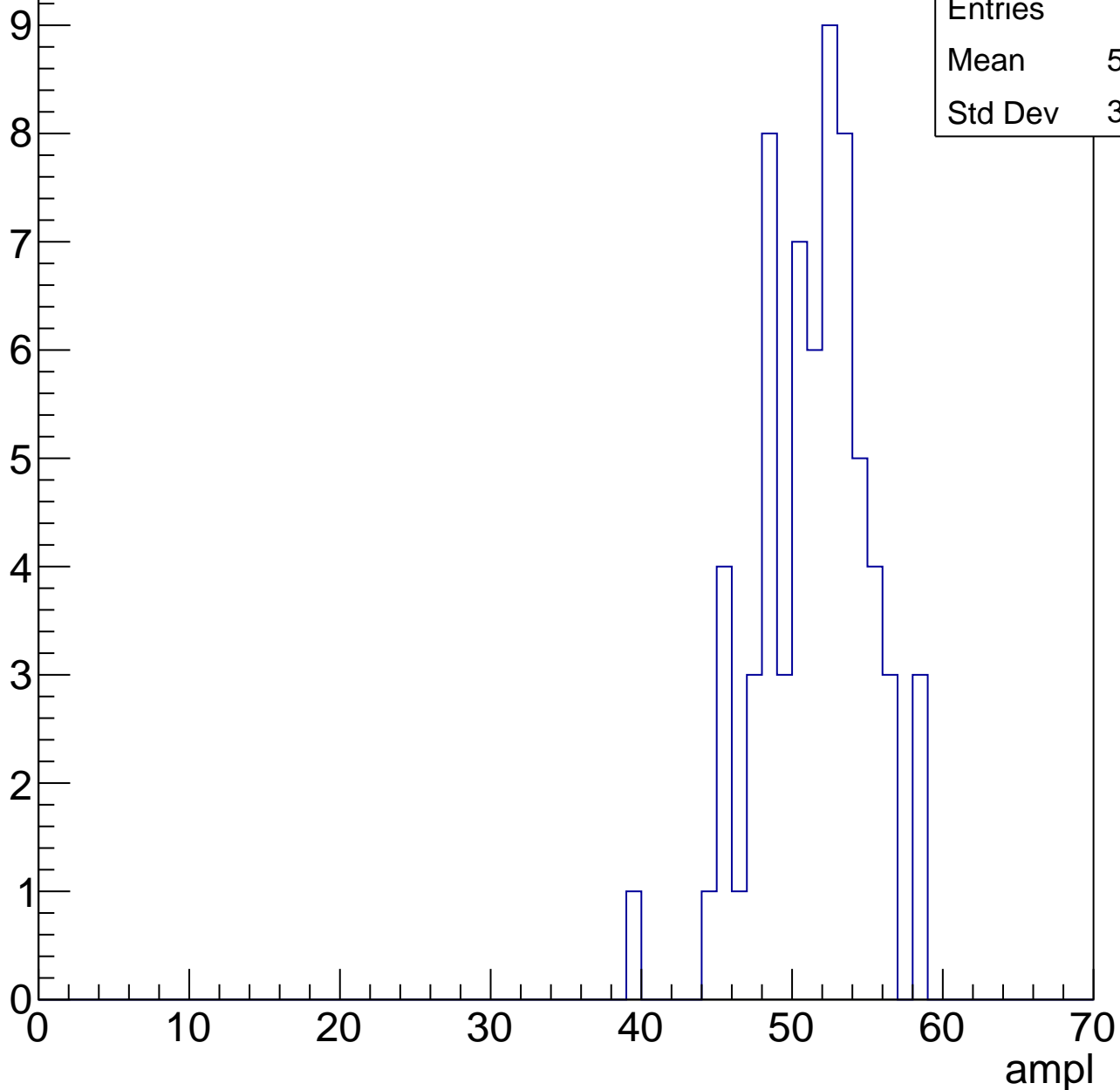
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	54
Mean	57.15
Std Dev	3.302

Entry

10

8

6

4

2

0

0

10

20

30

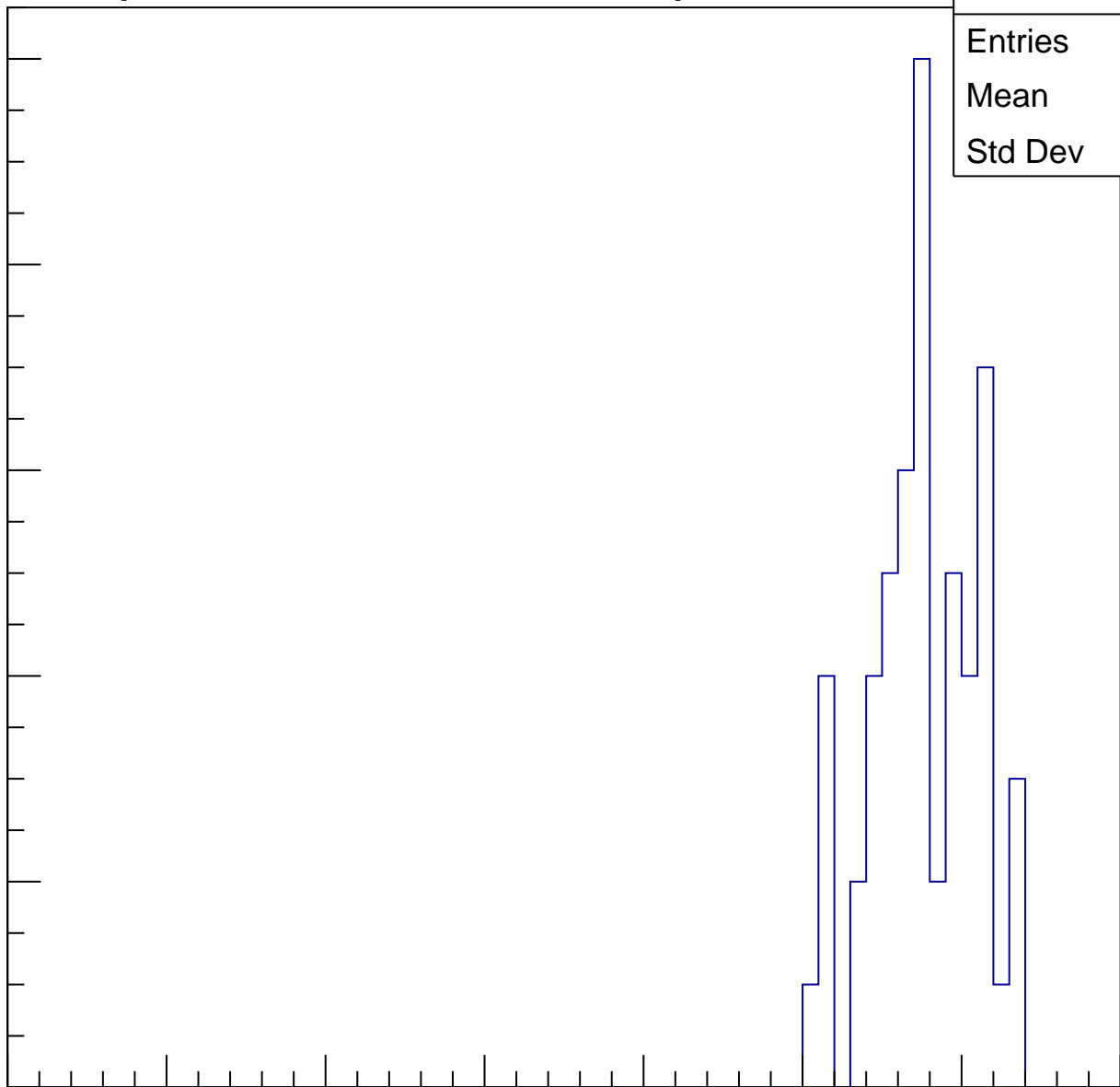
40

50

60

70

ampl

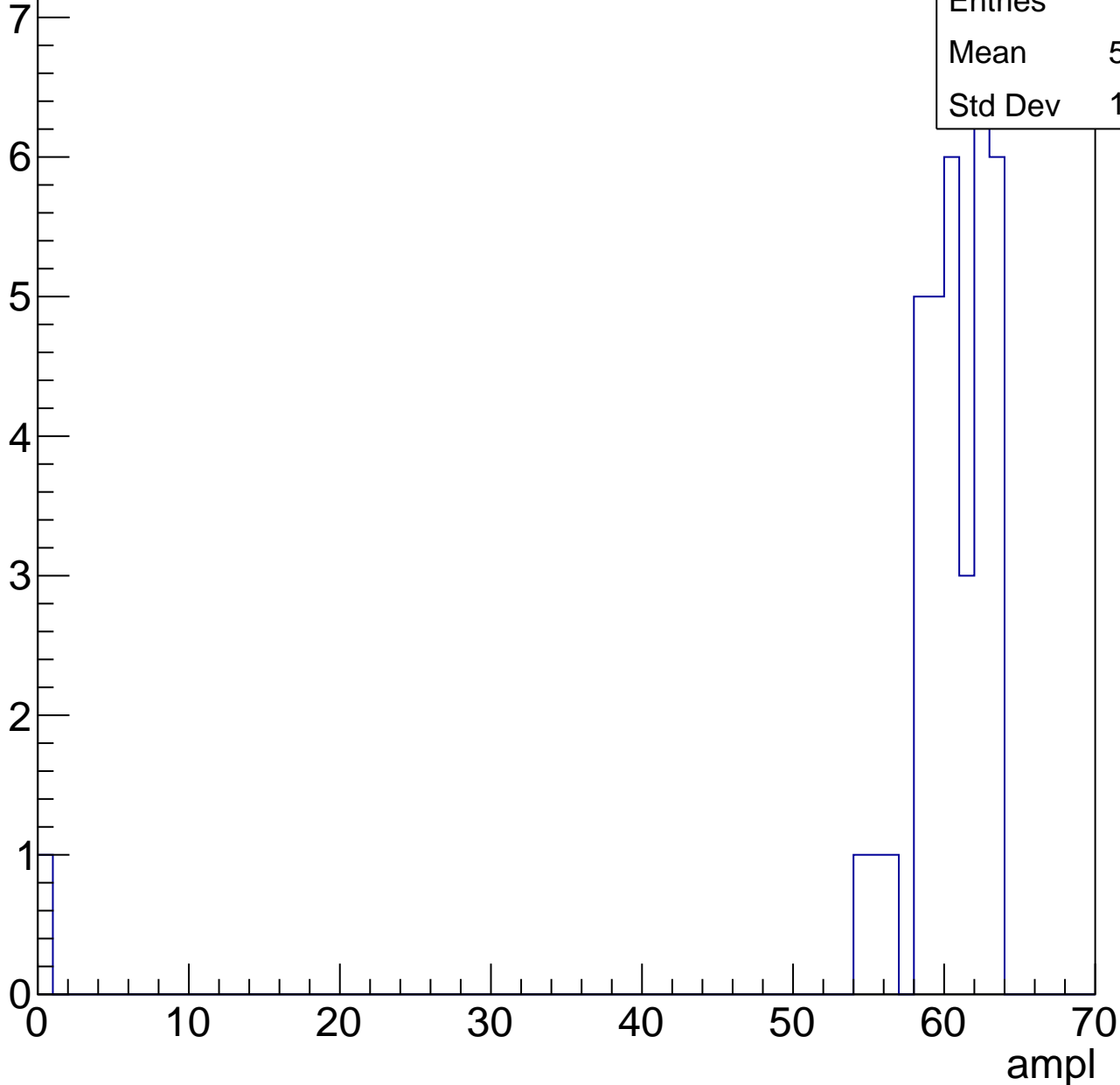


B1L100S, U12-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.47
Std Dev	10.14



B1L100S, U12-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L100S, U12-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L100S, U12-ch77, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29
Std Dev	3.145

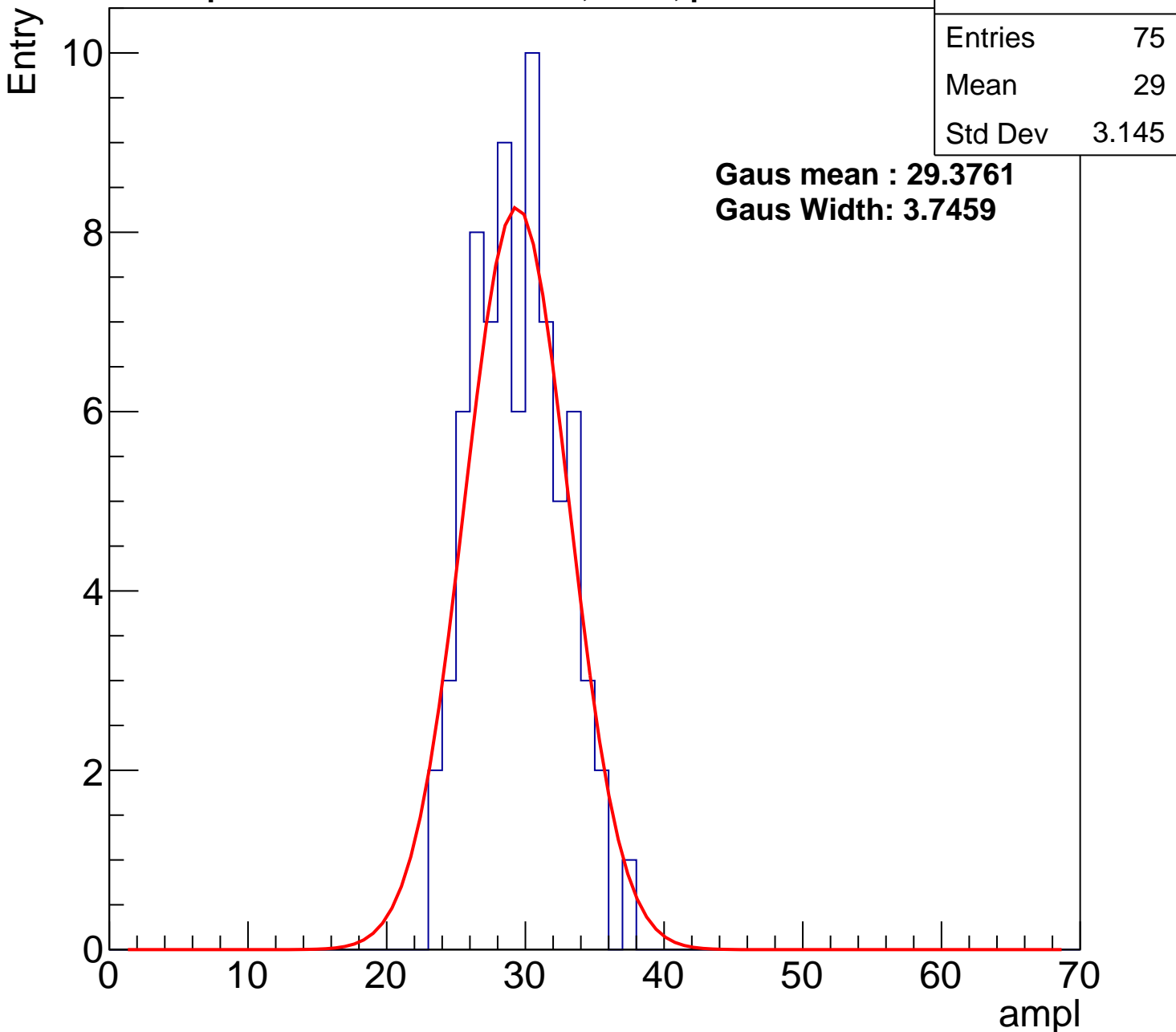
Gaus mean : 29.3761

Gaus Width: 3.7459

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch77, adc1

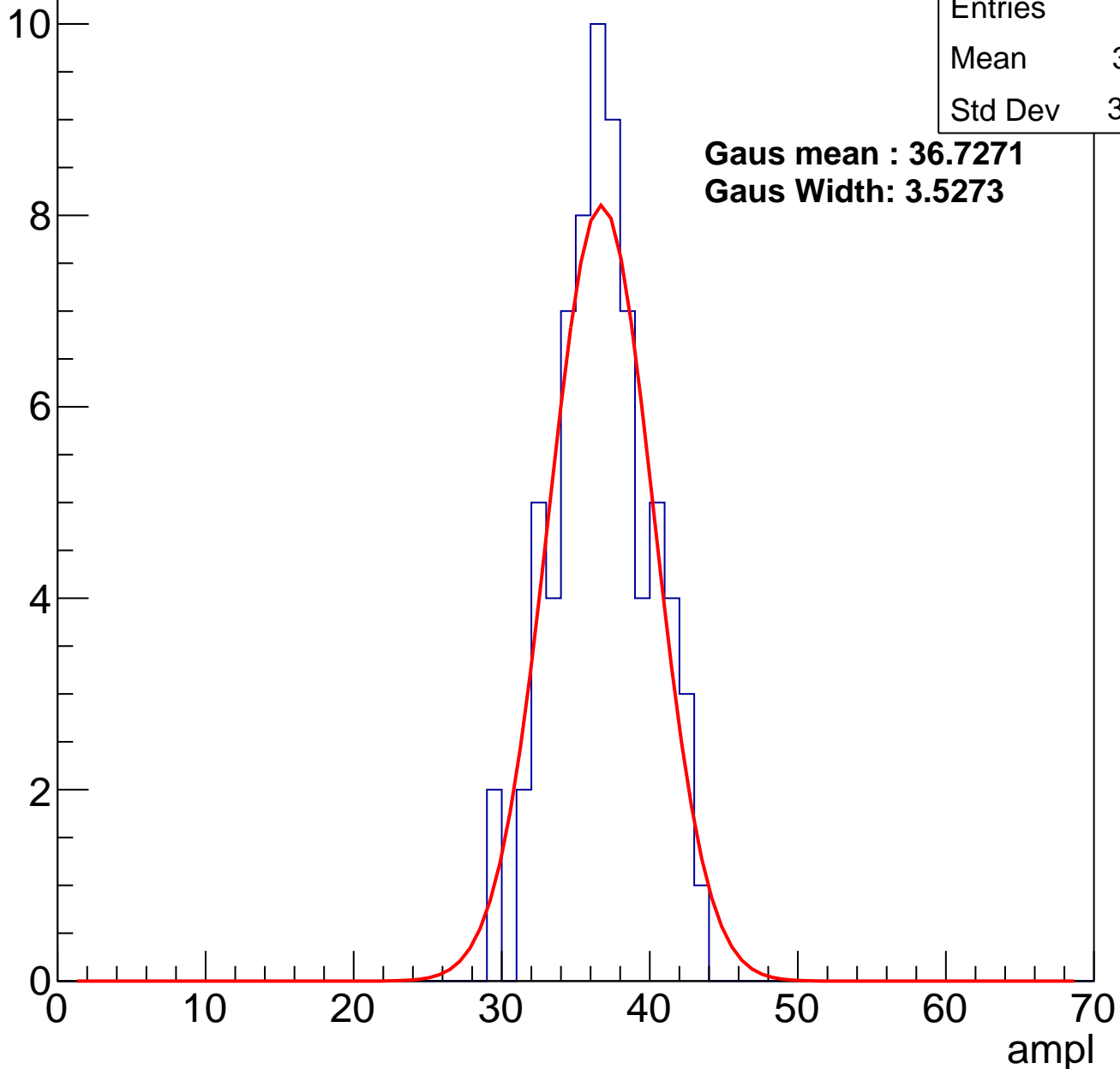
calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	36.31
Std Dev	3.147

Gaus mean : 36.7271

Gaus Width: 3.5273

Entry



B1L100S, U12-ch77, adc2

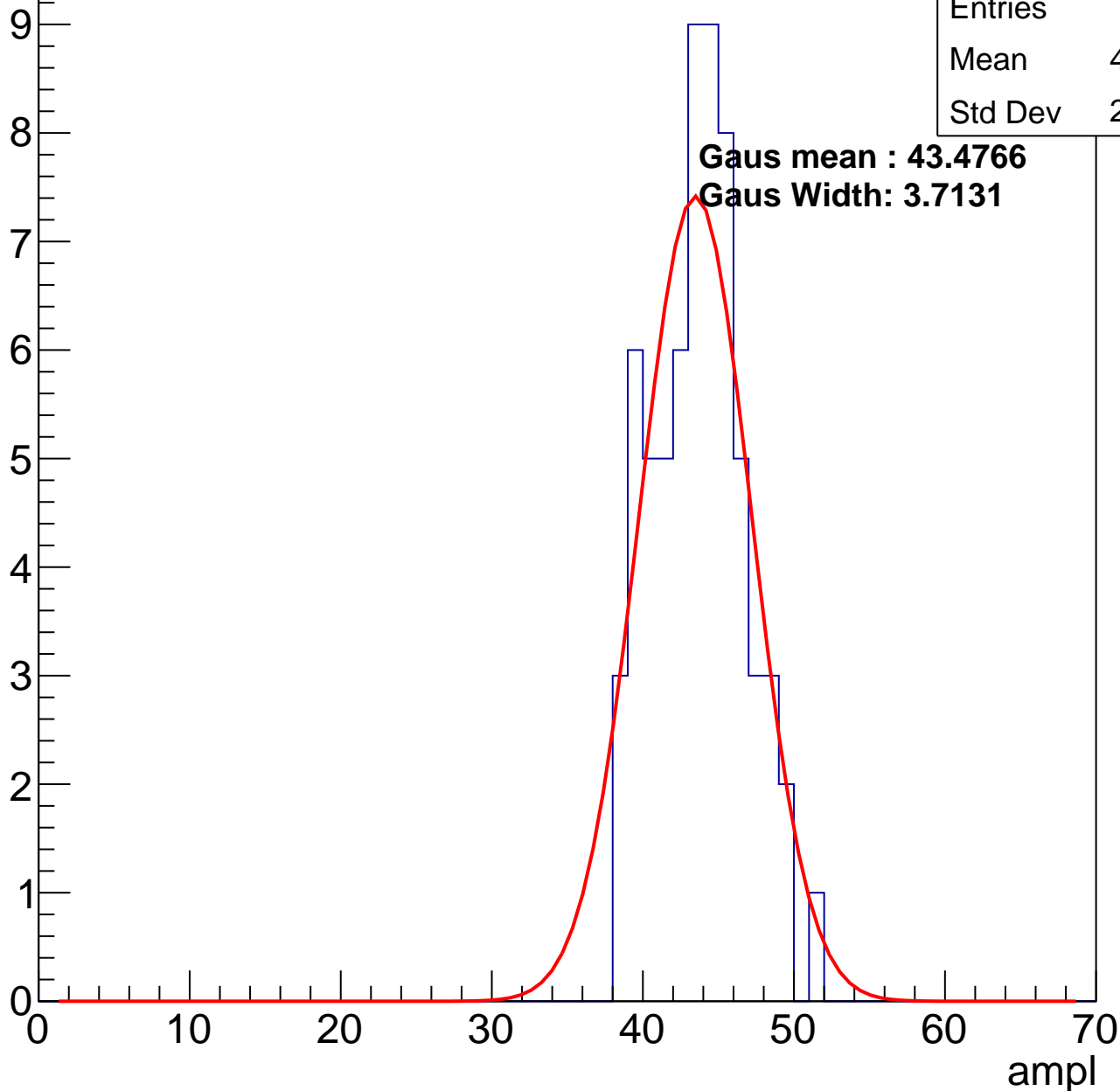
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	43.26
Std Dev	2.994

Gaus mean : 43.4766

Gaus Width: 3.7131

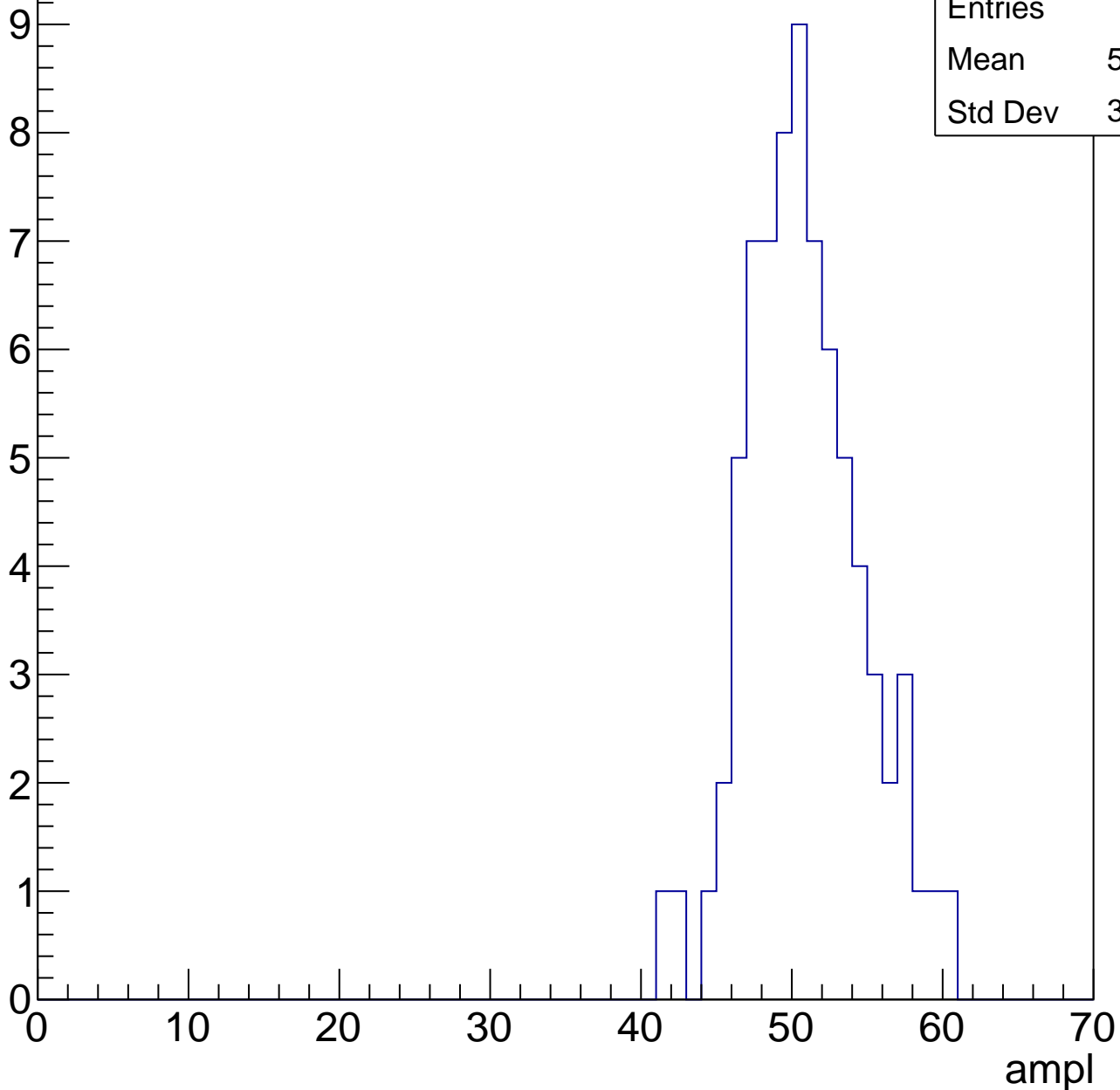


B1L100S, U12-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	50.39
Std Dev	3.816

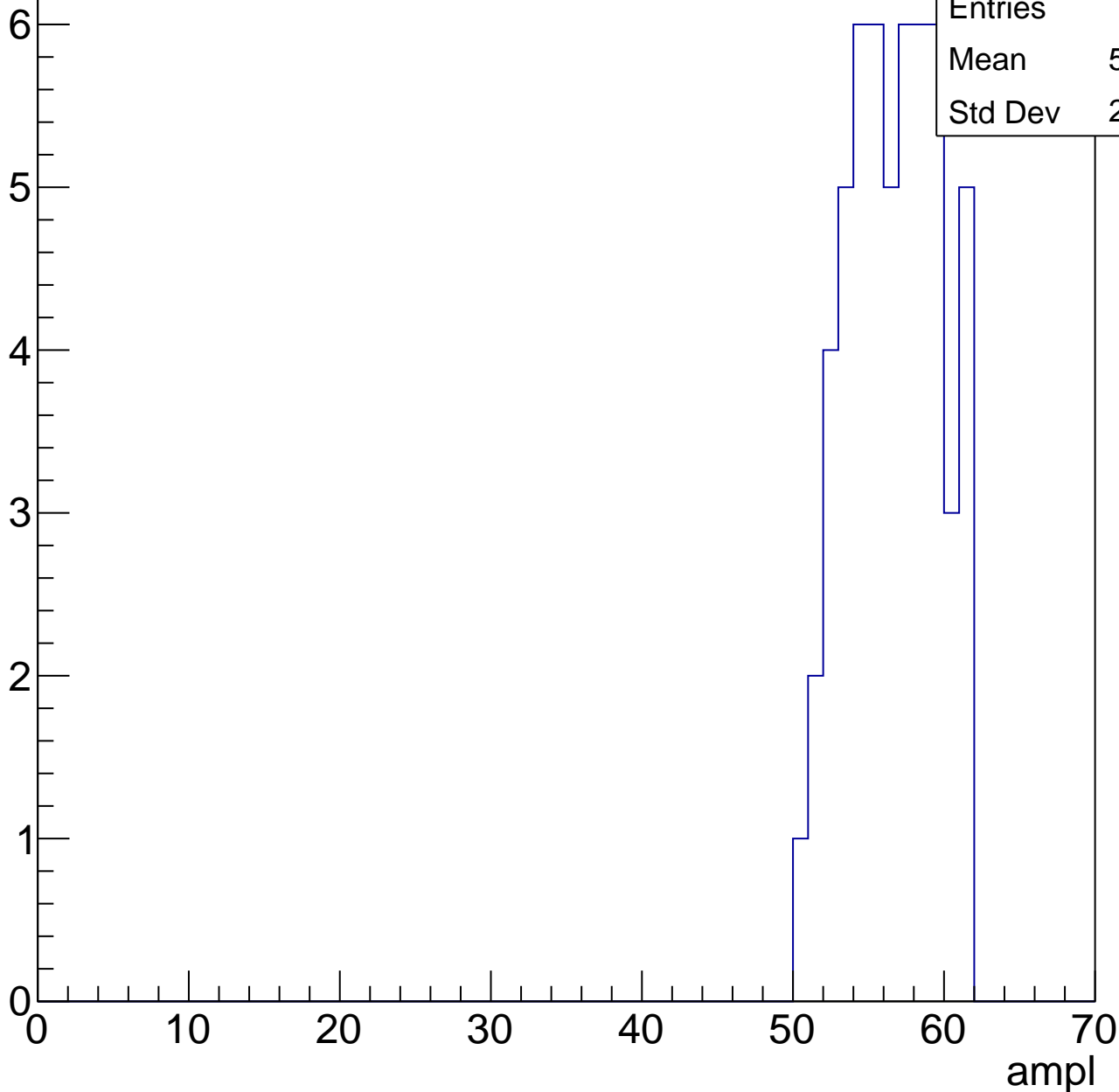


B1L100S, U12-ch77, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	56.15
Std Dev	2.957

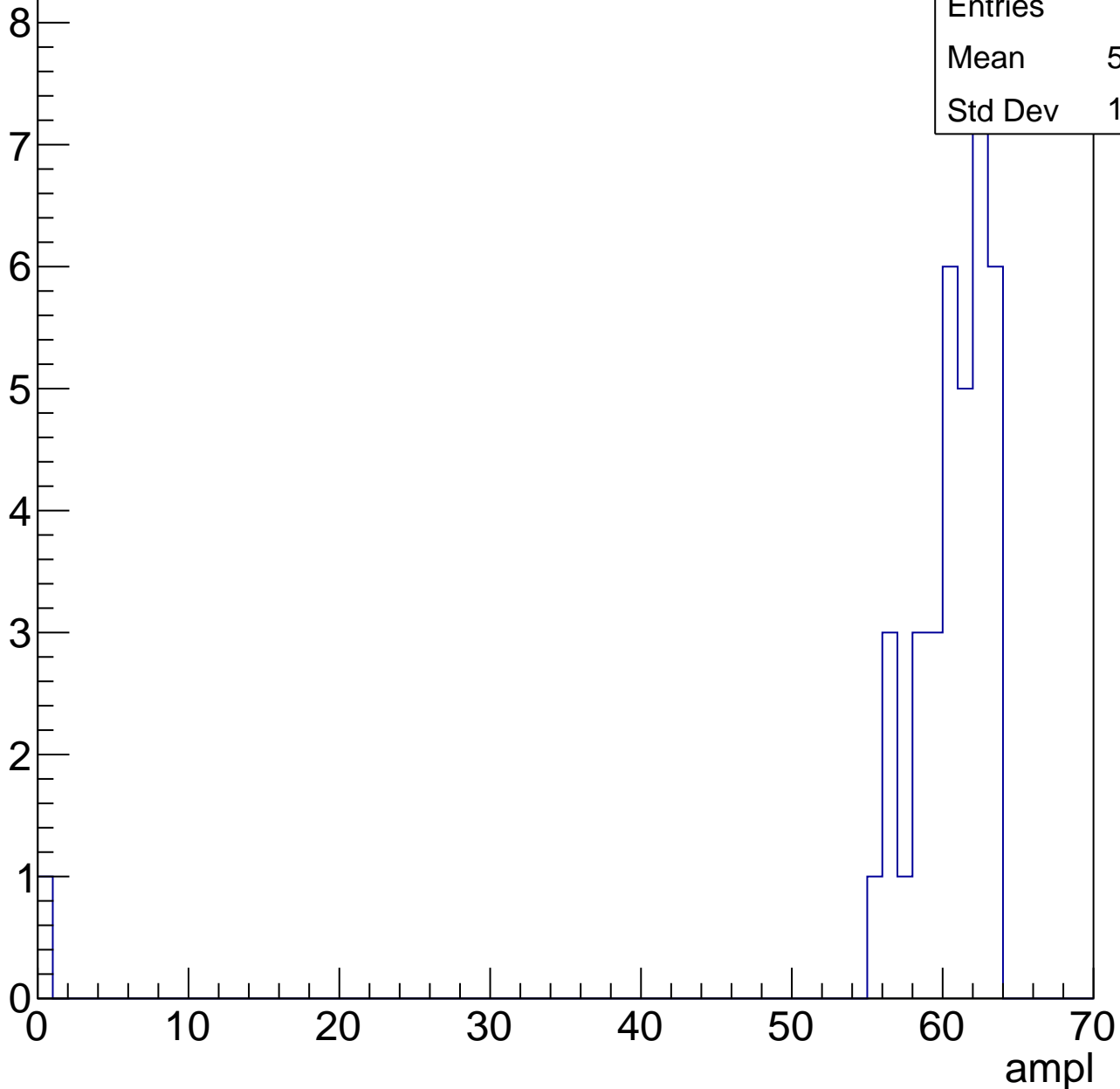


B1L100S, U12-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

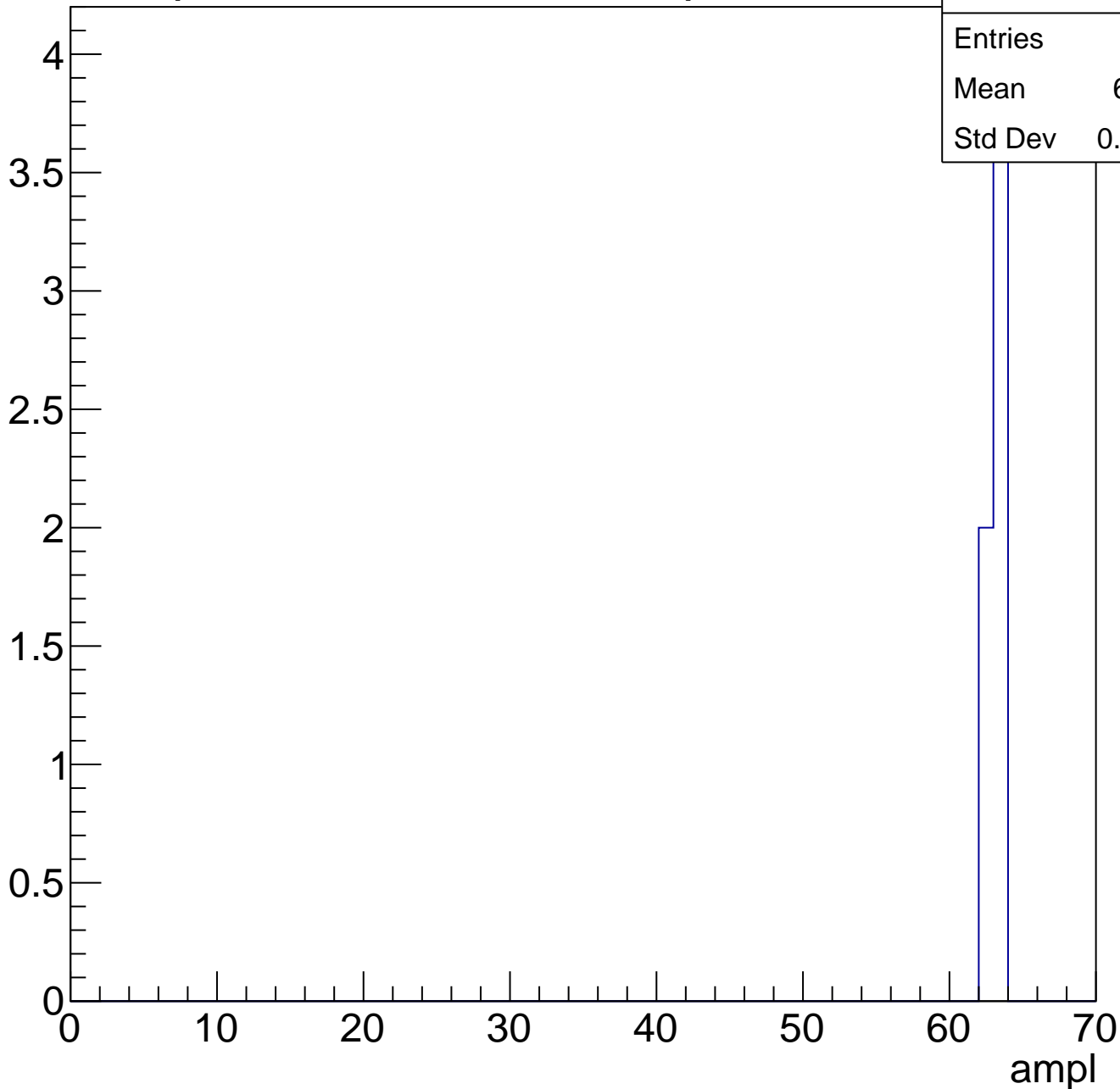
Entries	37
Mean	58.65
Std Dev	10.03



B1L100S, U12-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch78, adc0

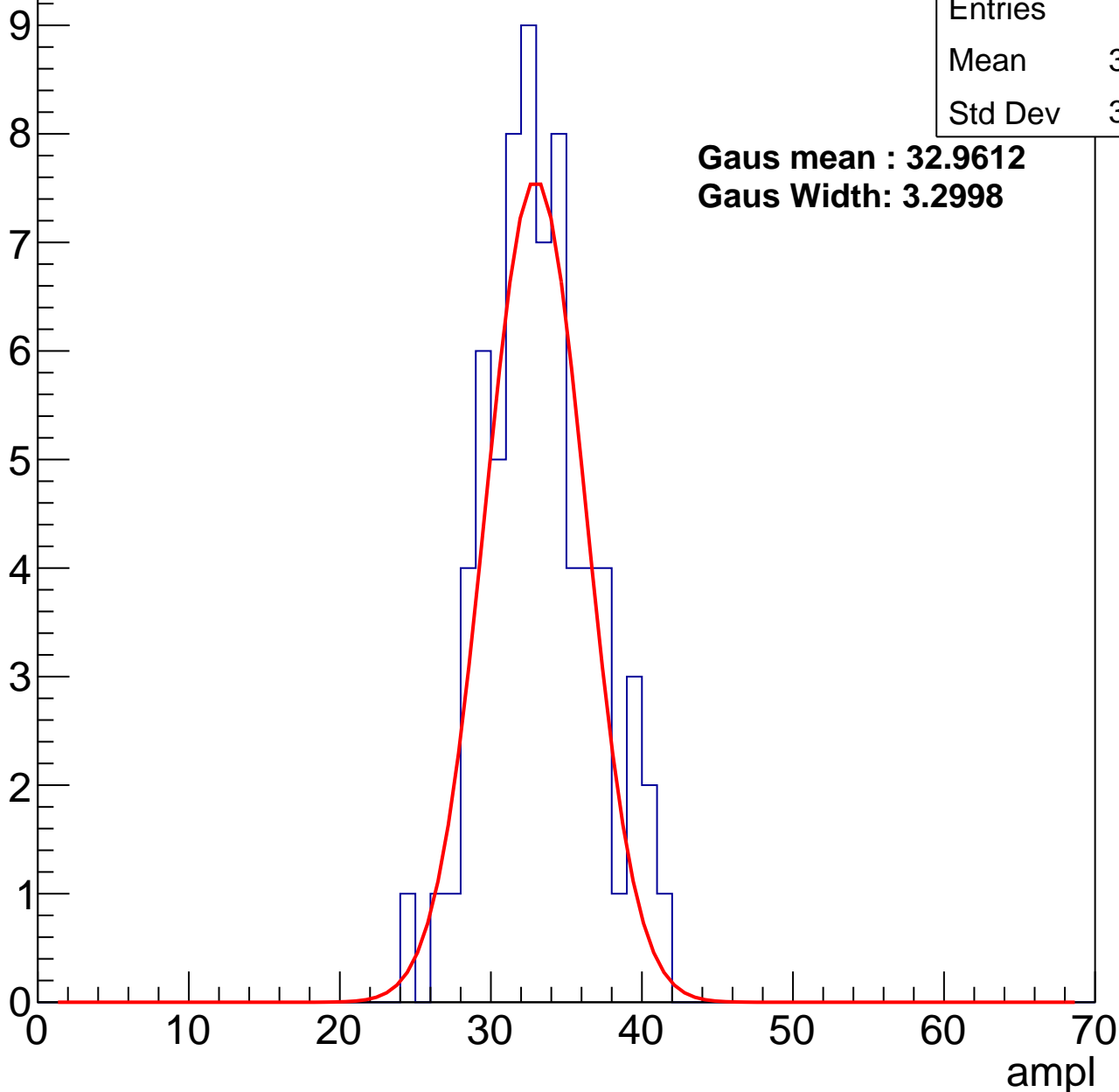
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	32.75
Std Dev	3.536

Gaus mean : 32.9612

Gaus Width: 3.2998



B1L100S, U12-ch78, adc1

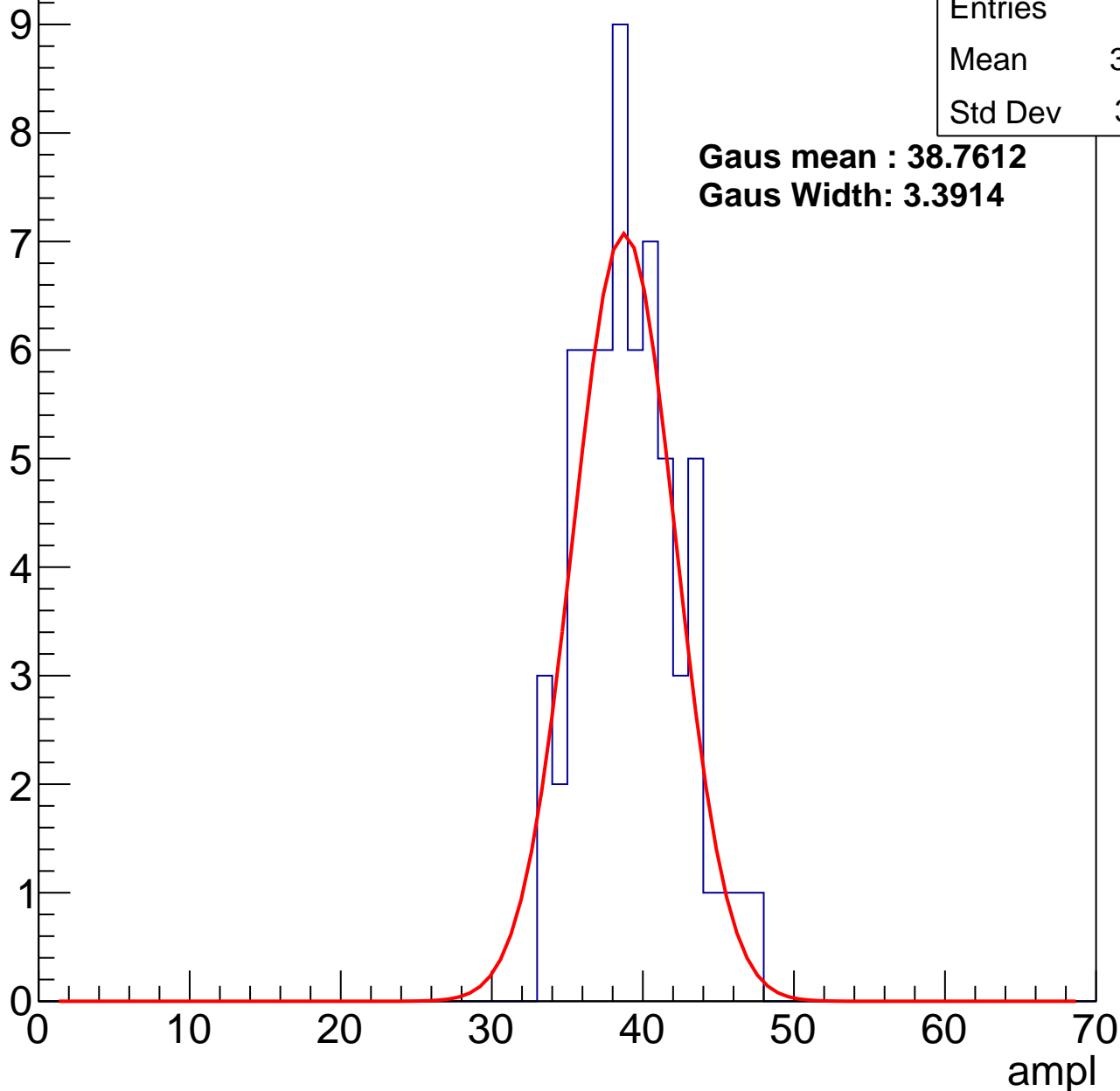
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	38.69
Std Dev	3.221

Gaus mean : 38.7612

Gaus Width: 3.3914



B1L100S, U12-ch78, adc2

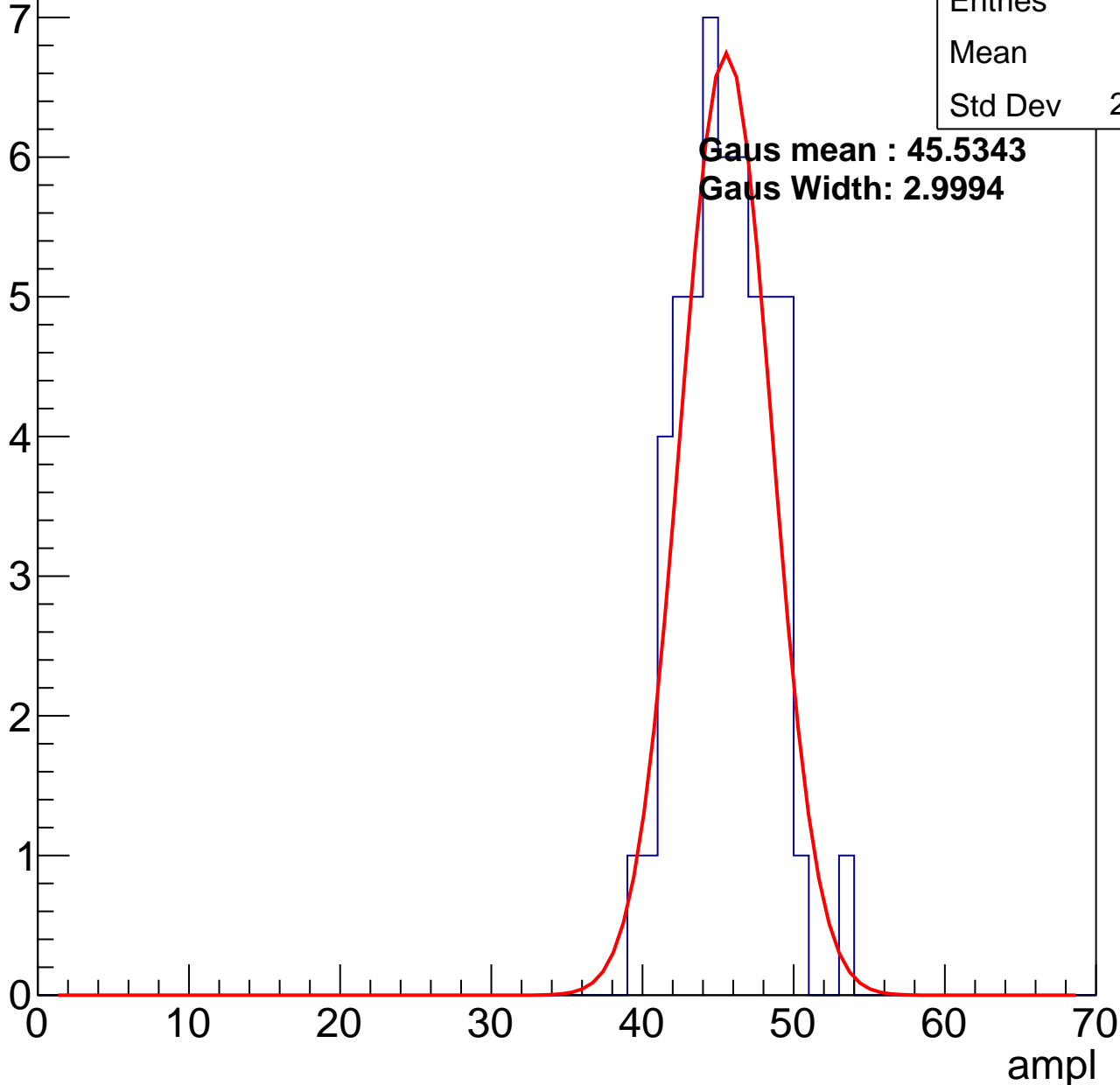
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	45.1
Std Dev	2.897

Gaus mean : 45.5343

Gaus Width: 2.9994



B1L100S, U12-ch78, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	51.25
Std Dev	3.69

Entry

10

8

6

4

2

0

0

10

20

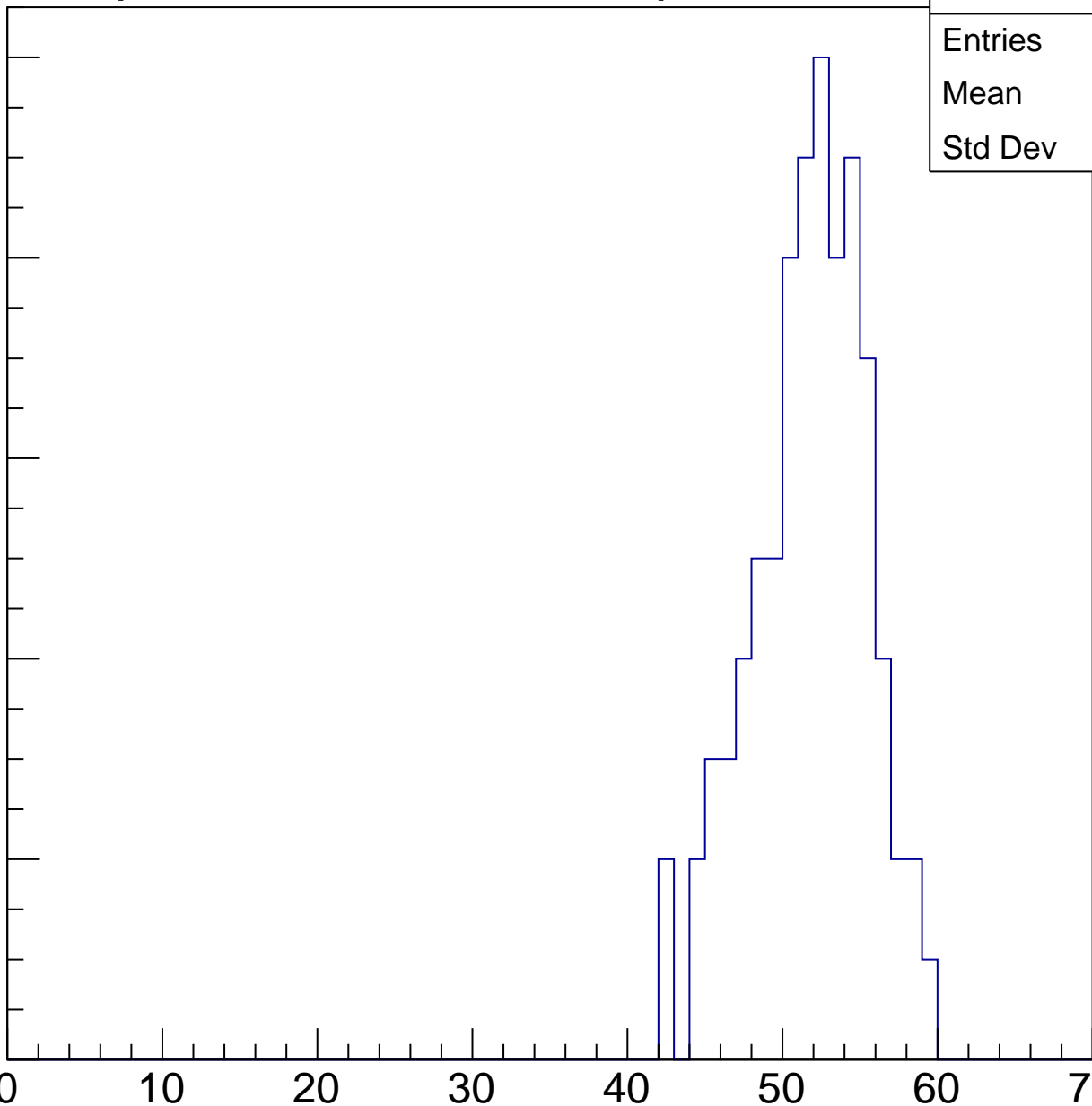
30

40

50

60

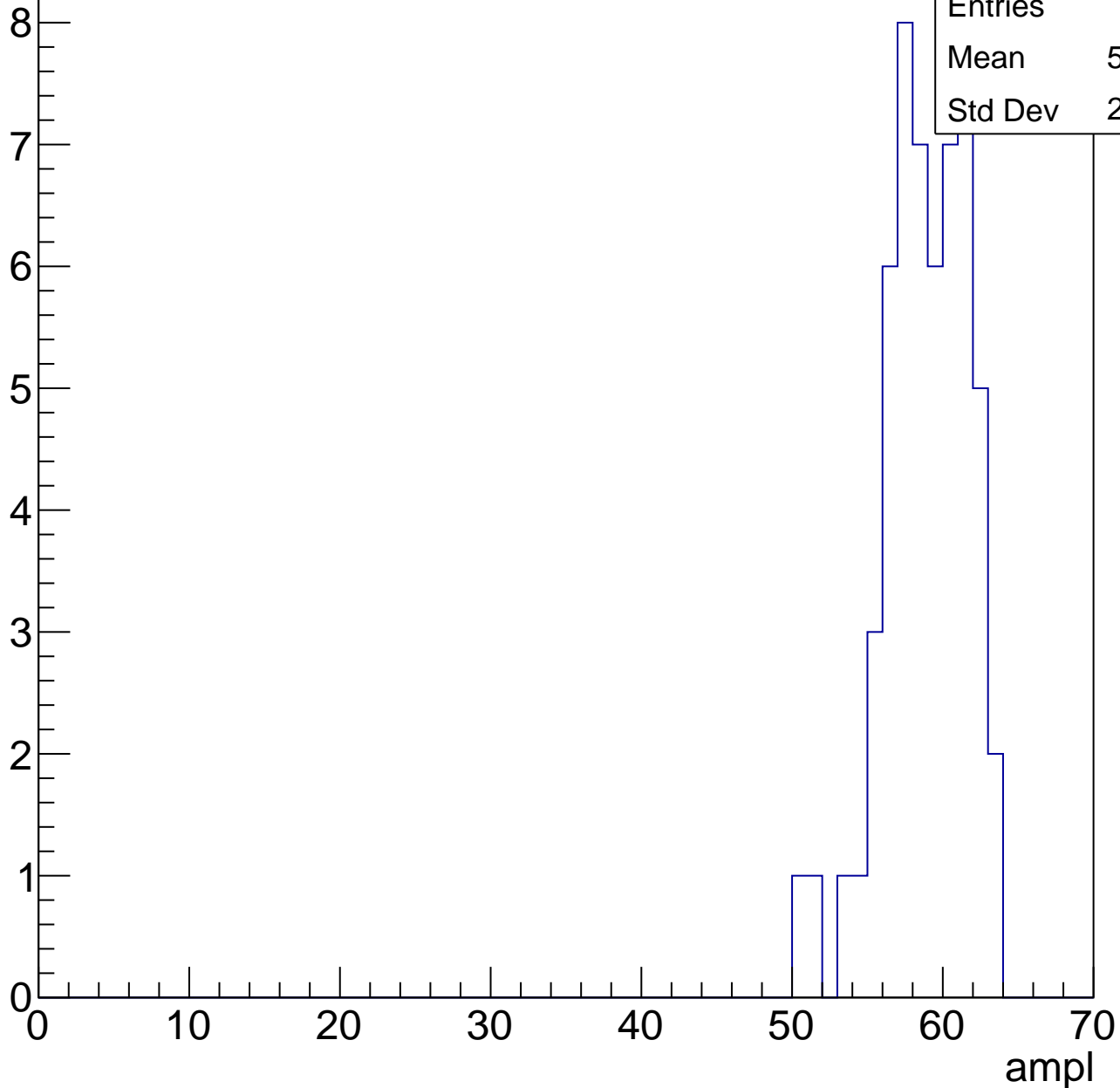
ampl



B1L100S, U12-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

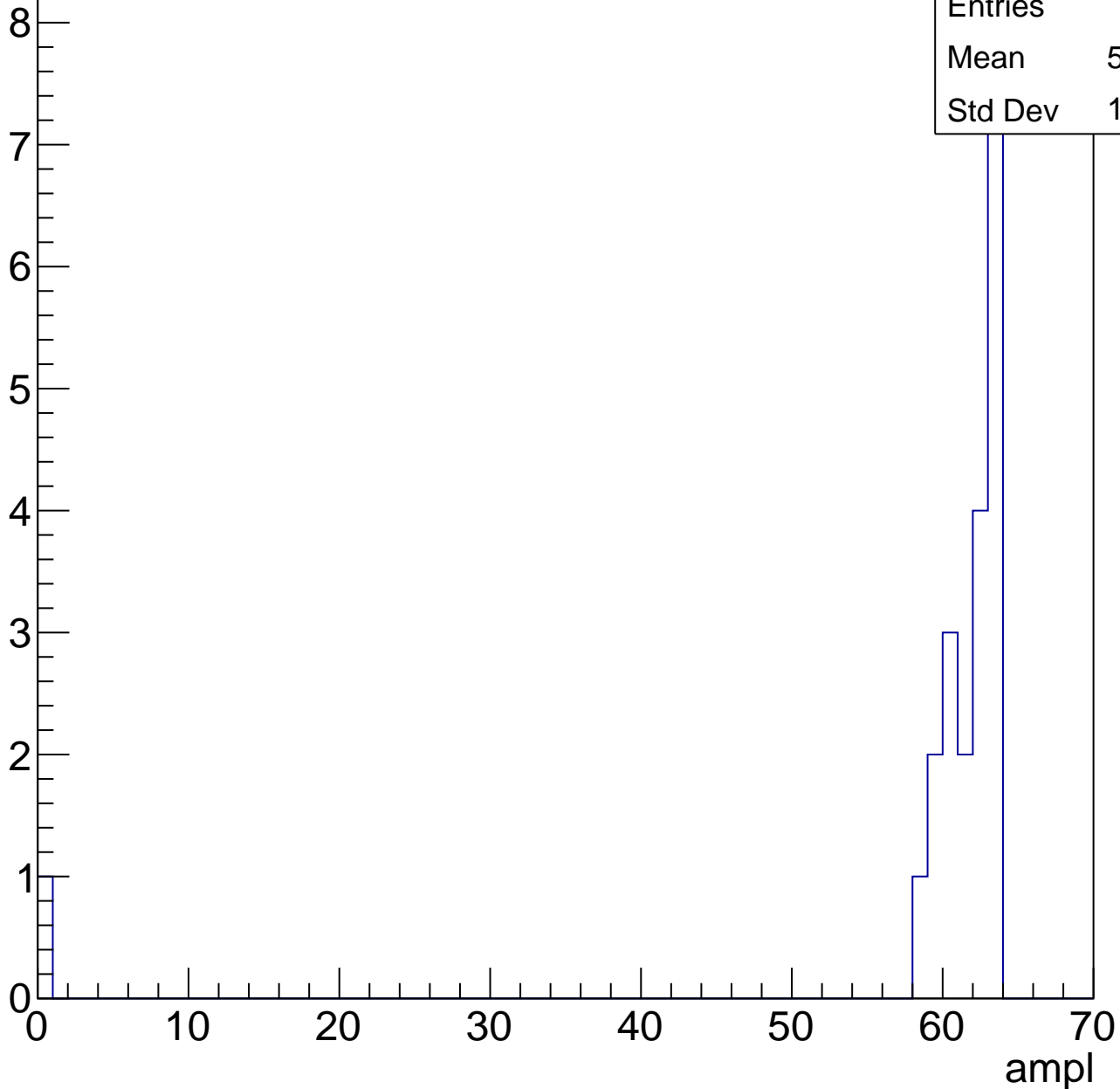


B1L100S, U12-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

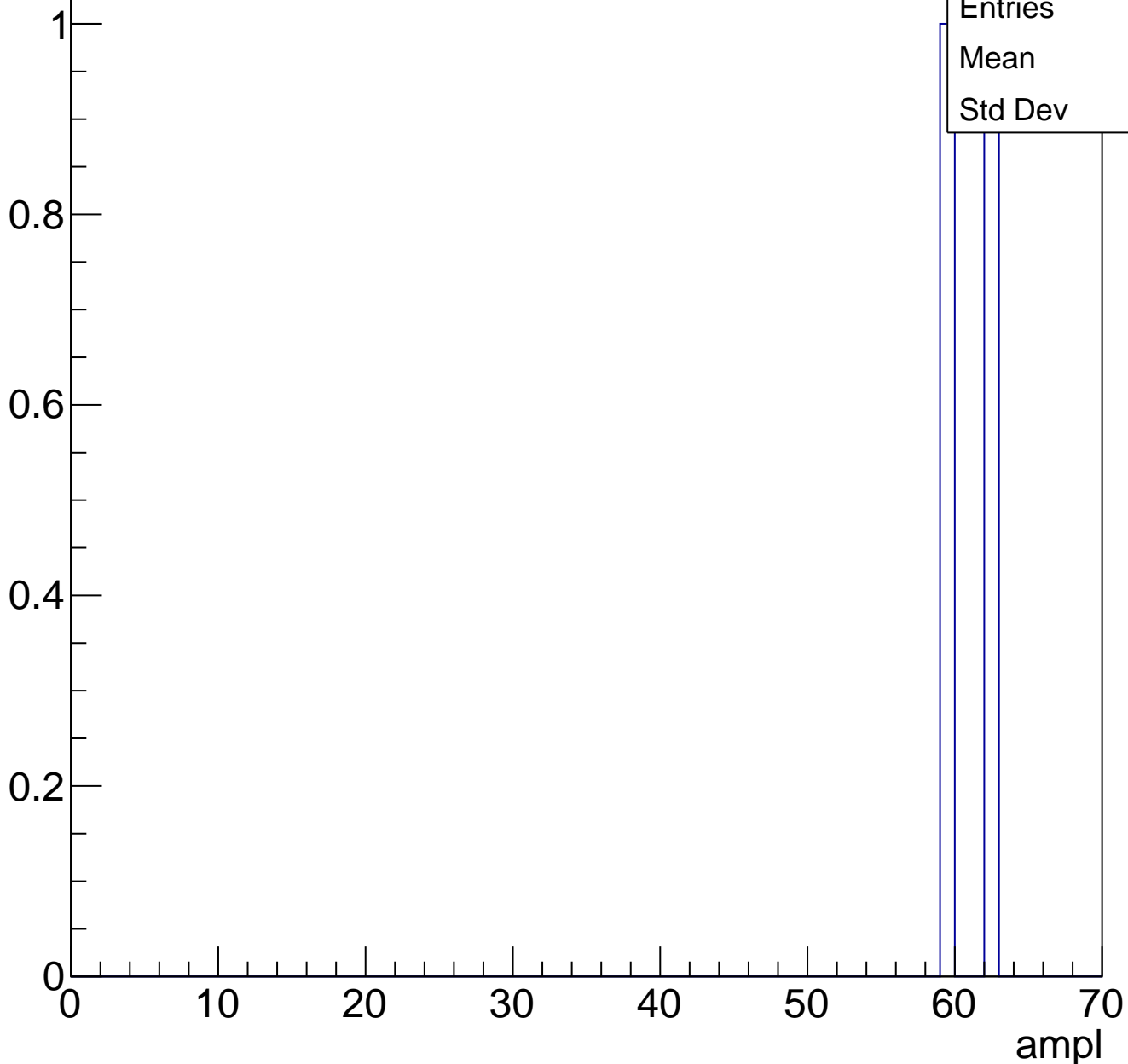
Entries	21
Mean	58.57
Std Dev	13.19



B1L100S, U12-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch79, adc0

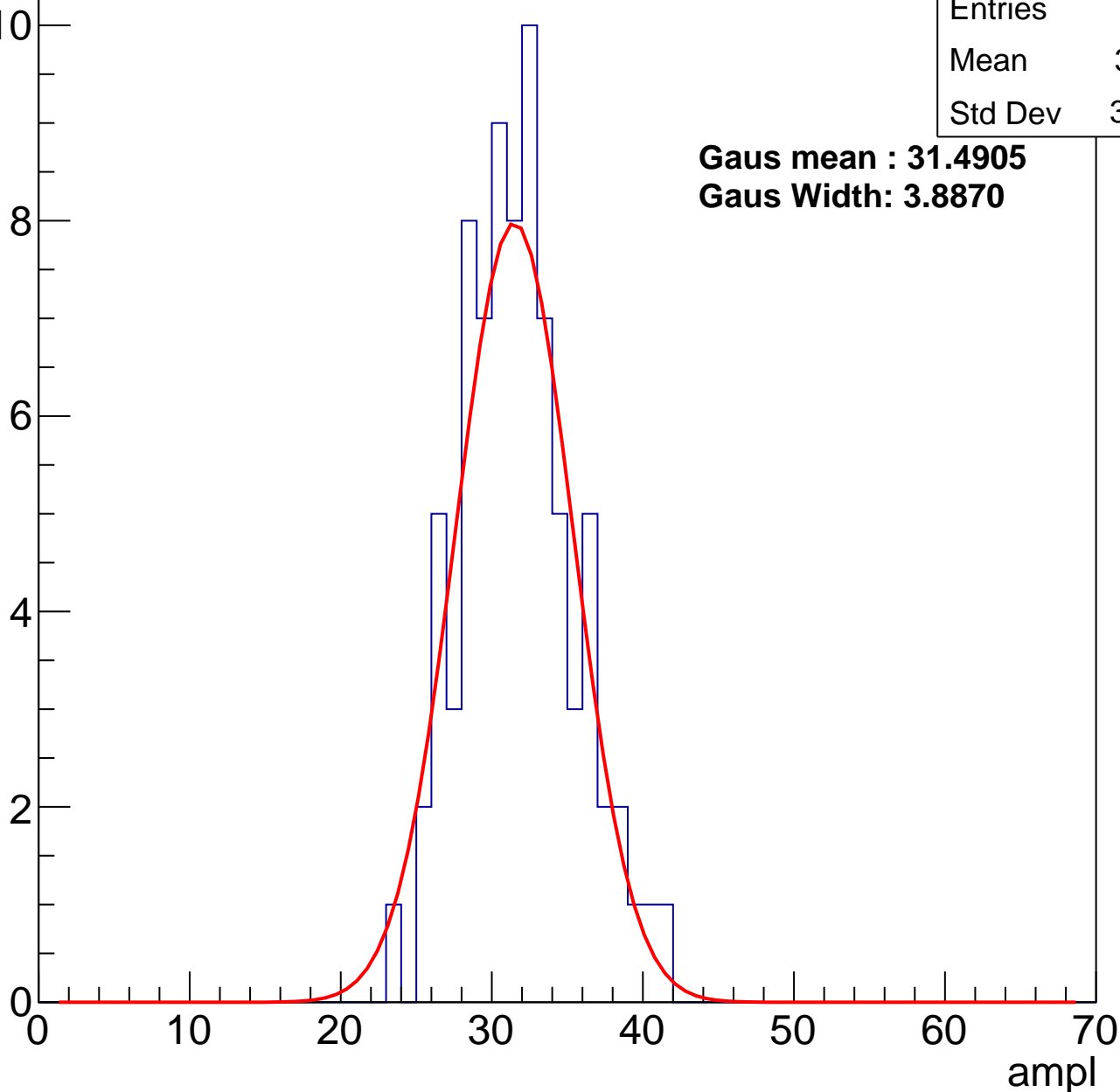
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	31.31
Std Dev	3.676

Gaus mean : 31.4905

Gaus Width: 3.8870



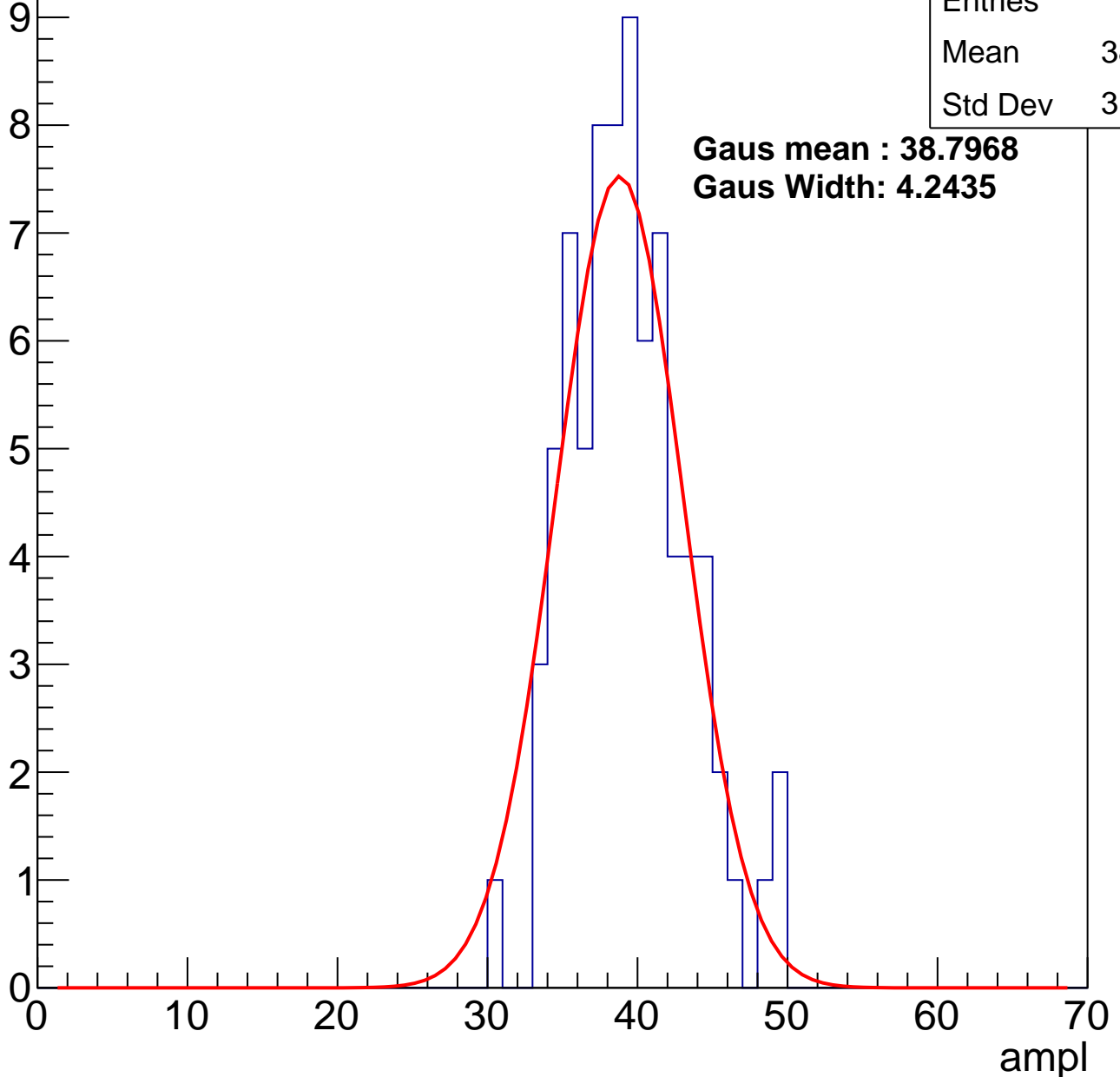
B1L100S, U12-ch79, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	38.96
Std Dev	3.859

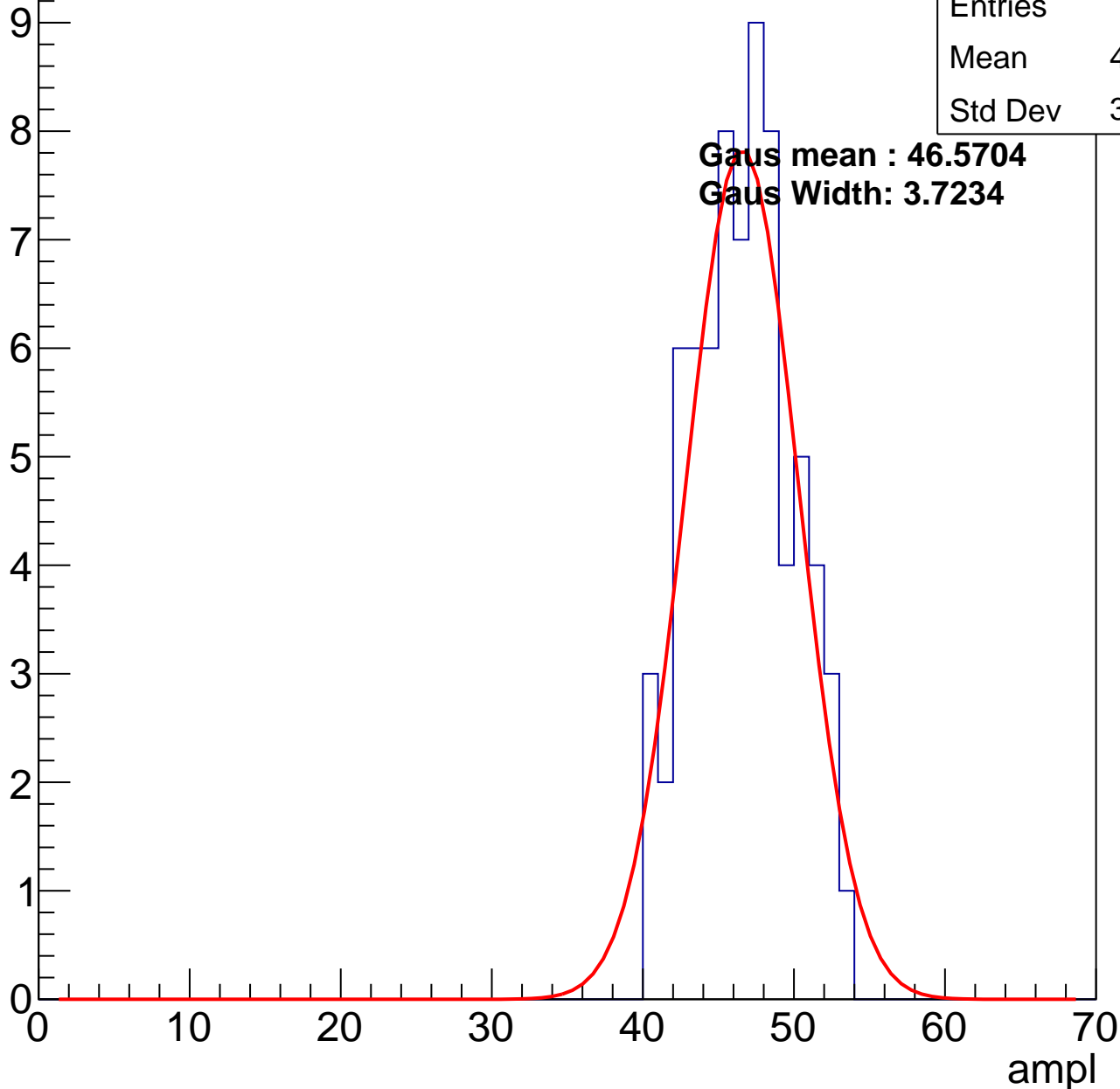
Gaus mean : 38.7968
Gaus Width: 4.2435



B1L100S, U12-ch79, adc2

calib_packv5_042523_0143.root, FC#4, port A2

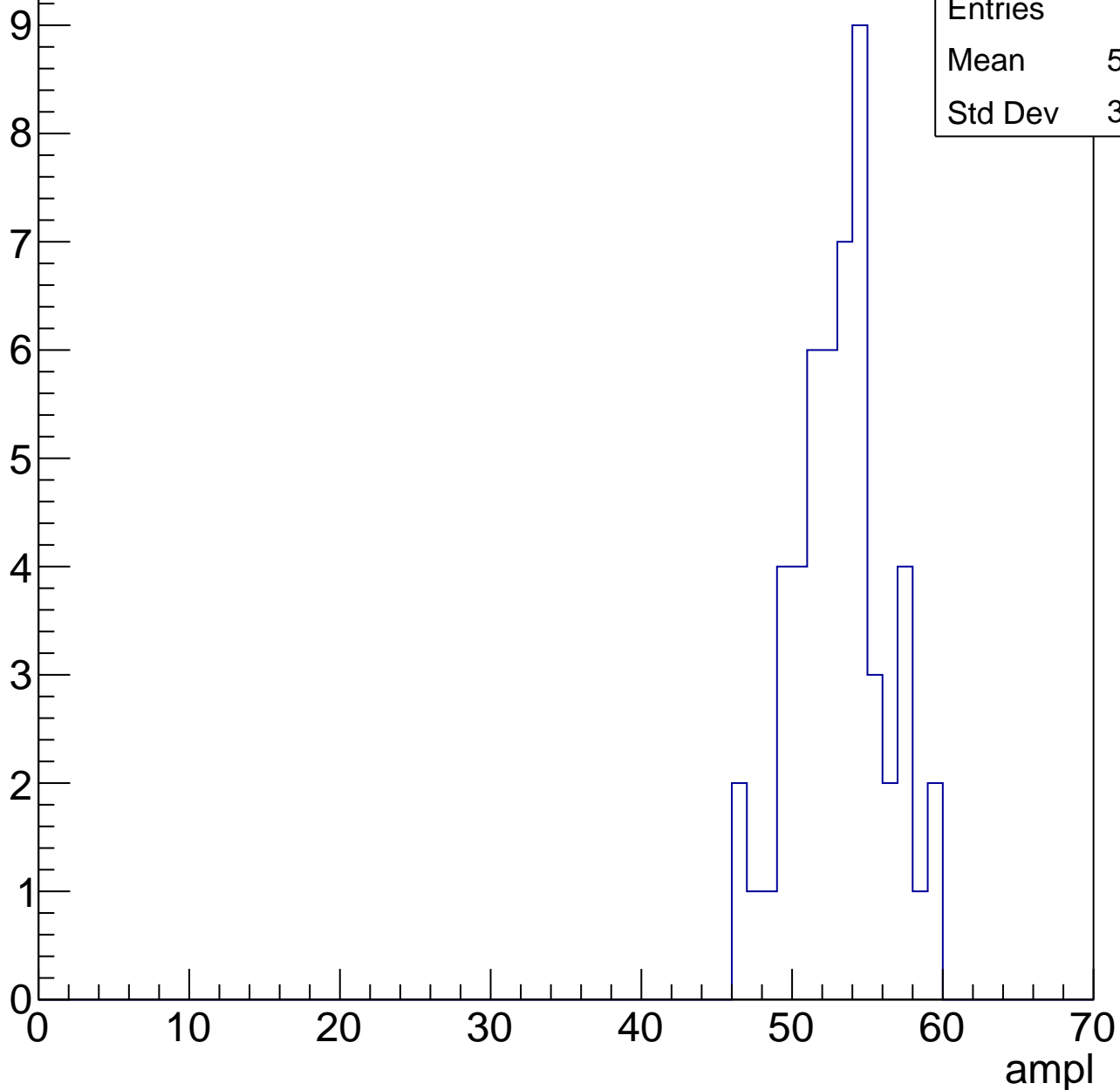
Entry



B1L100S, U12-ch79, adc3

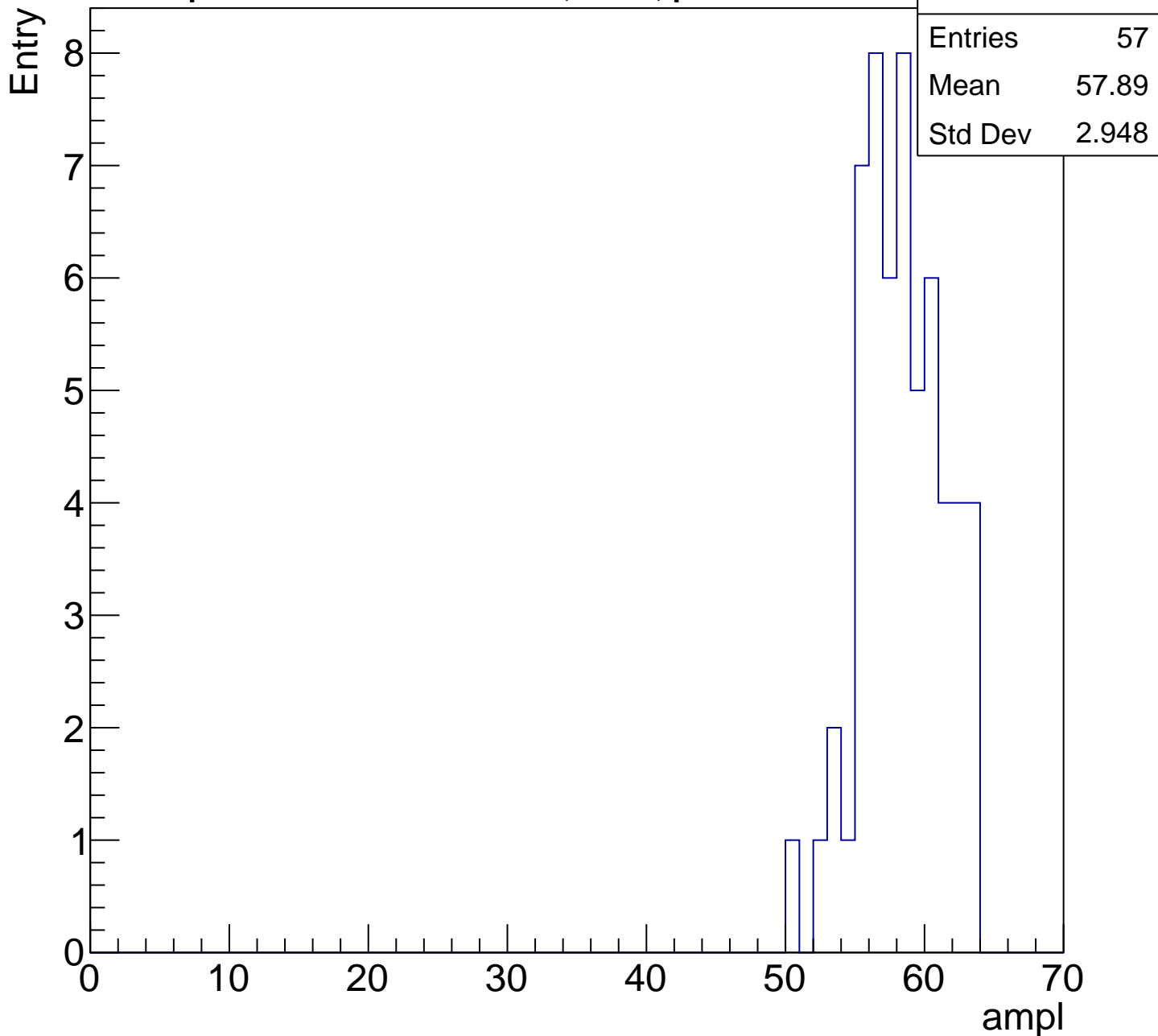
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	27
Mean	58.93
Std Dev	11.64

ampl

0

10

20

30

40

50

60

70

B1L100S, U12-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch80, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	29.43
Std Dev	4.843

Gaus mean : 30.1956

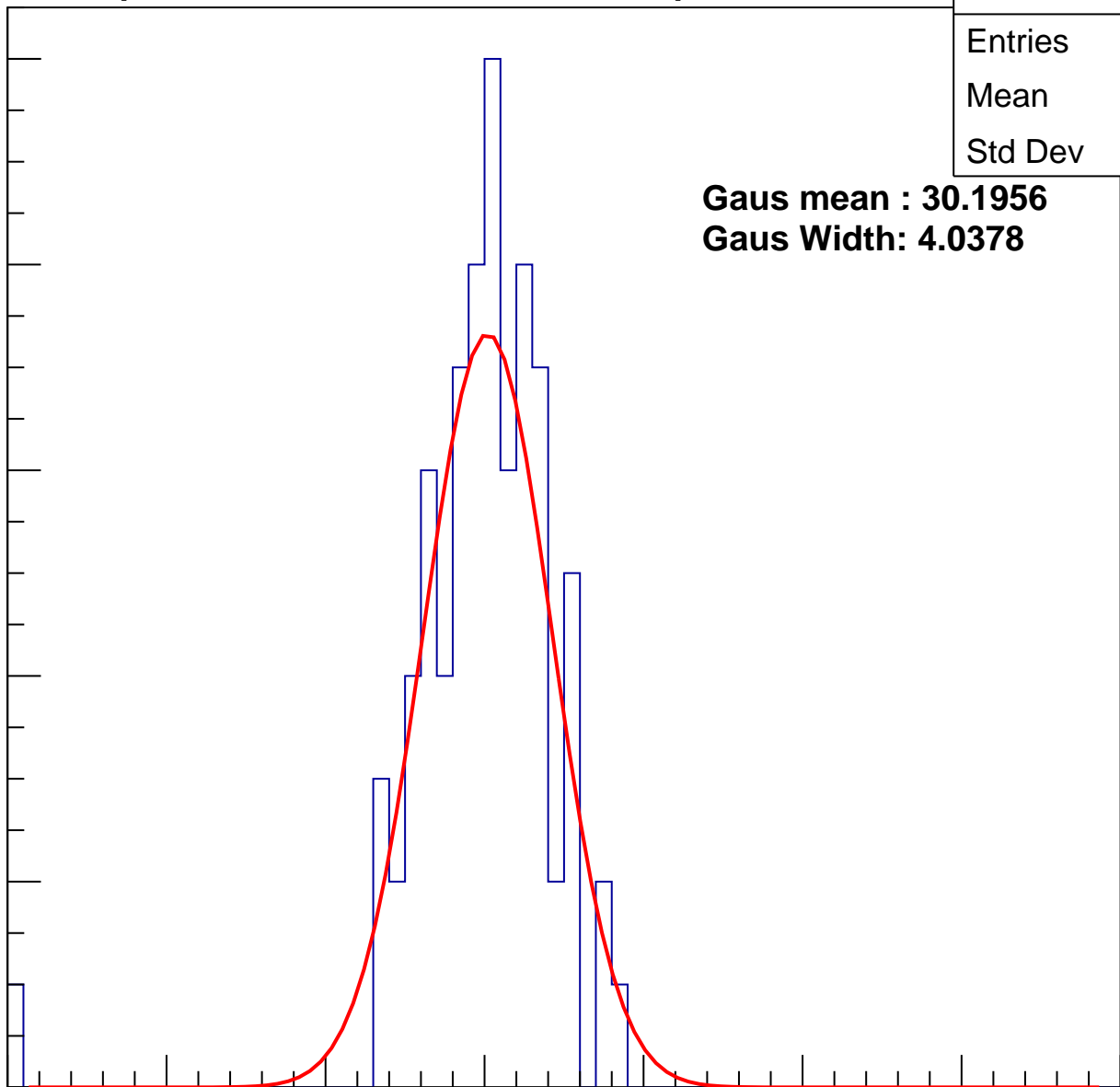
Gaus Width: 4.0378

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch80, adc1

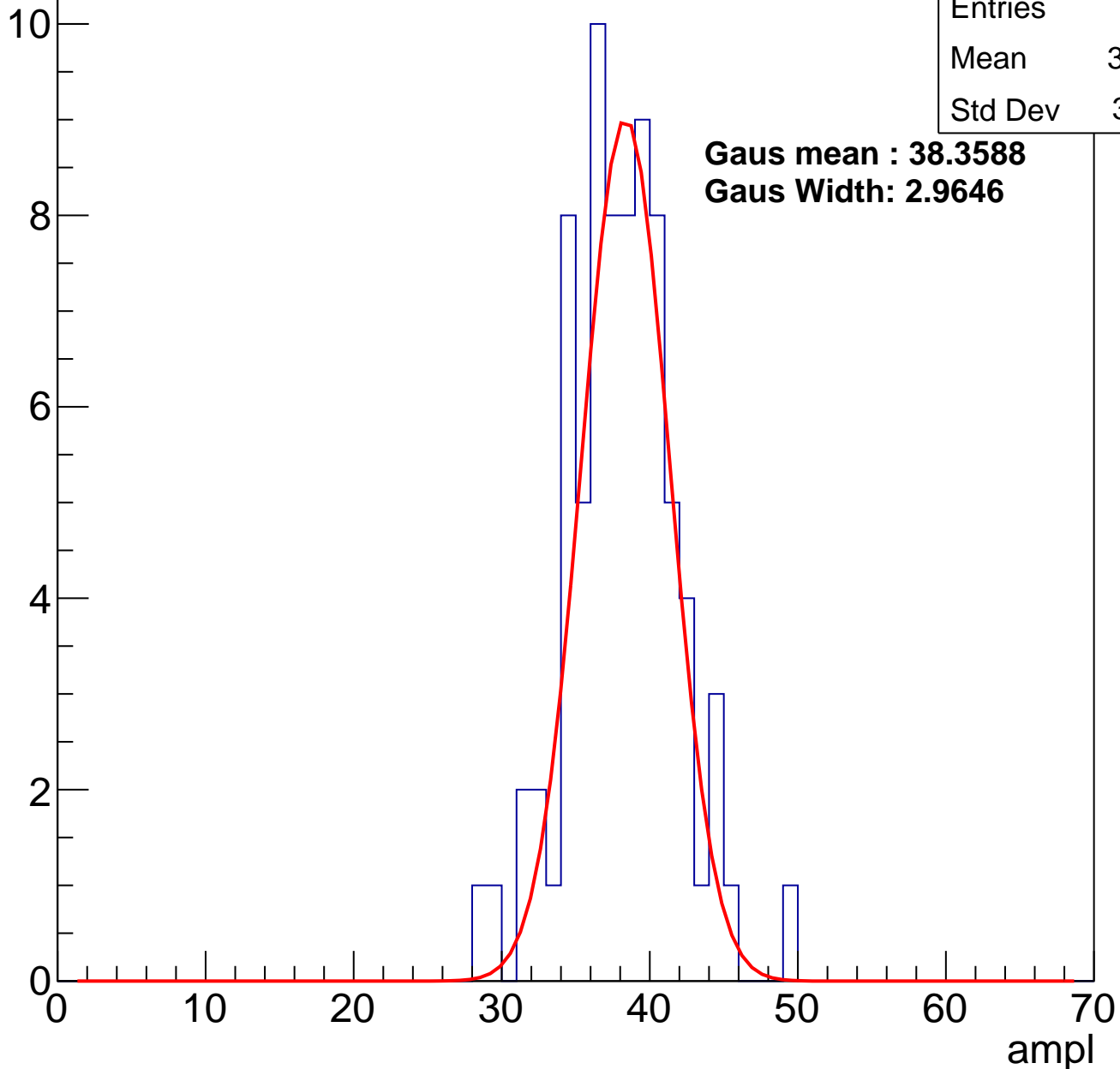
calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	37.64
Std Dev	3.651

Gaus mean : 38.3588

Gaus Width: 2.9646

Entry



B1L100S, U12-ch80, adc2

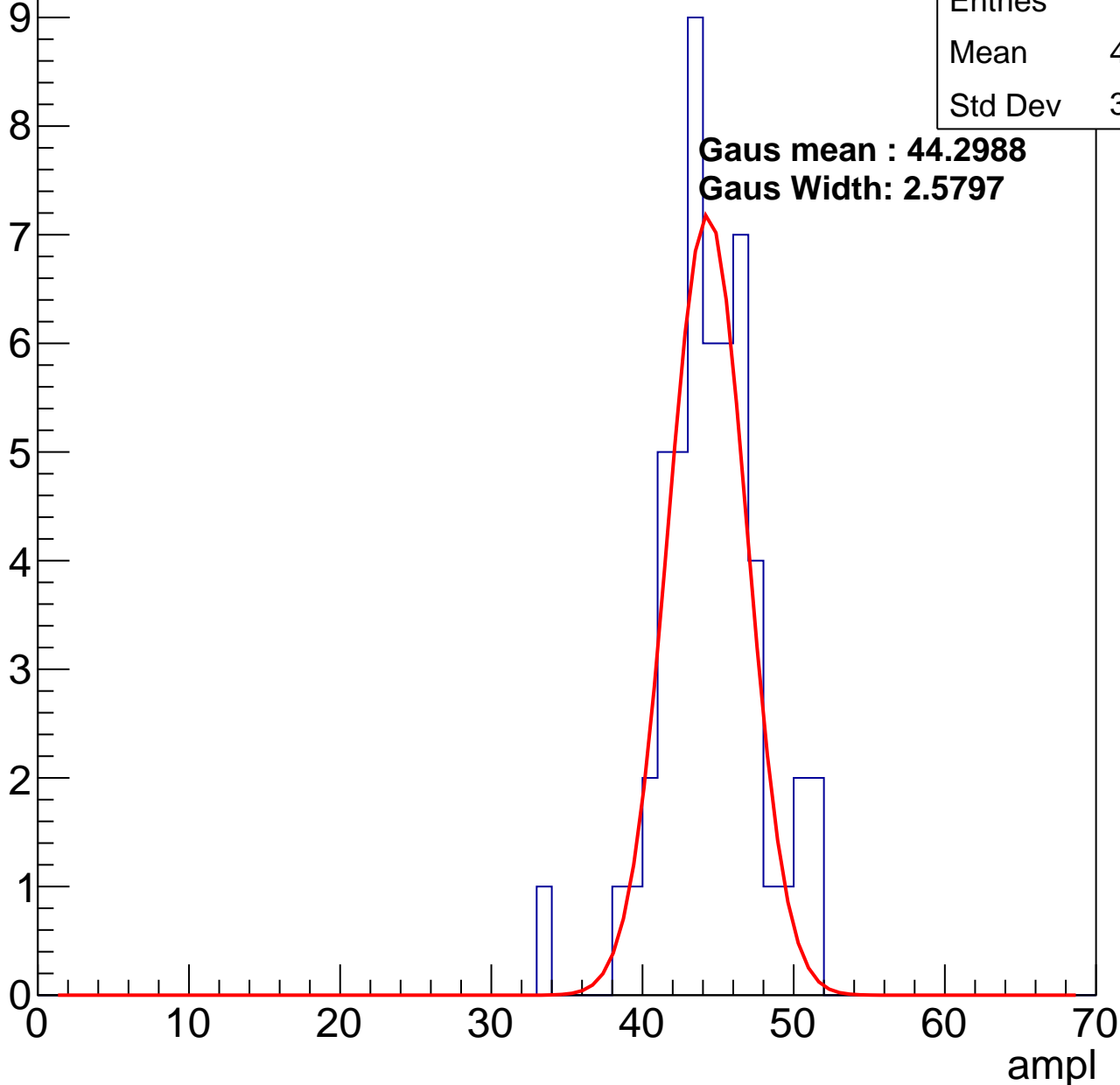
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	44.06
Std Dev	3.276

Gaus mean : 44.2988

Gaus Width: 2.5797

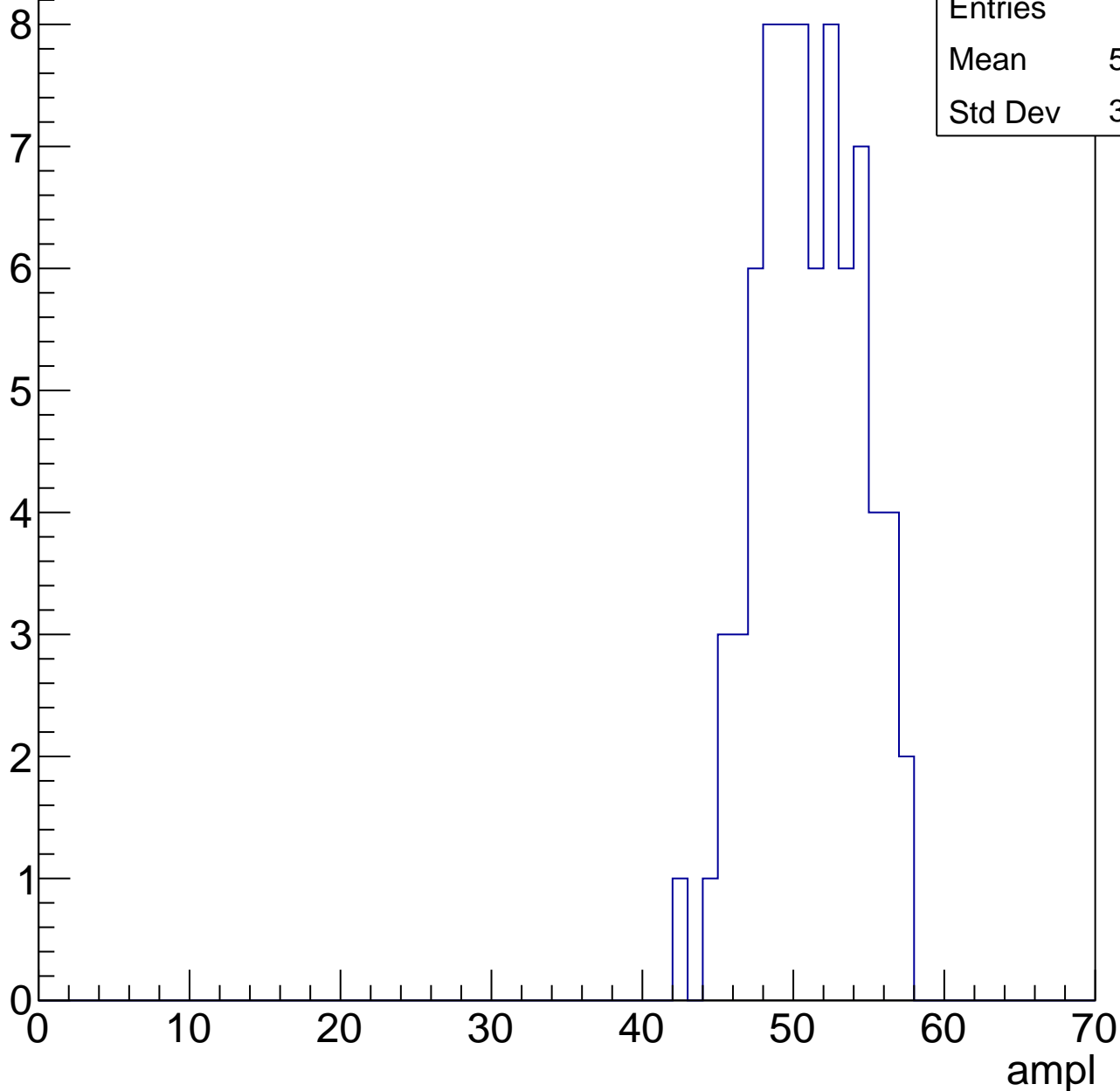


B1L100S, U12-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

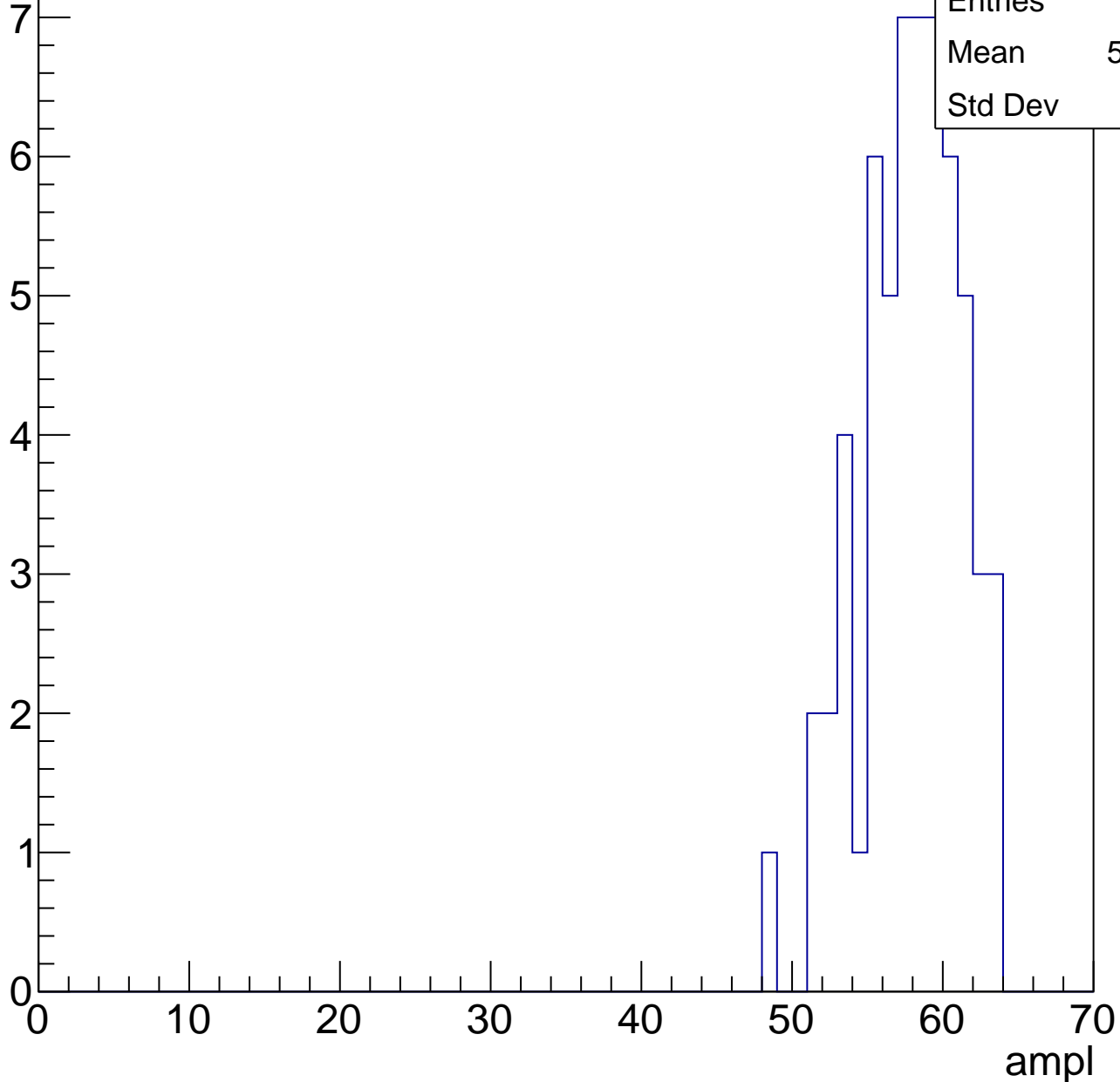
Entries	75
Mean	50.57
Std Dev	3.343



B1L100S, U12-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch80, adc5

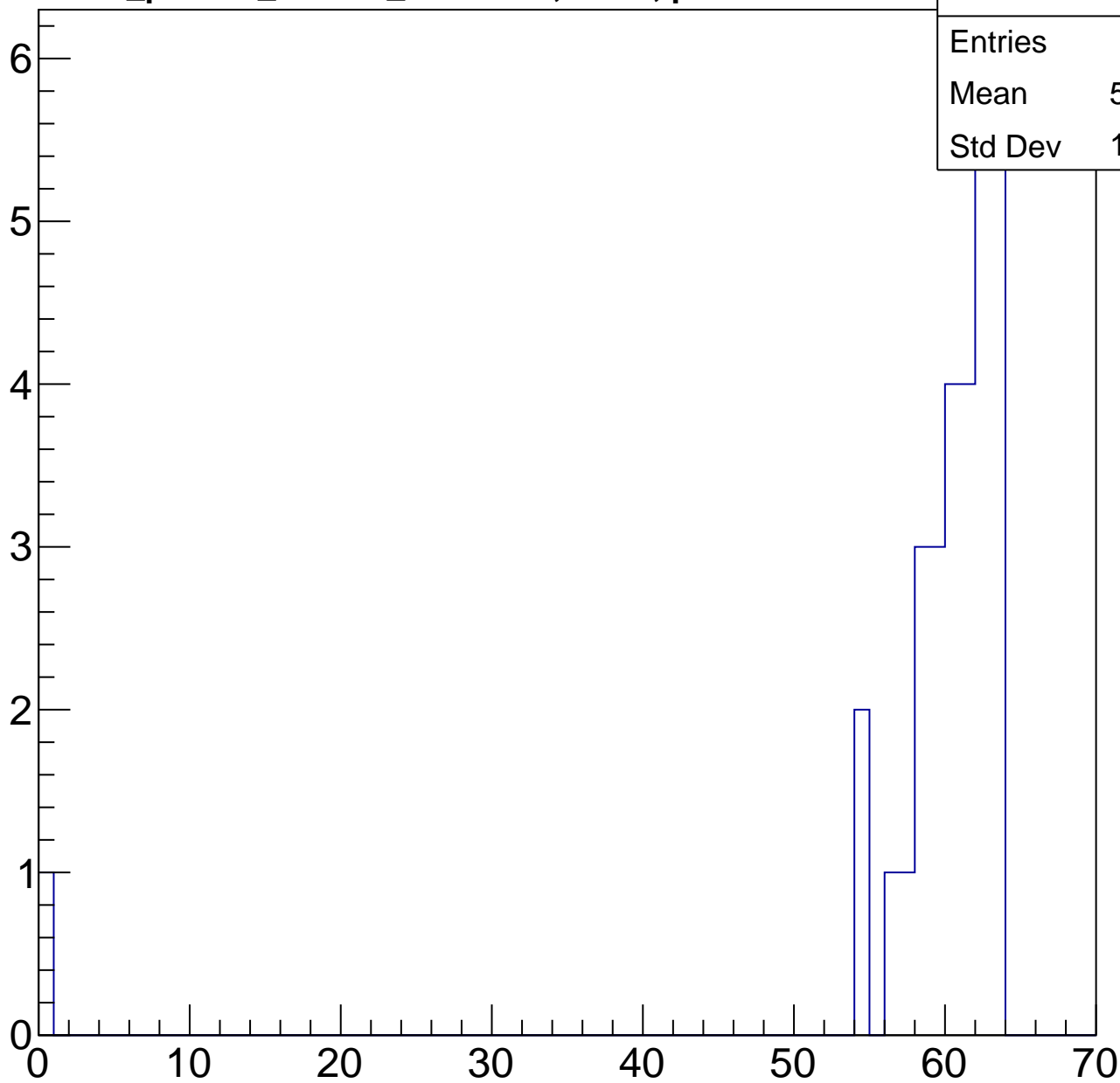
calib_packv5_042523_0143.root, FC#4, port A2

Entry

6
5
4
3
2
1
0

Entries	31
Mean	58.26
Std Dev	10.92

ampl



B1L100S, U12-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch81, adc0

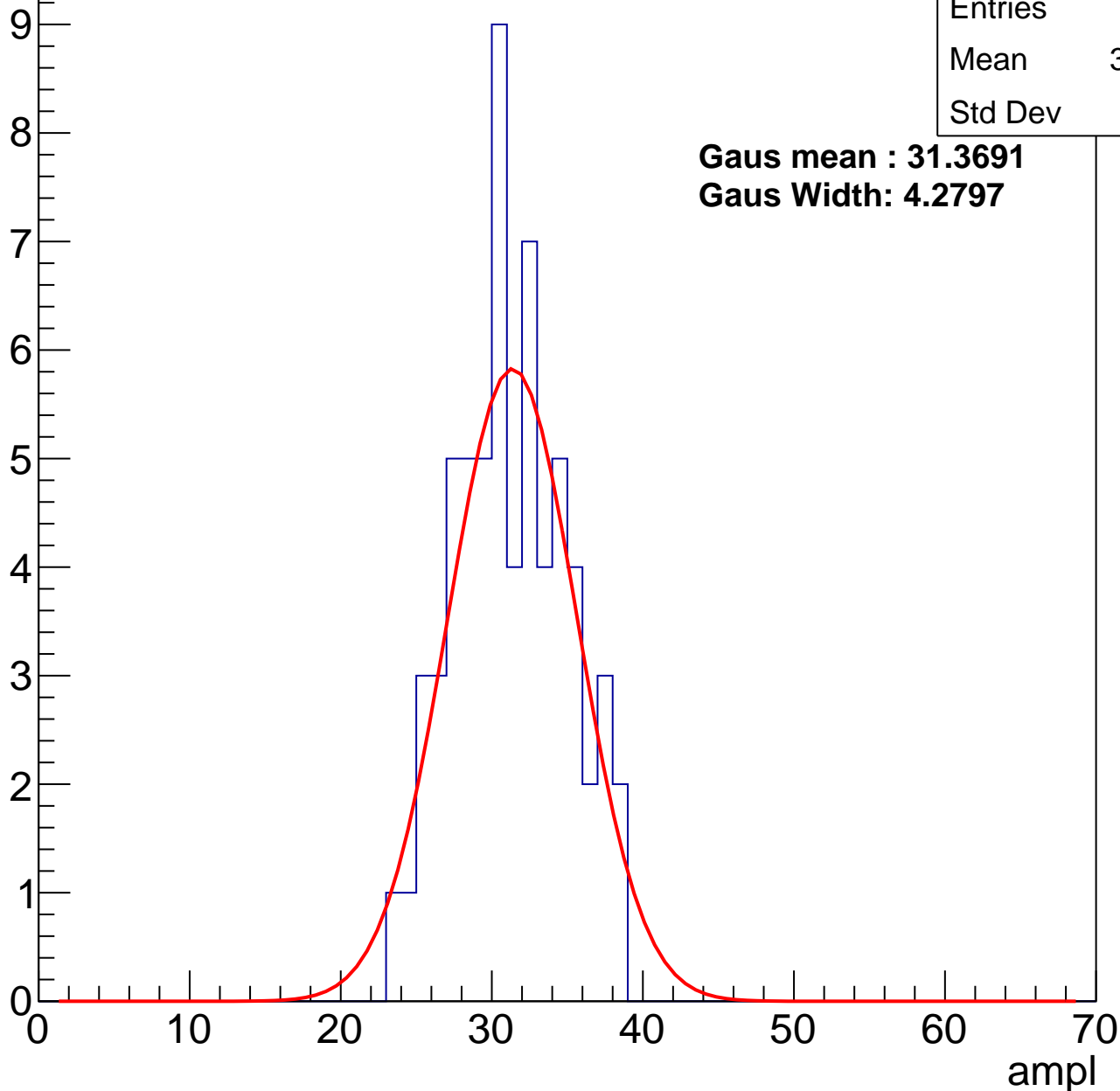
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	30.78
Std Dev	3.64

Gaus mean : 31.3691

Gaus Width: 4.2797



B1L100S, U12-ch81, adc1

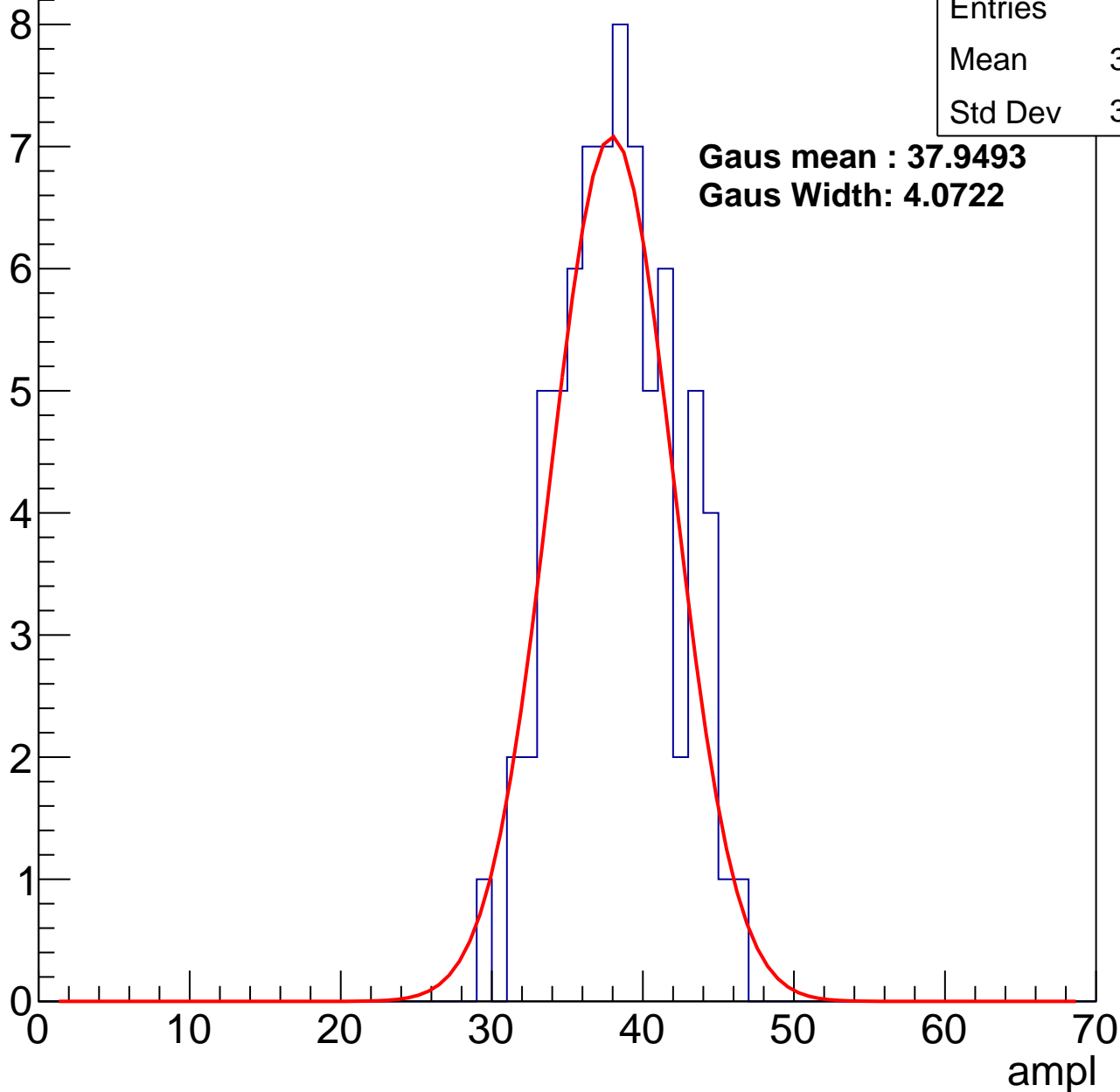
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.84
Std Dev	3.745

Gaus mean : 37.9493

Gaus Width: 4.0722



B1L100S, U12-ch81, adc2

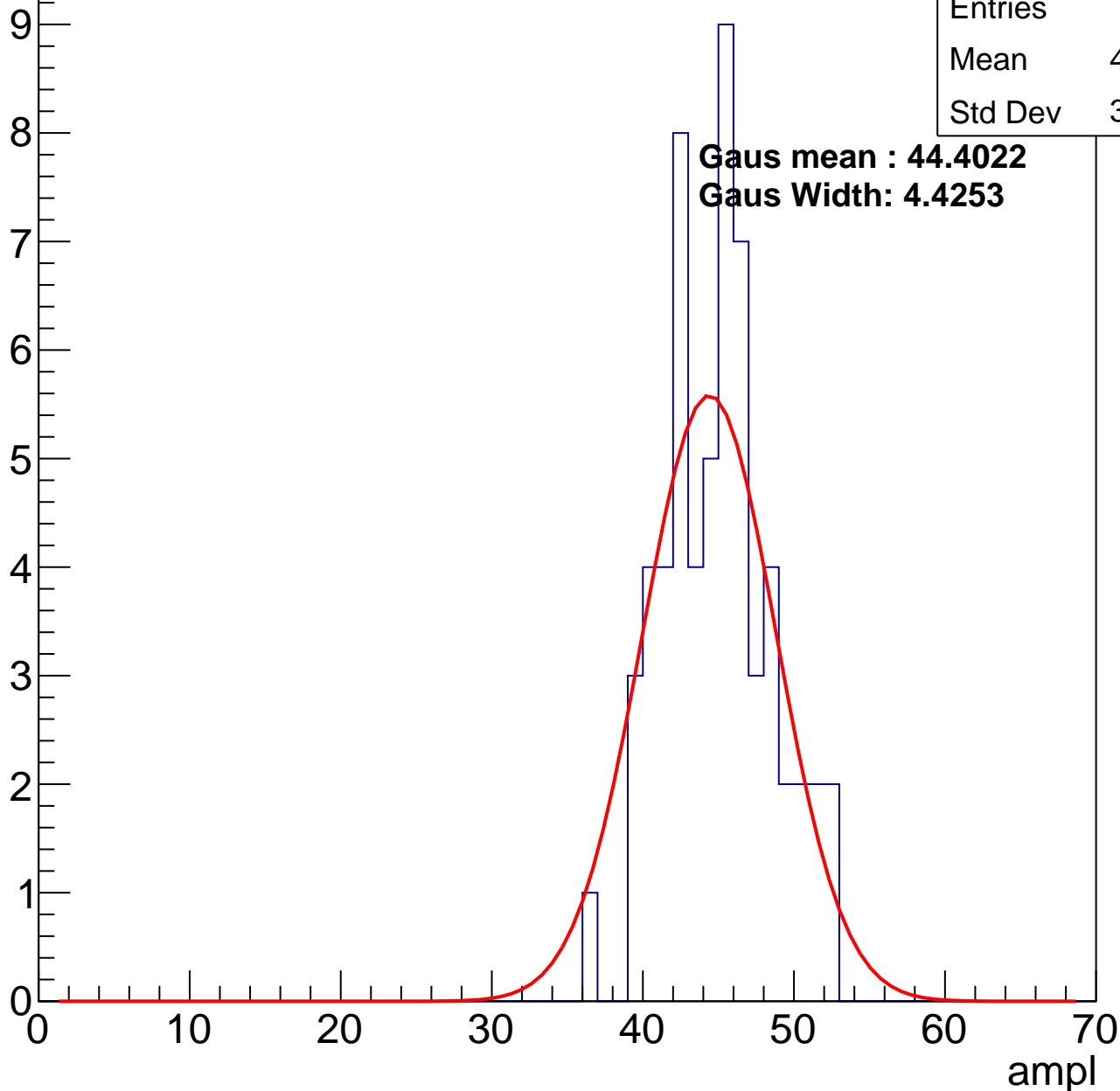
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.48
Std Dev	3.505

Gaus mean : 44.4022

Gaus Width: 4.4253

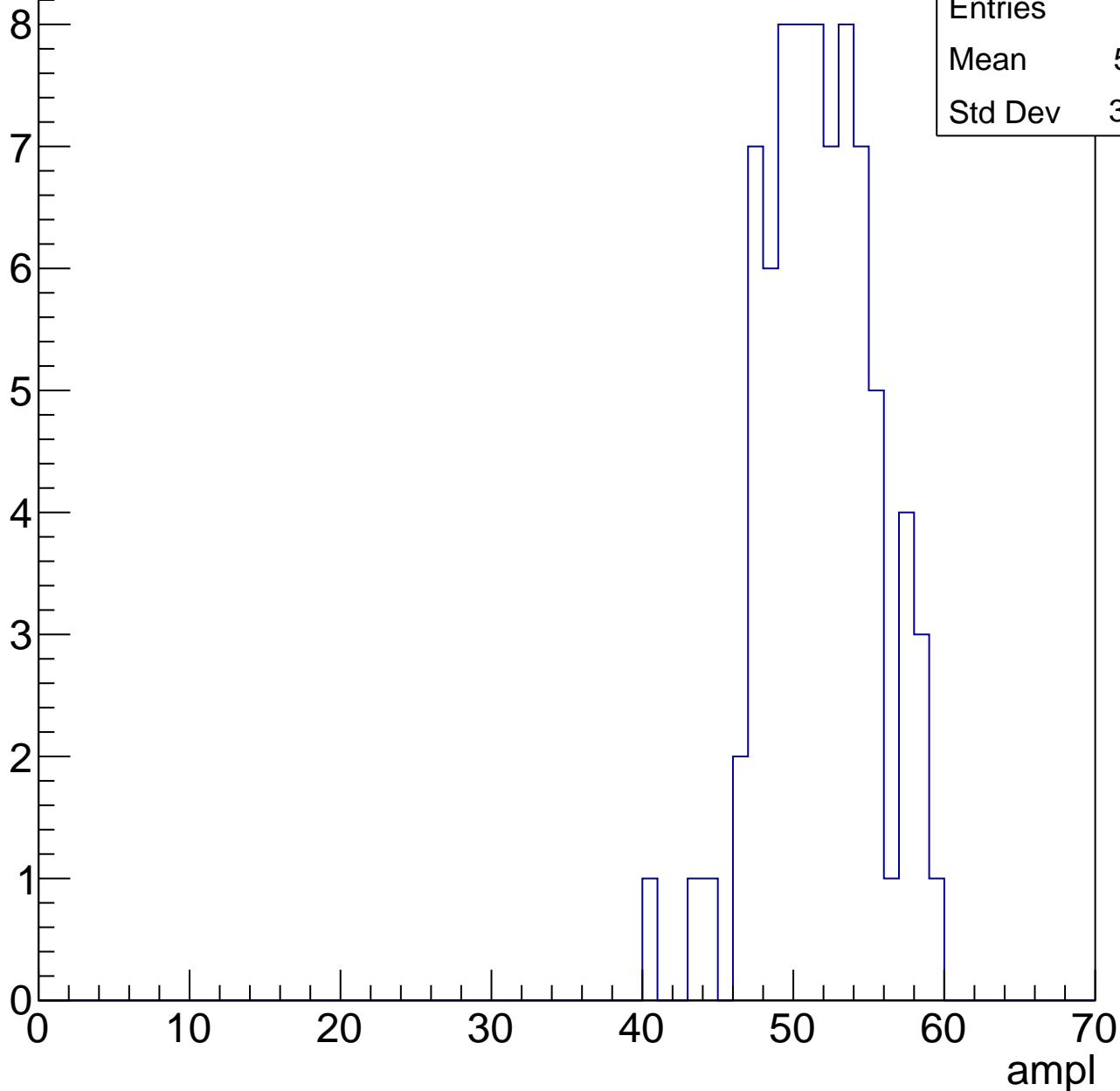


B1L100S, U12-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

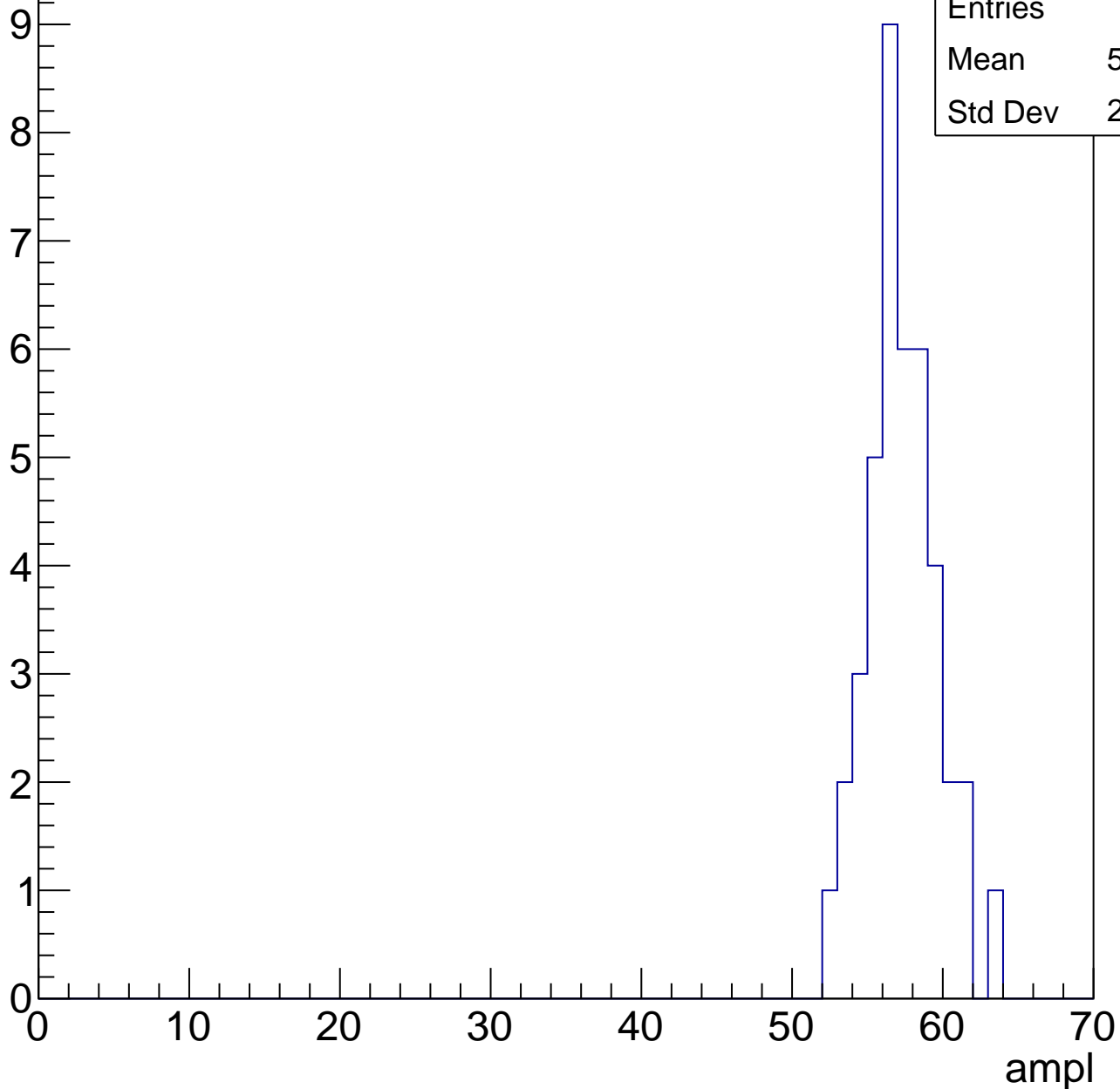
Entries	78
Mean	51.21
Std Dev	3.667



B1L100S, U12-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

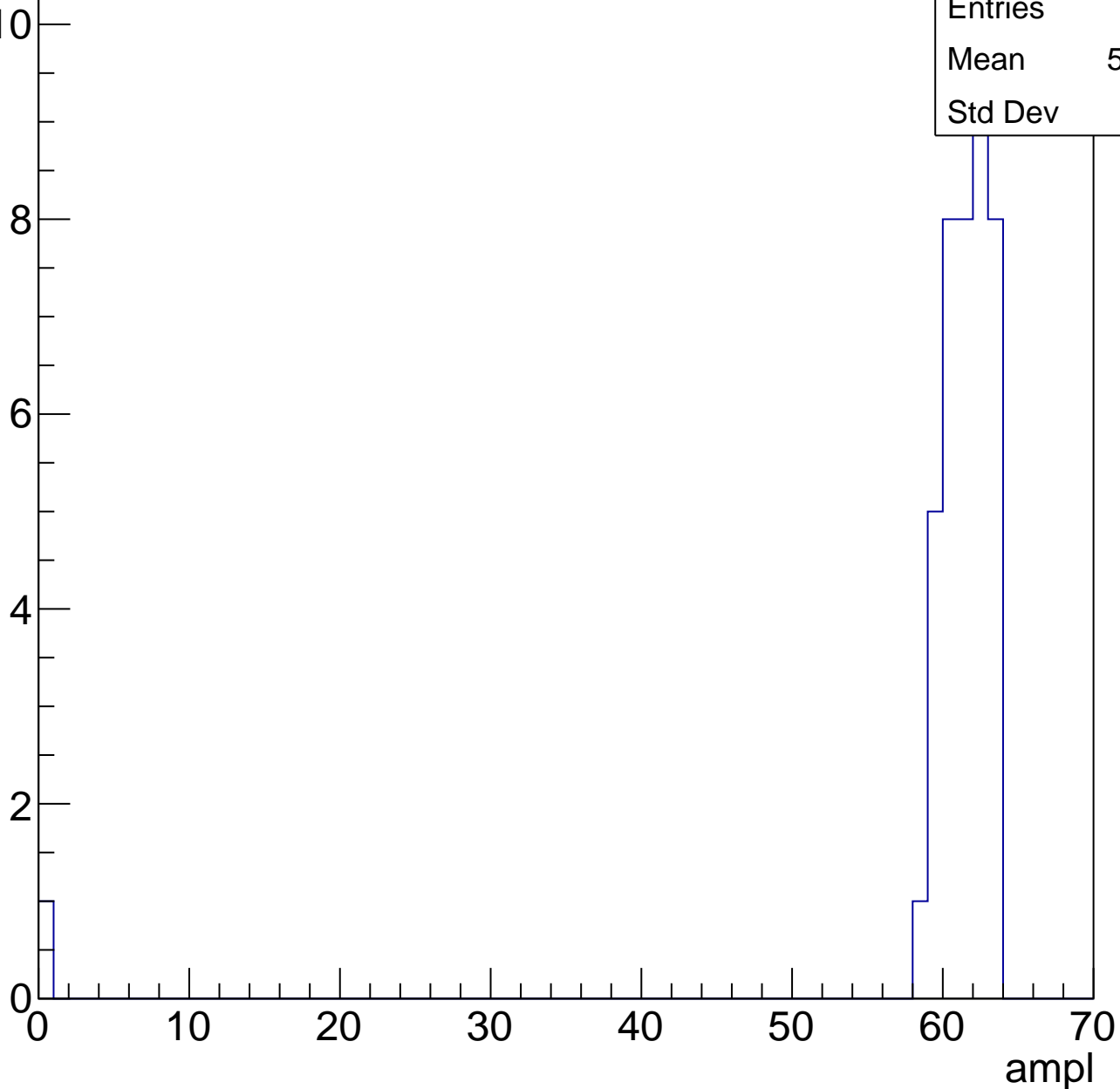


B1L100S, U12-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	59.63
Std Dev	9.53



B1L100S, U12-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch82, adc0

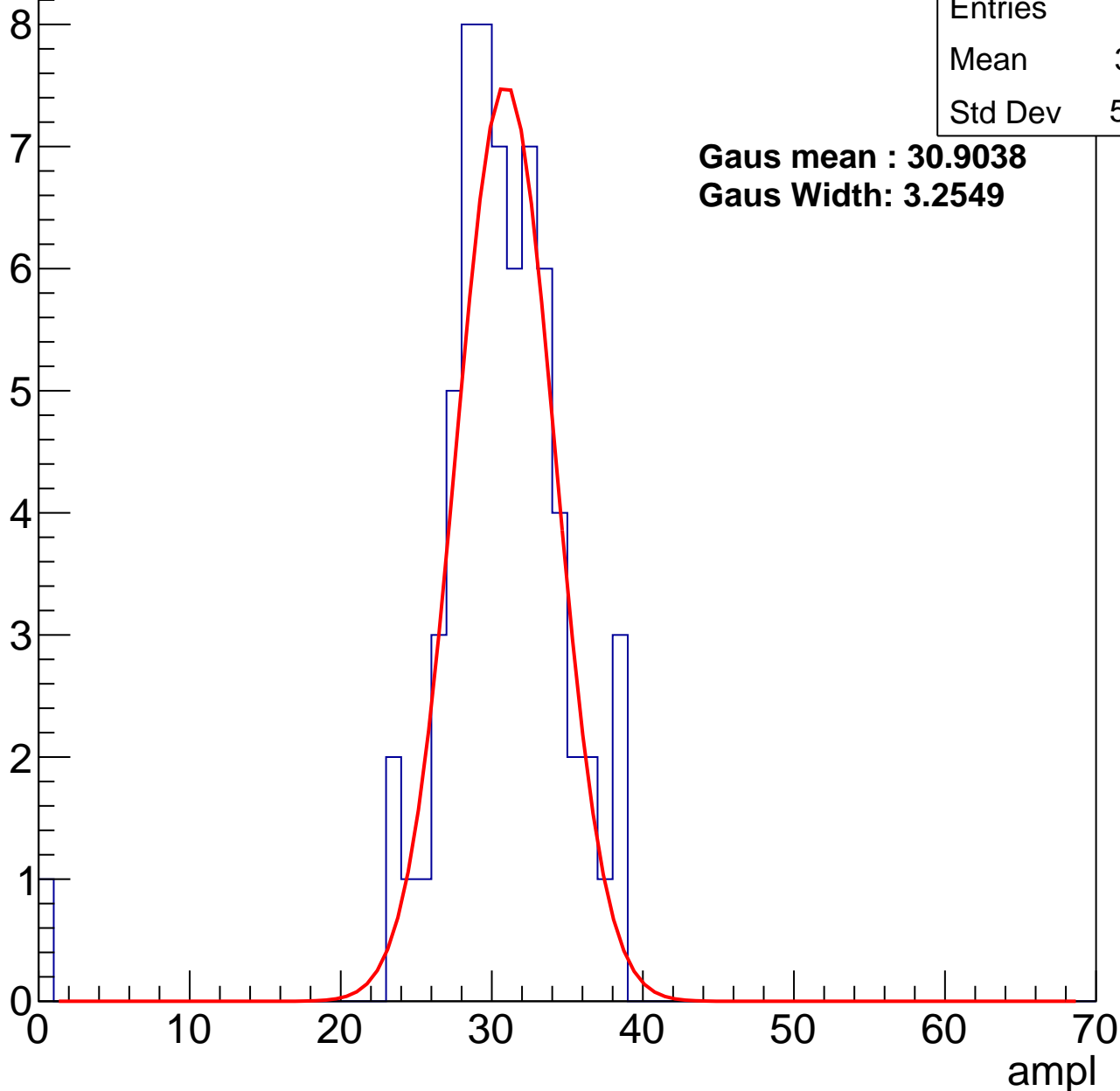
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.01
Std Dev	5.048

Gaus mean : 30.9038

Gaus Width: 3.2549



B1L100S, U12-ch82, adc1

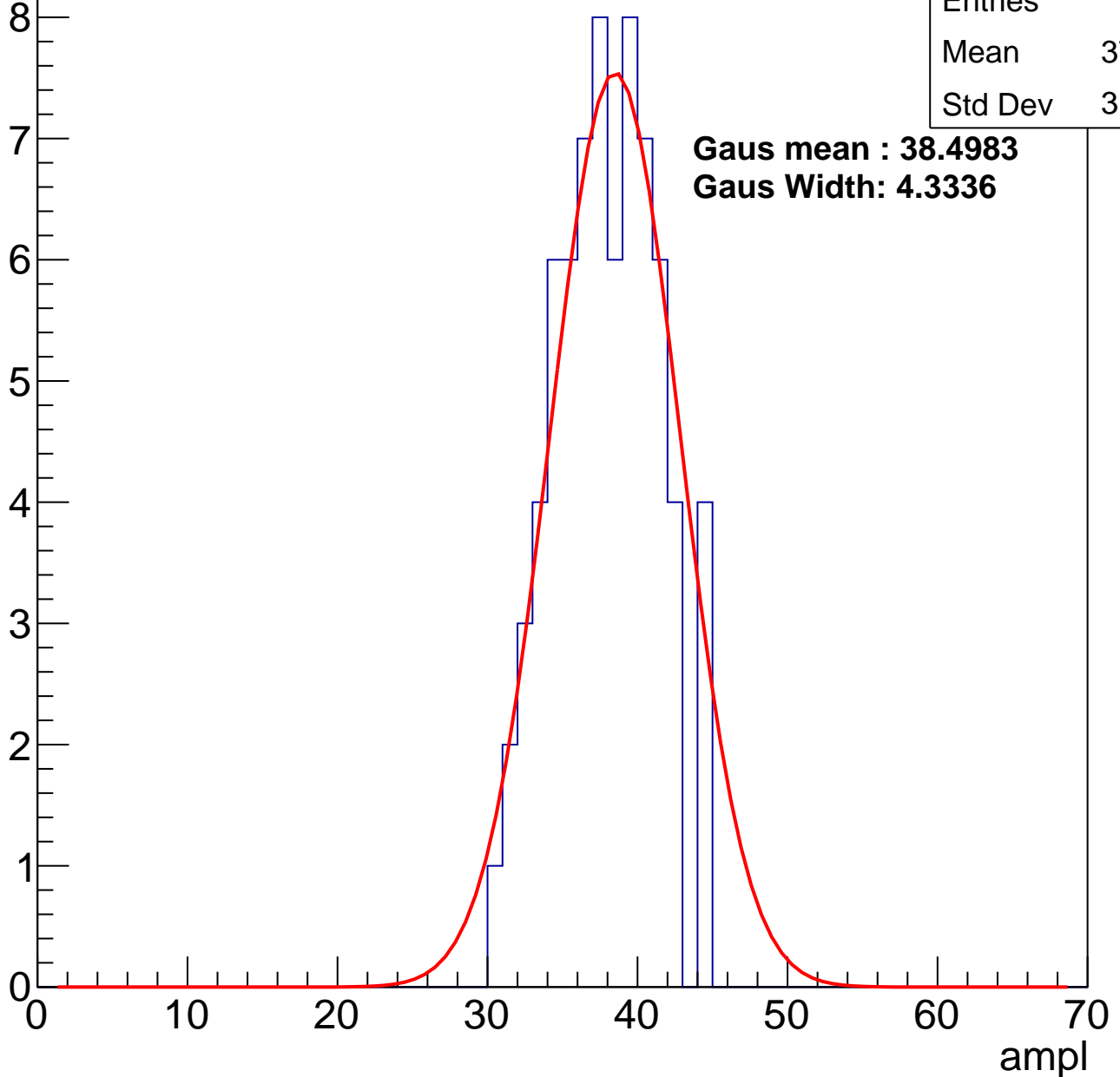
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.39
Std Dev	3.389

Gaus mean : 38.4983

Gaus Width: 4.3336



B1L100S, U12-ch82, adc2

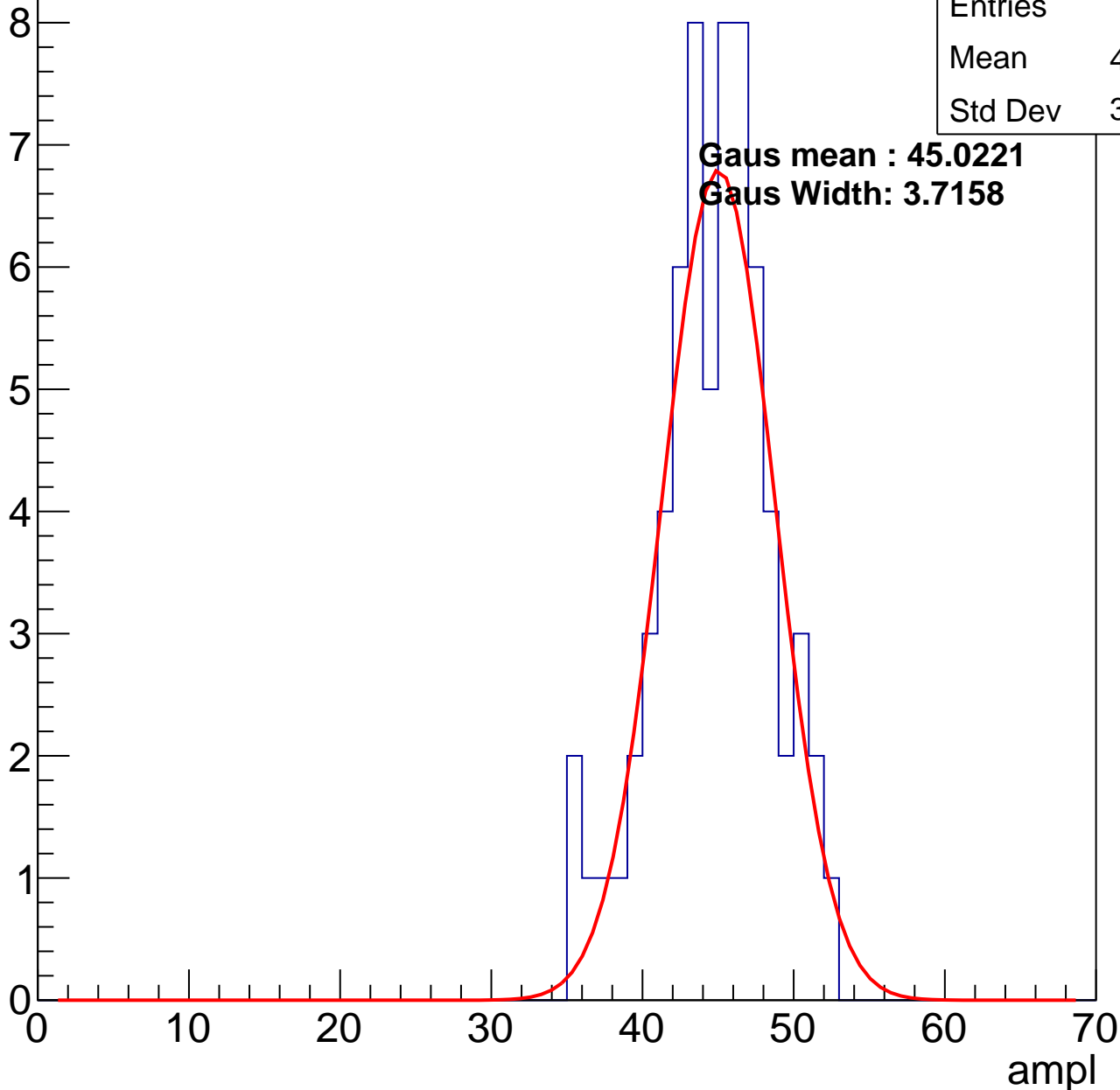
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.22
Std Dev	3.769

Gaus mean : 45.0221

Gaus Width: 3.7158

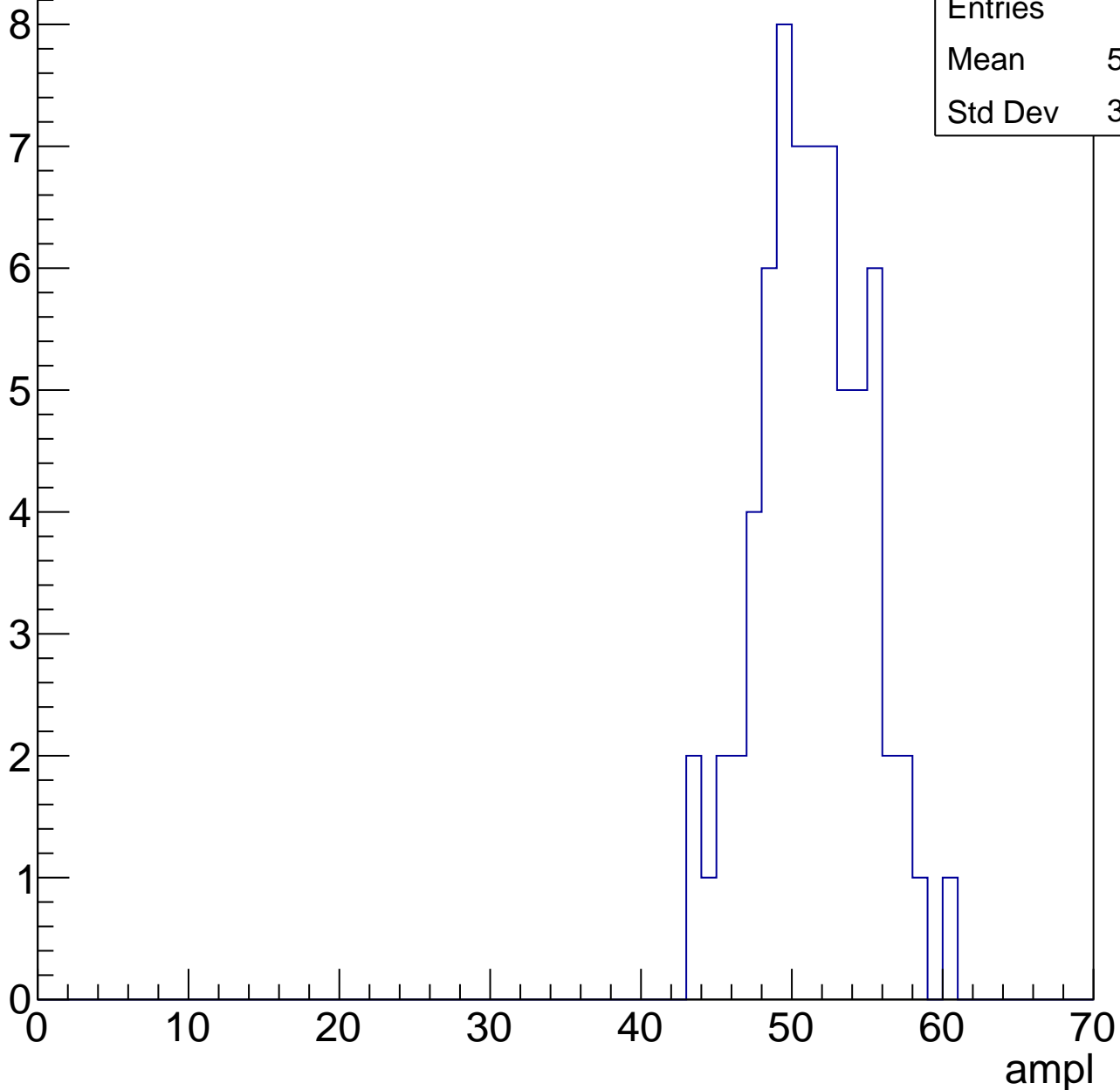


B1L100S, U12-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.88
Std Dev	3.587

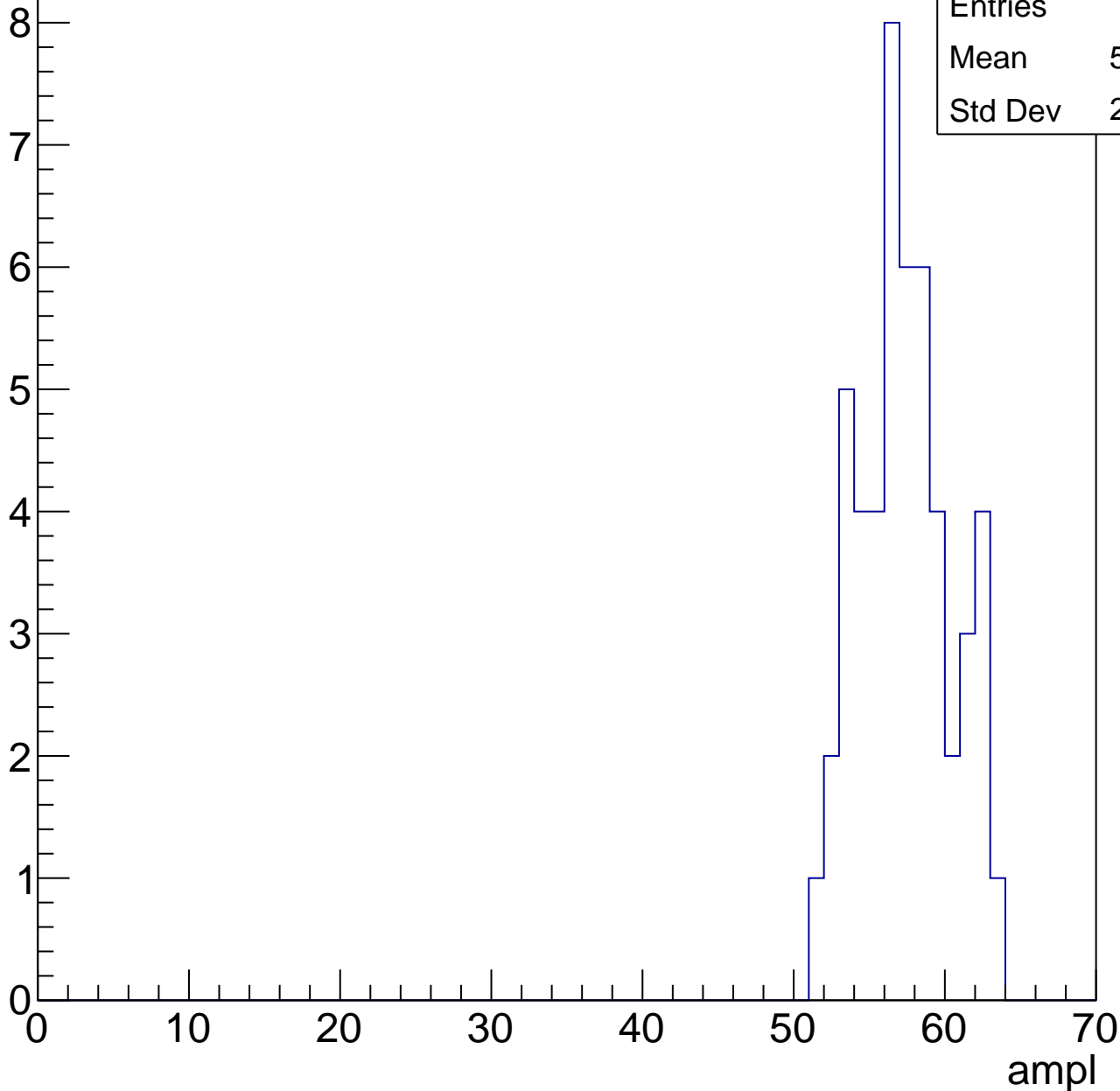


B1L100S, U12-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	56.88
Std Dev	2.998

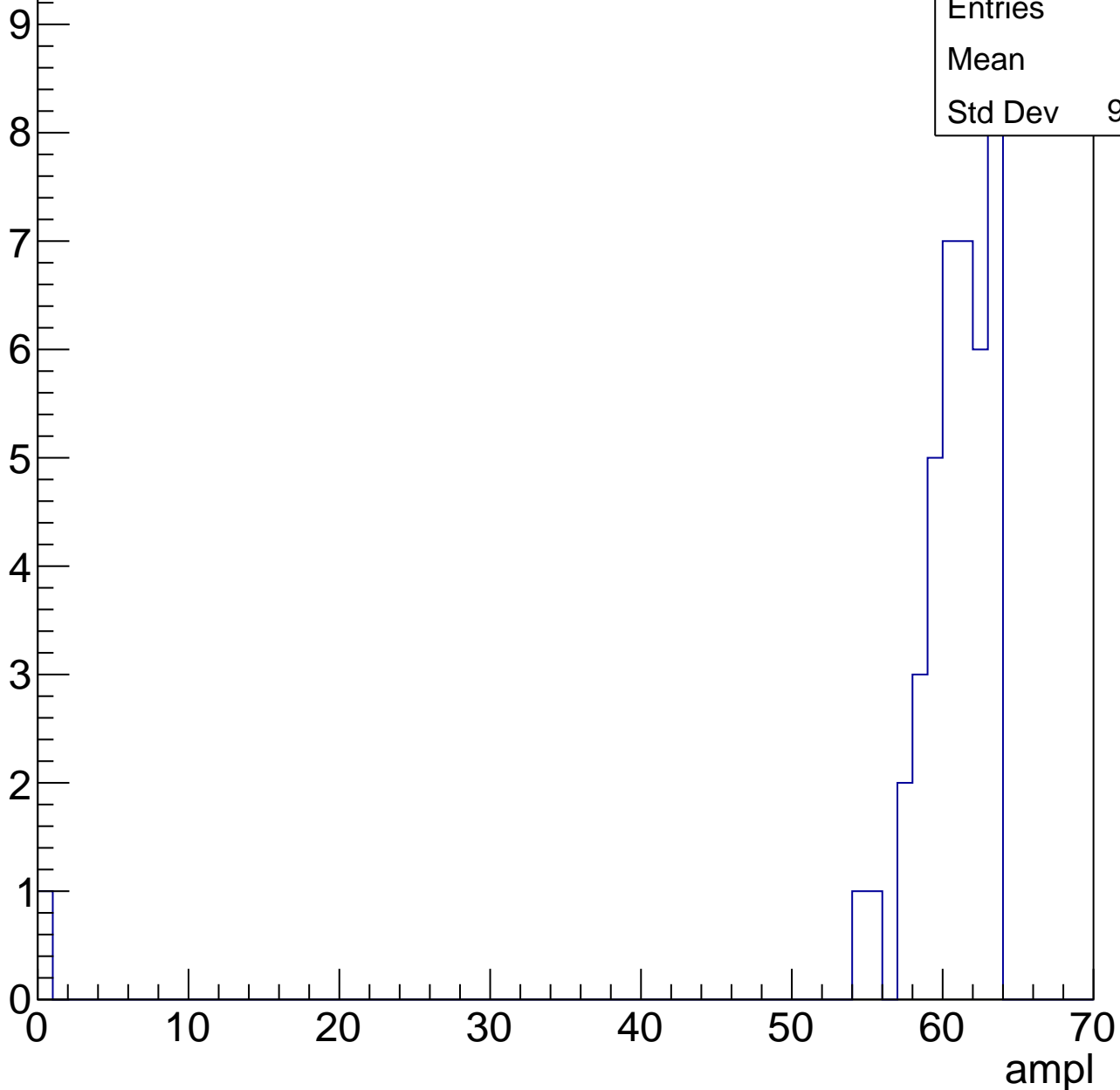


B1L100S, U12-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	42
Mean	59
Std Dev	9.469



B1L100S, U12-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	1
Mean	59
Std Dev	0

B1L100S, U12-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch83, adc0

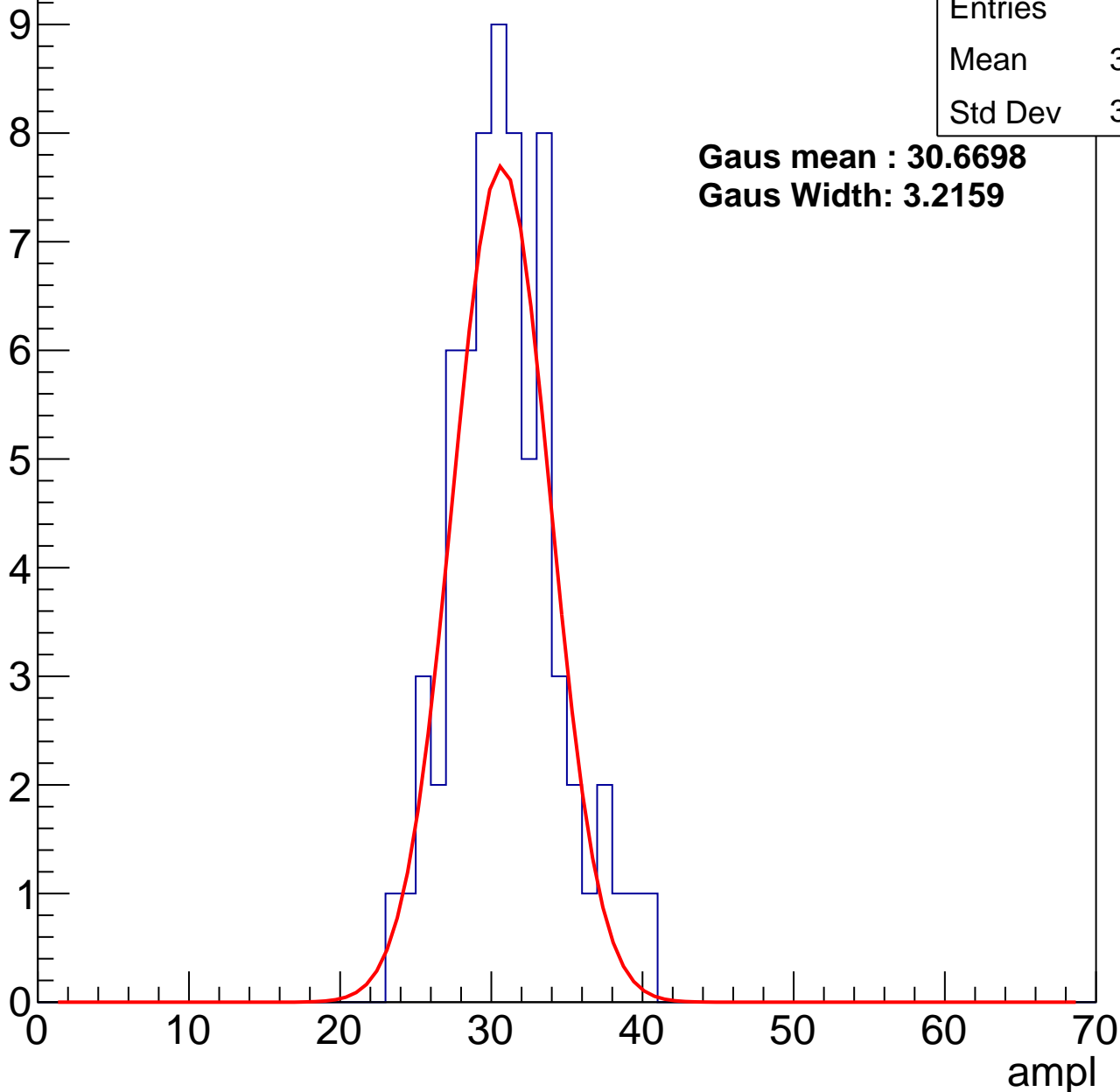
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	30.54
Std Dev	3.504

Gaus mean : 30.6698

Gaus Width: 3.2159



B1L100S, U12-ch83, adc1

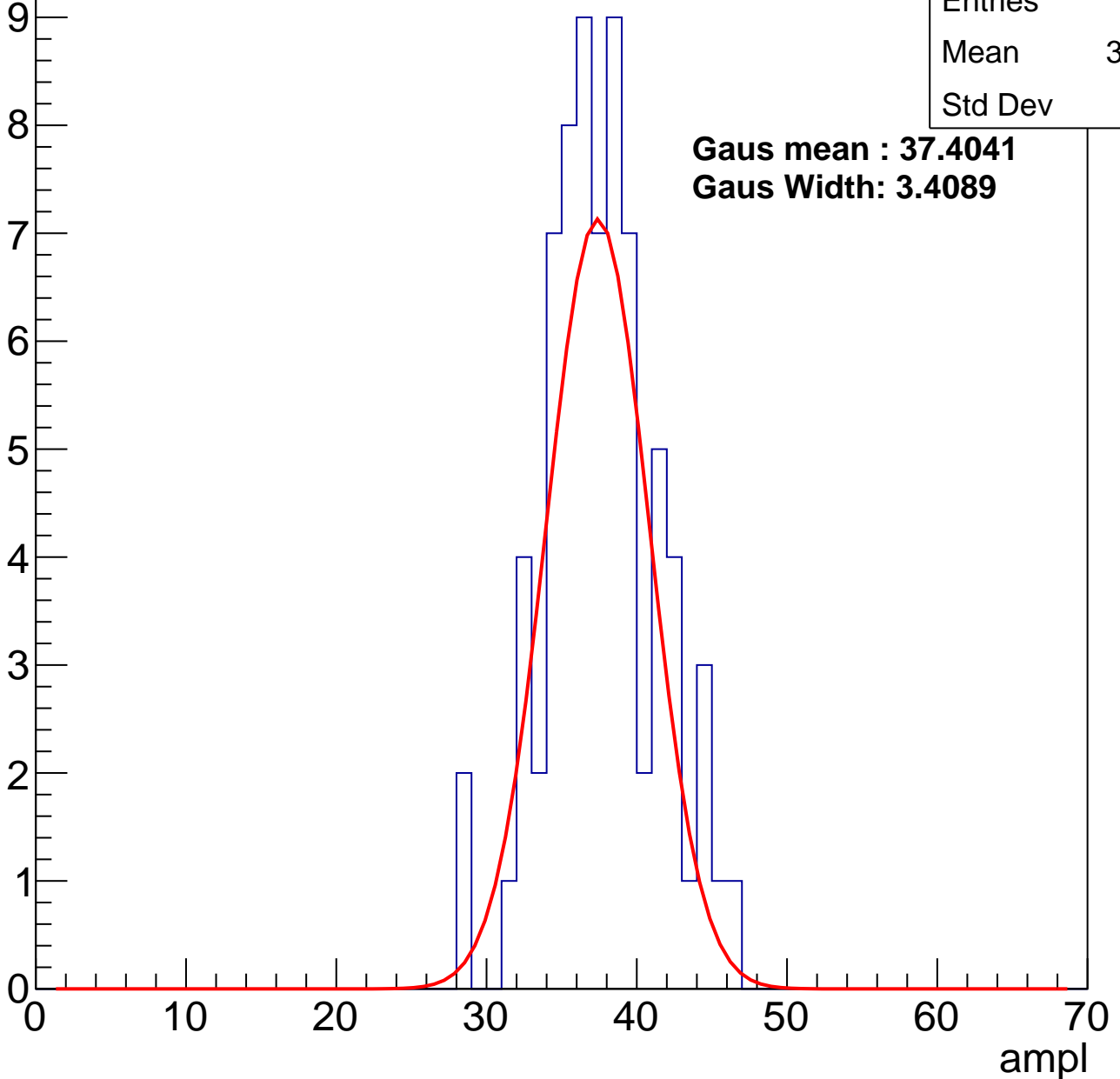
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.21
Std Dev	3.69

Gaus mean : 37.4041

Gaus Width: 3.4089



B1L100S, U12-ch83, adc2

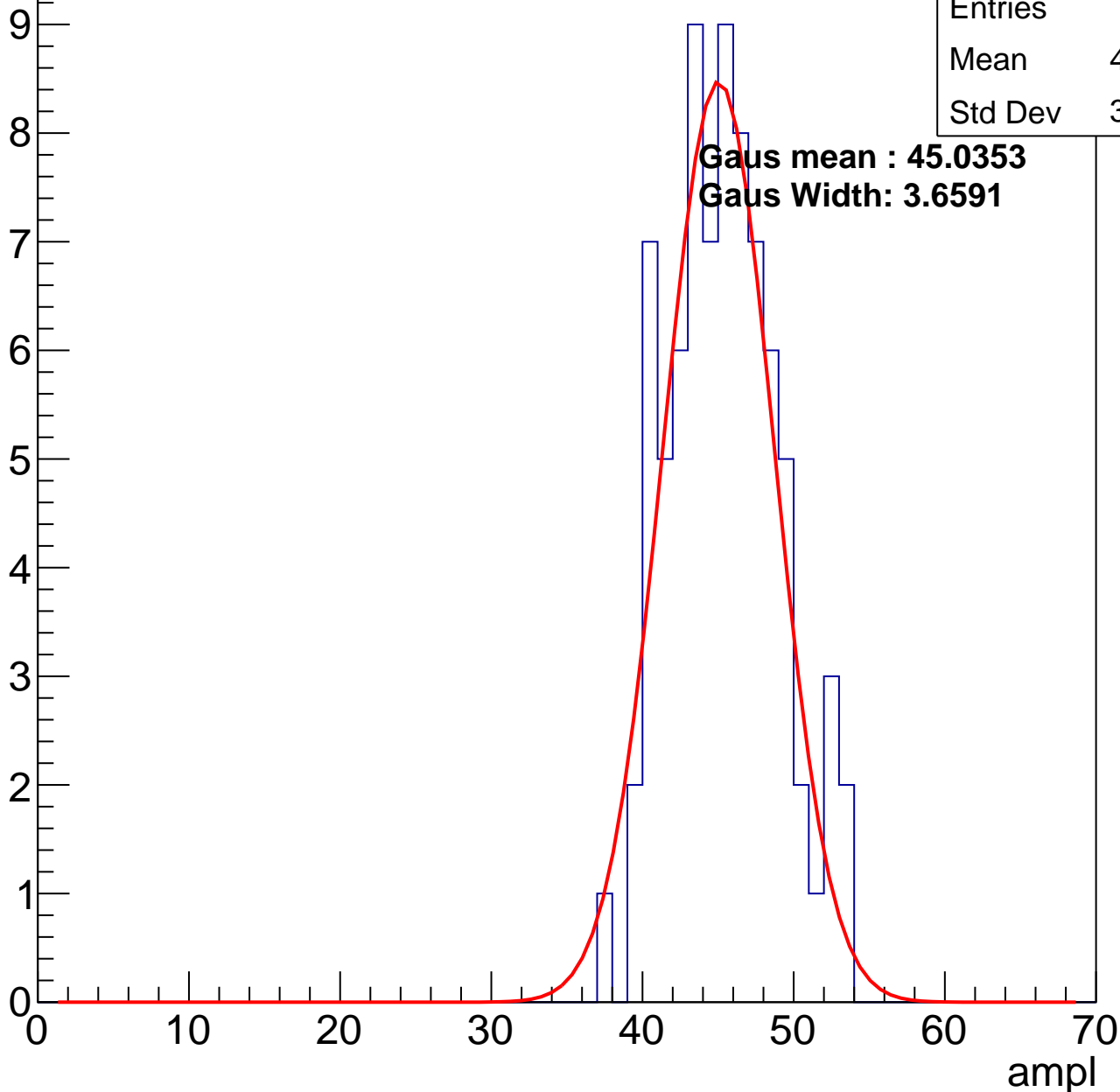
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	44.94
Std Dev	3.568

Gaus mean : 45.0353

Gaus Width: 3.6591

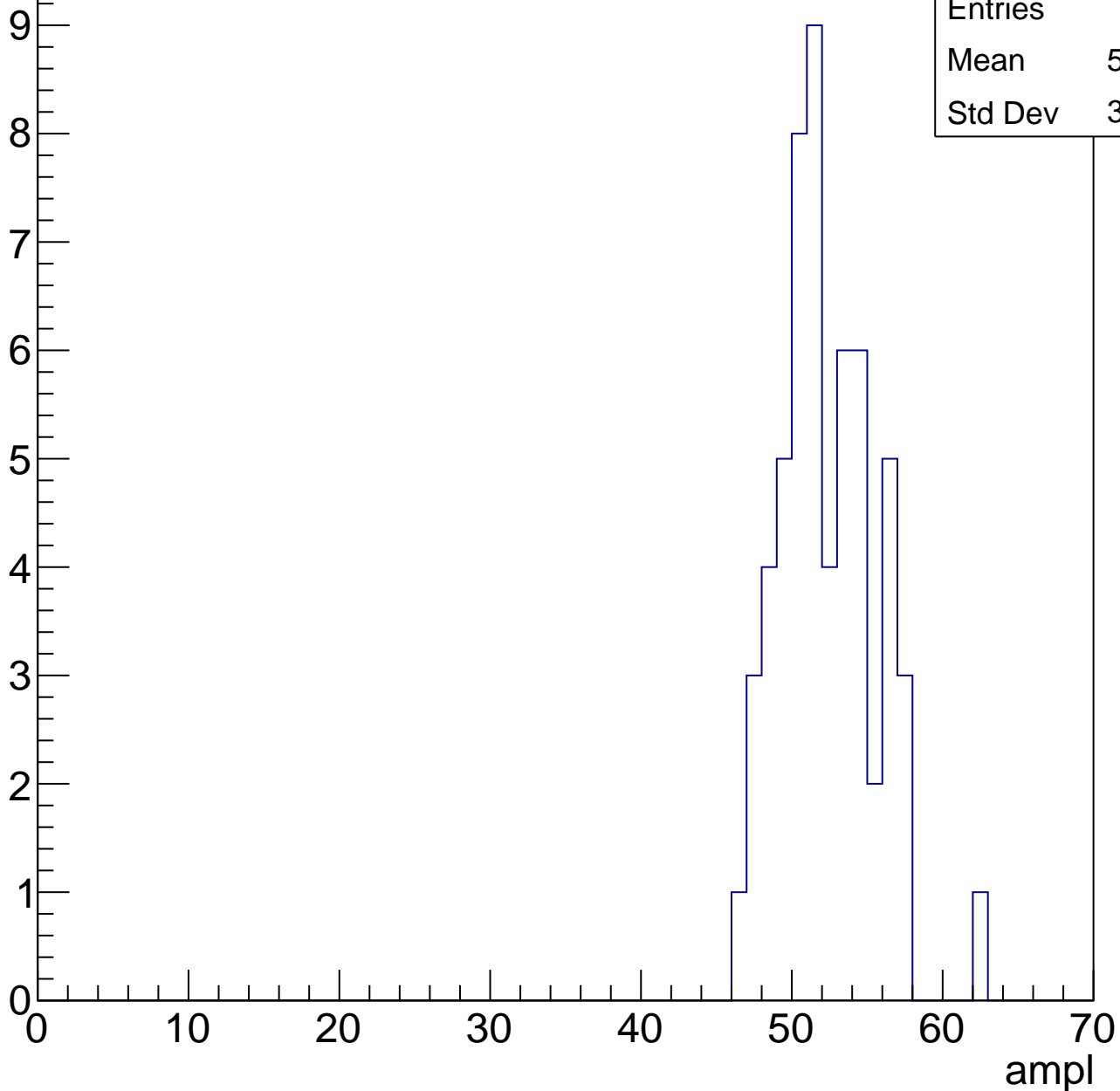


B1L100S, U12-ch83, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

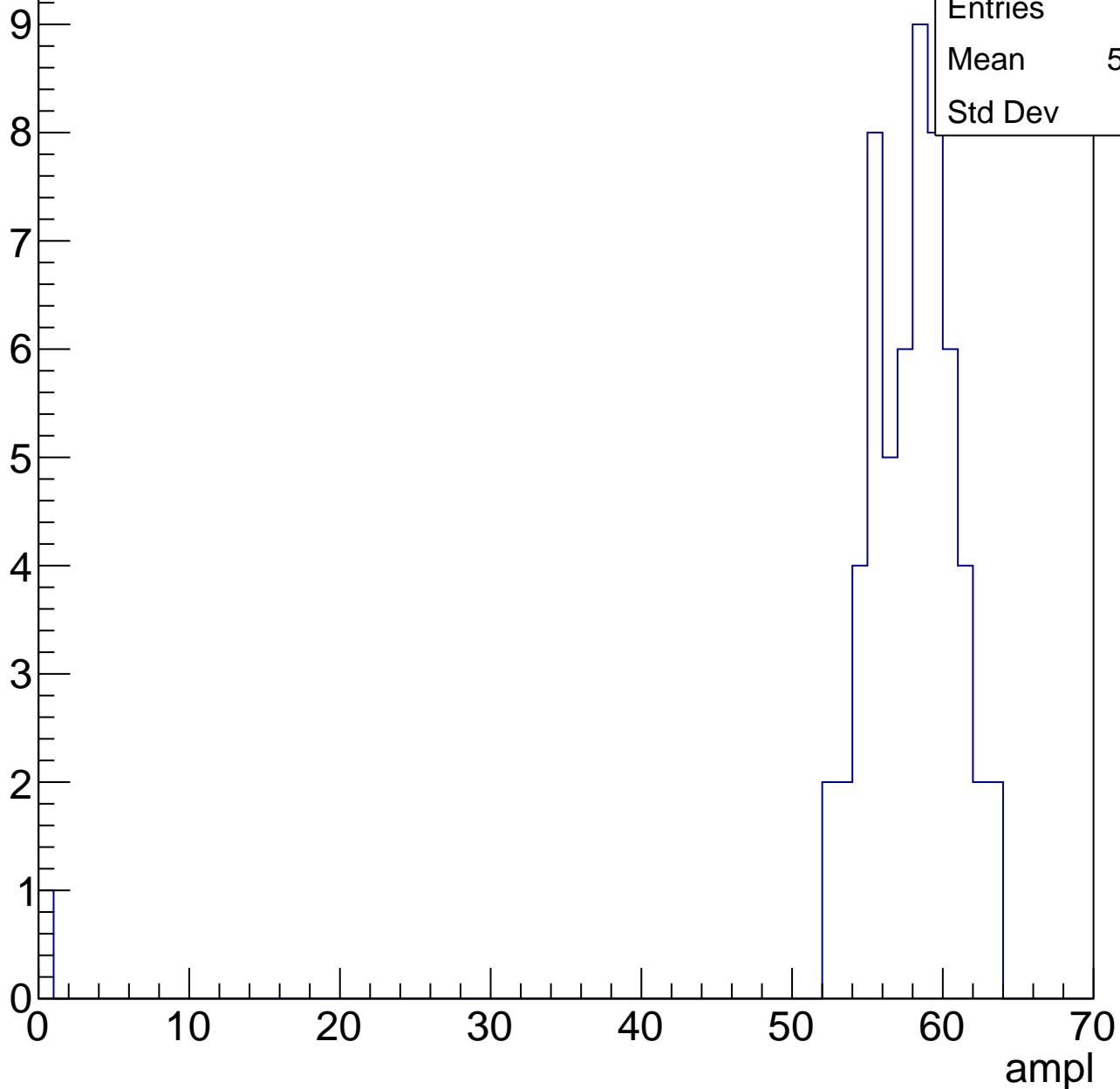
Entries	57
Mean	51.86
Std Dev	3.142



B1L100S, U12-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



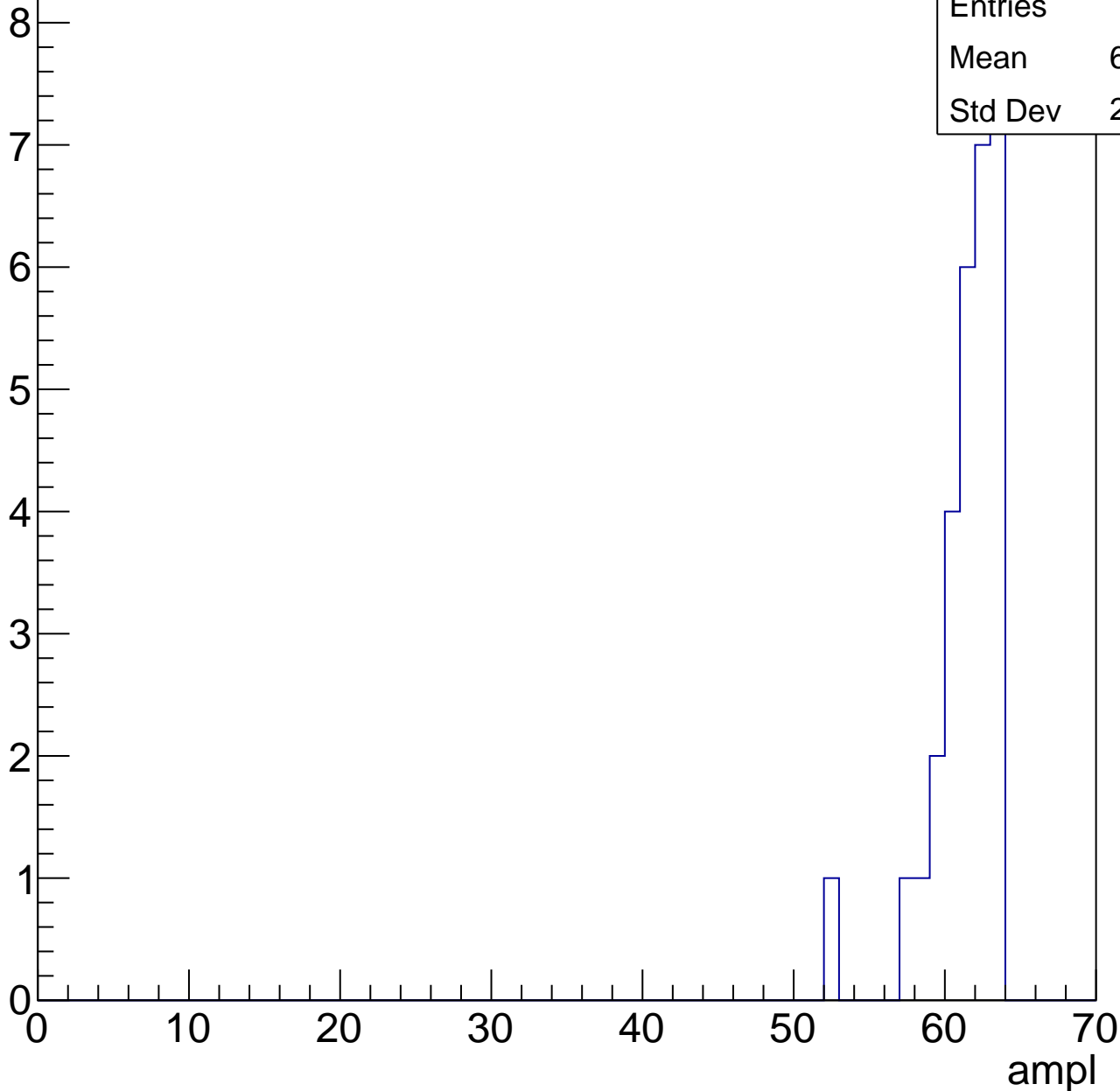
Entries	59
Mean	56.54
Std Dev	7.89

B1L100S, U12-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.97
Std Dev	2.287



B1L100S, U12-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch84, adc0

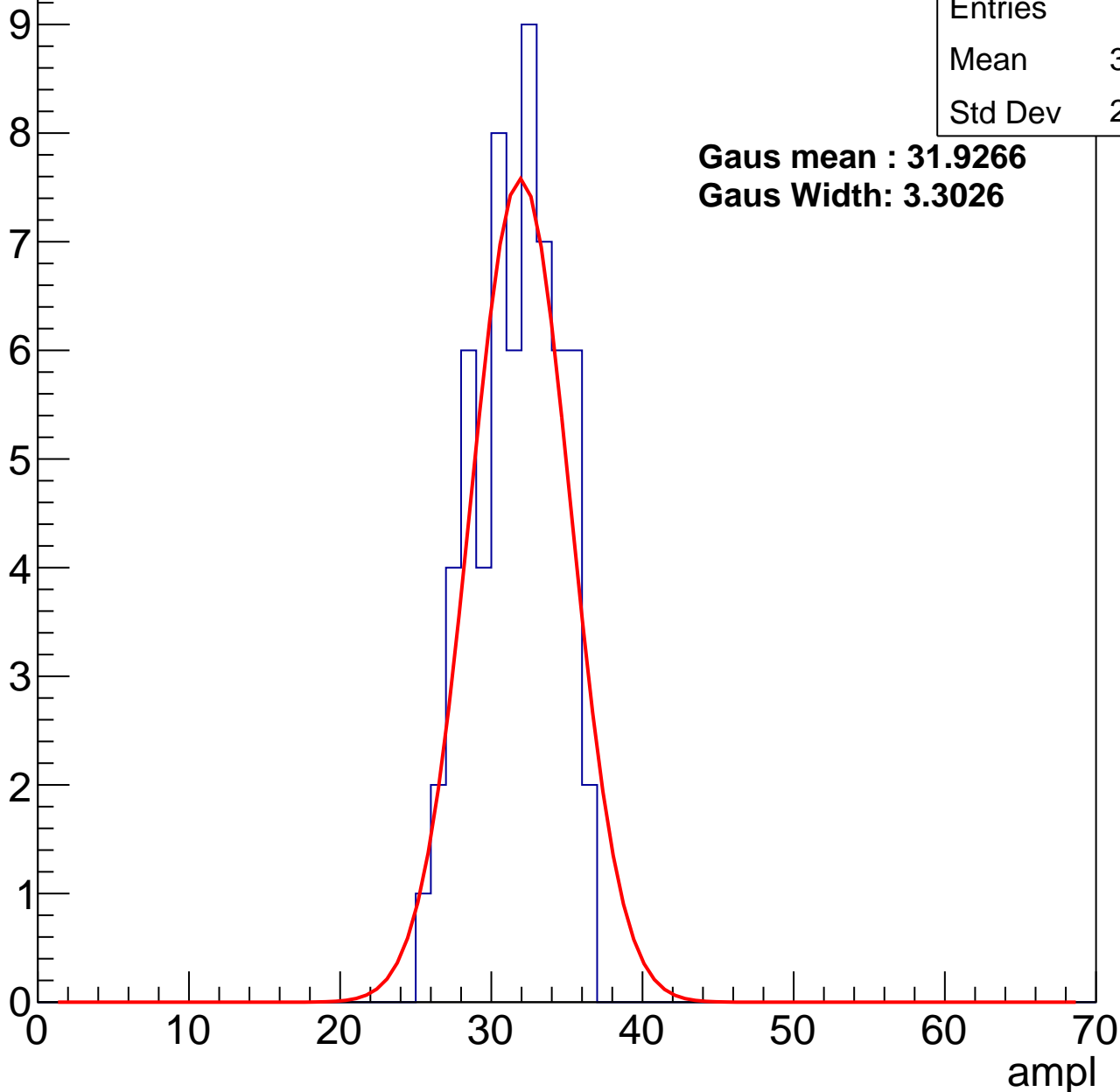
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	31.15
Std Dev	2.757

Gaus mean : 31.9266

Gaus Width: 3.3026



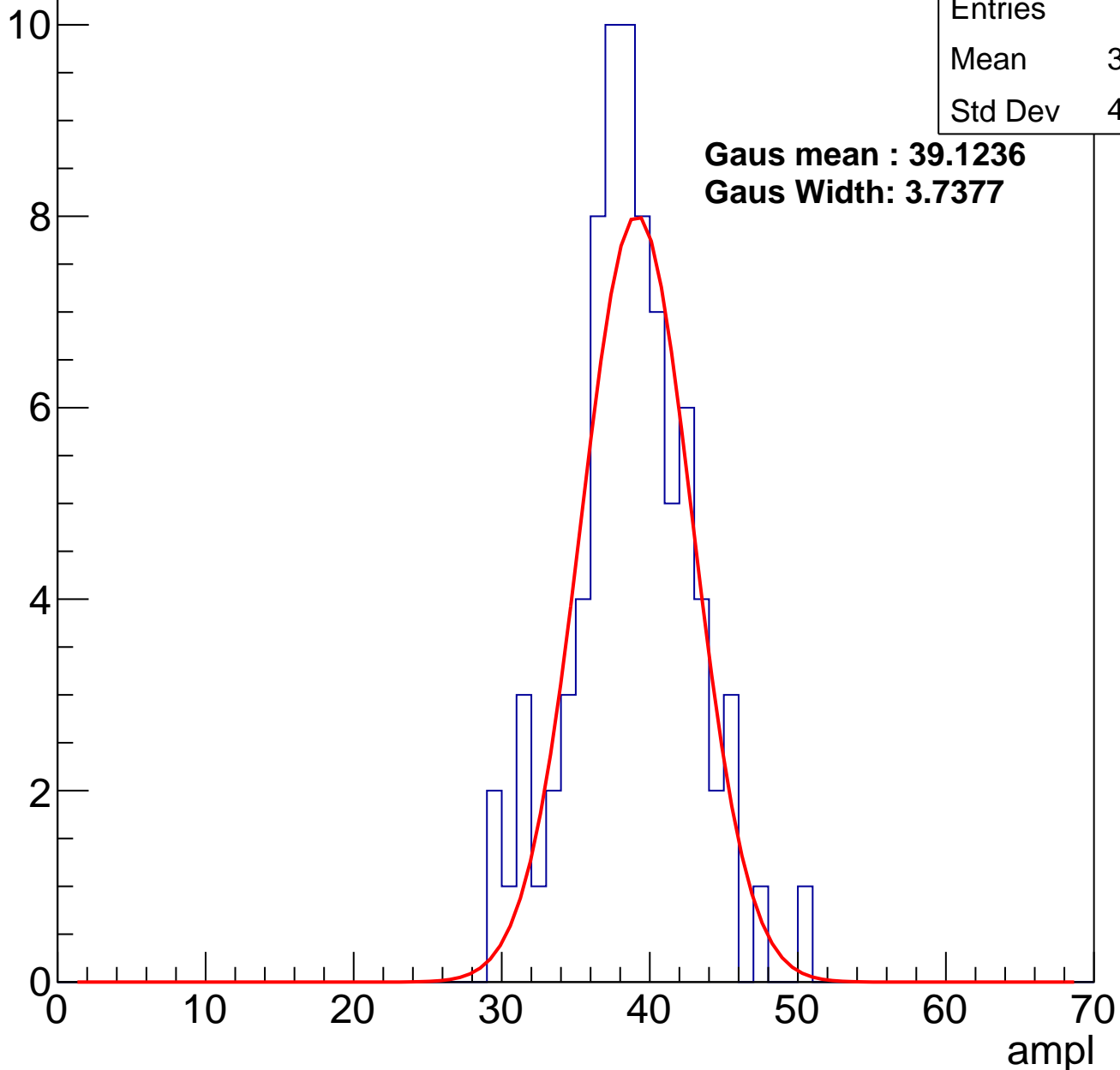
B1L100S, U12-ch84, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	38.27
Std Dev	4.006

Gaus mean : 39.1236
Gaus Width: 3.7377

Entry



B1L100S, U12-ch84, adc2

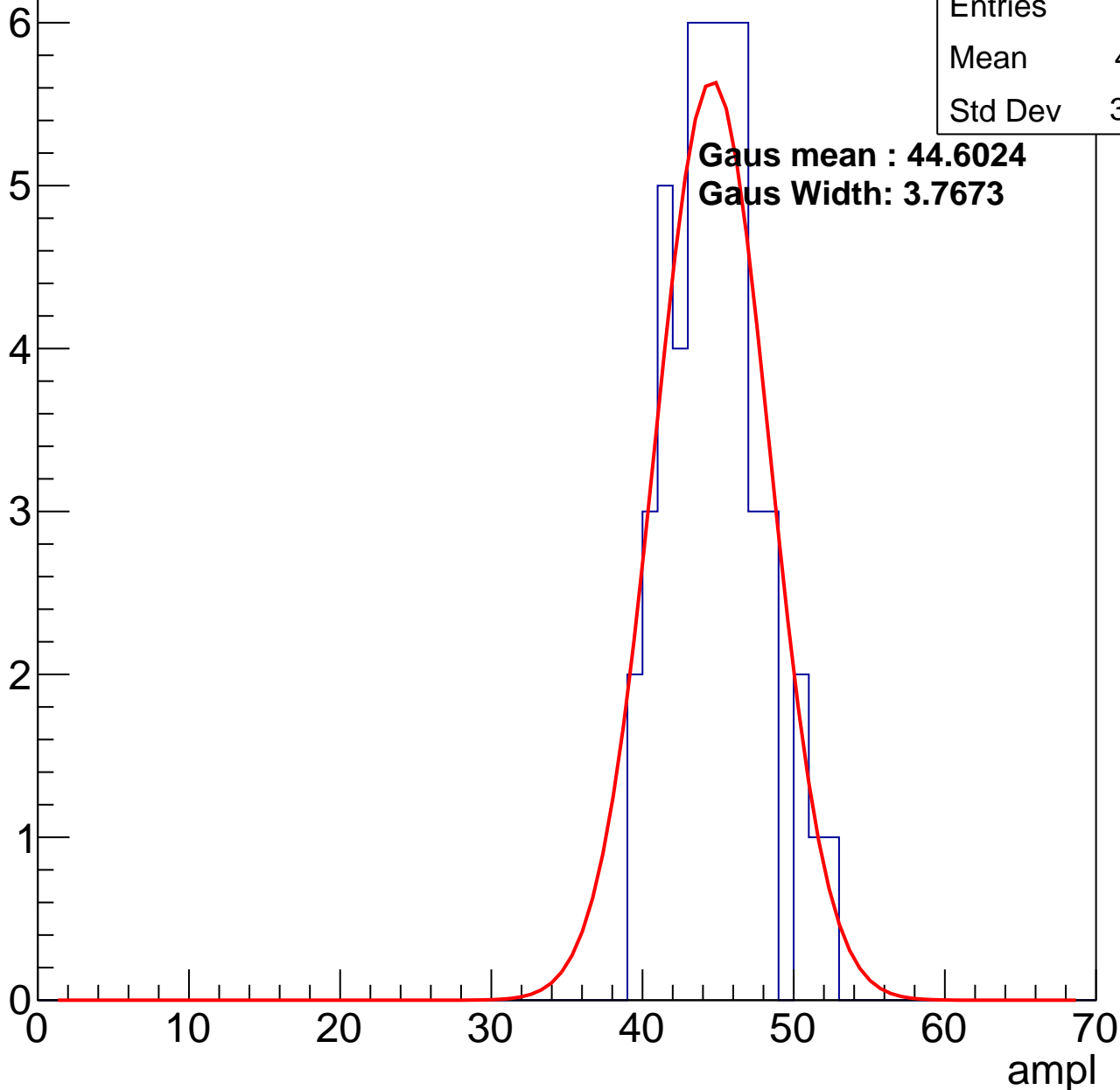
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	44.31
Std Dev	3.063

Gaus mean : 44.6024

Gaus Width: 3.7673

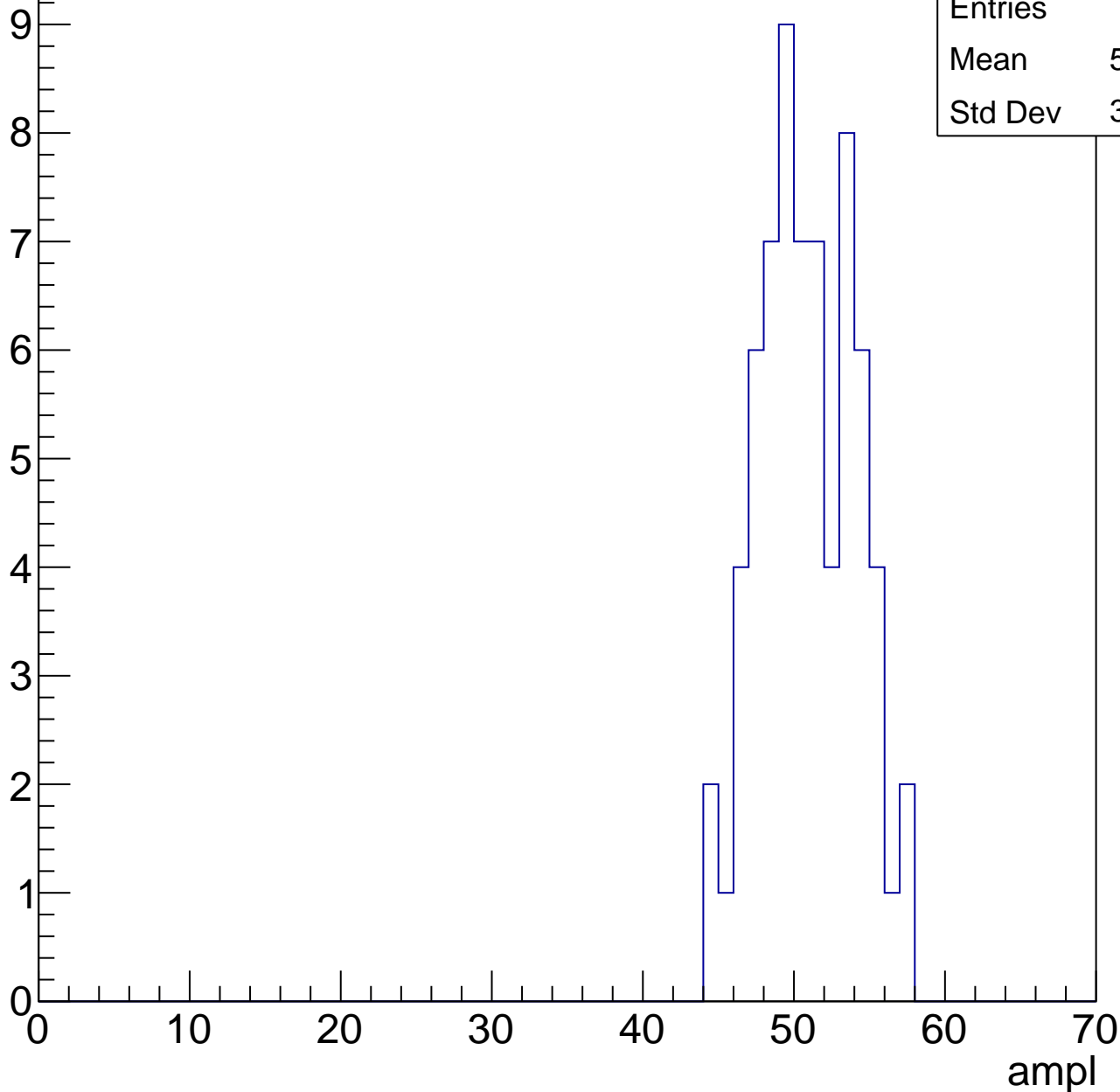


B1L100S, U12-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	50.43
Std Dev	3.126

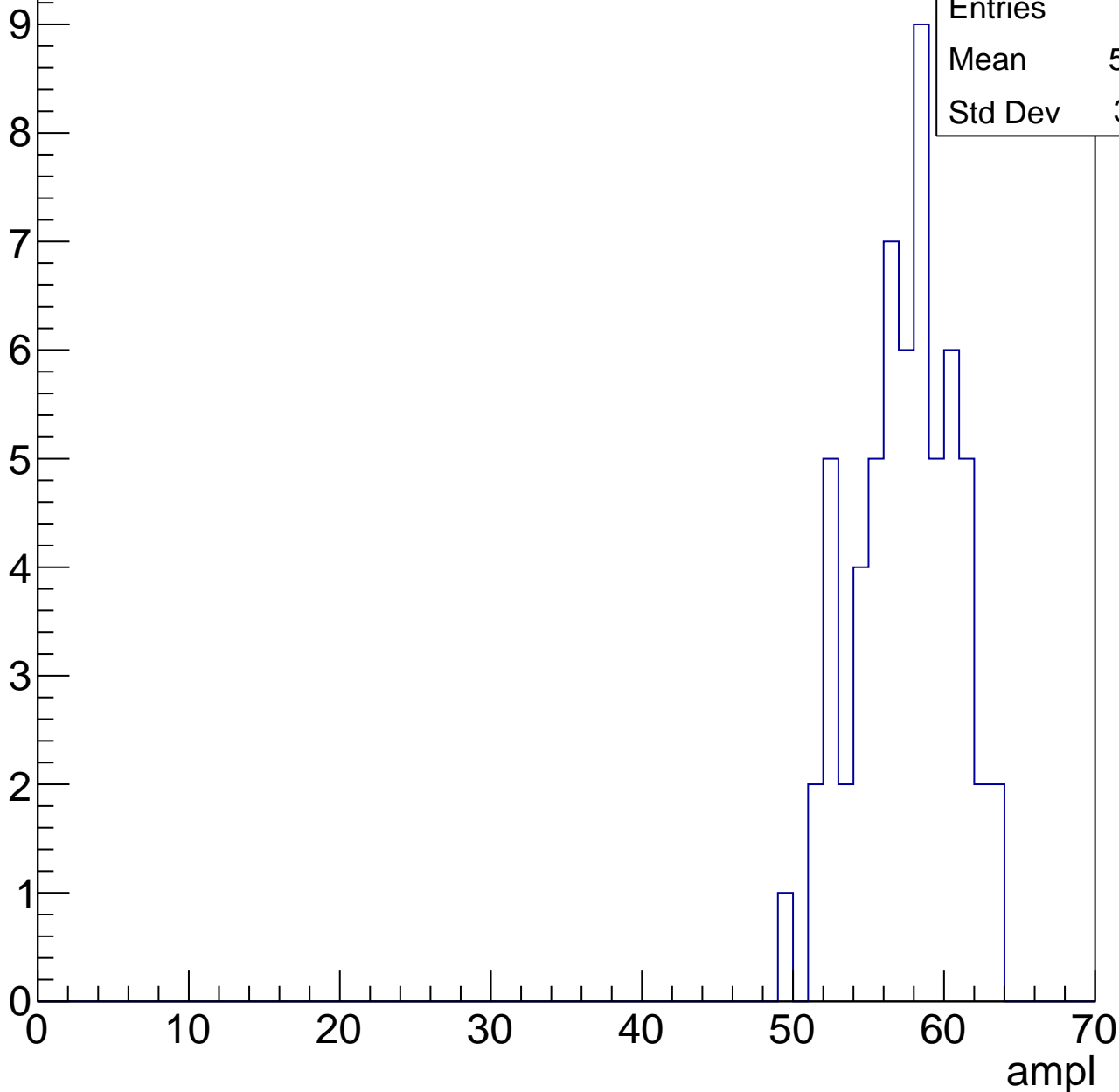


B1L100S, U12-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.95
Std Dev	3.241

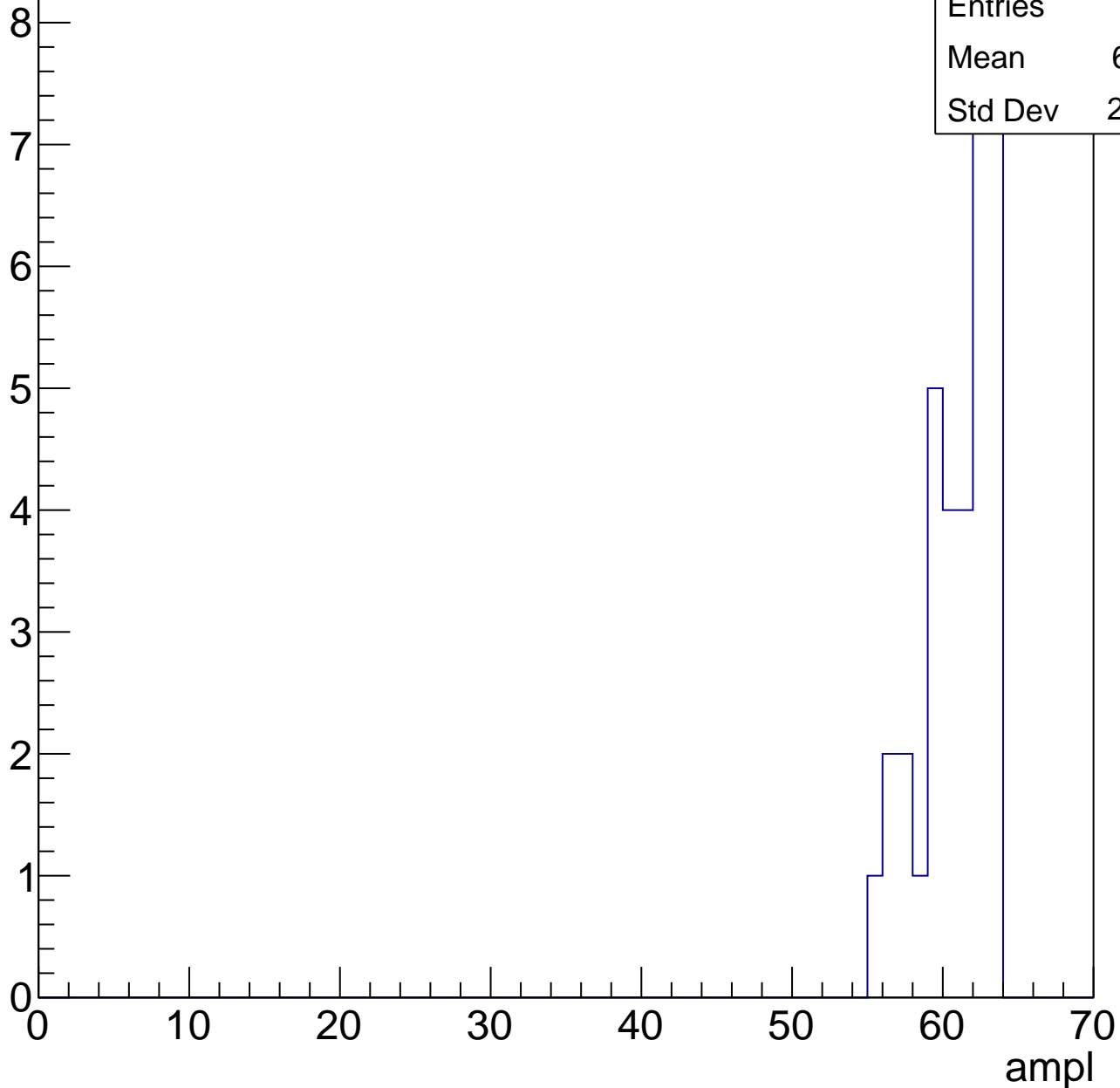


B1L100S, U12-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

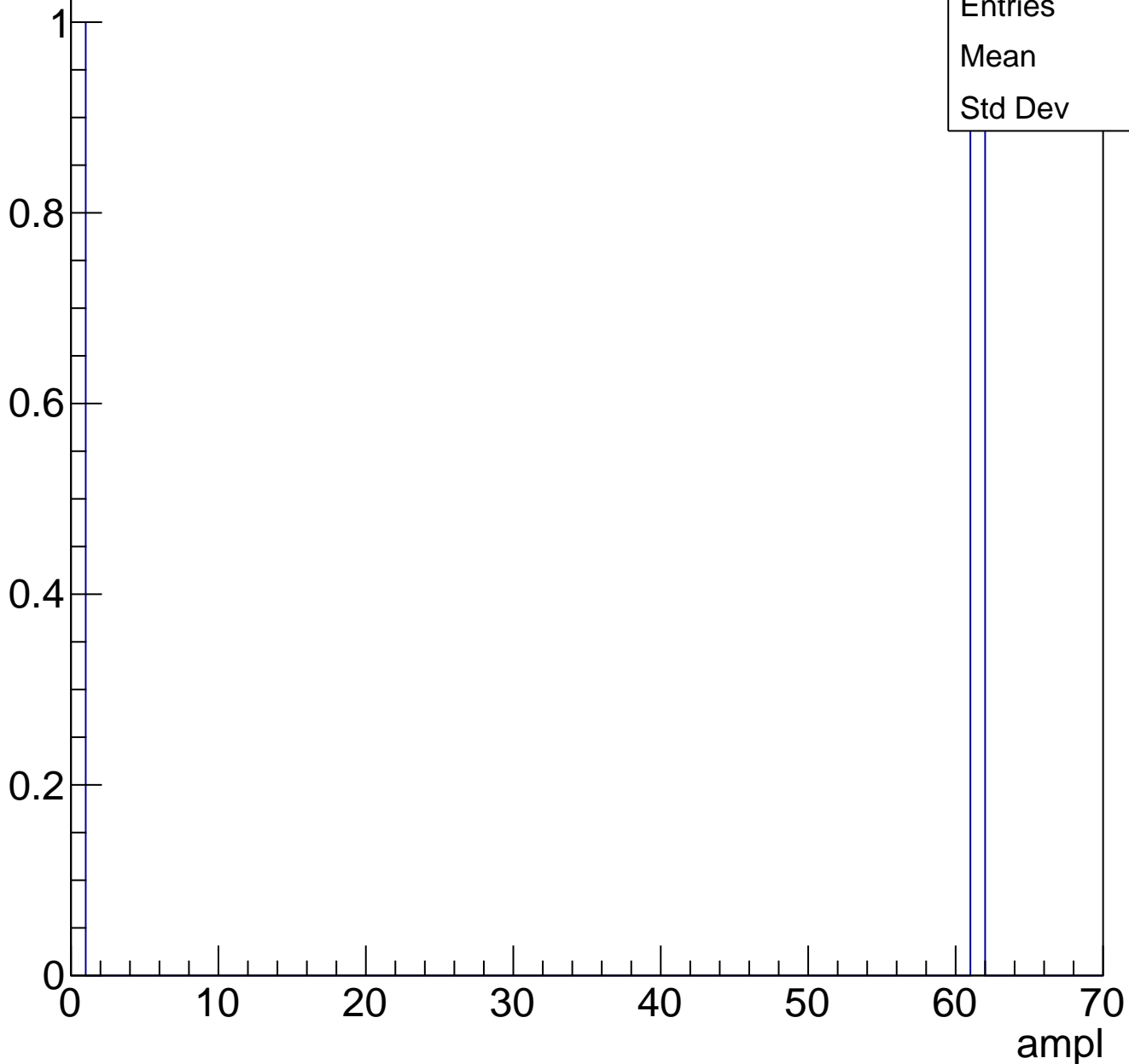
Entries	35
Mean	60.51
Std Dev	2.285



B1L100S, U12-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch85, adc0

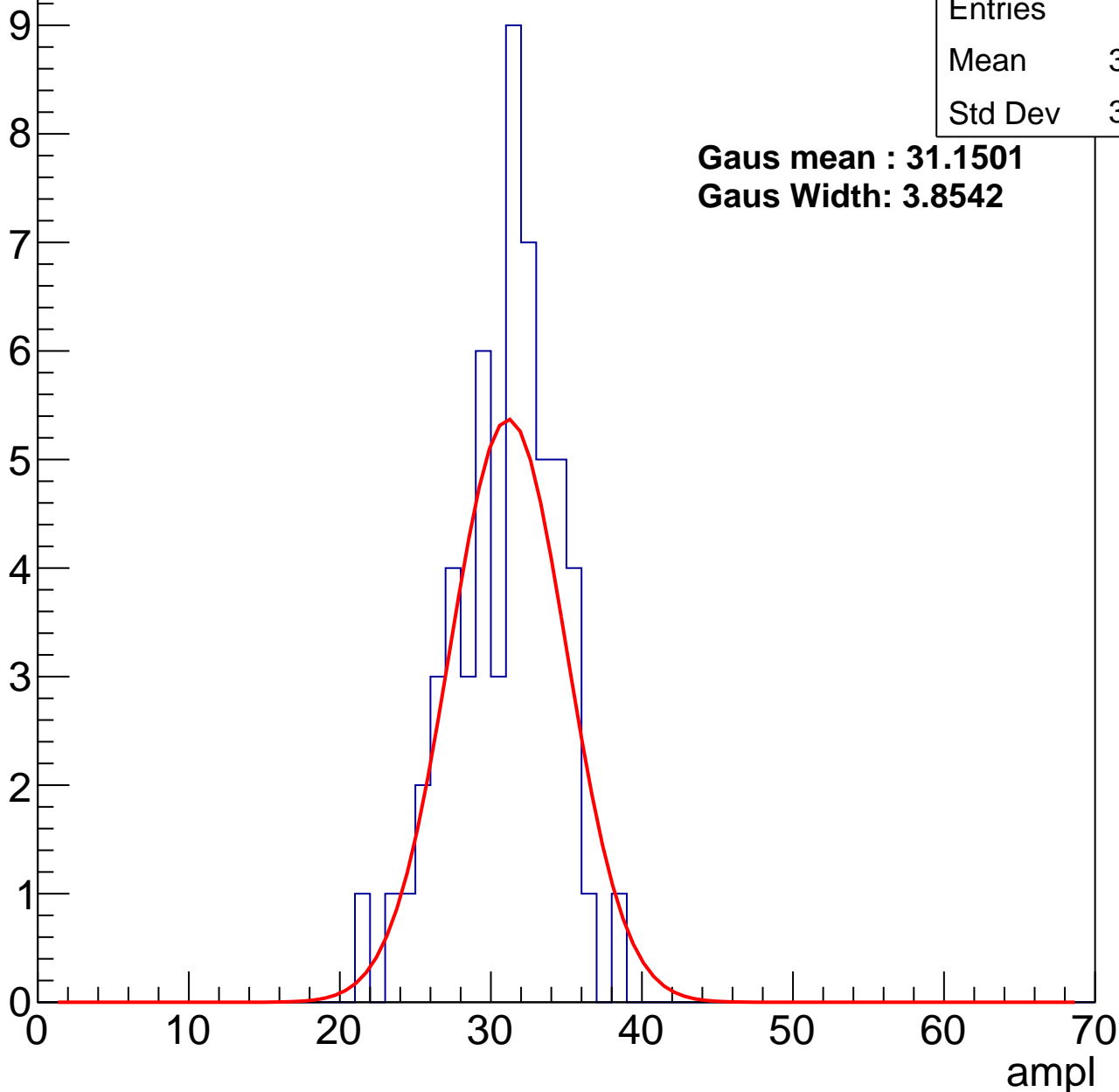
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.43
Std Dev	3.458

Gaus mean : 31.1501

Gaus Width: 3.8542



B1L100S, U12-ch85, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	37.75
Std Dev	3.525

Gaus mean : 37.8794

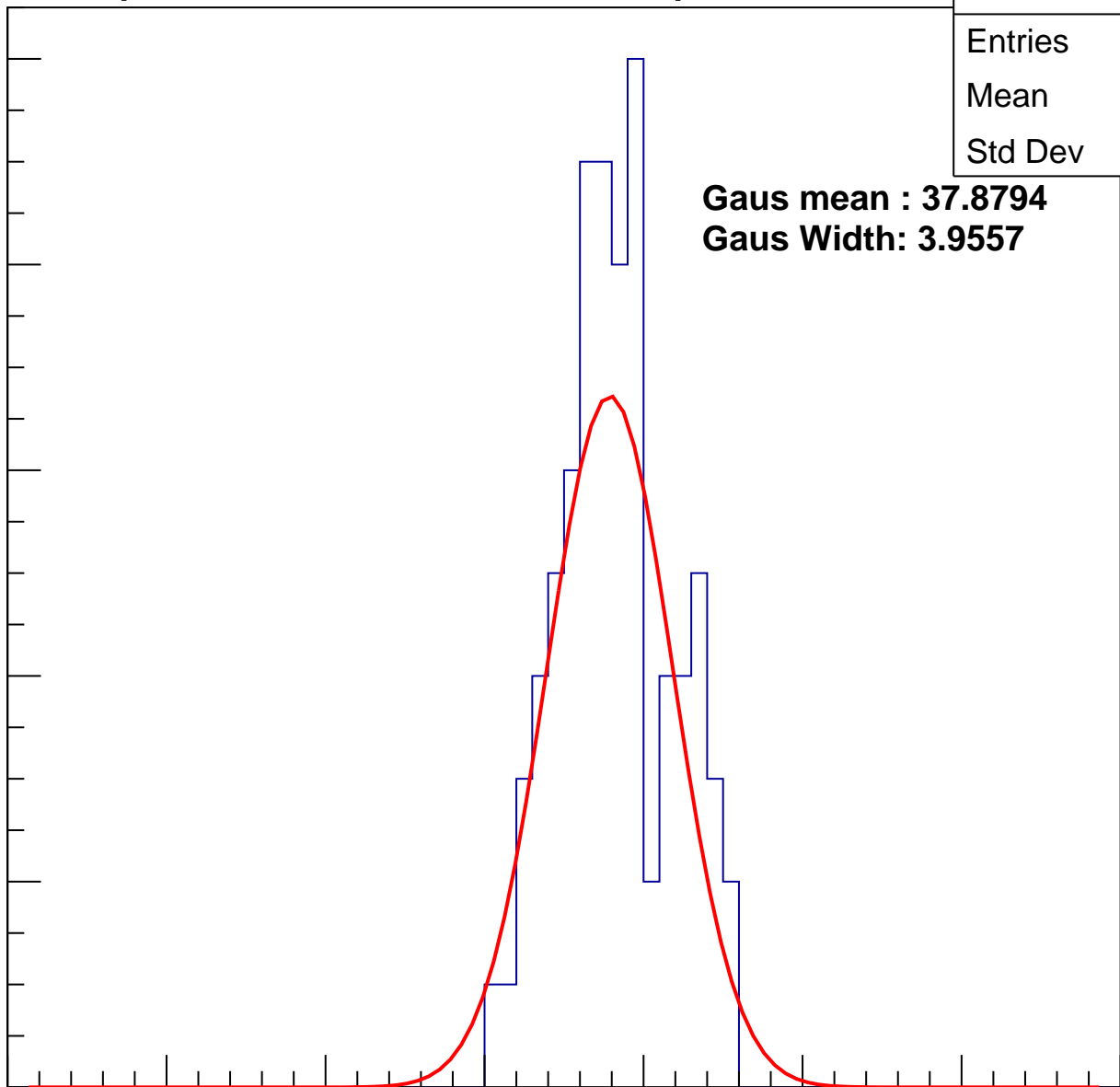
Gaus Width: 3.9557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch85, adc2

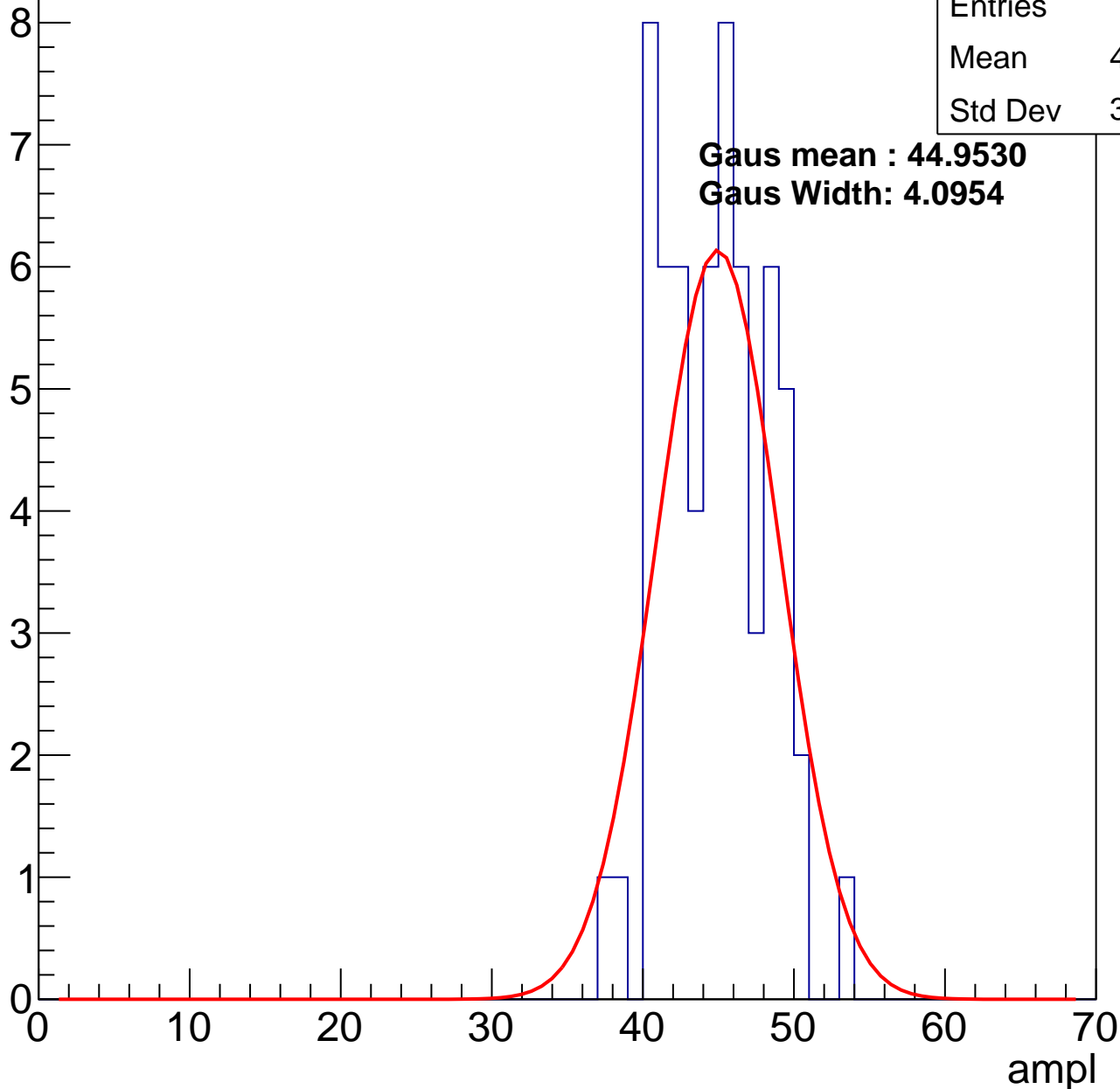
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.32
Std Dev	3.384

Gaus mean : 44.9530

Gaus Width: 4.0954

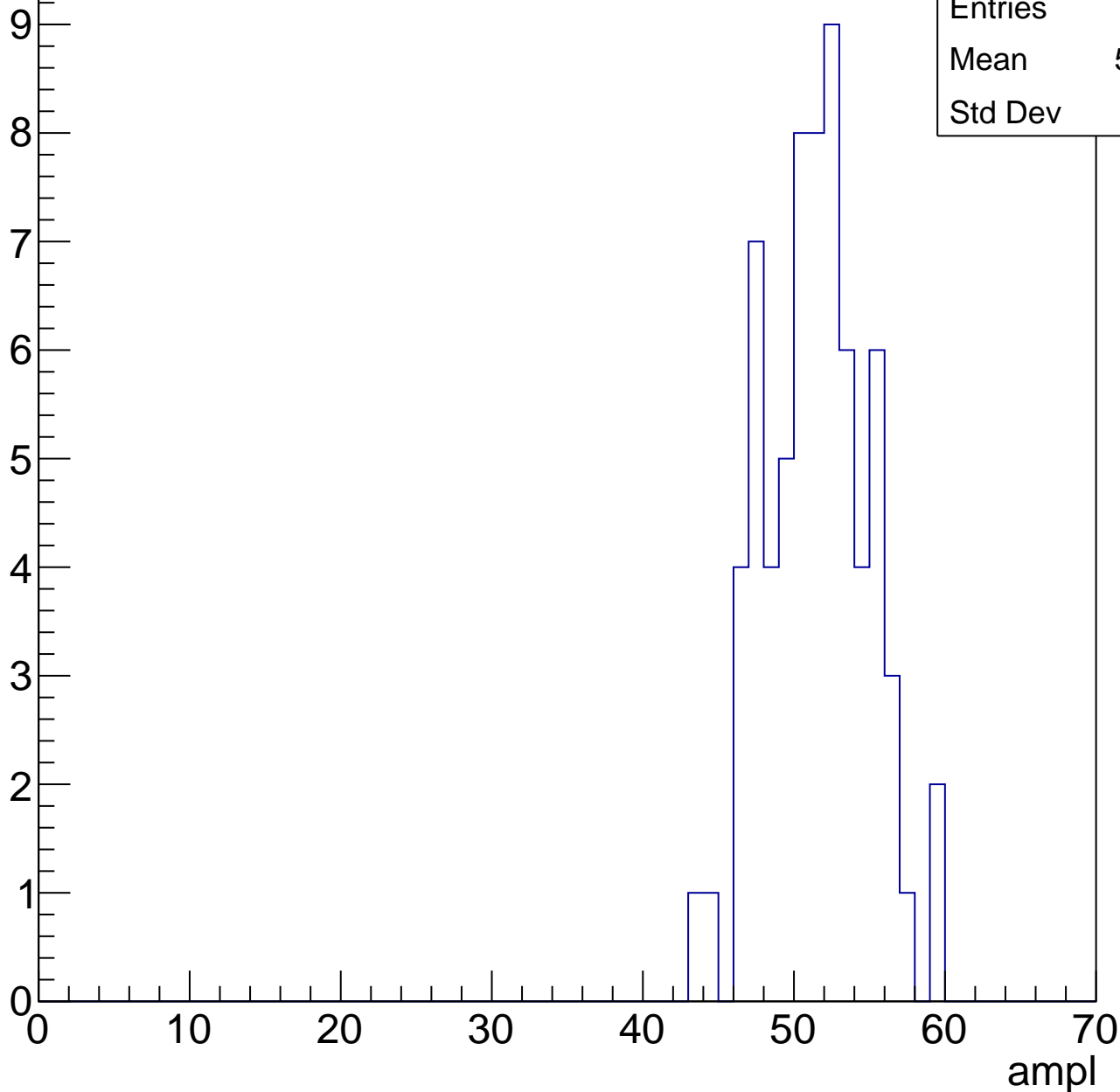


B1L100S, U12-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	51.01
Std Dev	3.39

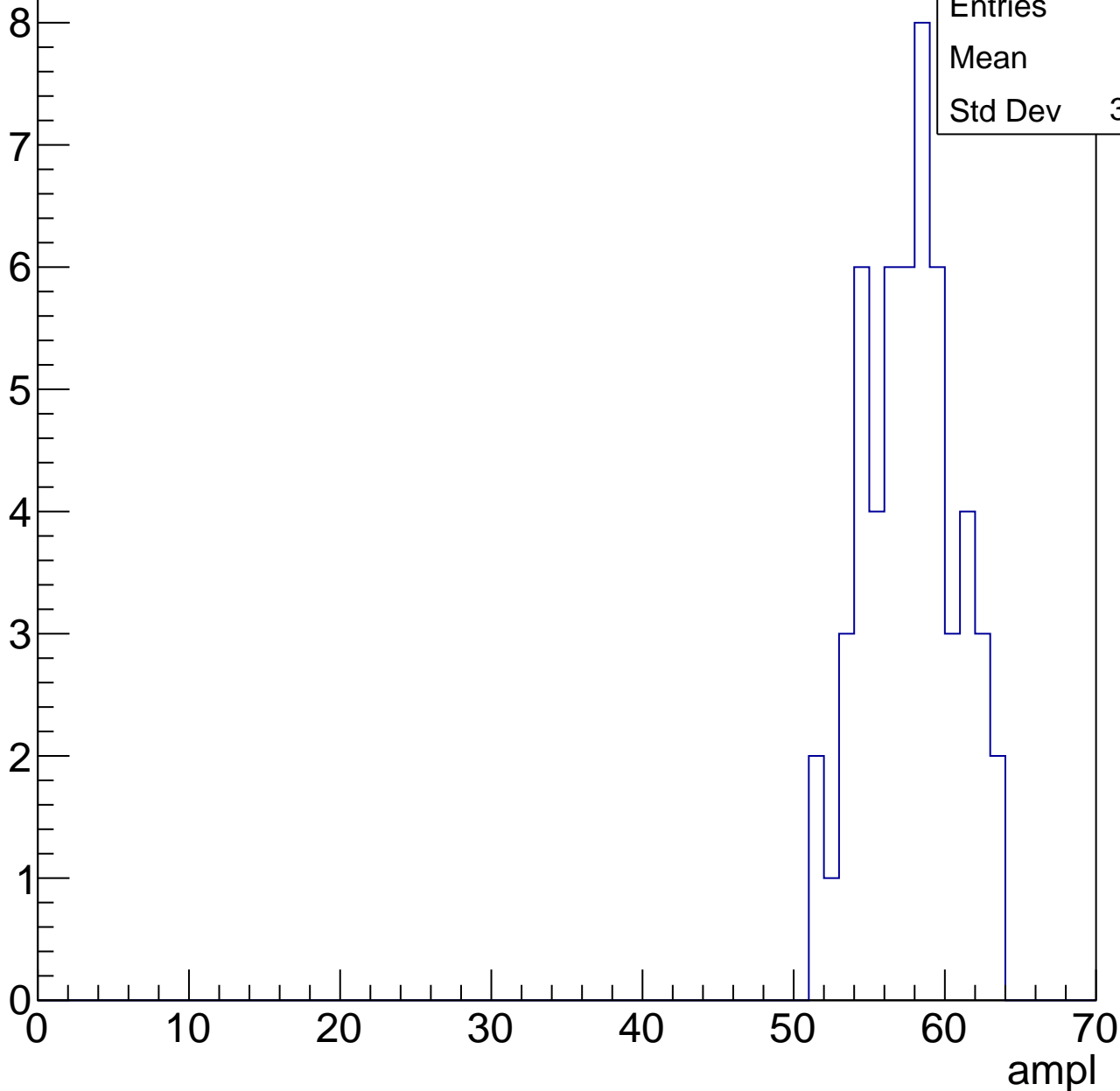


B1L100S, U12-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

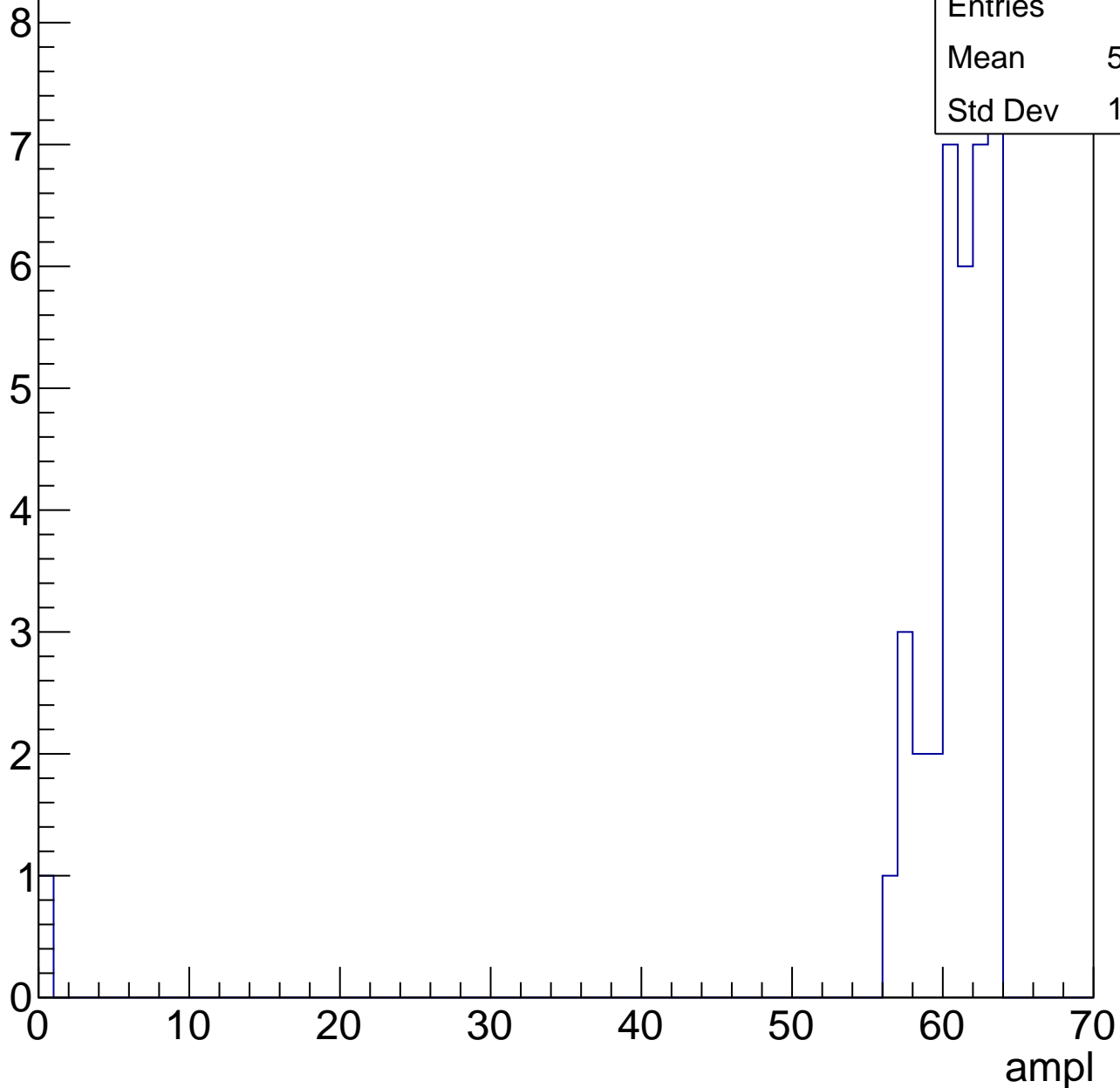
Entries	54
Mean	57.2
Std Dev	3.009



B1L100S, U12-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch86, adc0

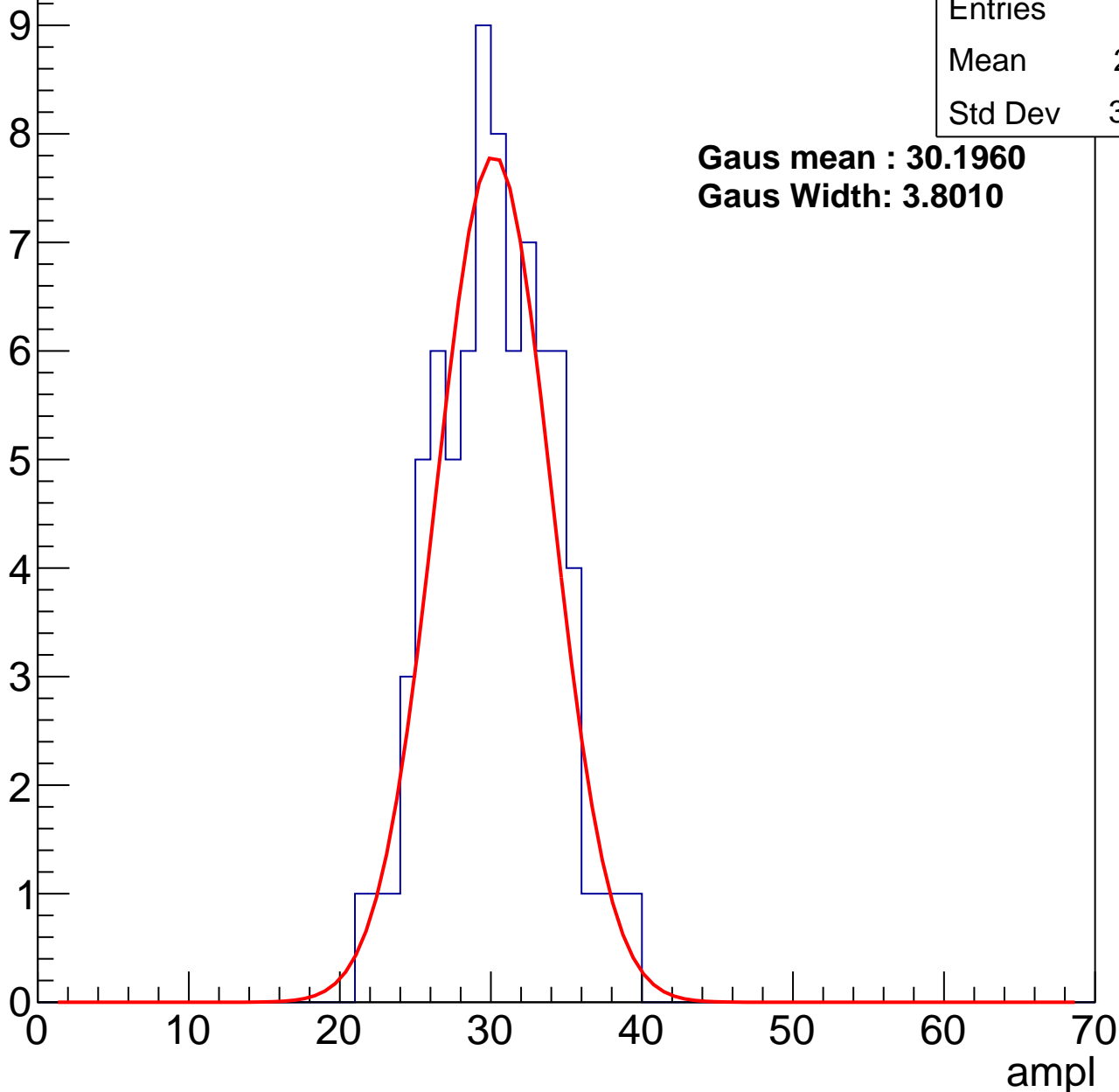
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	29.81
Std Dev	3.776

Gaus mean : 30.1960

Gaus Width: 3.8010



B1L100S, U12-ch86, adc1

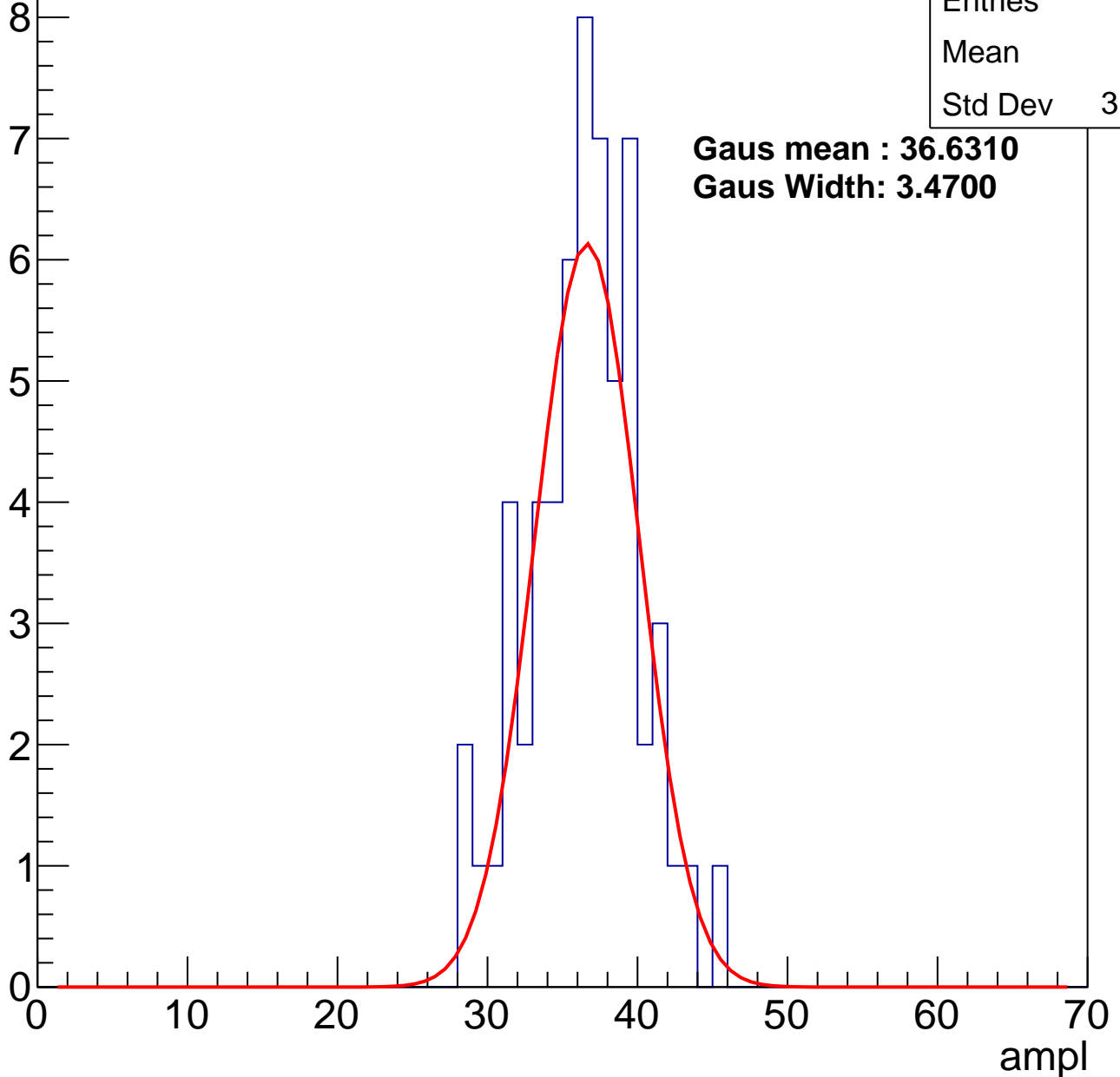
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36
Std Dev	3.603

Gaus mean : 36.6310

Gaus Width: 3.4700



B1L100S, U12-ch86, adc2

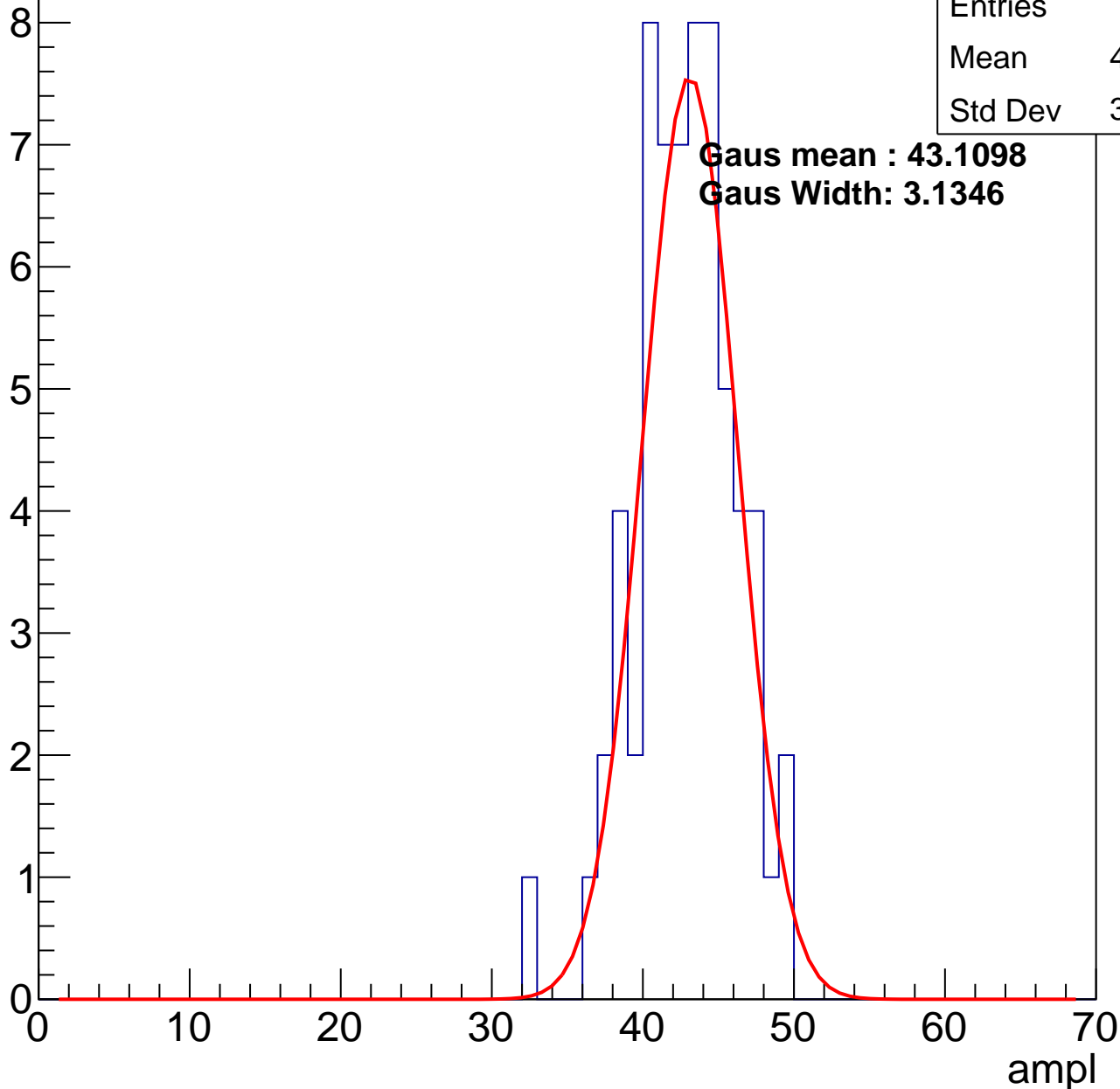
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.38
Std Dev	3.262

Gaus mean : 43.1098

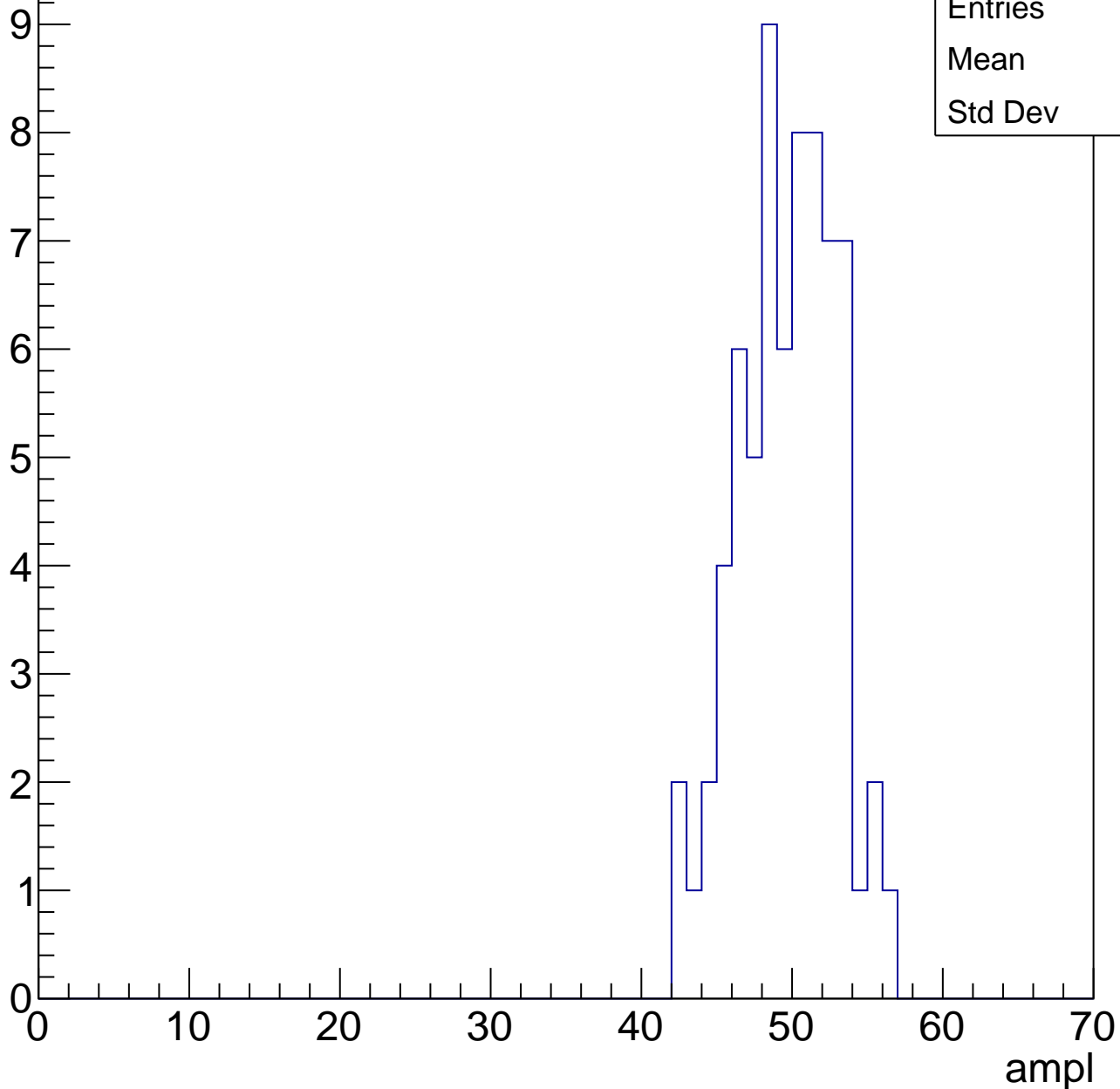
Gaus Width: 3.1346



B1L100S, U12-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

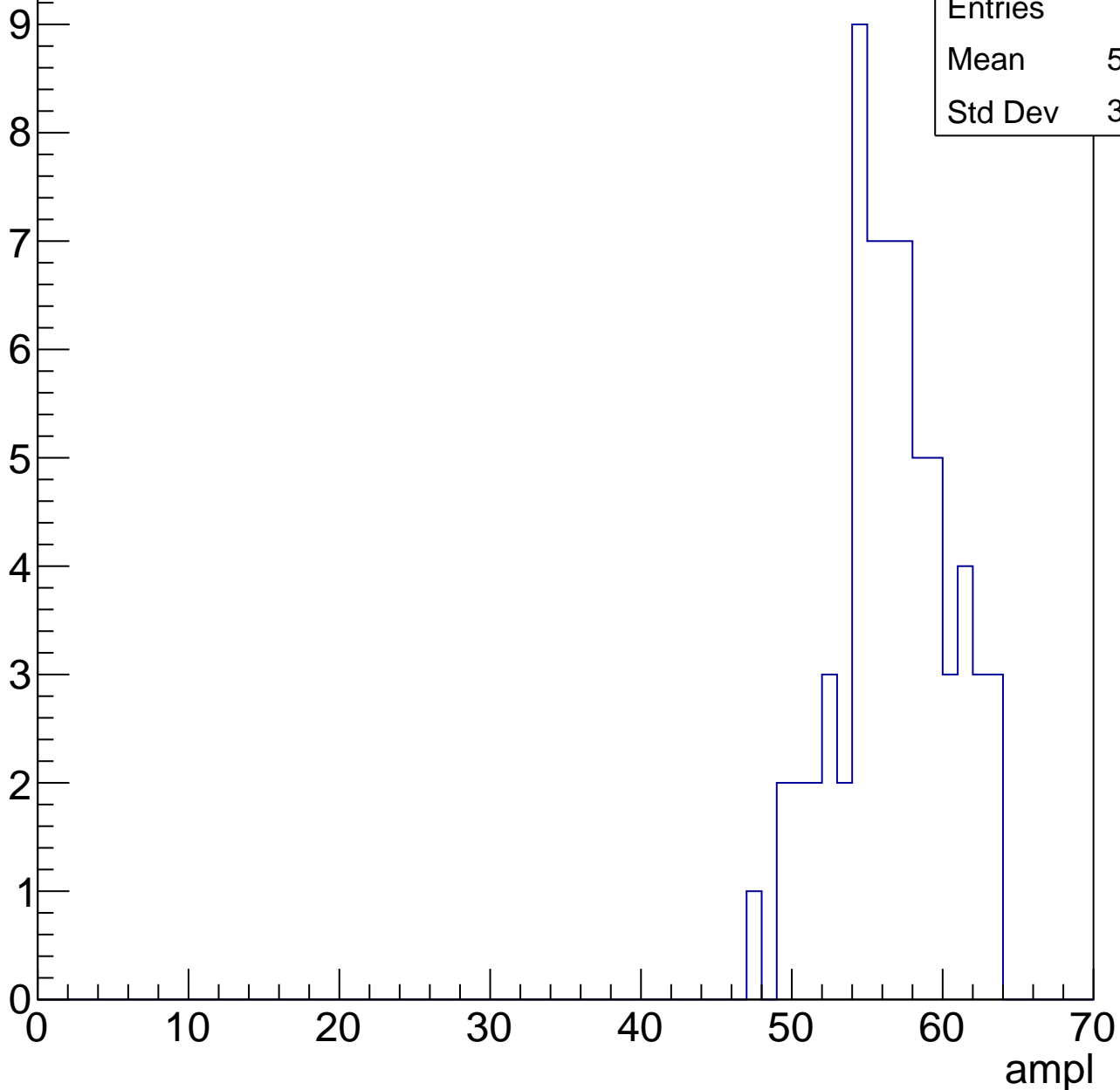


B1L100S, U12-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	56.23
Std Dev	3.683



B1L100S, U12-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 58.5

Std Dev 9.228

ampl

0

10

20

30

40

50

60

70

B1L100S, U12-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L100S, U12-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch87, adc0

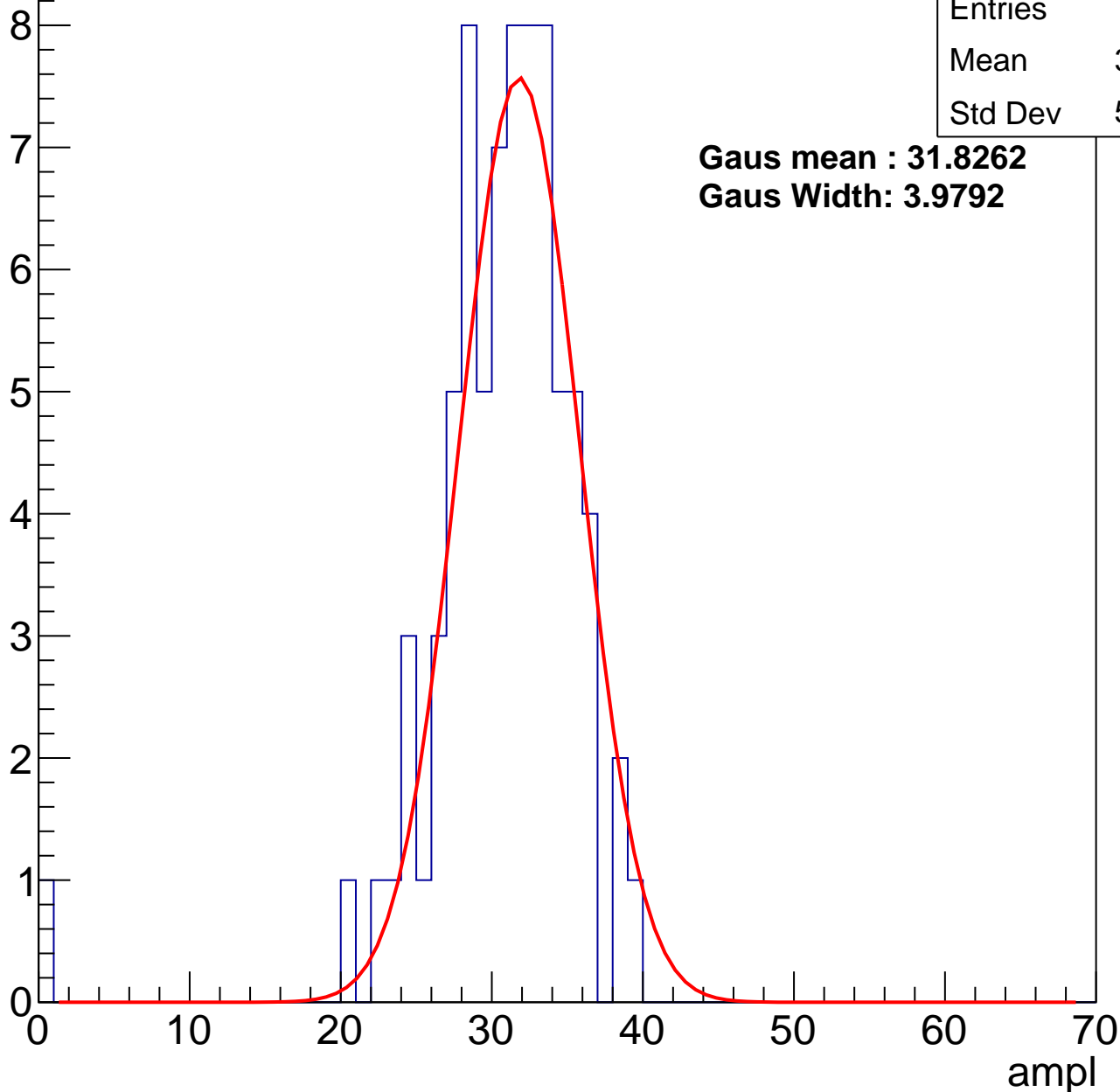
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	30.21
Std Dev	5.151

Gaus mean : 31.8262

Gaus Width: 3.9792



B1L100S, U12-ch87, adc1

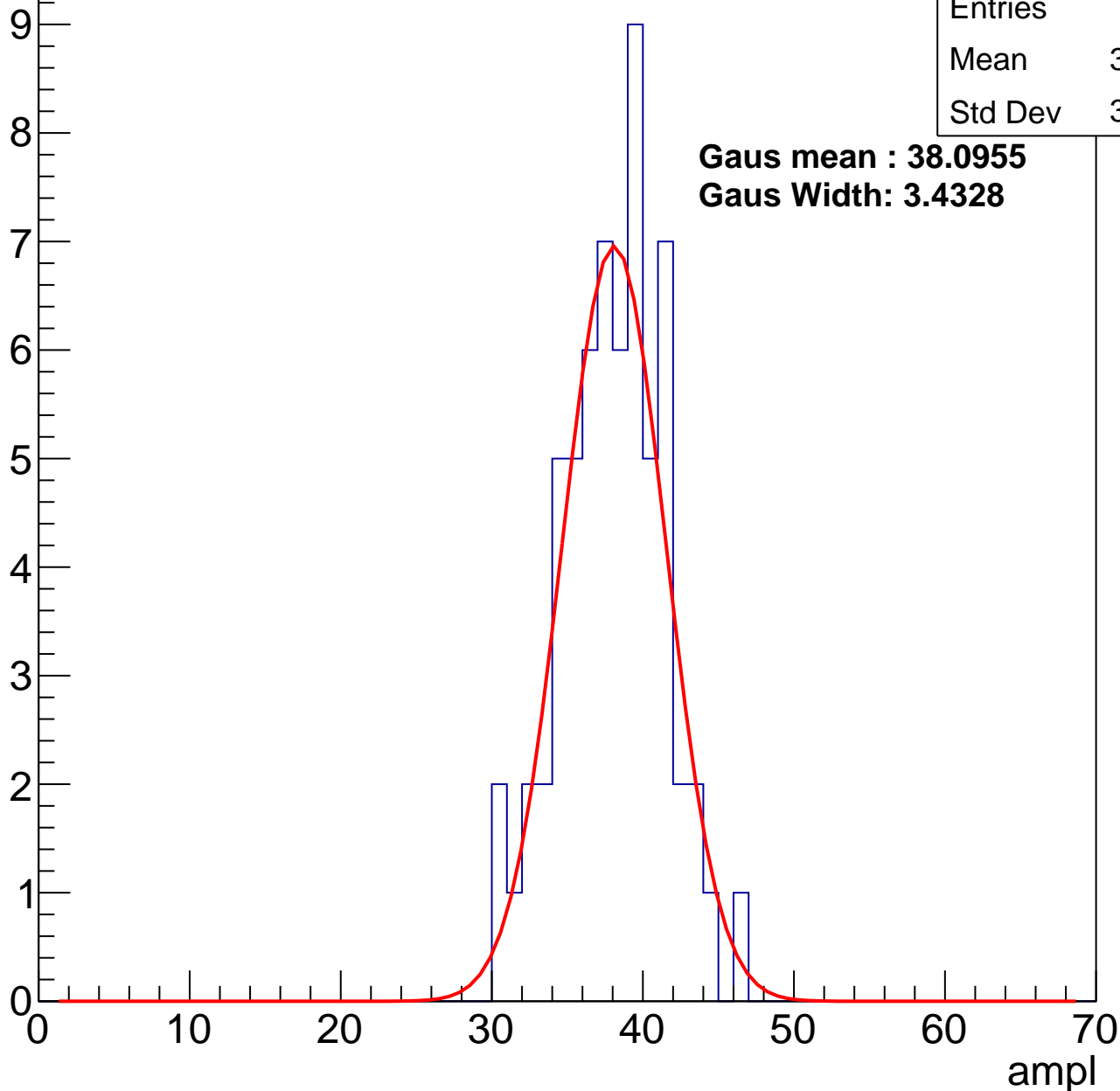
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.57
Std Dev	3.375

Gaus mean : 38.0955

Gaus Width: 3.4328



B1L100S, U12-ch87, adc2

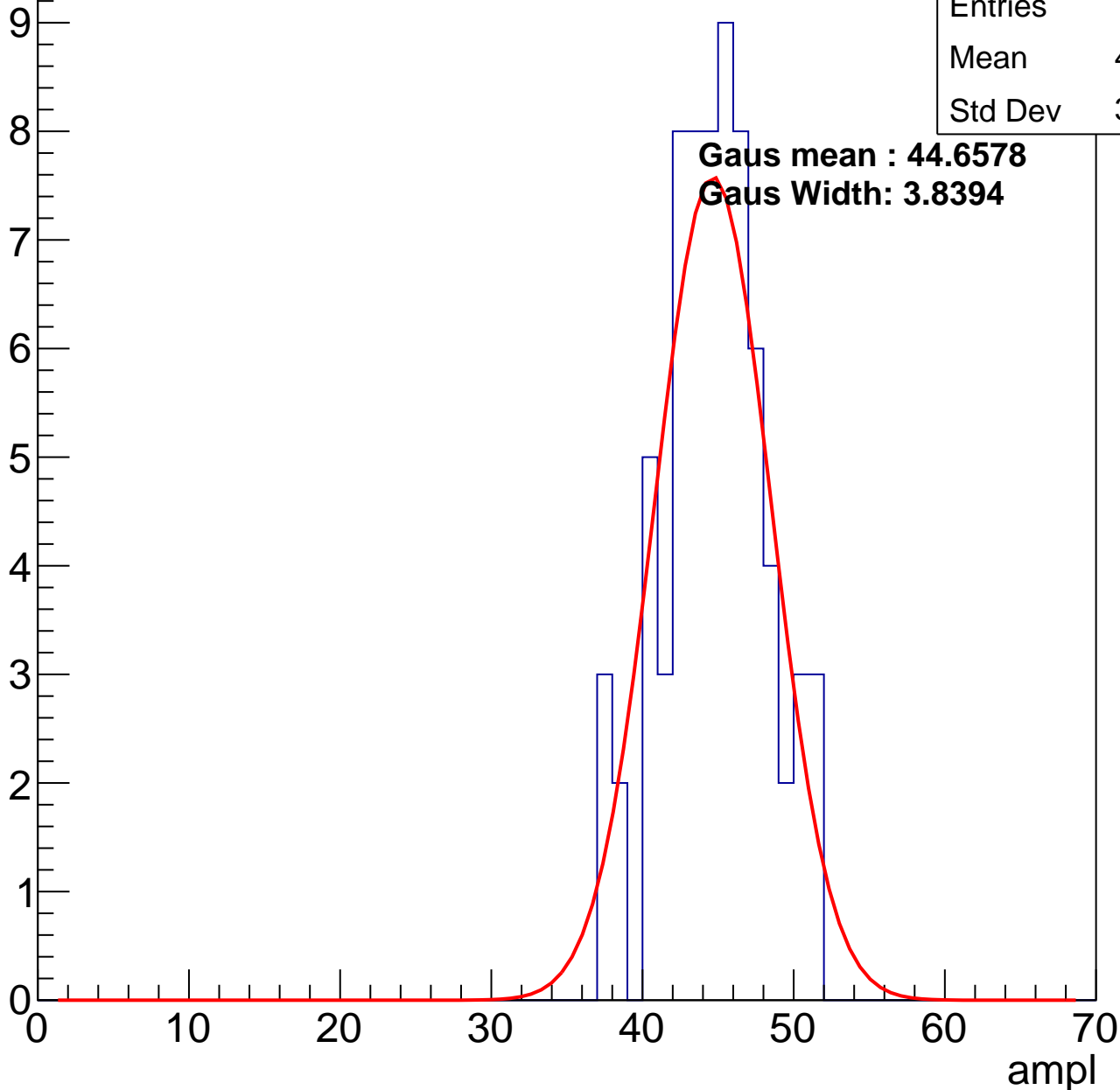
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.31
Std Dev	3.381

Gaus mean : 44.6578

Gaus Width: 3.8394

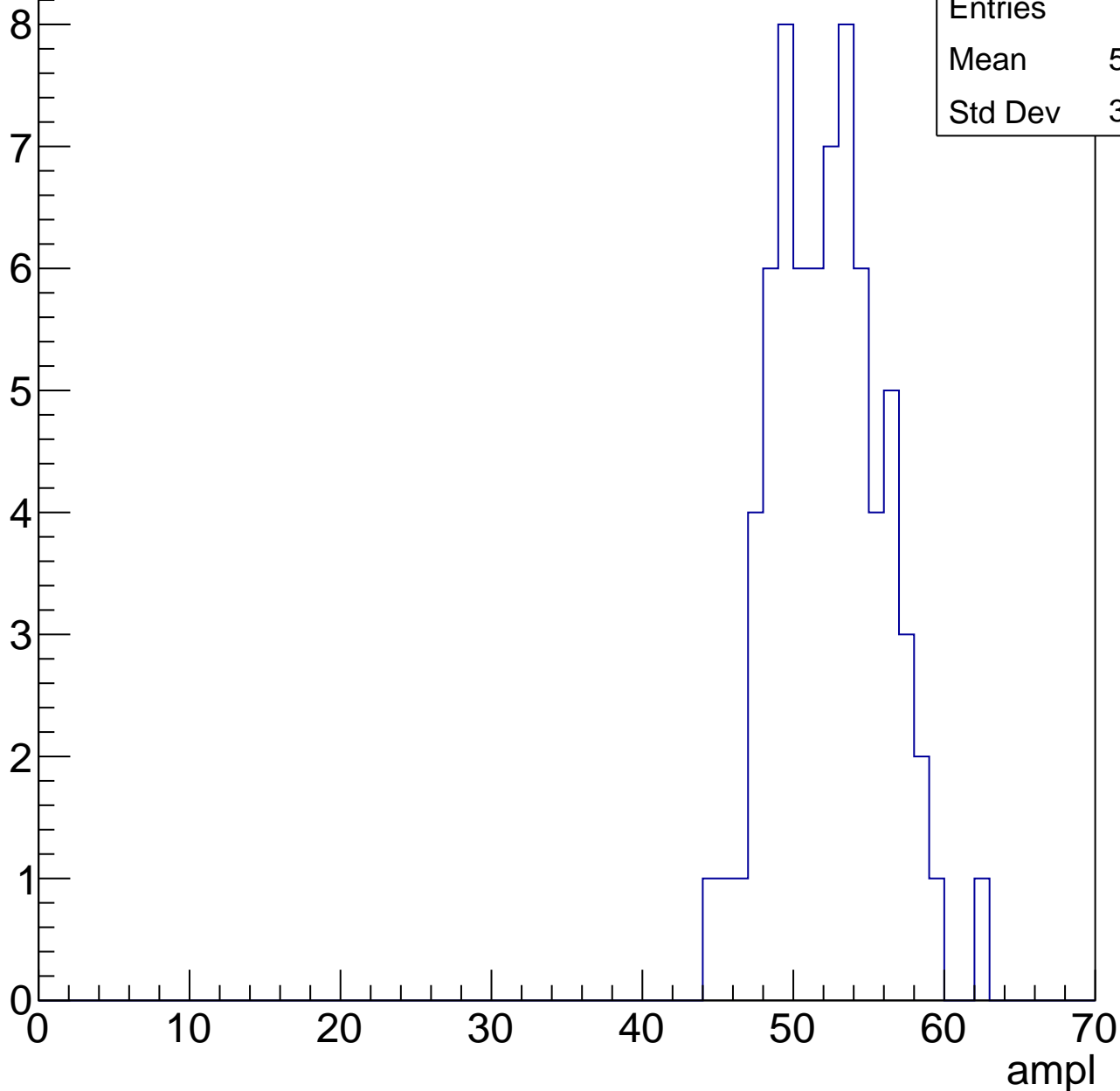


B1L100S, U12-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	51.84
Std Dev	3.564

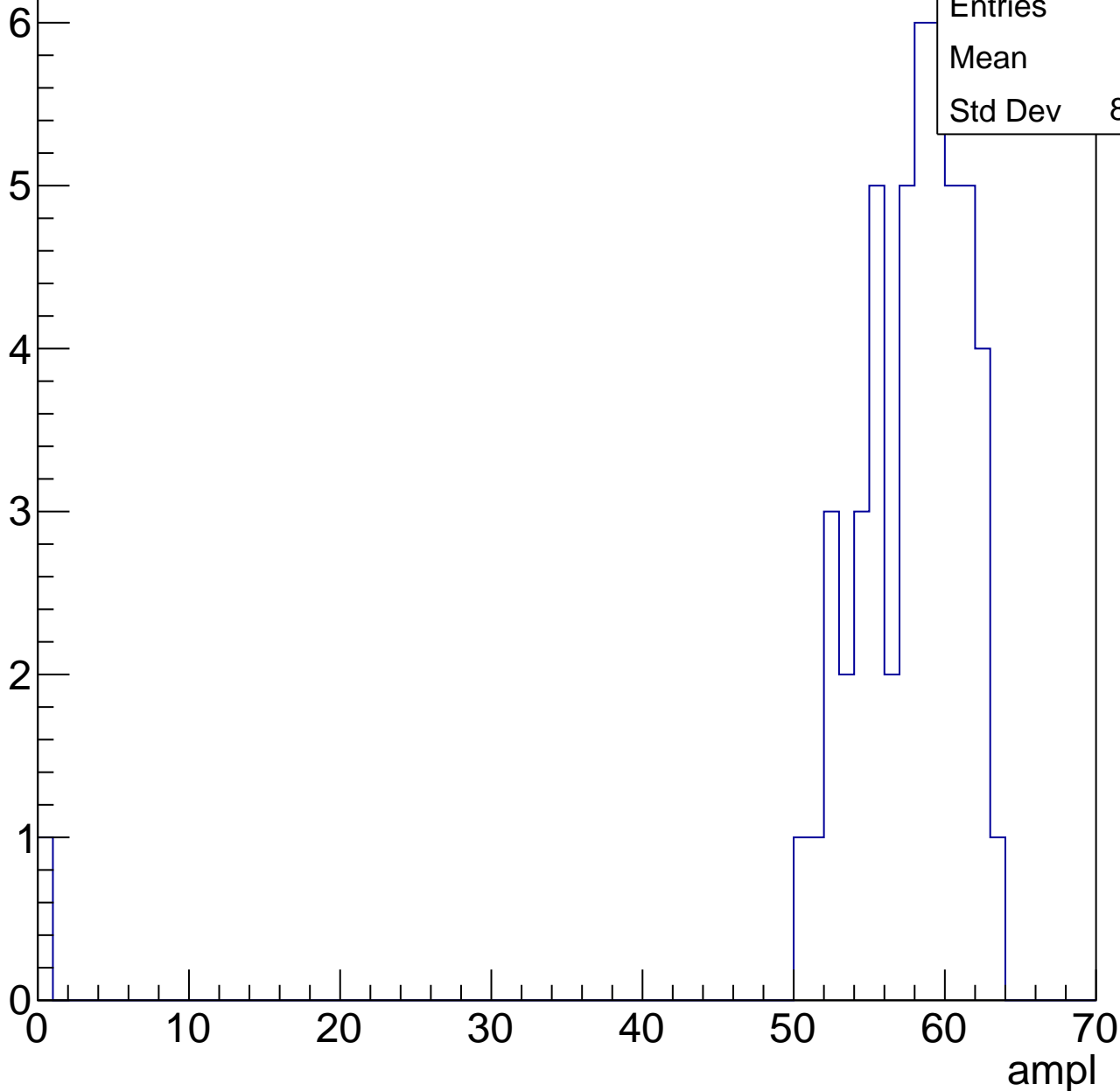


B1L100S, U12-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	56.3
Std Dev	8.668

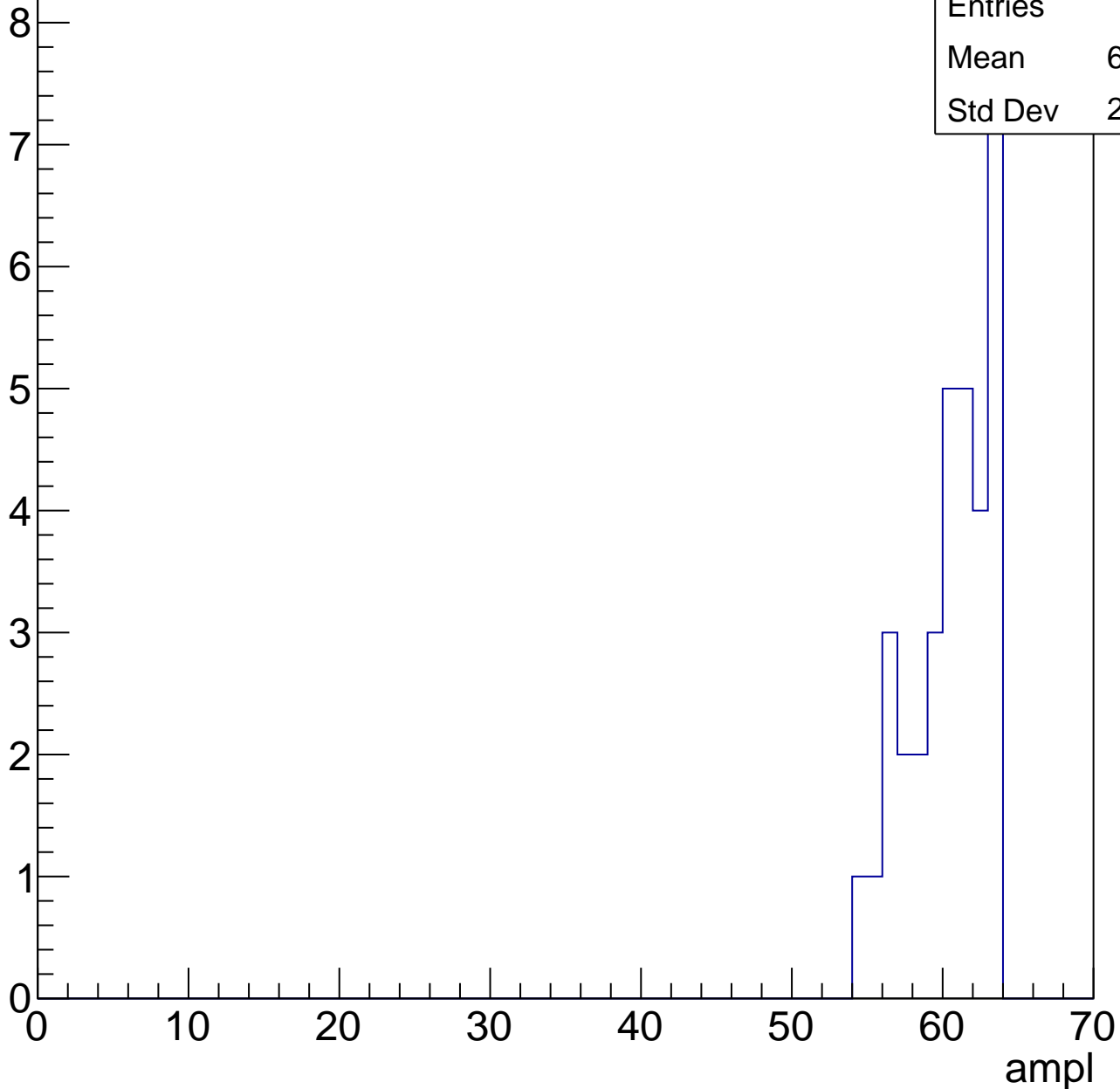


B1L100S, U12-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	34
Mean	60.03
Std Dev	2.606



B1L100S, U12-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U12-ch88, adc0

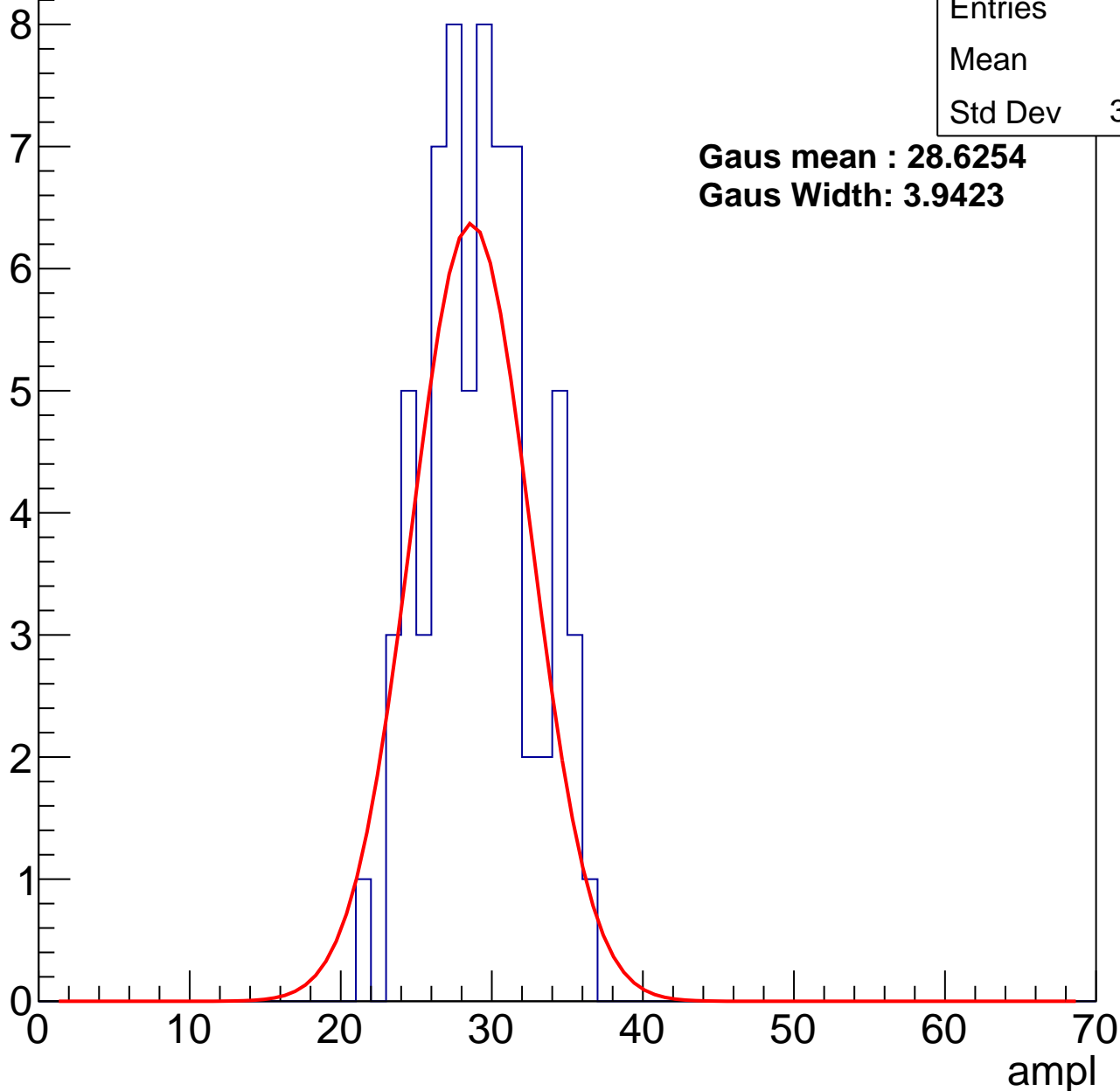
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.7
Std Dev	3.468

Gaus mean : 28.6254

Gaus Width: 3.9423



B1L100S, U12-ch88, adc1

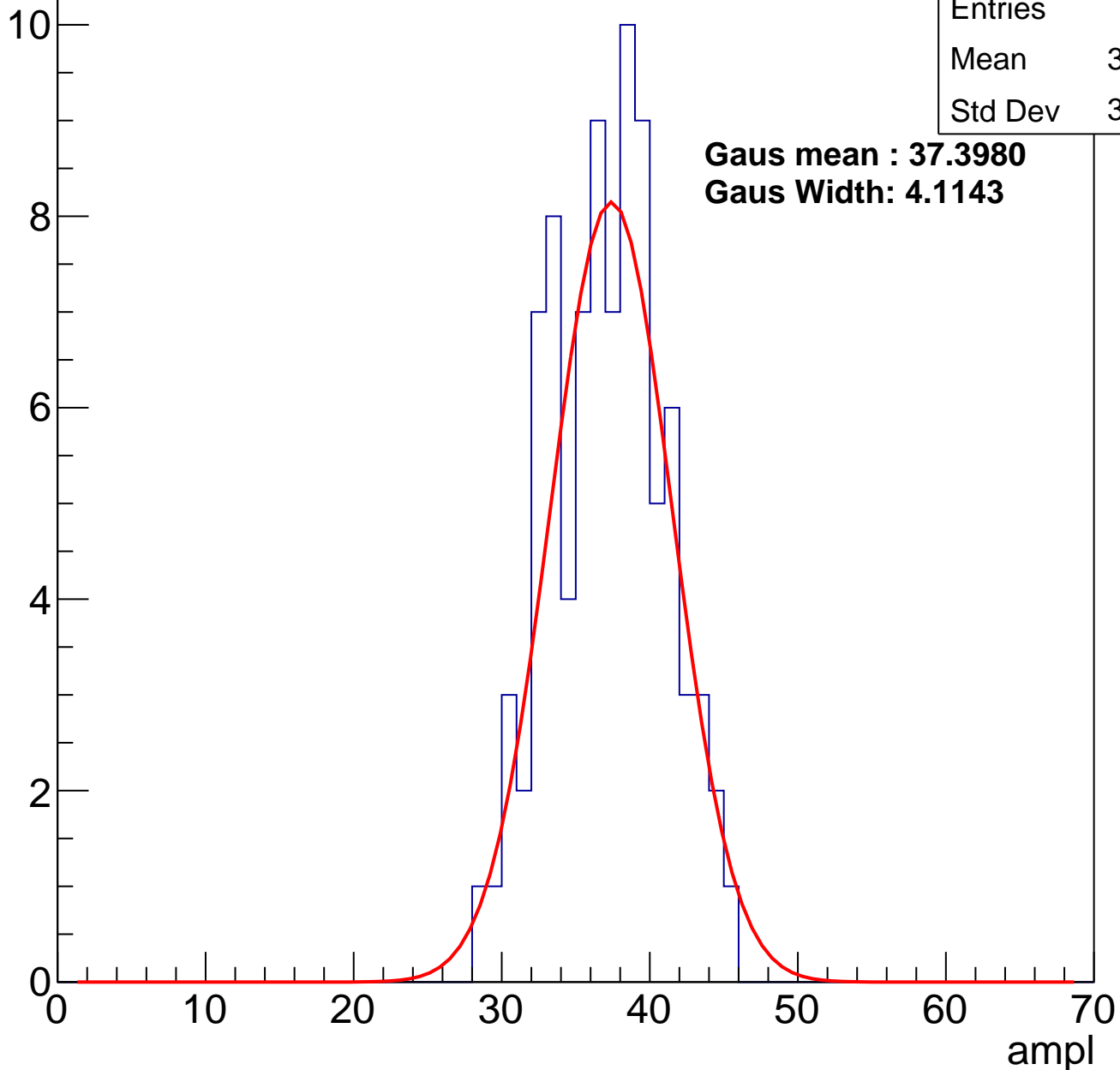
calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	36.66
Std Dev	3.787

Gaus mean : 37.3980

Gaus Width: 4.1143

Entry



B1L100S, U12-ch88, adc2

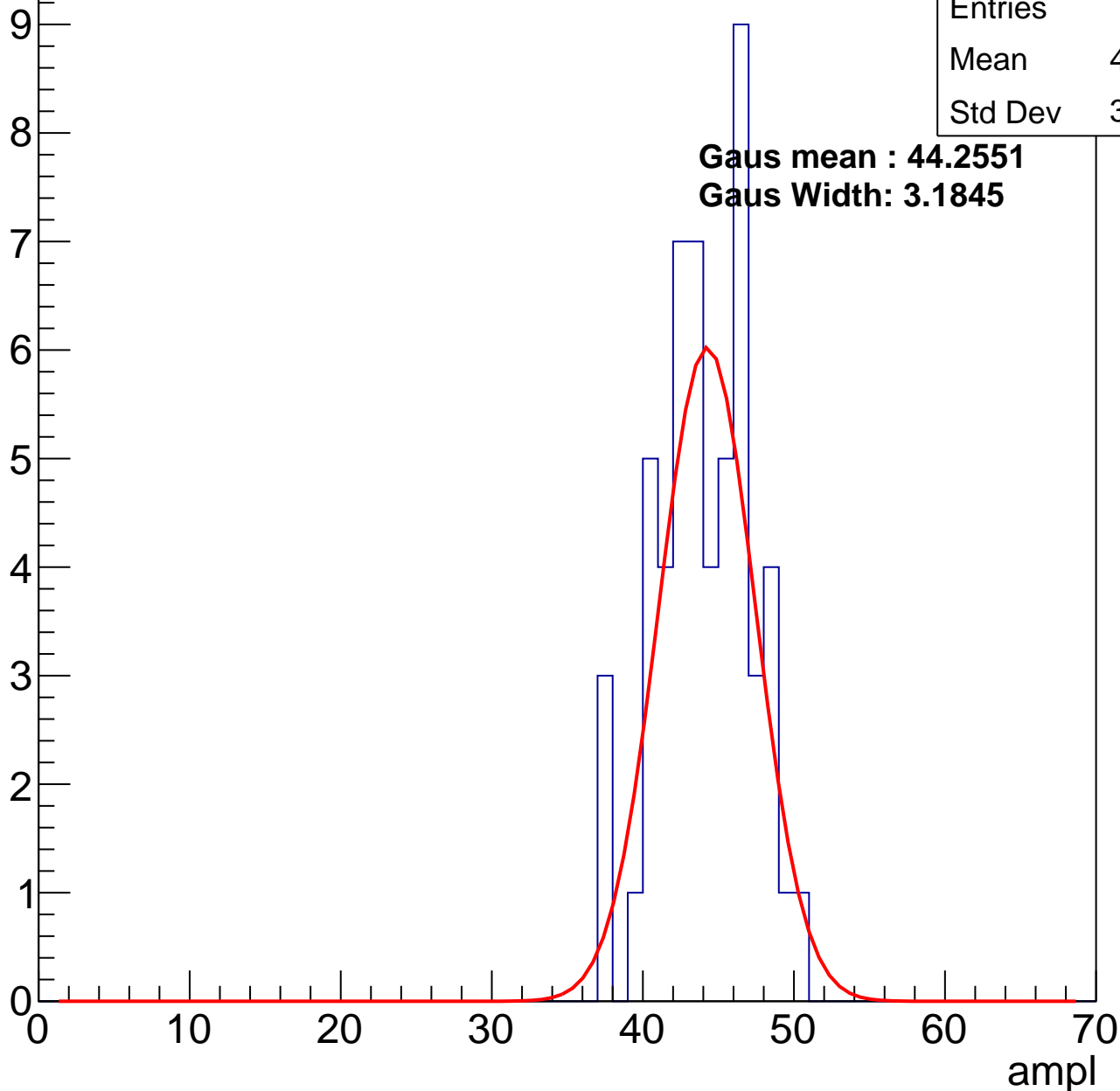
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	43.63
Std Dev	3.069

Gaus mean : 44.2551

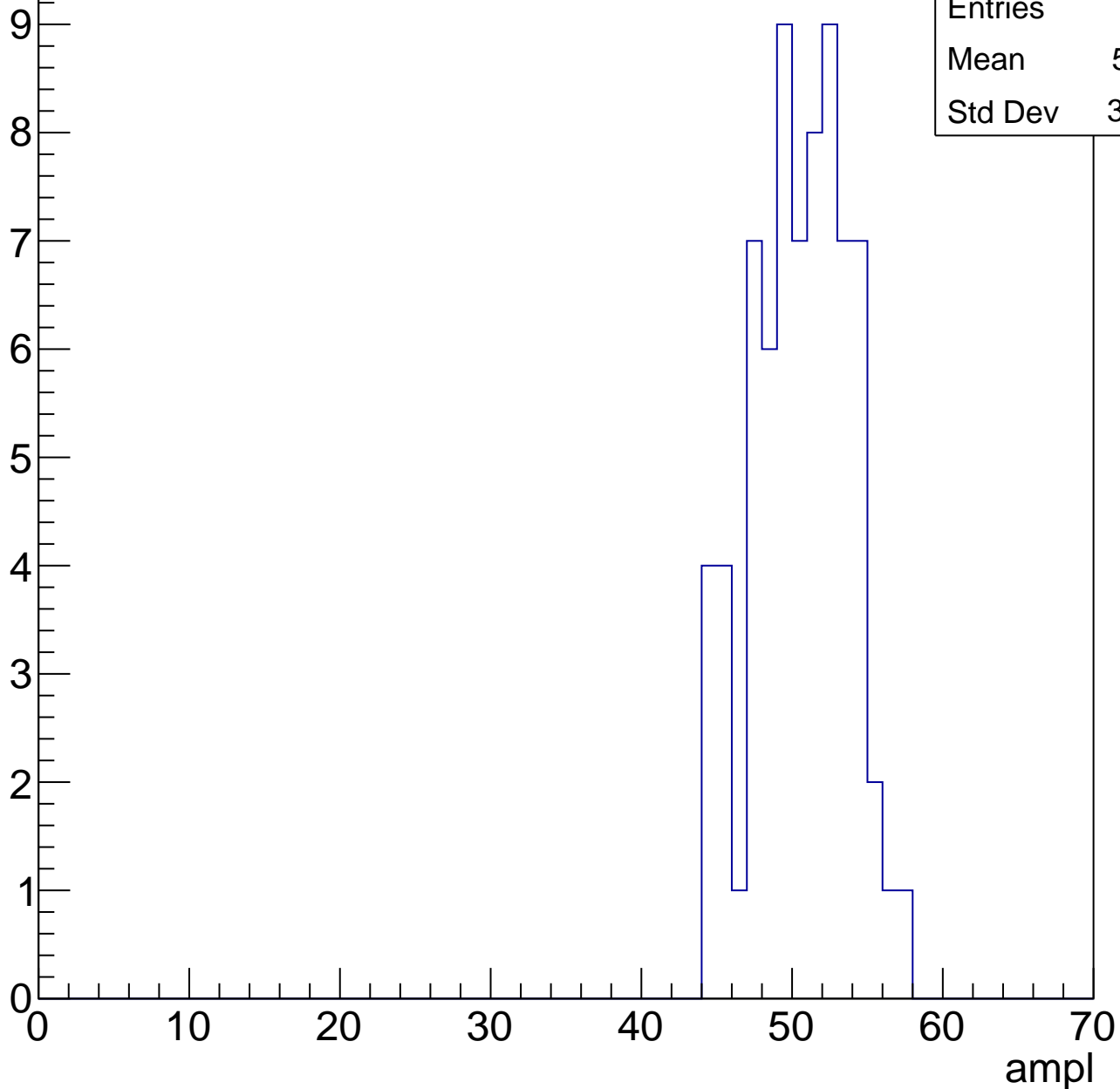
Gaus Width: 3.1845



B1L100S, U12-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



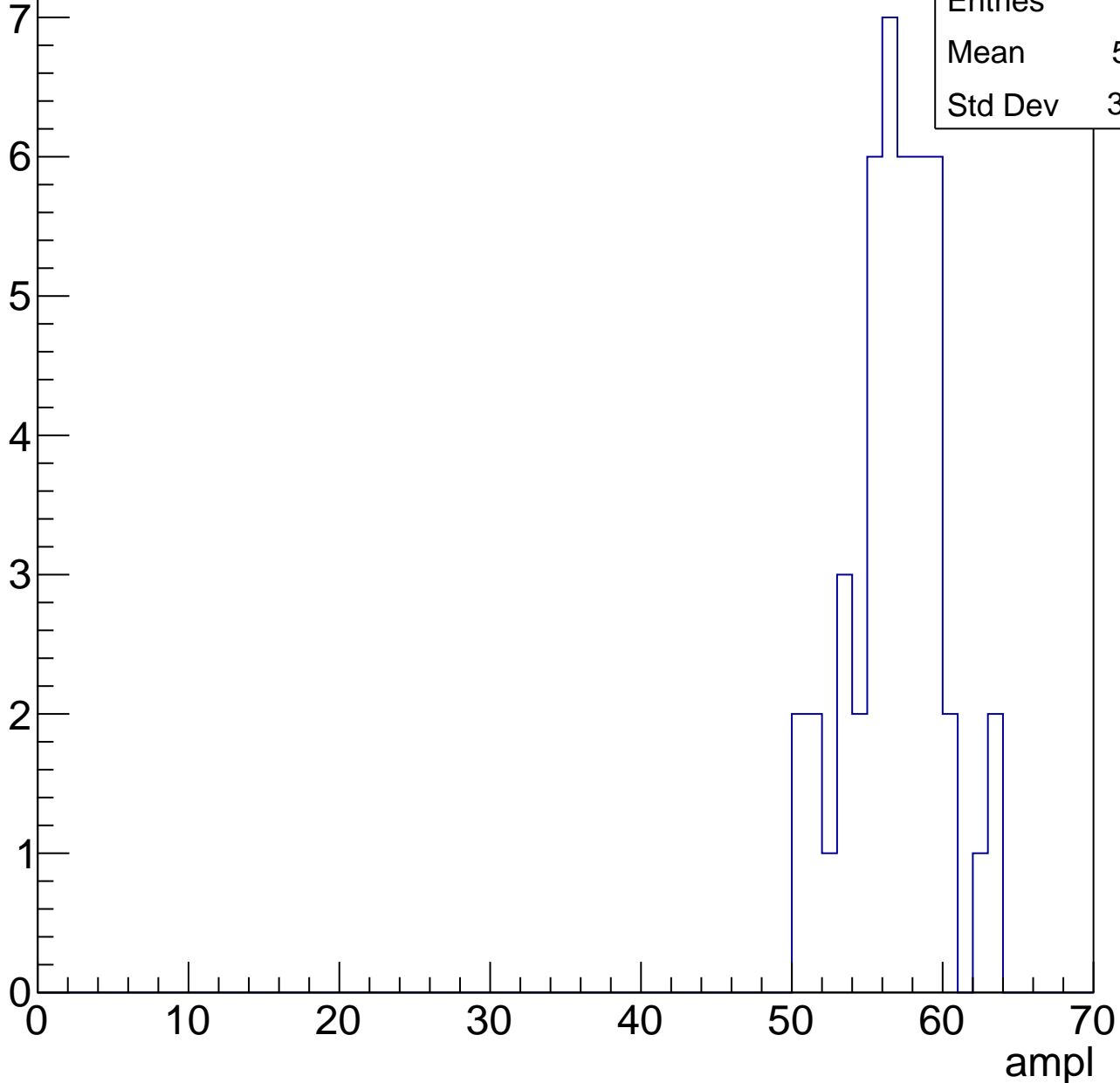
Entries	73
Mean	50.11
Std Dev	3.117

B1L100S, U12-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	56.41
Std Dev	3.026



B1L100S, U12-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

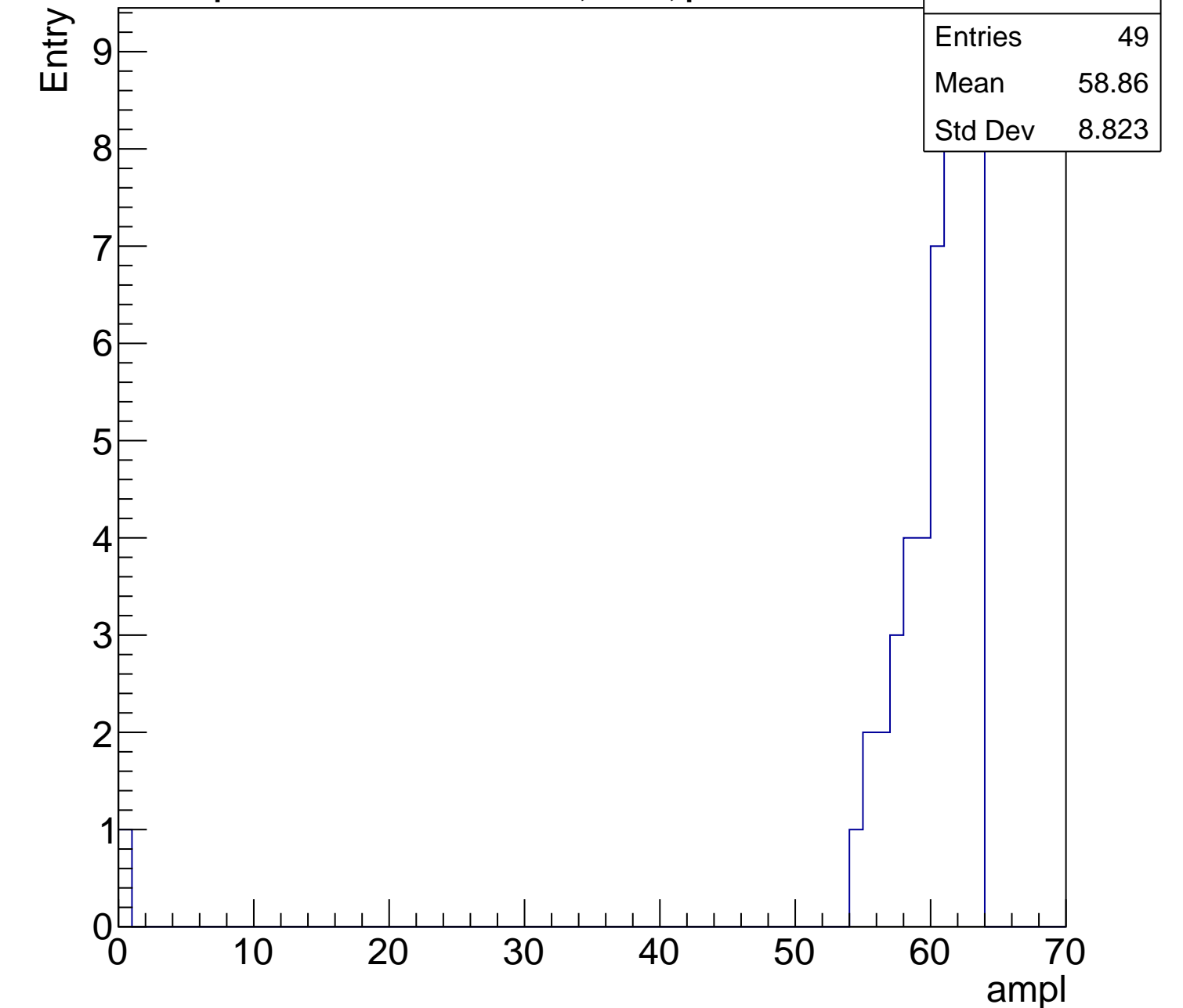
Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.86
Std Dev	8.823

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch89, adc0

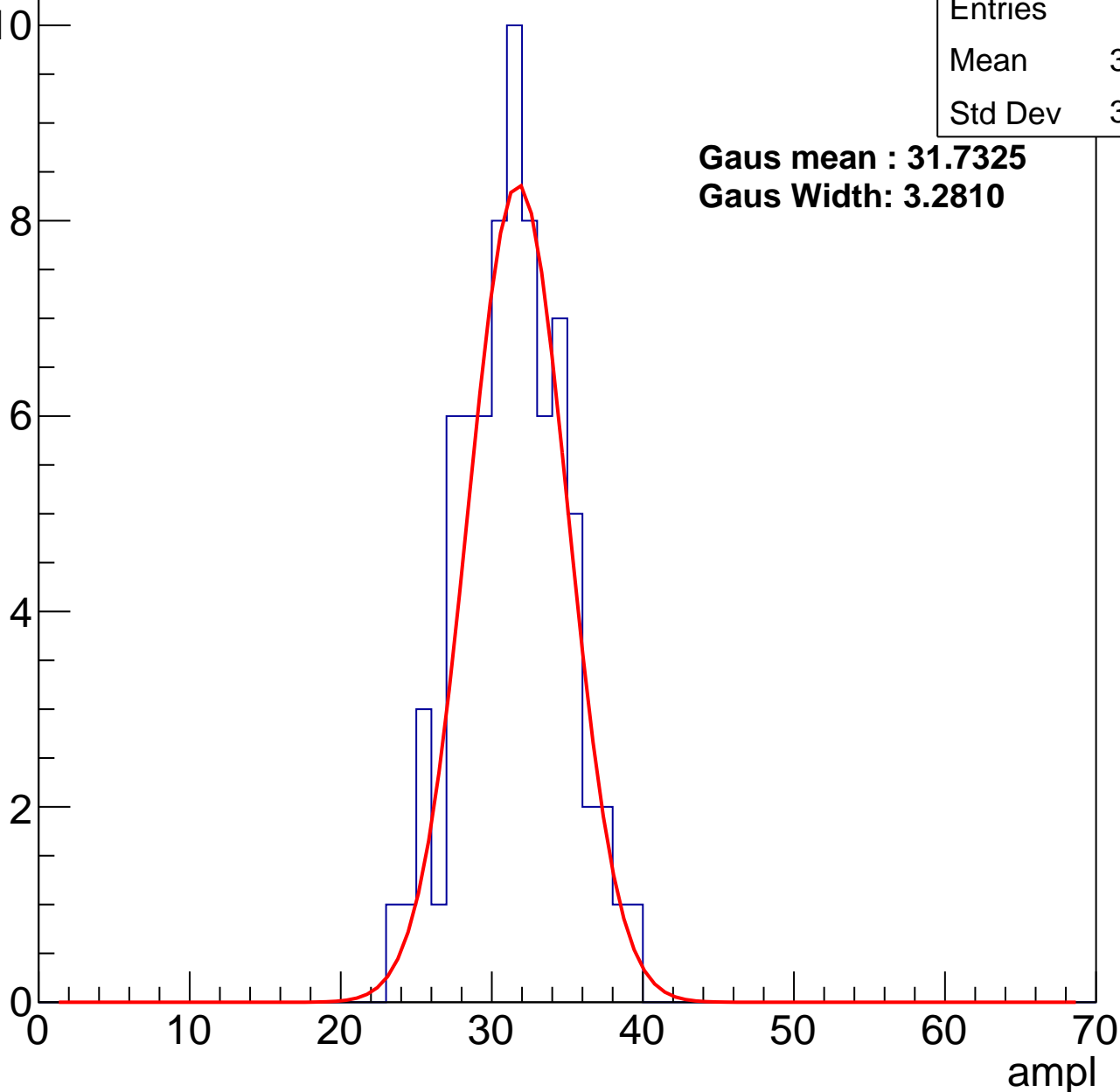
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.97
Std Dev	3.365

Gaus mean : 31.7325

Gaus Width: 3.2810



B1L100S, U12-ch89, adc1

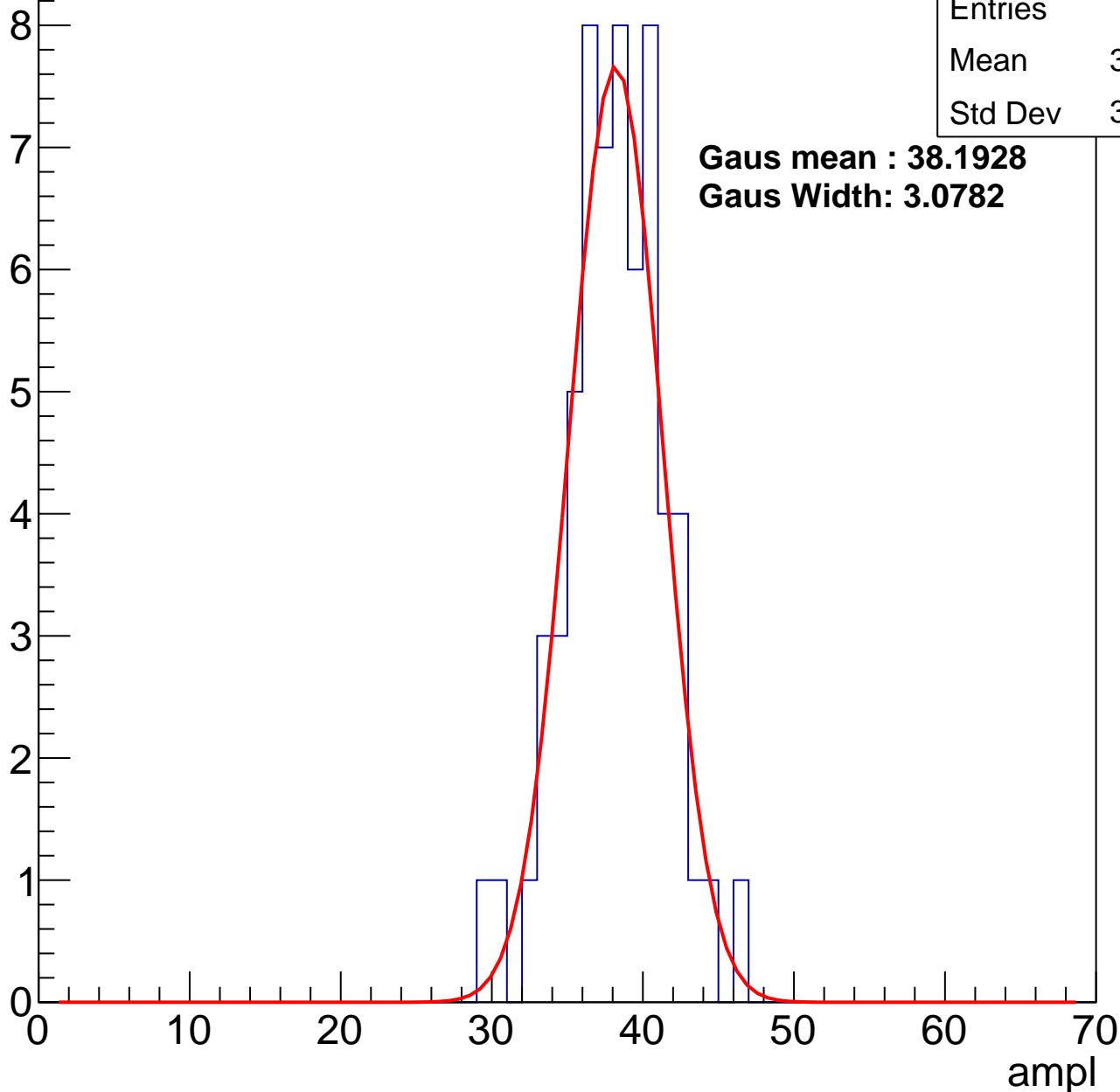
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	37.69
Std Dev	3.226

Gaus mean : 38.1928

Gaus Width: 3.0782



B1L100S, U12-ch89, adc2

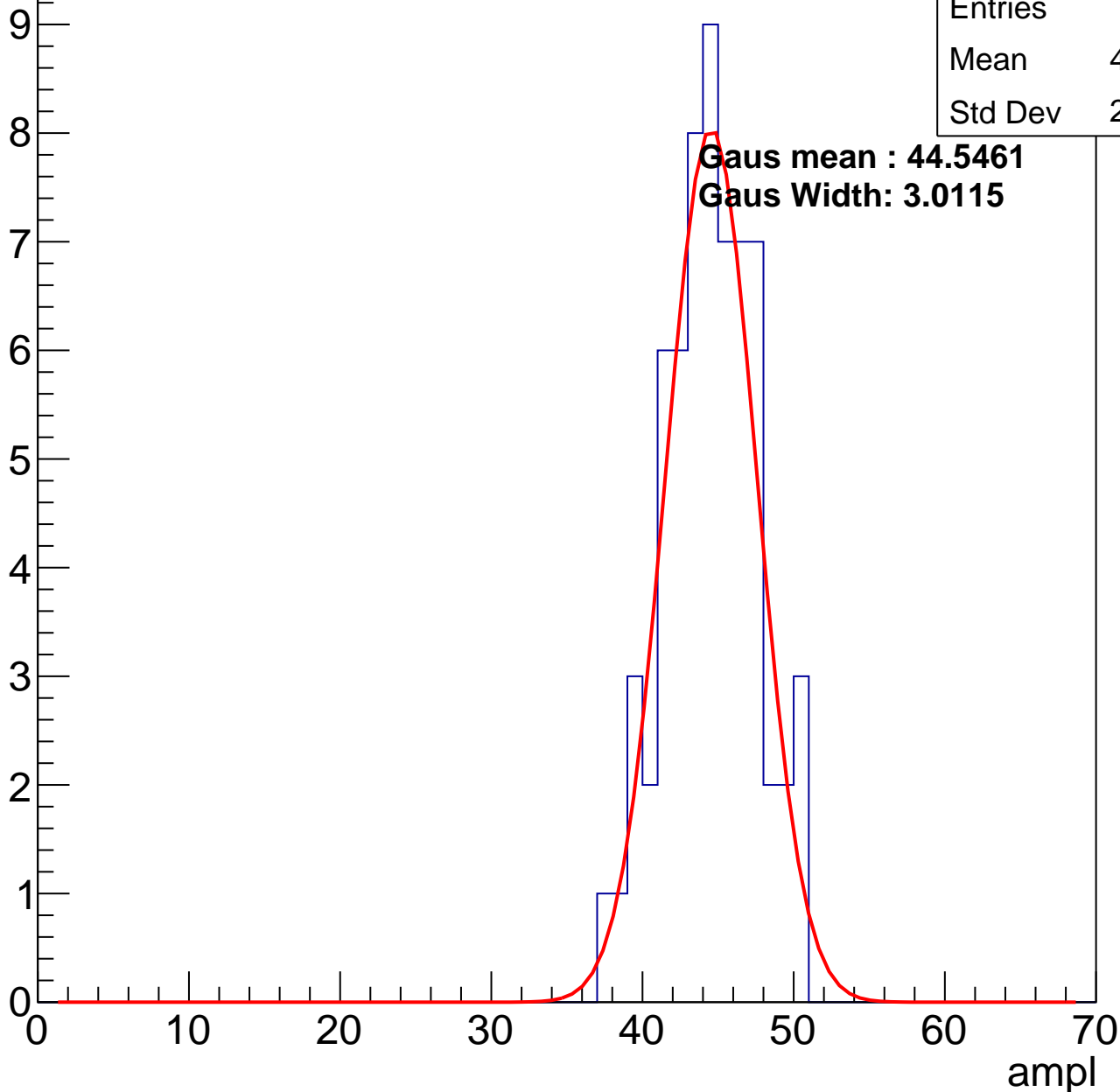
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.06
Std Dev	2.973

Gaus mean : 44.5461

Gaus Width: 3.0115

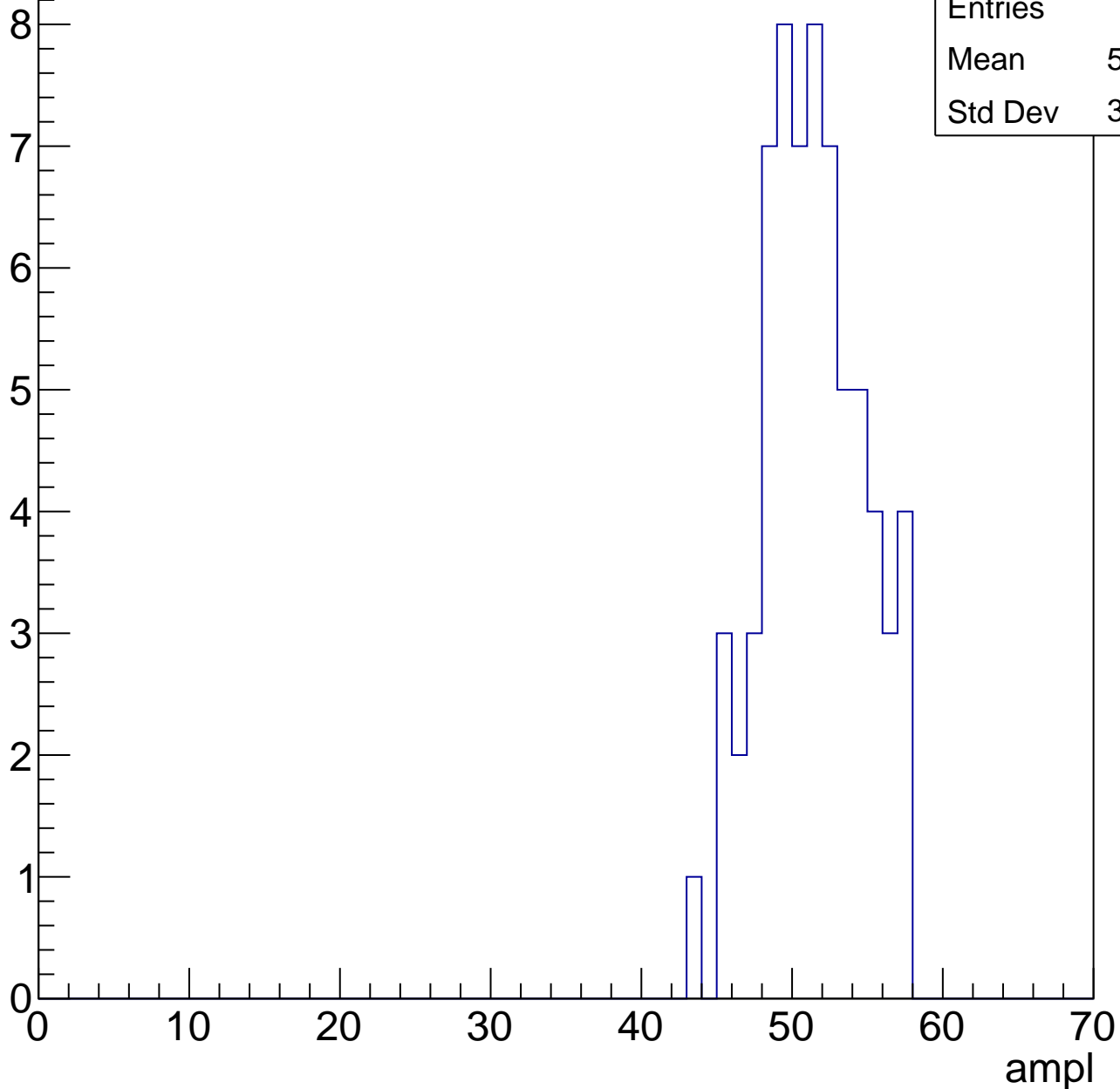


B1L100S, U12-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

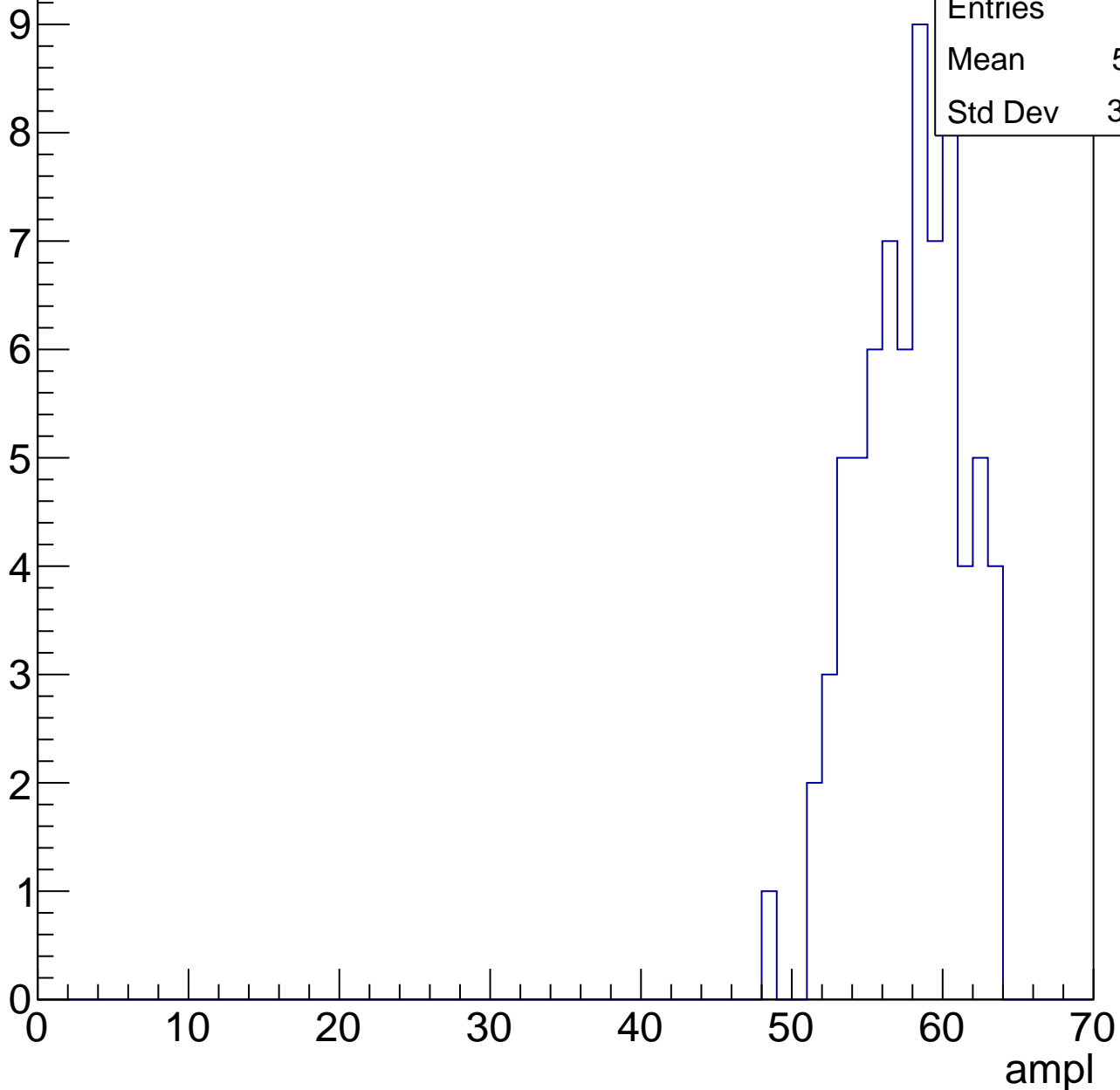
Entries	67
Mean	50.93
Std Dev	3.293



B1L100S, U12-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

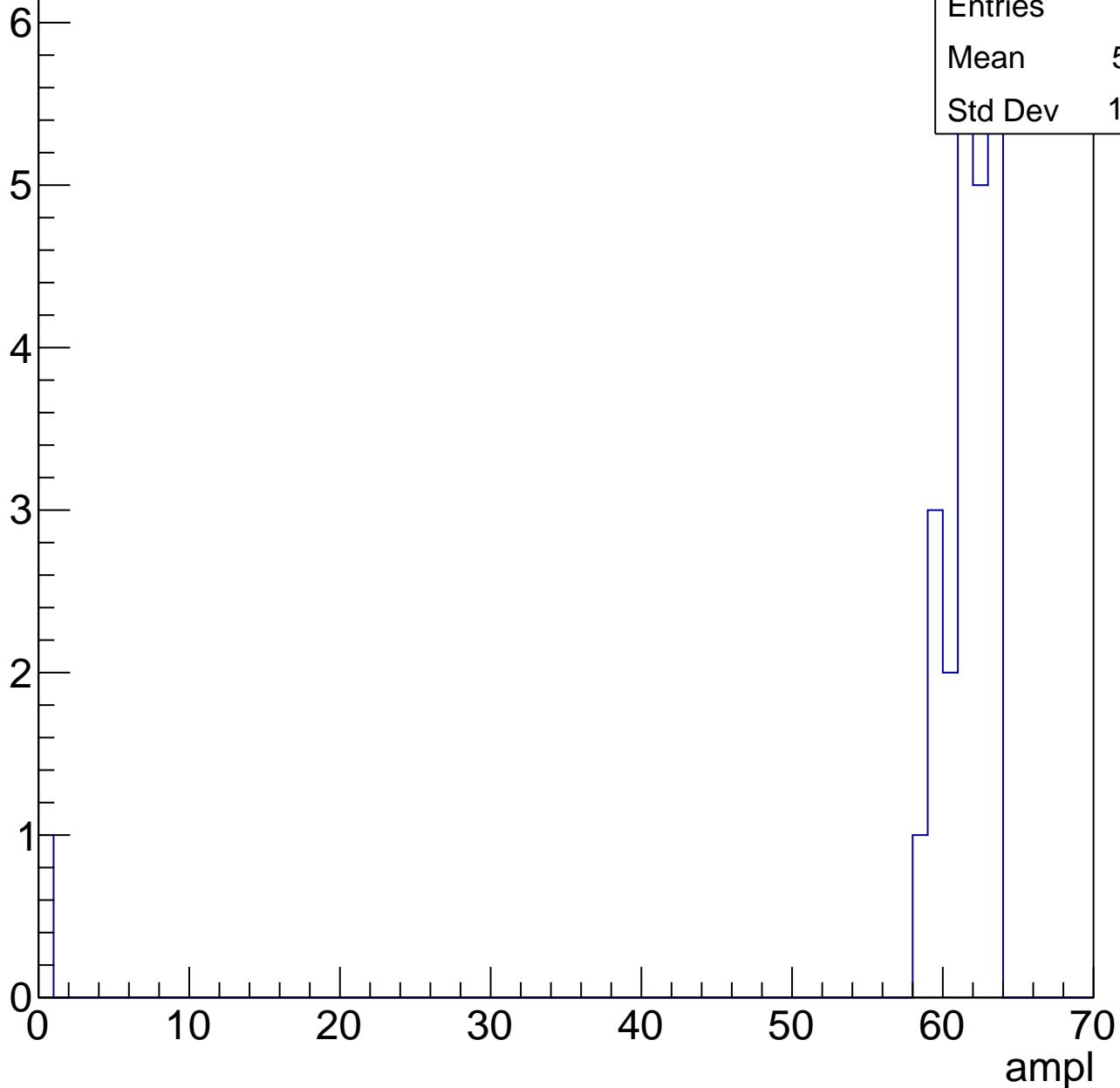


Entries	72
Mean	57.31
Std Dev	3.373

B1L100S, U12-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

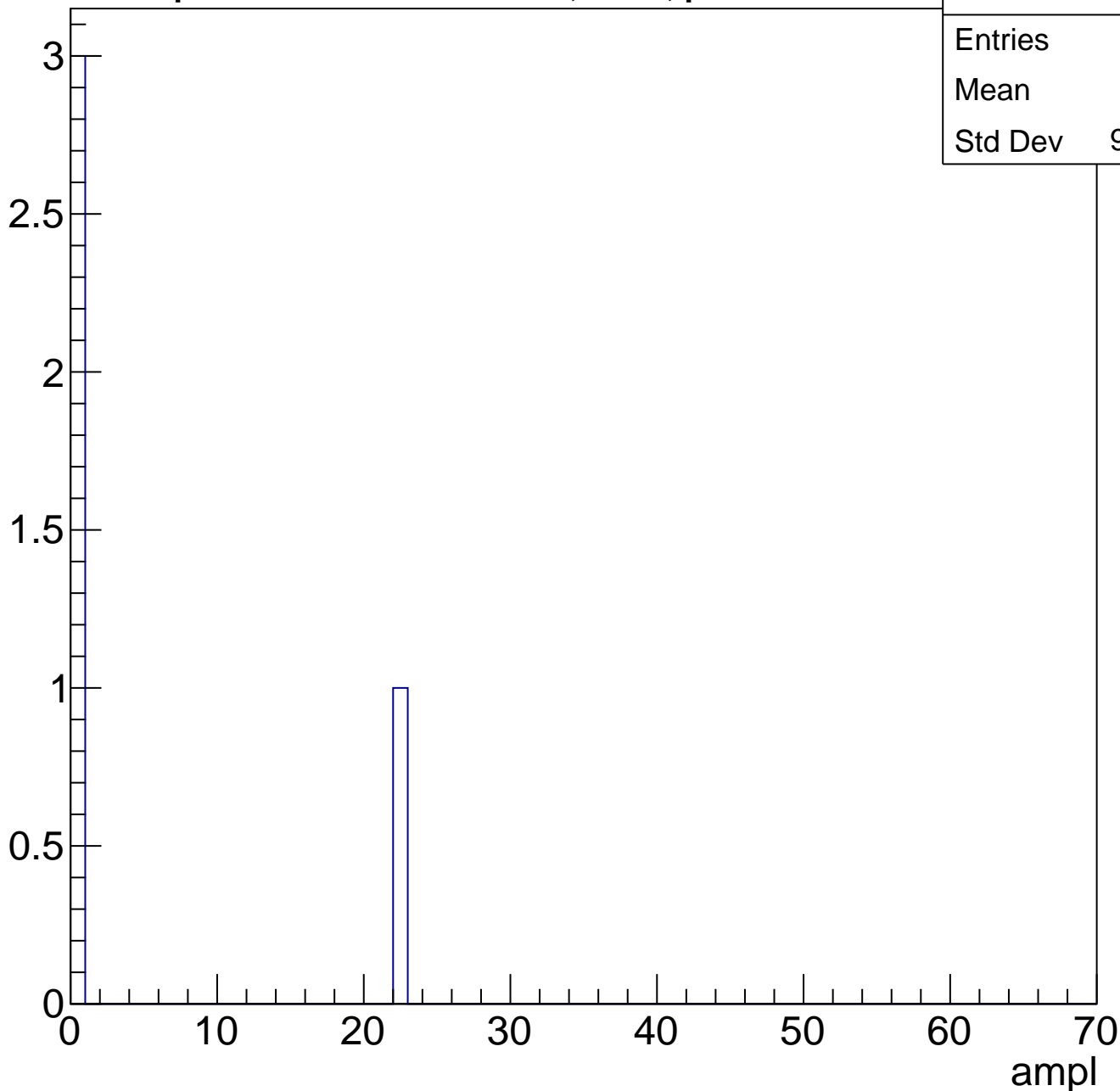


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B1L100S, U12-ch90, adc0

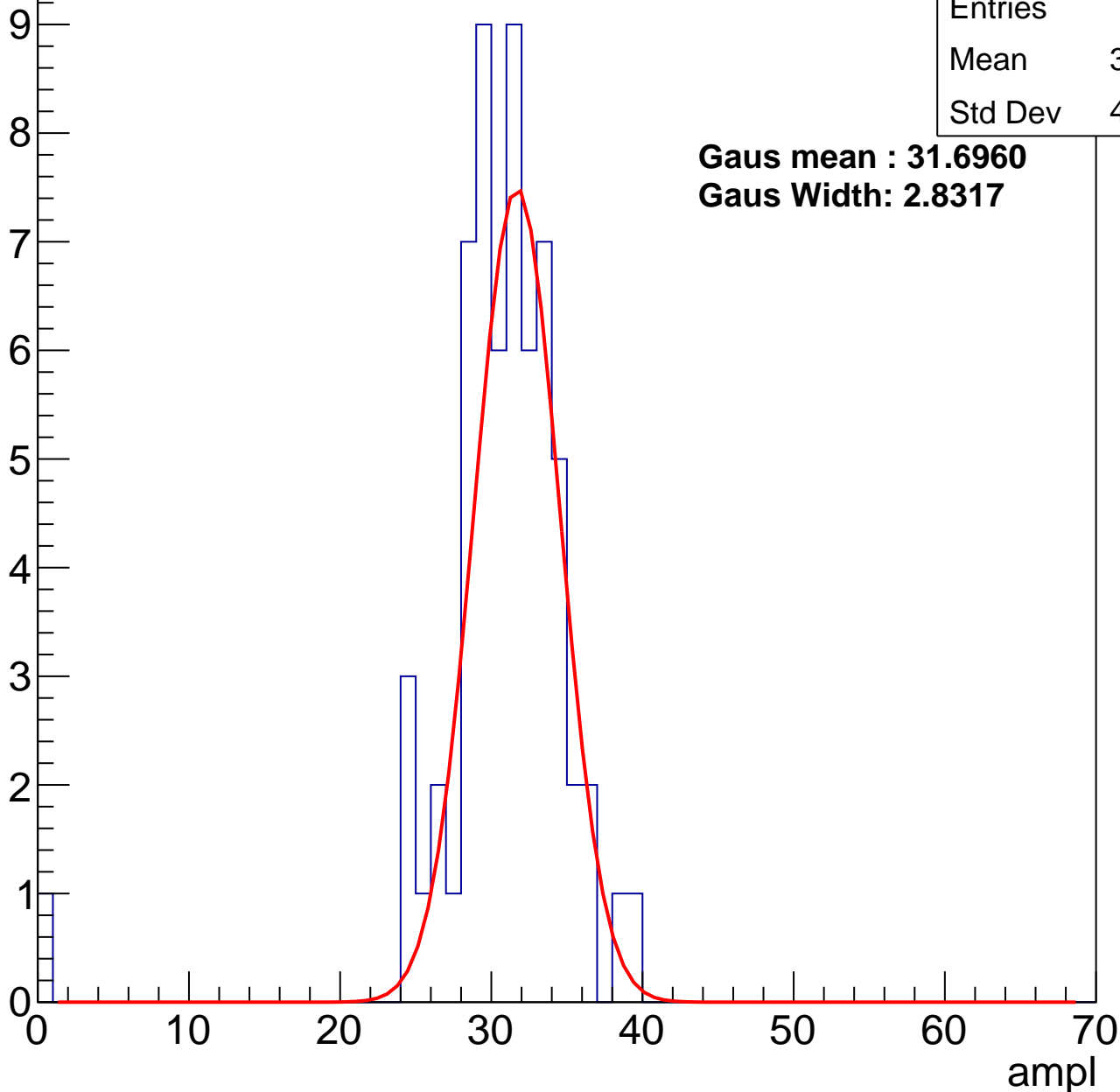
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	30.22
Std Dev	4.962

Gaus mean : 31.6960

Gaus Width: 2.8317



B1L100S, U12-ch90, adc1

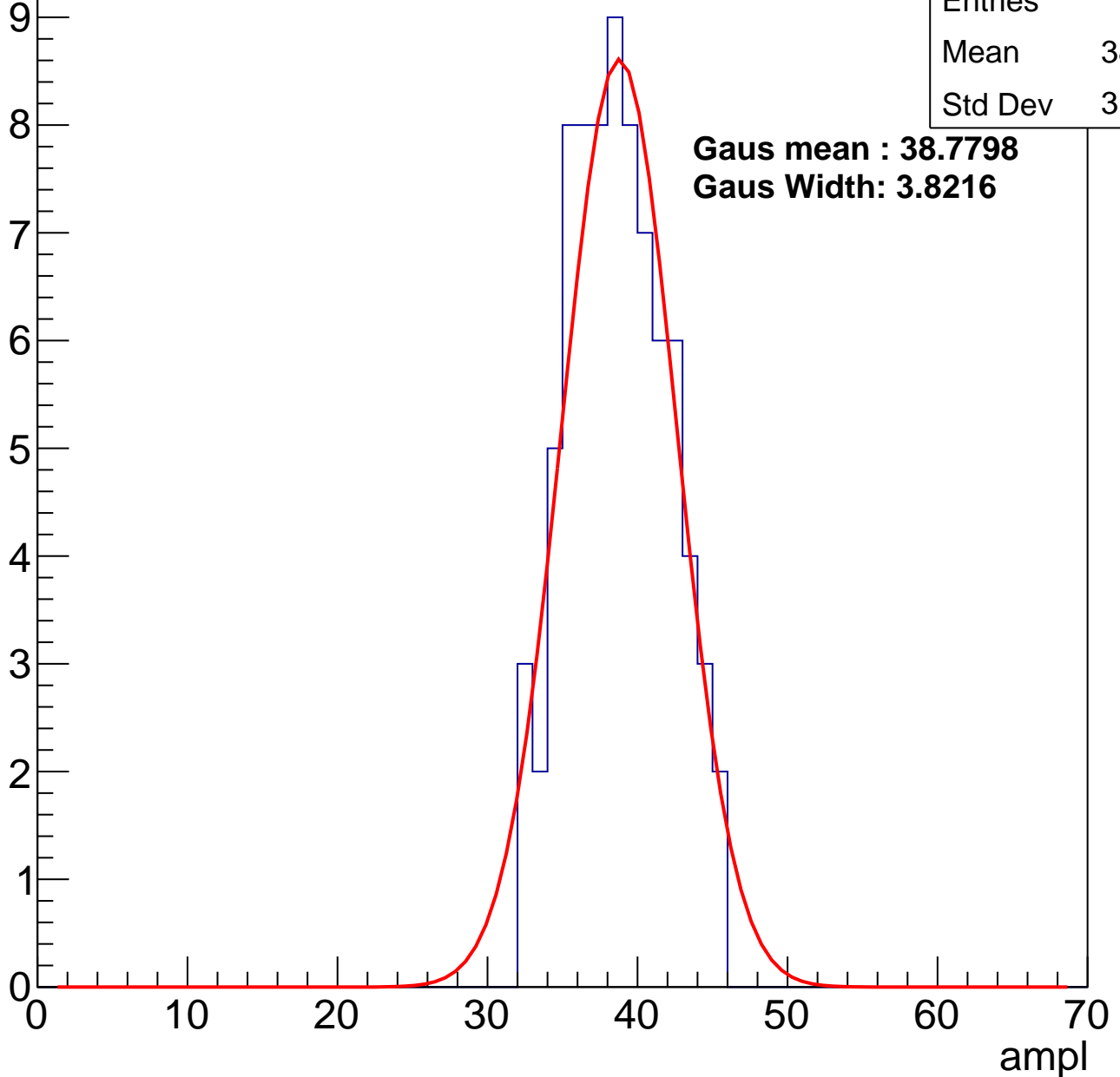
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	38.25
Std Dev	3.255

Gaus mean : 38.7798

Gaus Width: 3.8216



B1L100S, U12-ch90, adc2

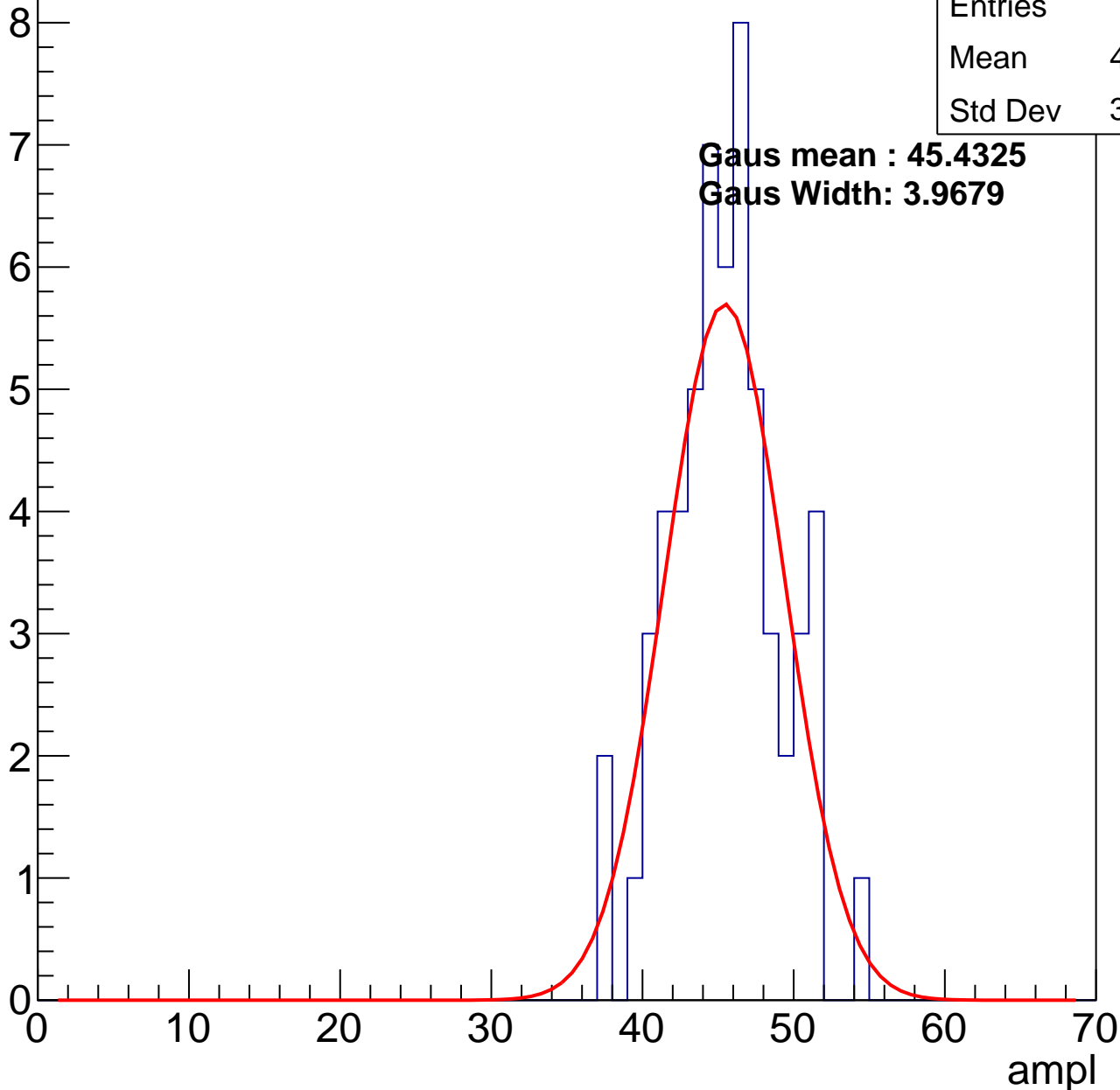
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	45.02
Std Dev	3.603

Gaus mean : 45.4325

Gaus Width: 3.9679

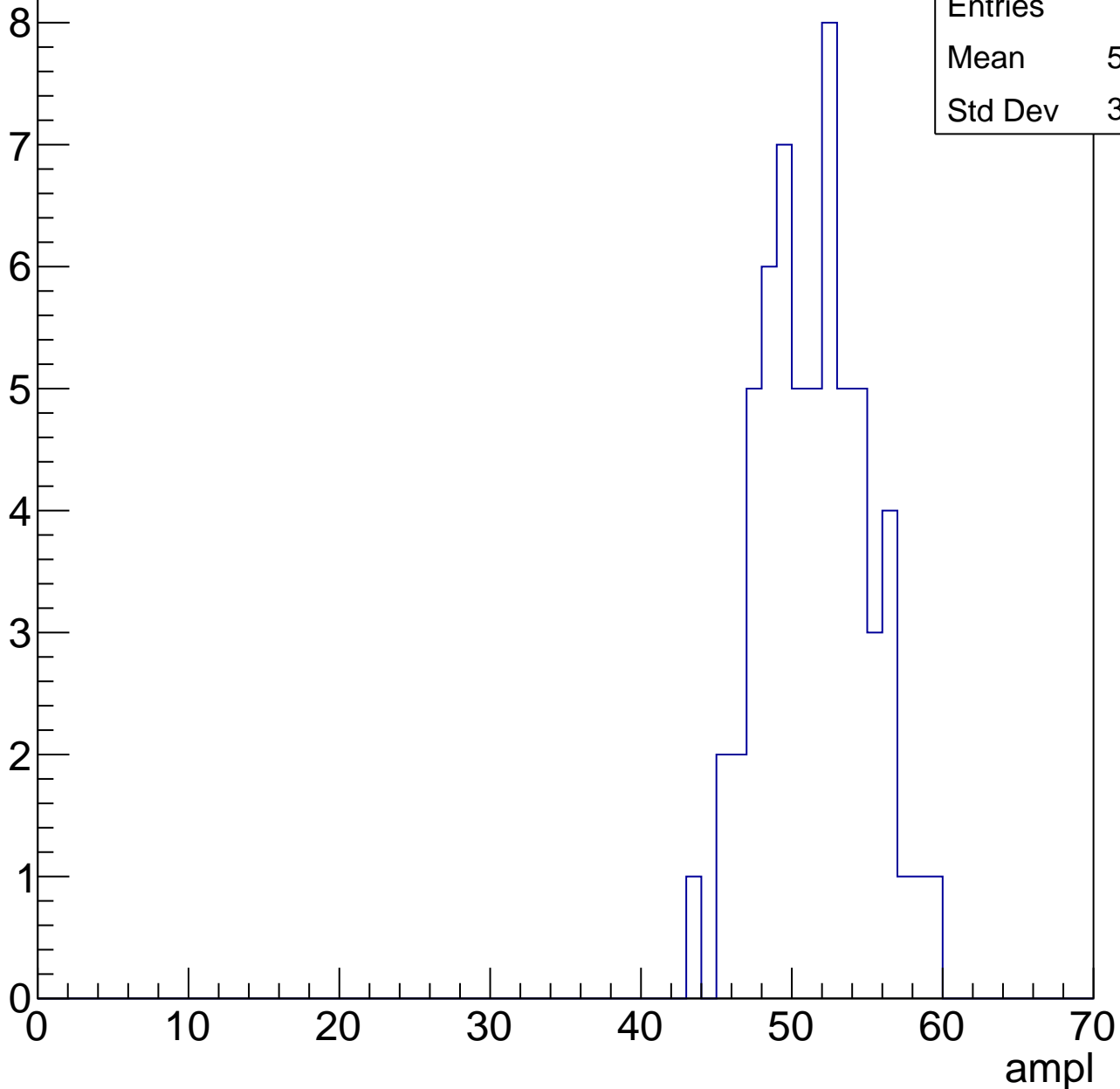


B1L100S, U12-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.98
Std Dev	3.443

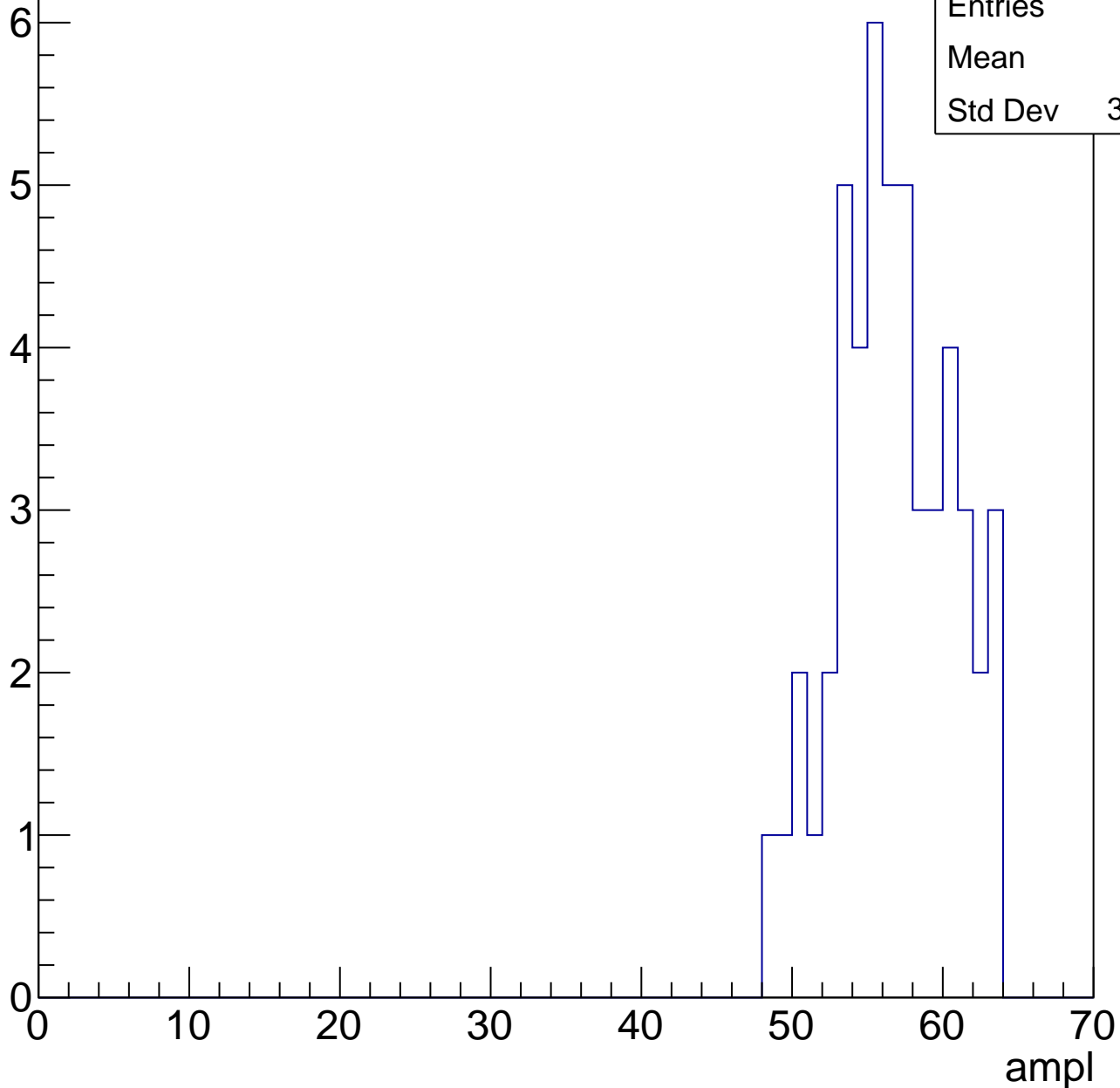


B1L100S, U12-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

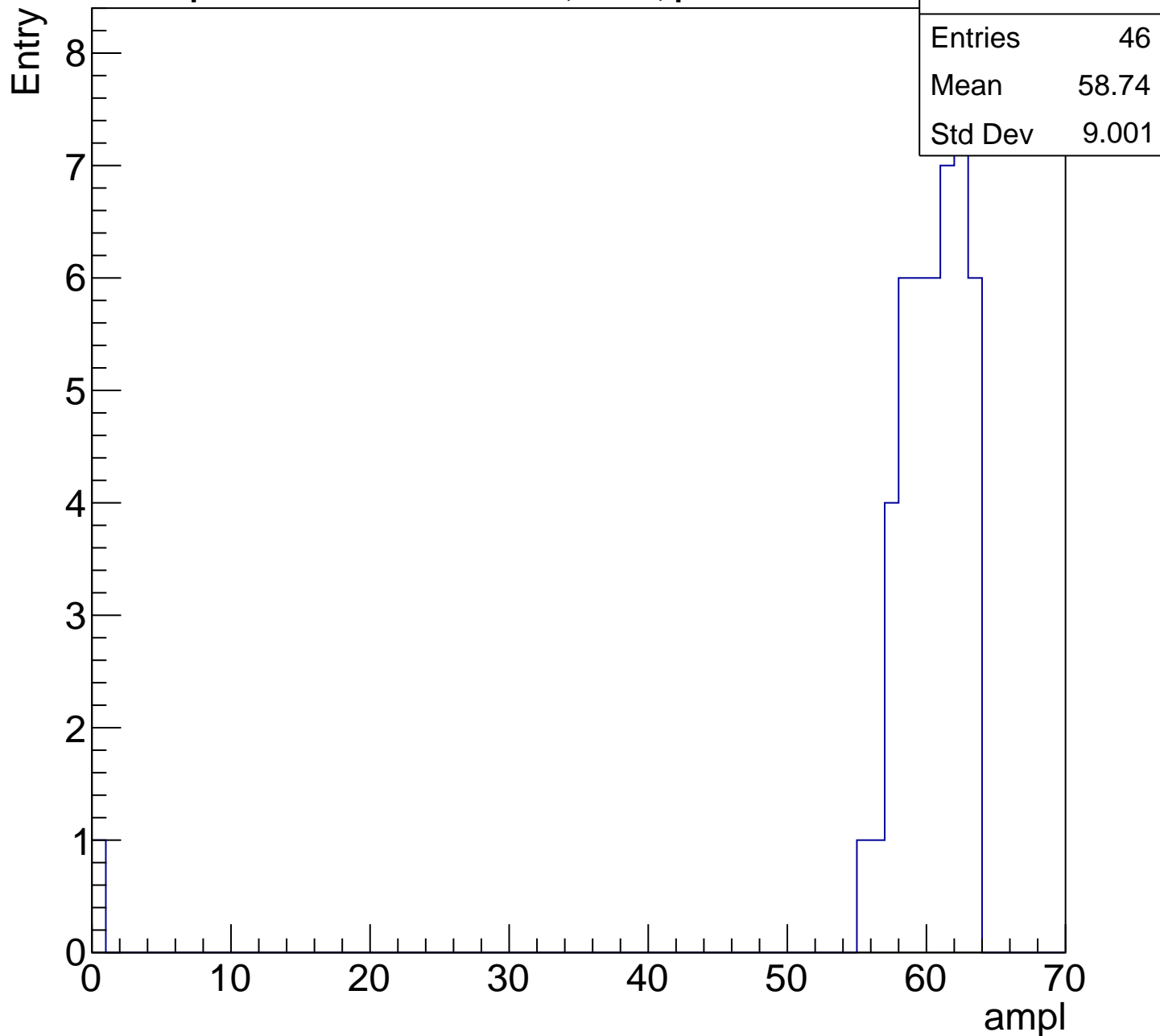
Entry

Entries	50
Mean	56.3
Std Dev	3.759



B1L100S, U12-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U12-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L100S, U12-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch91, adc0

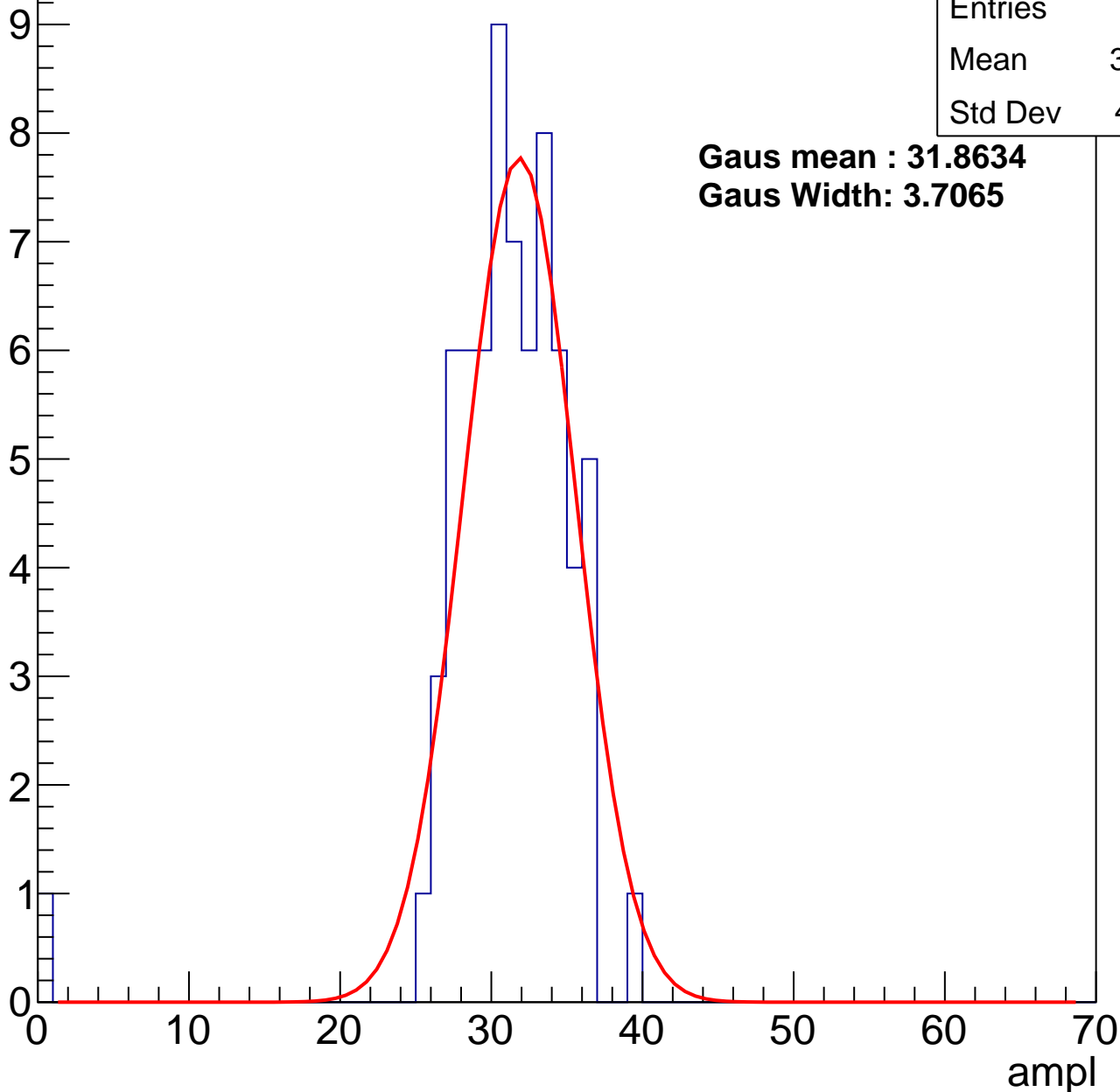
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	30.62
Std Dev	4.801

Gaus mean : 31.8634

Gaus Width: 3.7065



B1L100S, U12-ch91, adc1

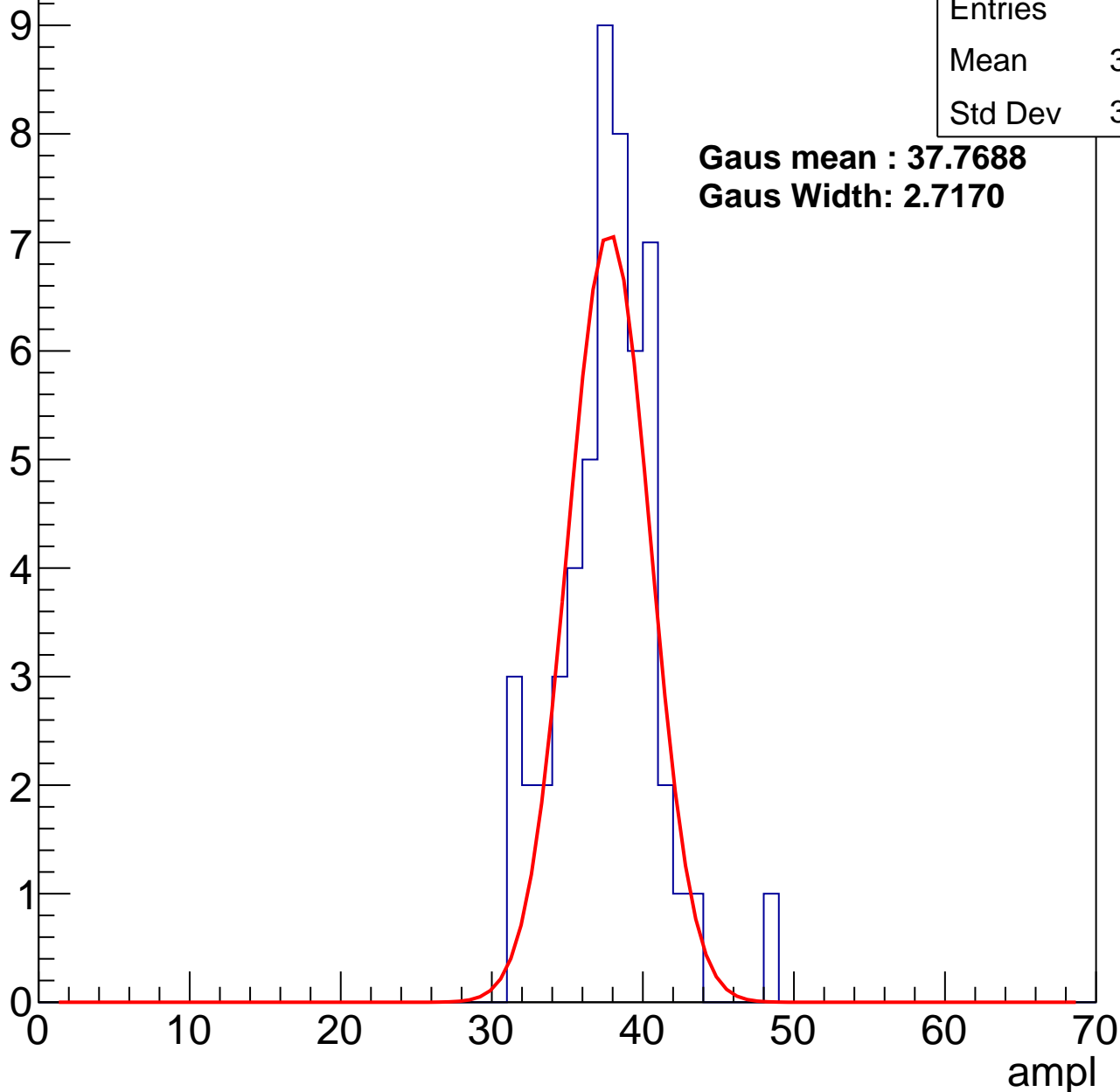
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	37.24
Std Dev	3.174

Gaus mean : 37.7688

Gaus Width: 2.7170



B1L100S, U12-ch91, adc2

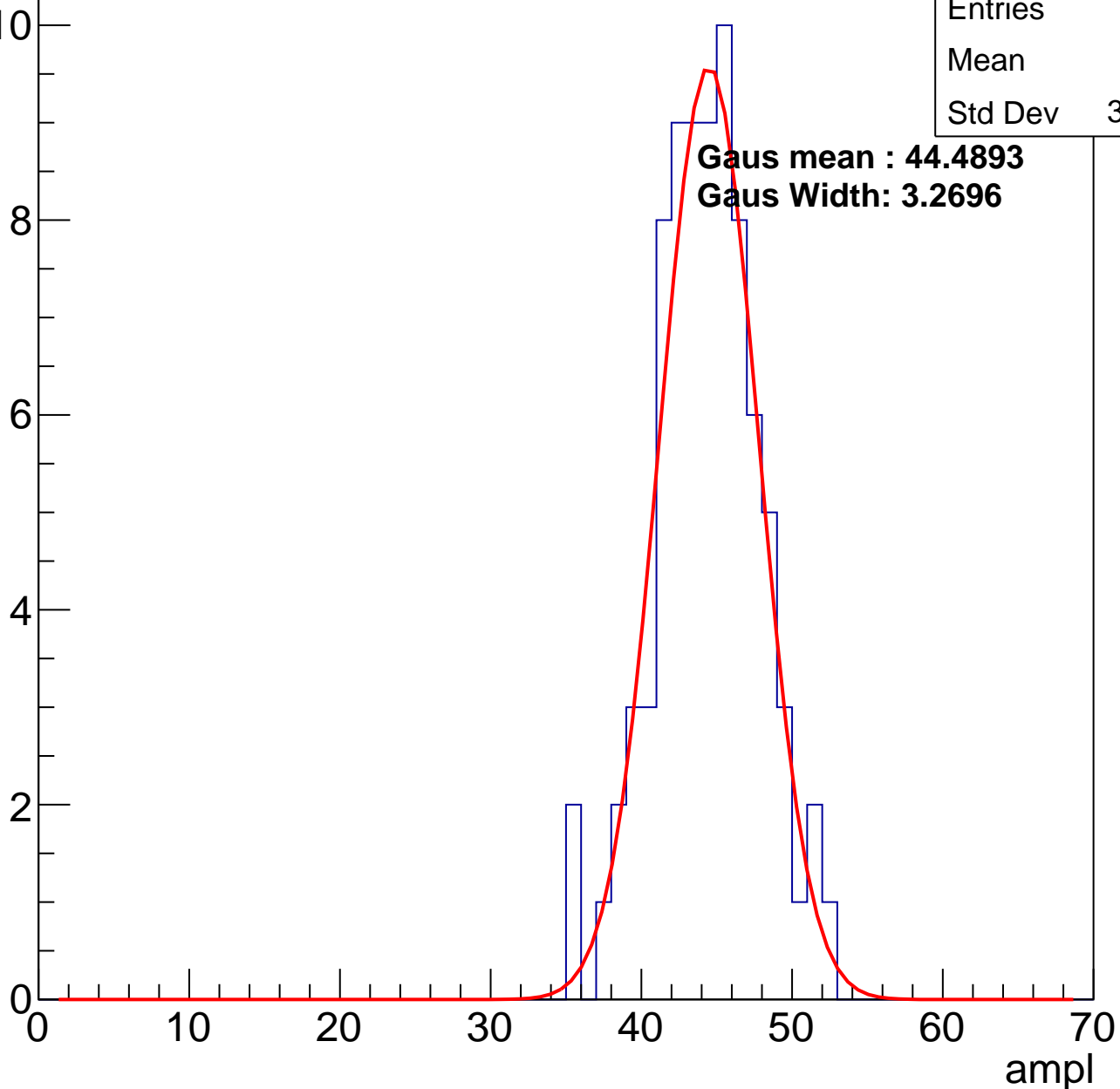
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	43.9
Std Dev	3.442

Gaus mean : 44.4893

Gaus Width: 3.2696

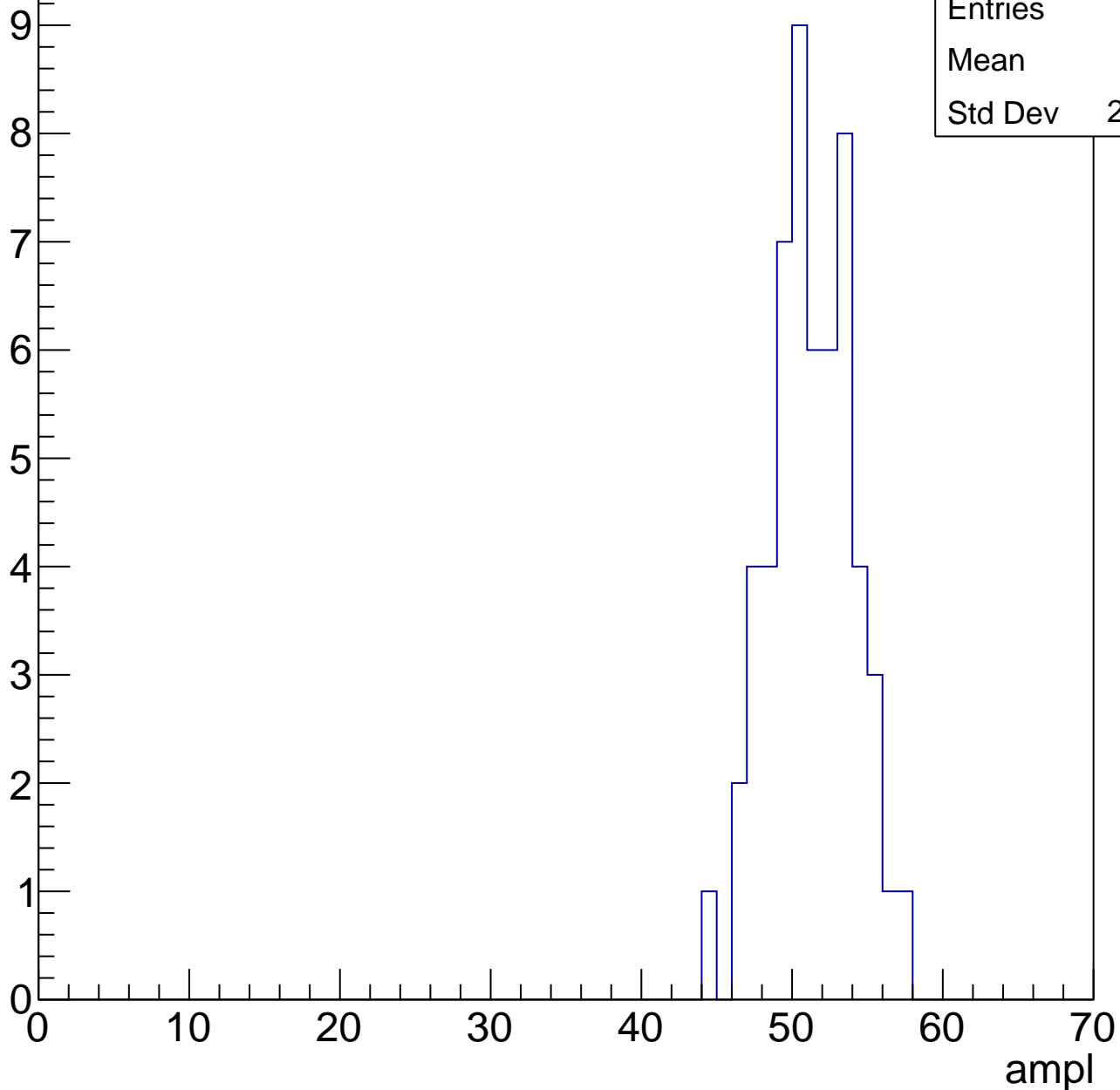


B1L100S, U12-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	50.8
Std Dev	2.728

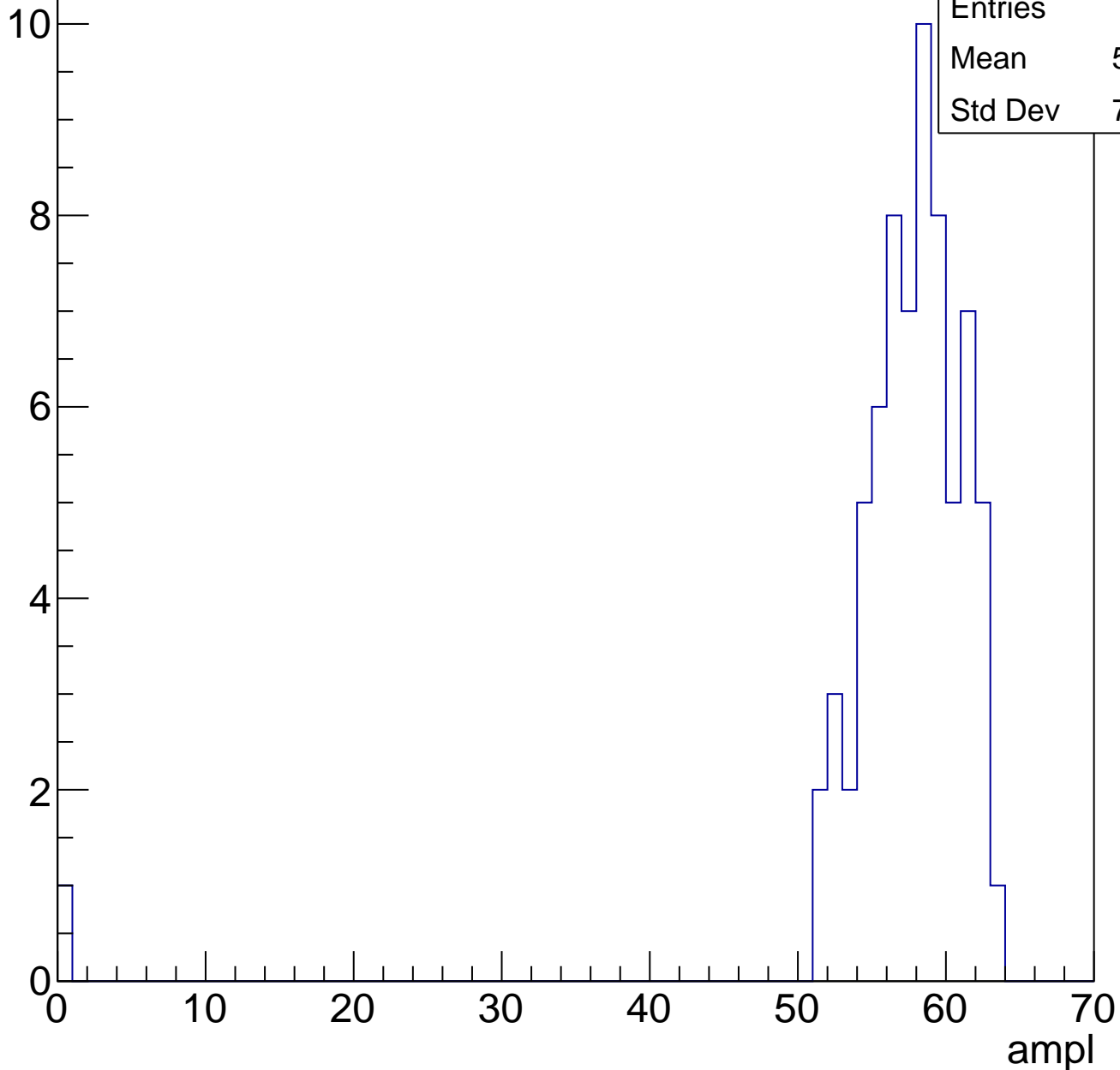


B1L100S, U12-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	56.61
Std Dev	7.421

Entry

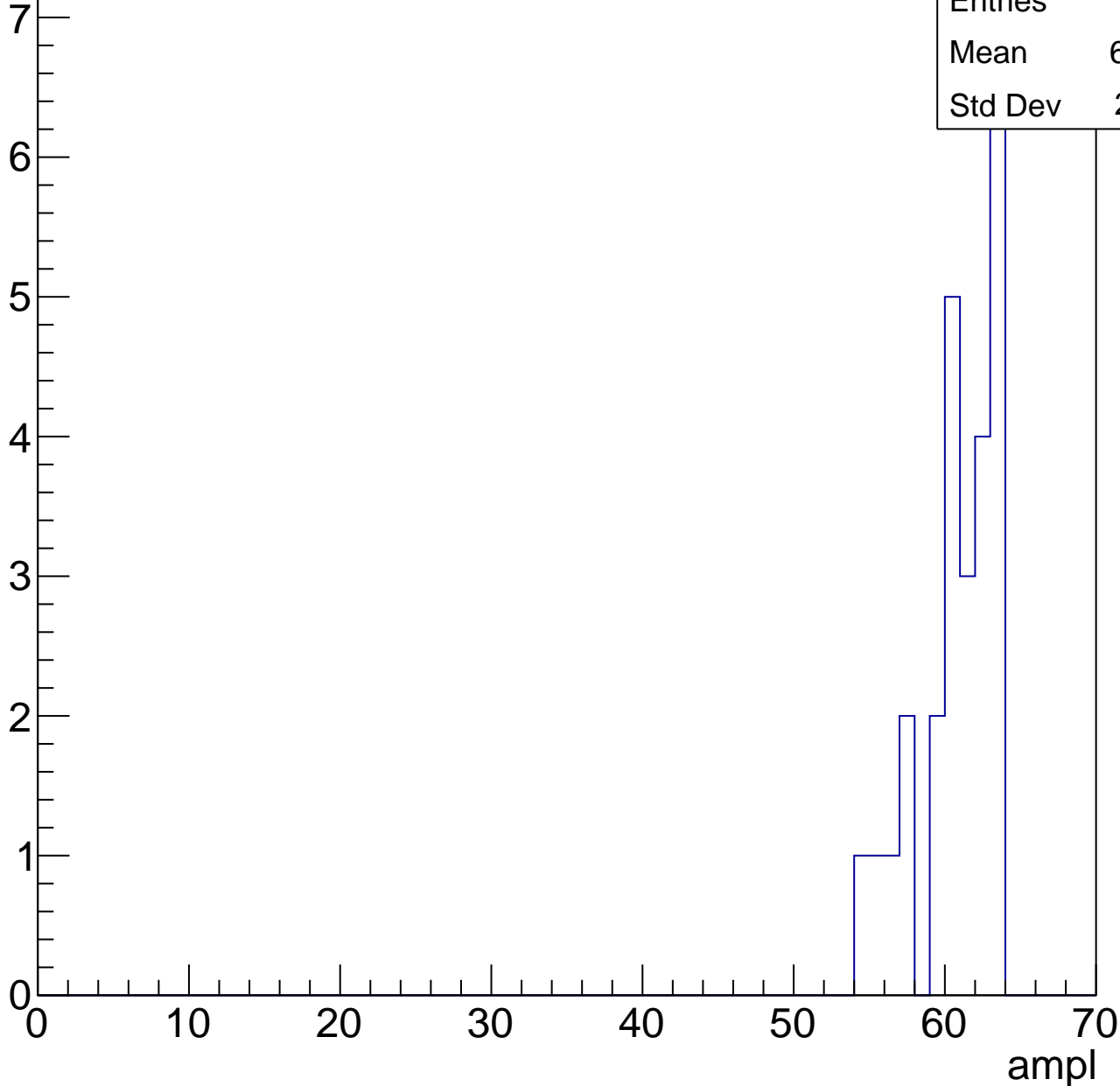


B1L100S, U12-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	60.35
Std Dev	2.601



B1L100S, U12-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch92, adc0

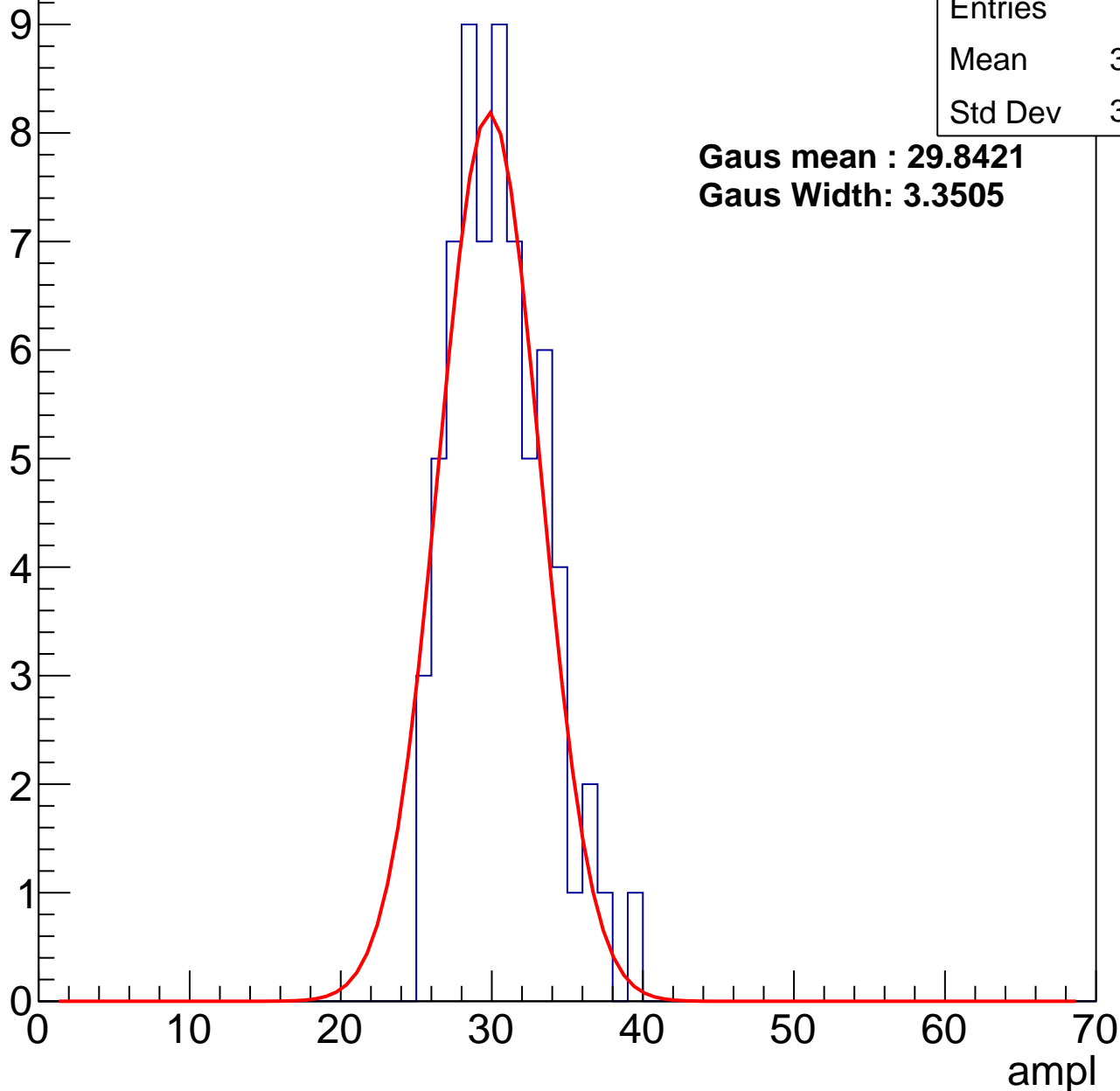
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	30.04
Std Dev	3.073

Gaus mean : 29.8421

Gaus Width: 3.3505



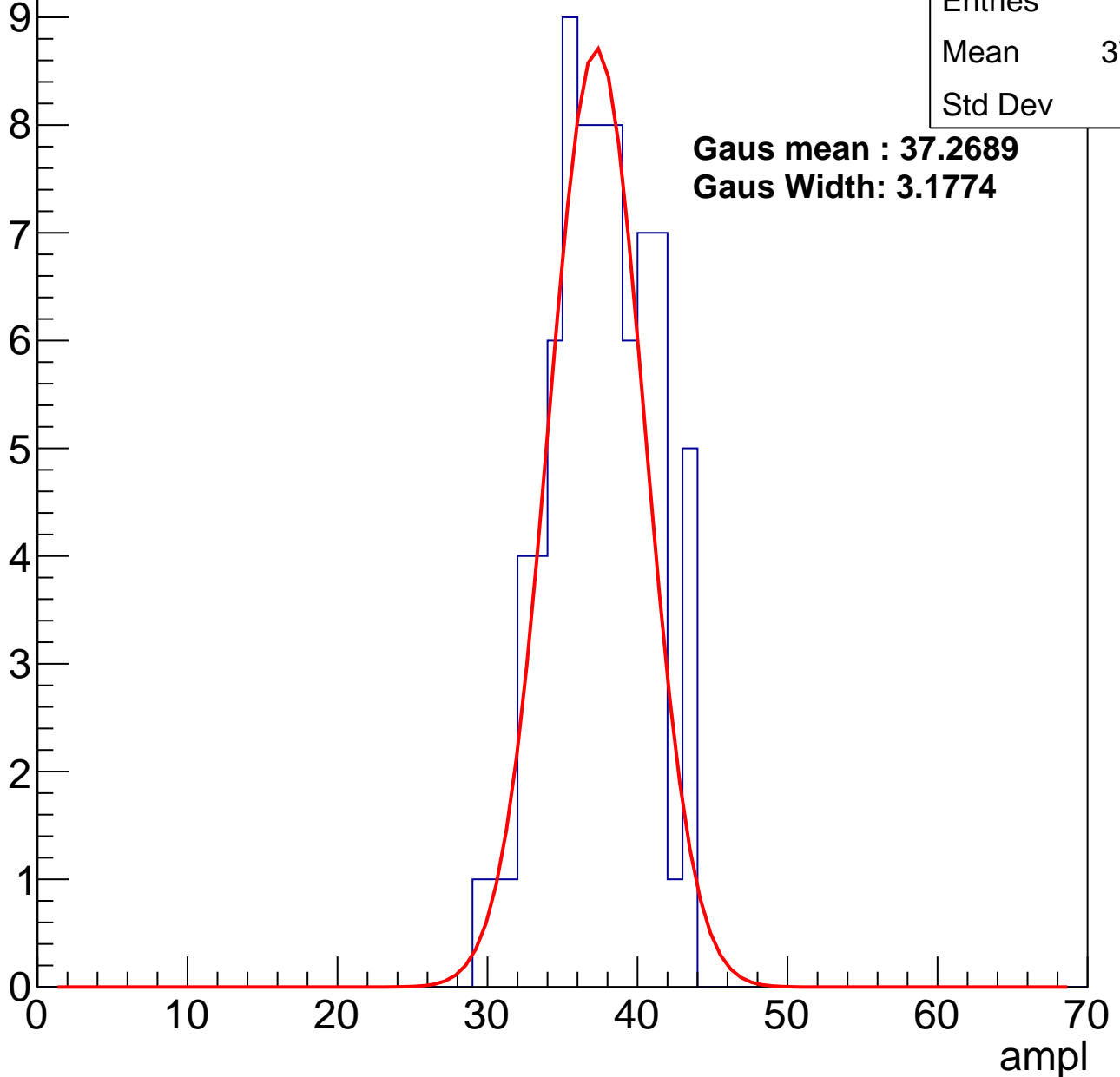
B1L100S, U12-ch92, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	37.04
Std Dev	3.29

Gaus mean : 37.2689
Gaus Width: 3.1774



B1L100S, U12-ch92, adc2

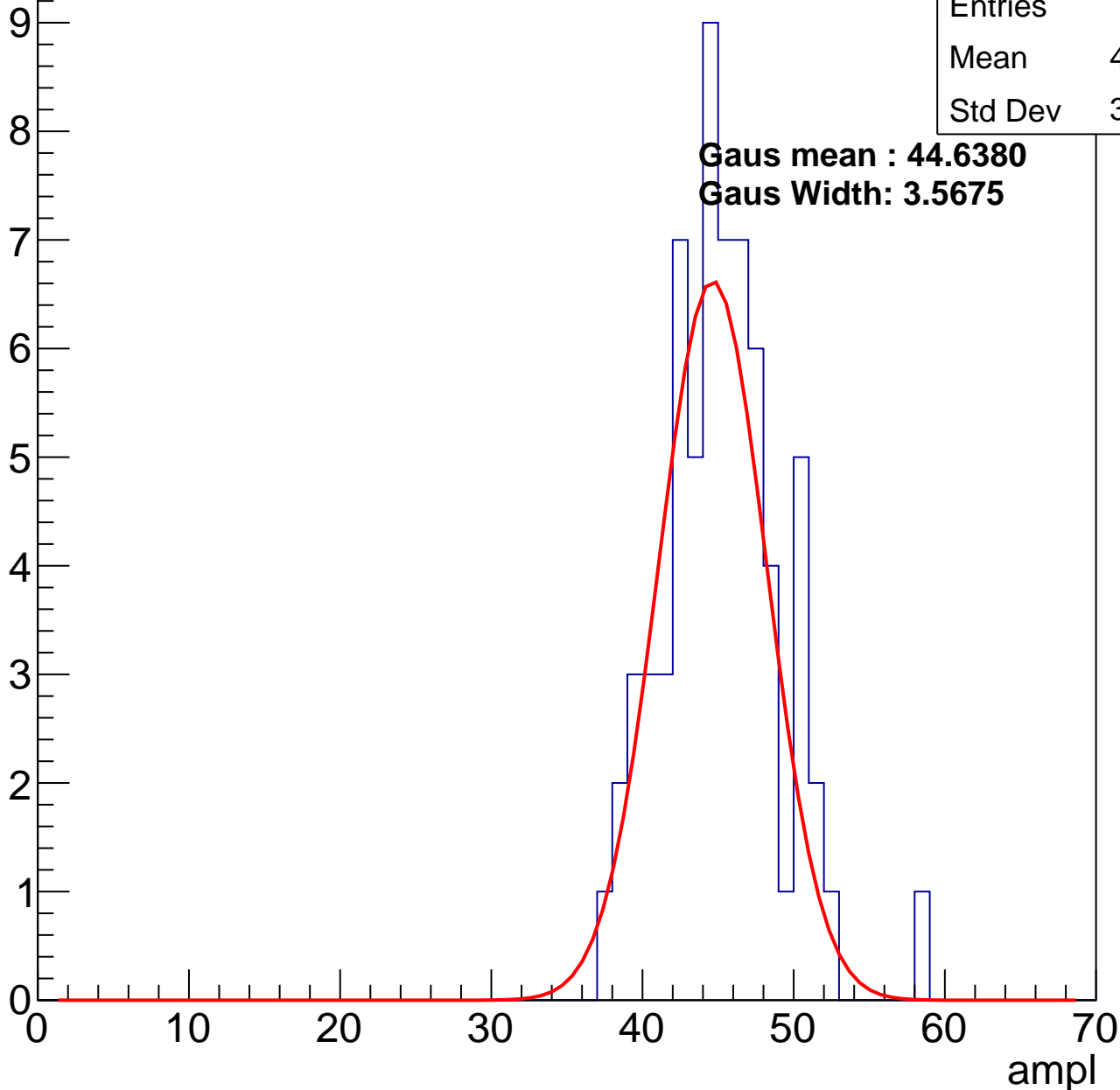
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.78
Std Dev	3.824

Gaus mean : 44.6380

Gaus Width: 3.5675

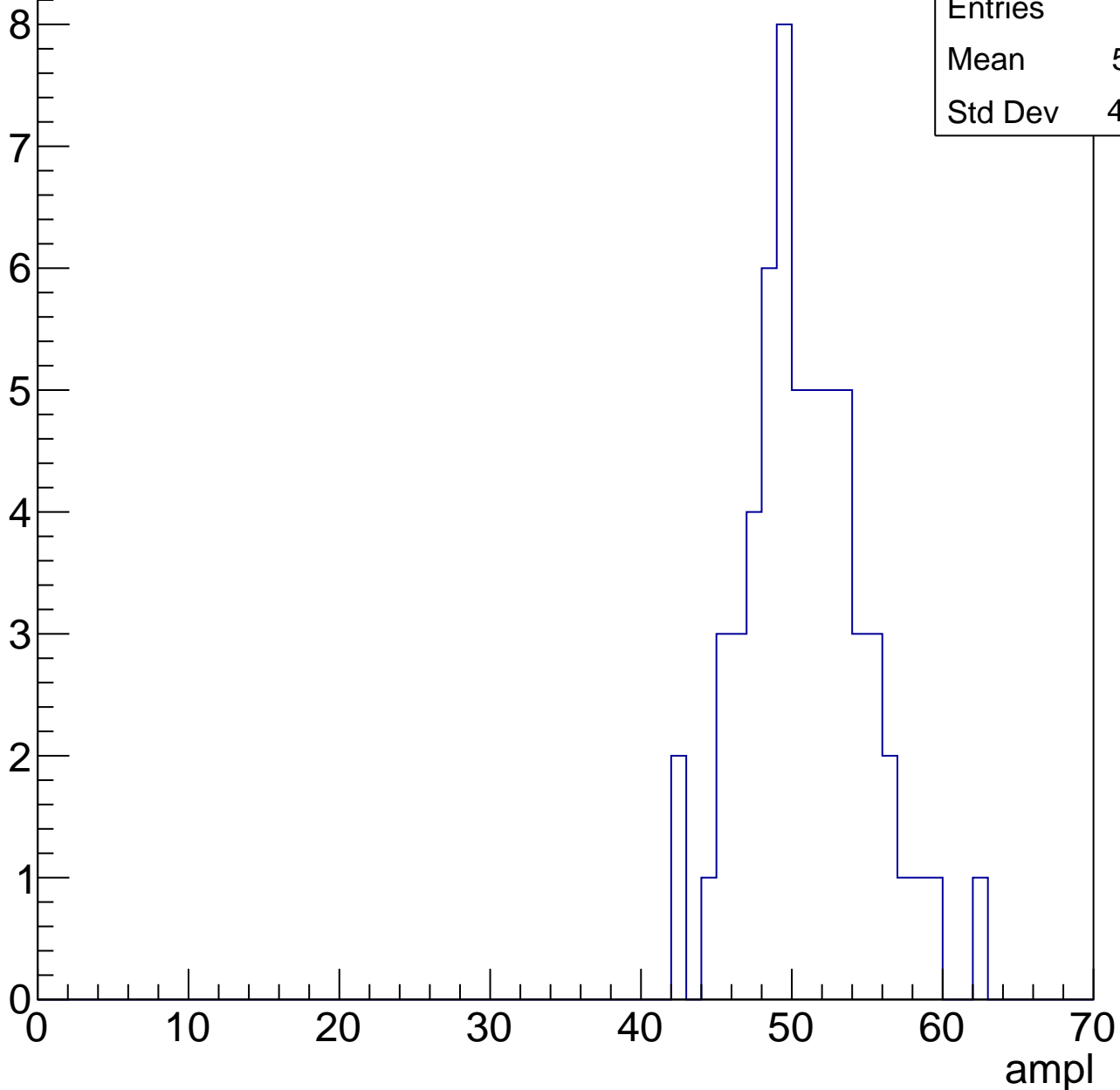


B1L100S, U12-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

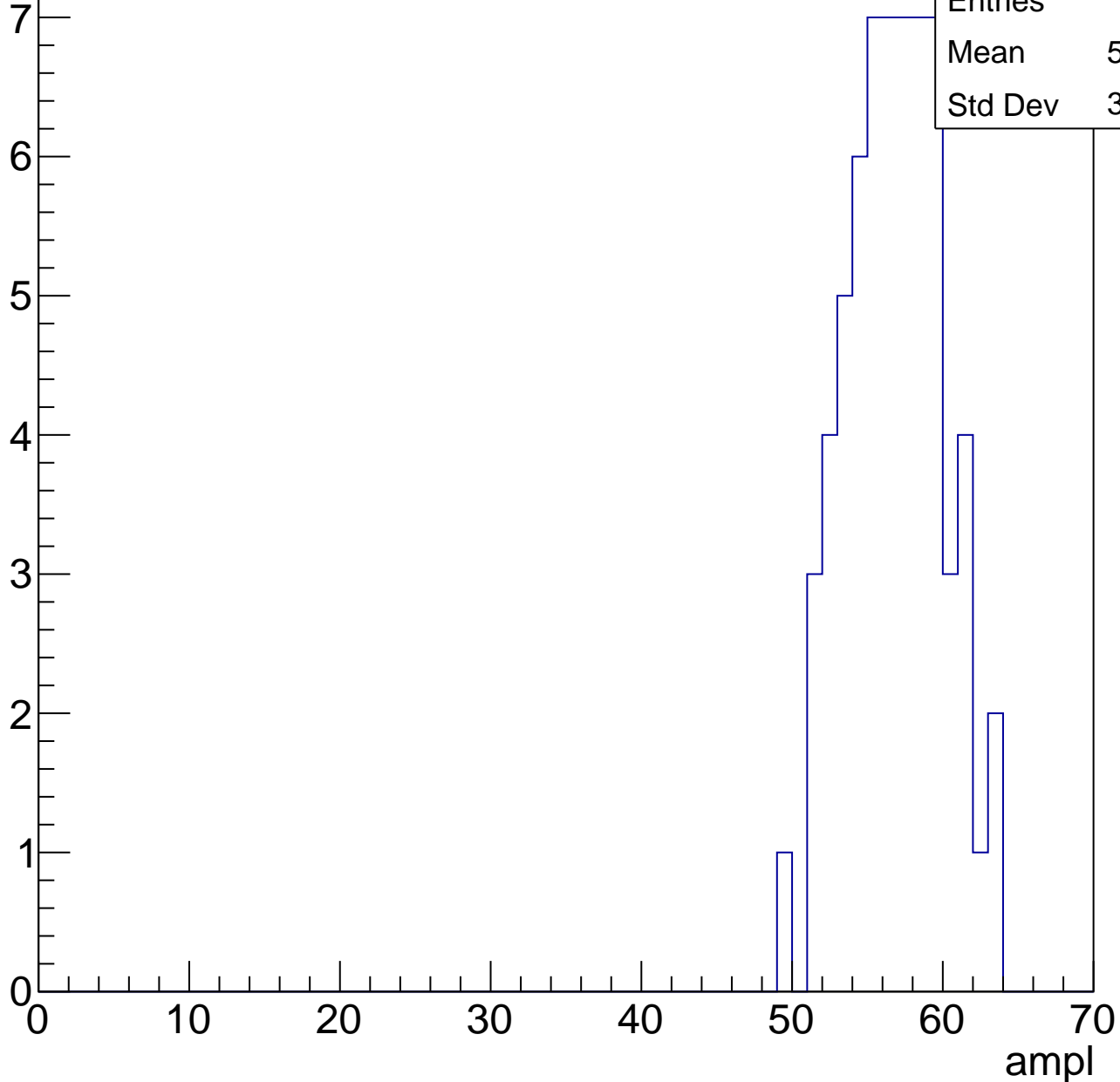
Entries	59
Mean	50.41
Std Dev	4.009



B1L100S, U12-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2

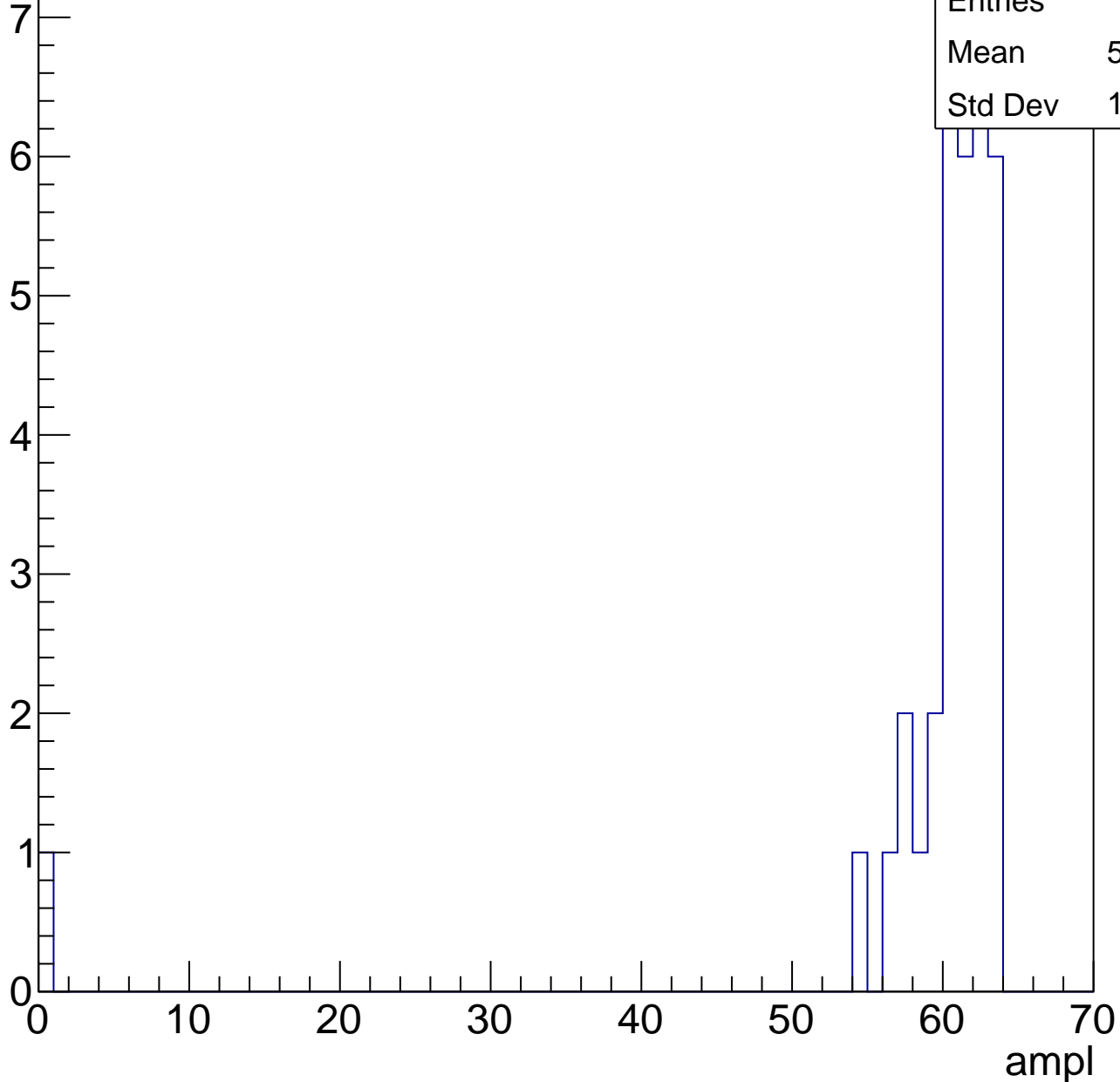
Entry



B1L100S, U12-ch92, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

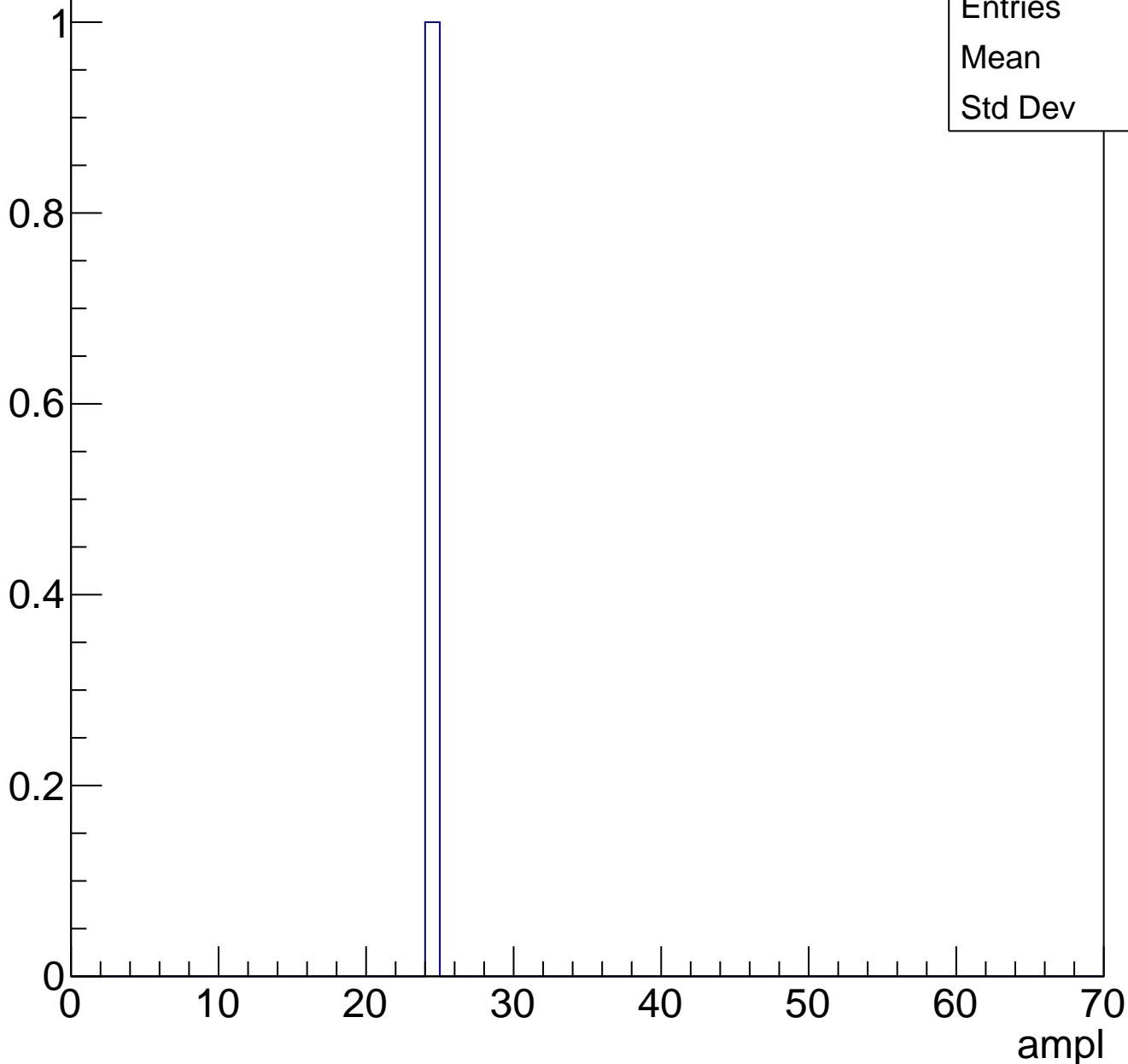
Entry



B1L100S, U12-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch93, adc0

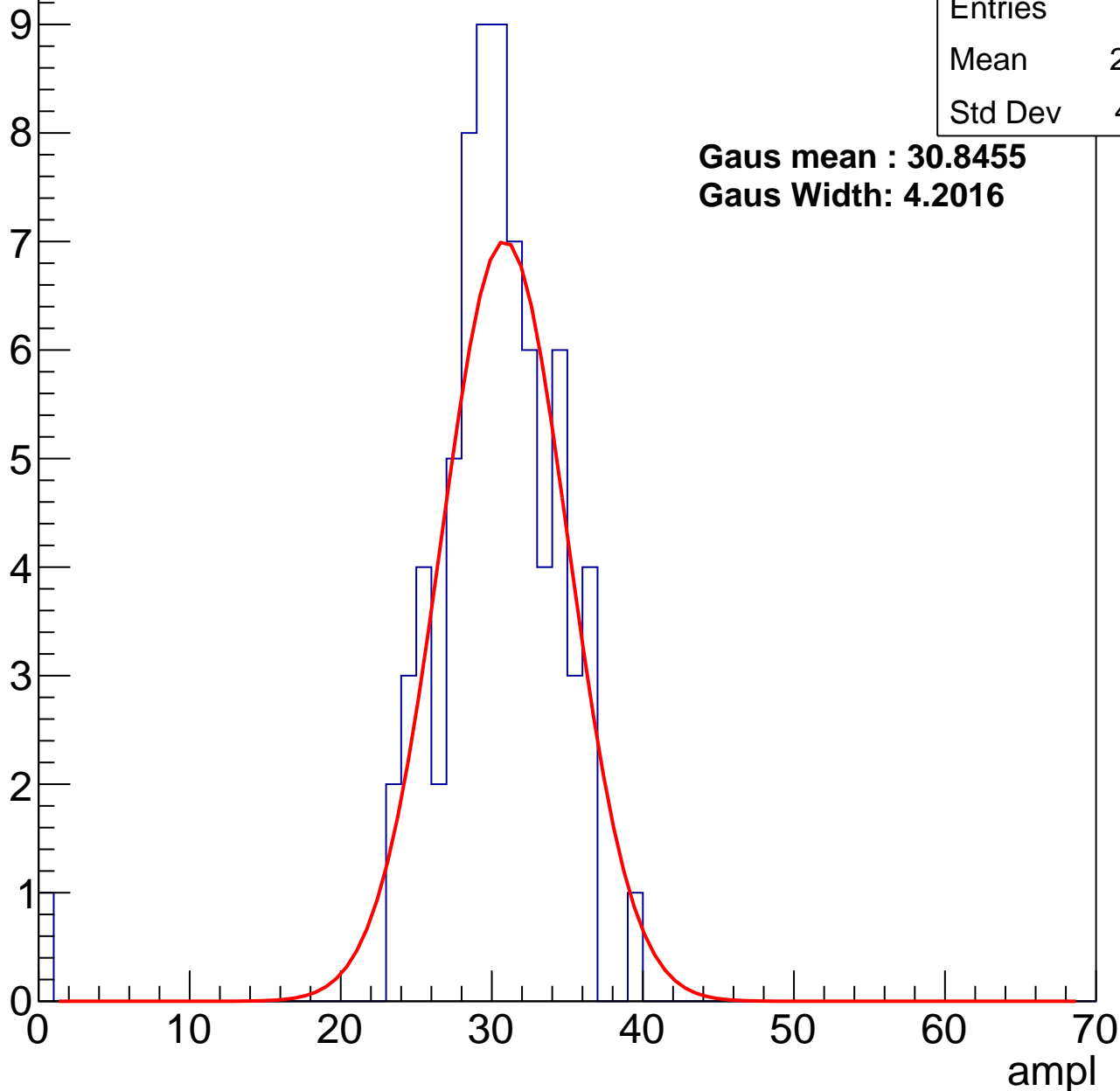
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	29.64
Std Dev	4.901

Gaus mean : 30.8455

Gaus Width: 4.2016



B1L100S, U12-ch93, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	37.12
Std Dev	3.395

Gaus mean : 37.2698

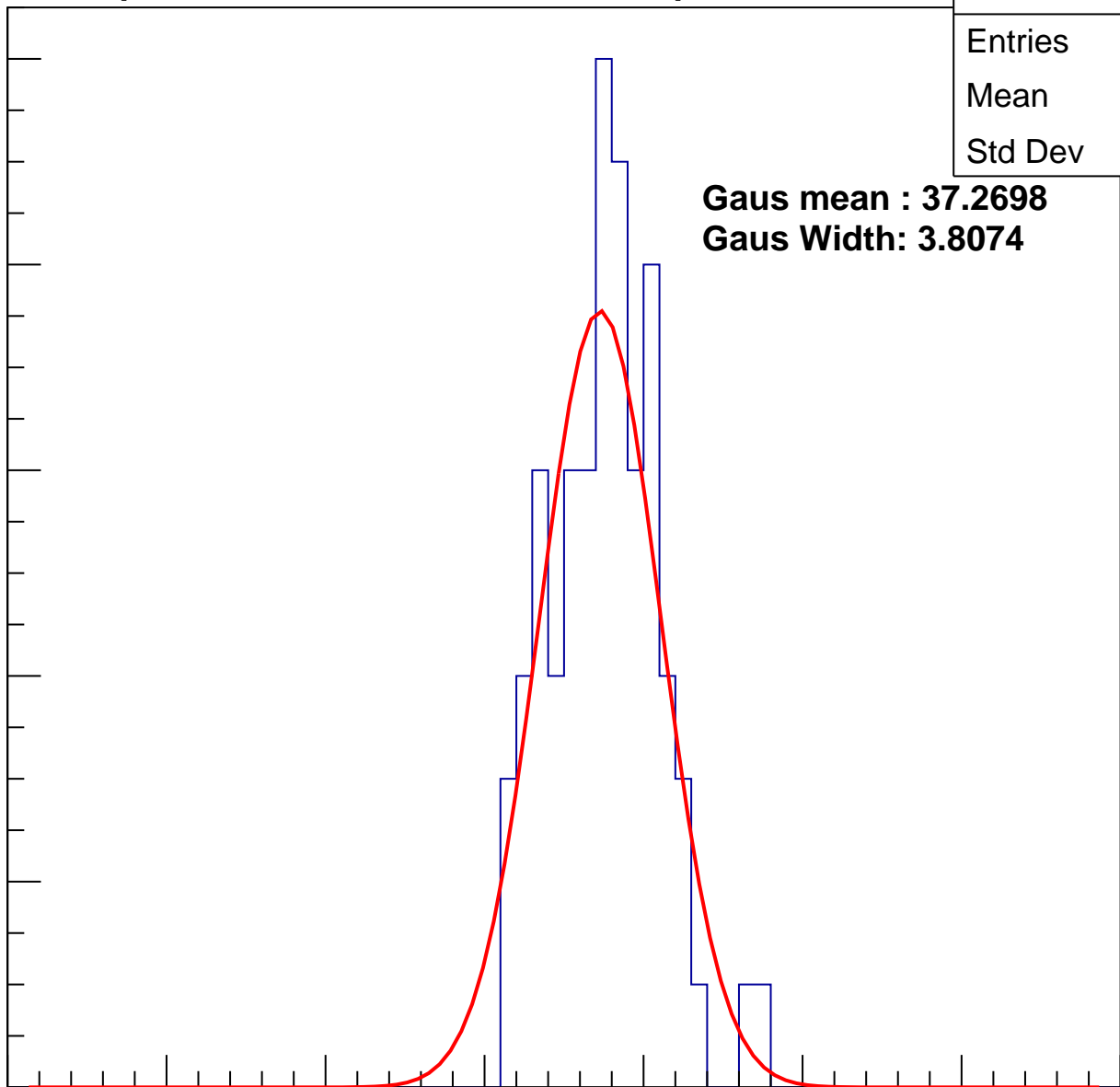
Gaus Width: 3.8074

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch93, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	43.72
Std Dev	3.142

Gaus mean : 44.5404

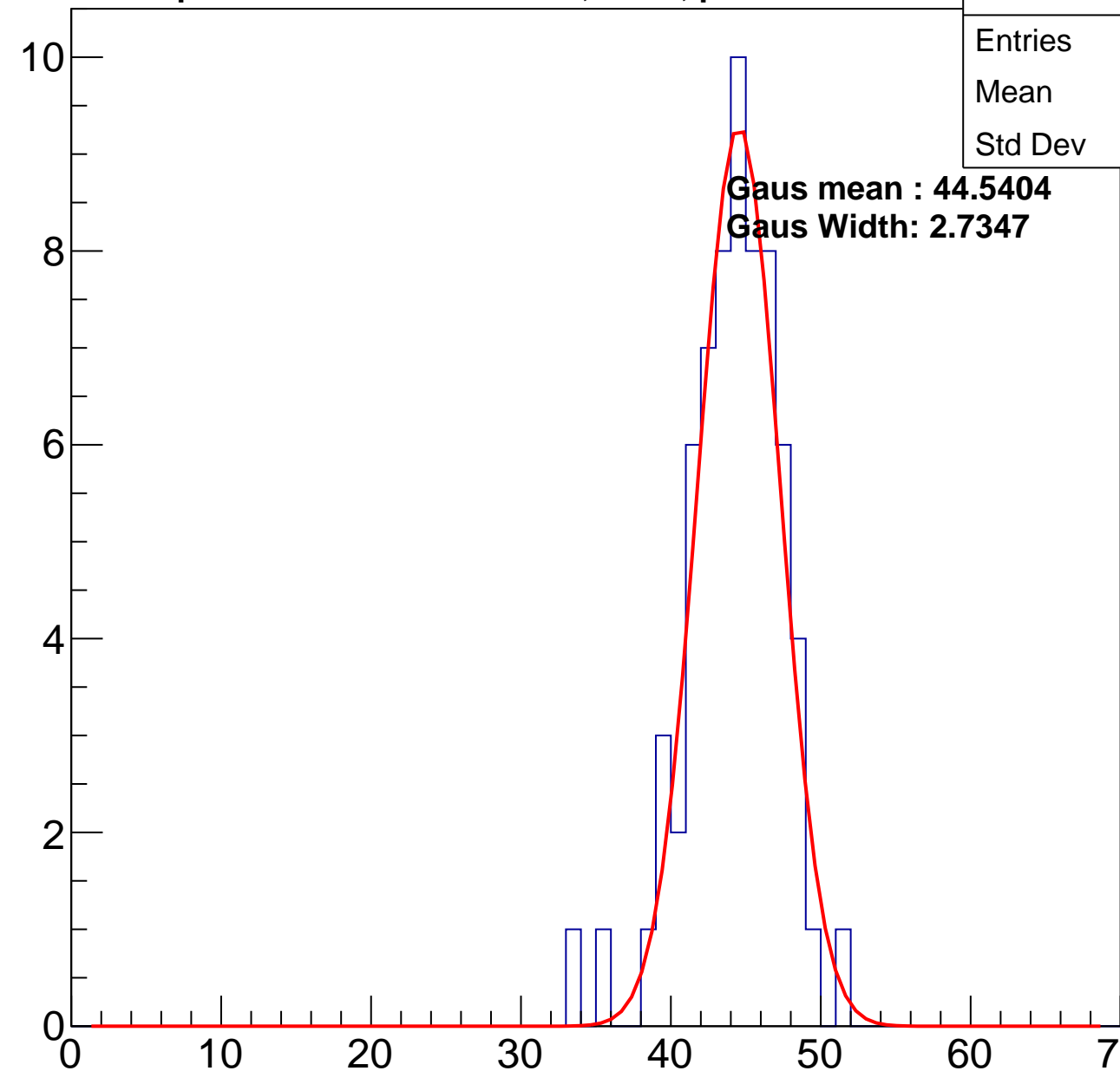
Gaus Width: 2.7347

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

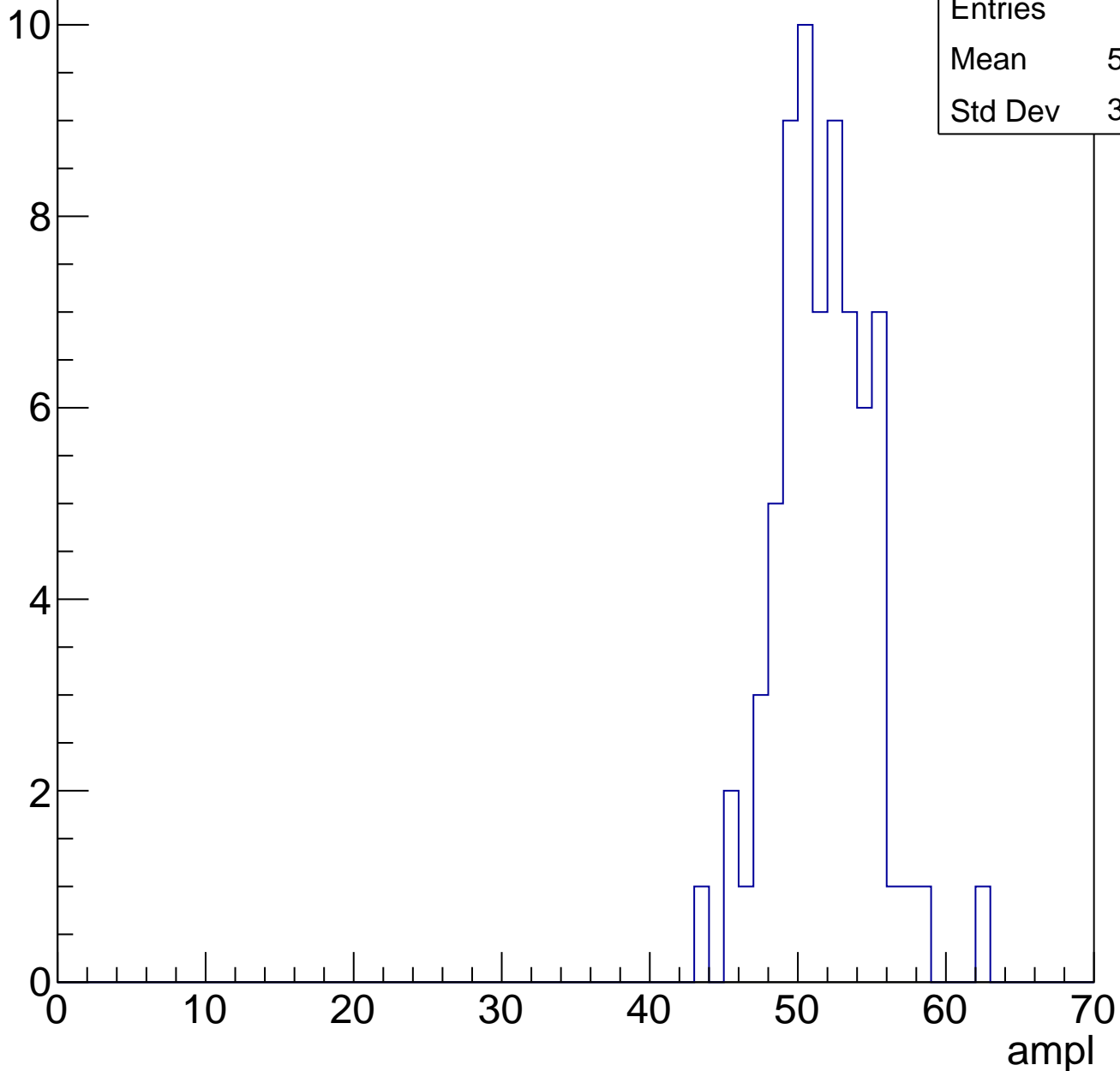


B1L100S, U12-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	51.25
Std Dev	3.205

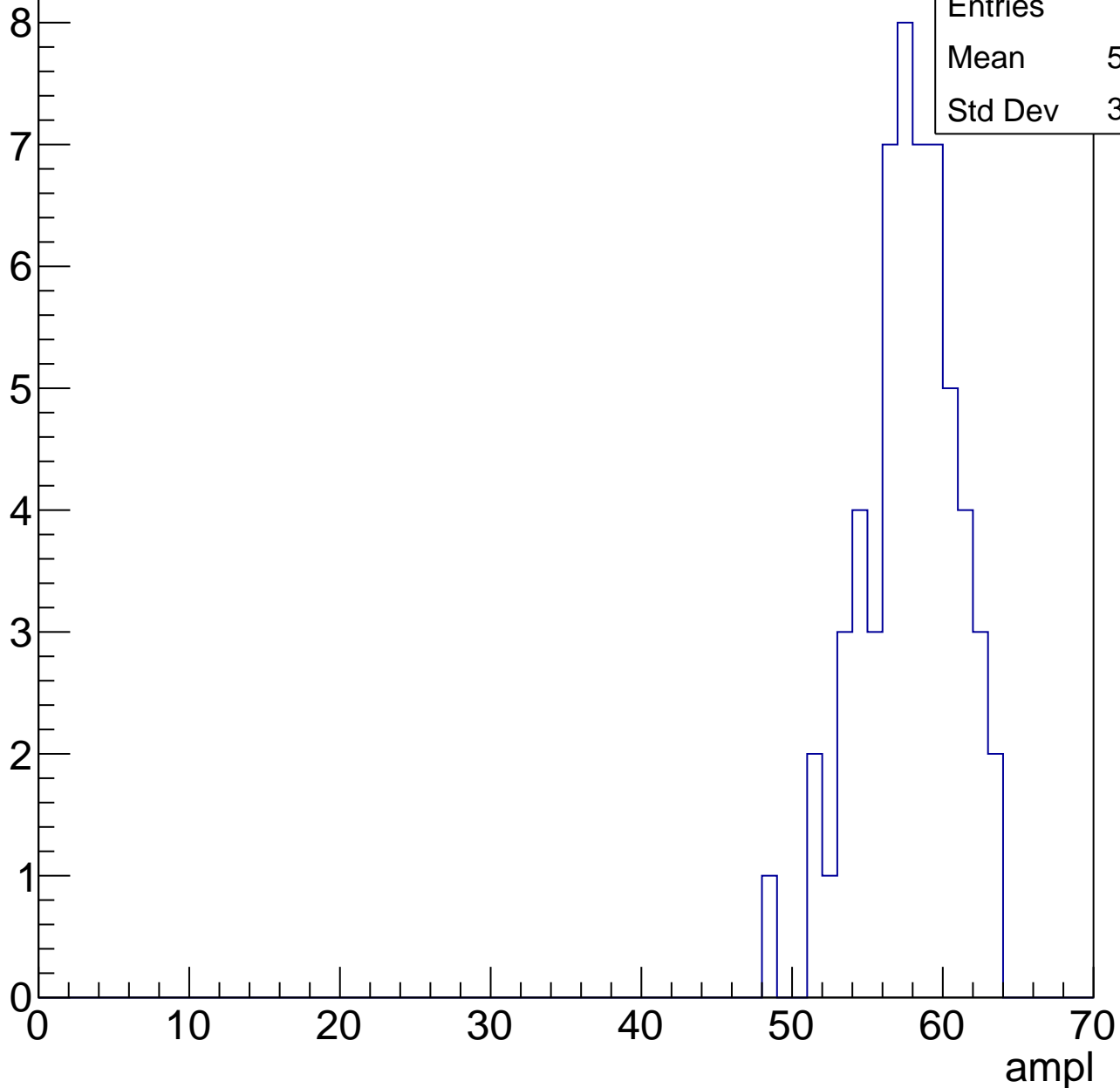
Entry



B1L100S, U12-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

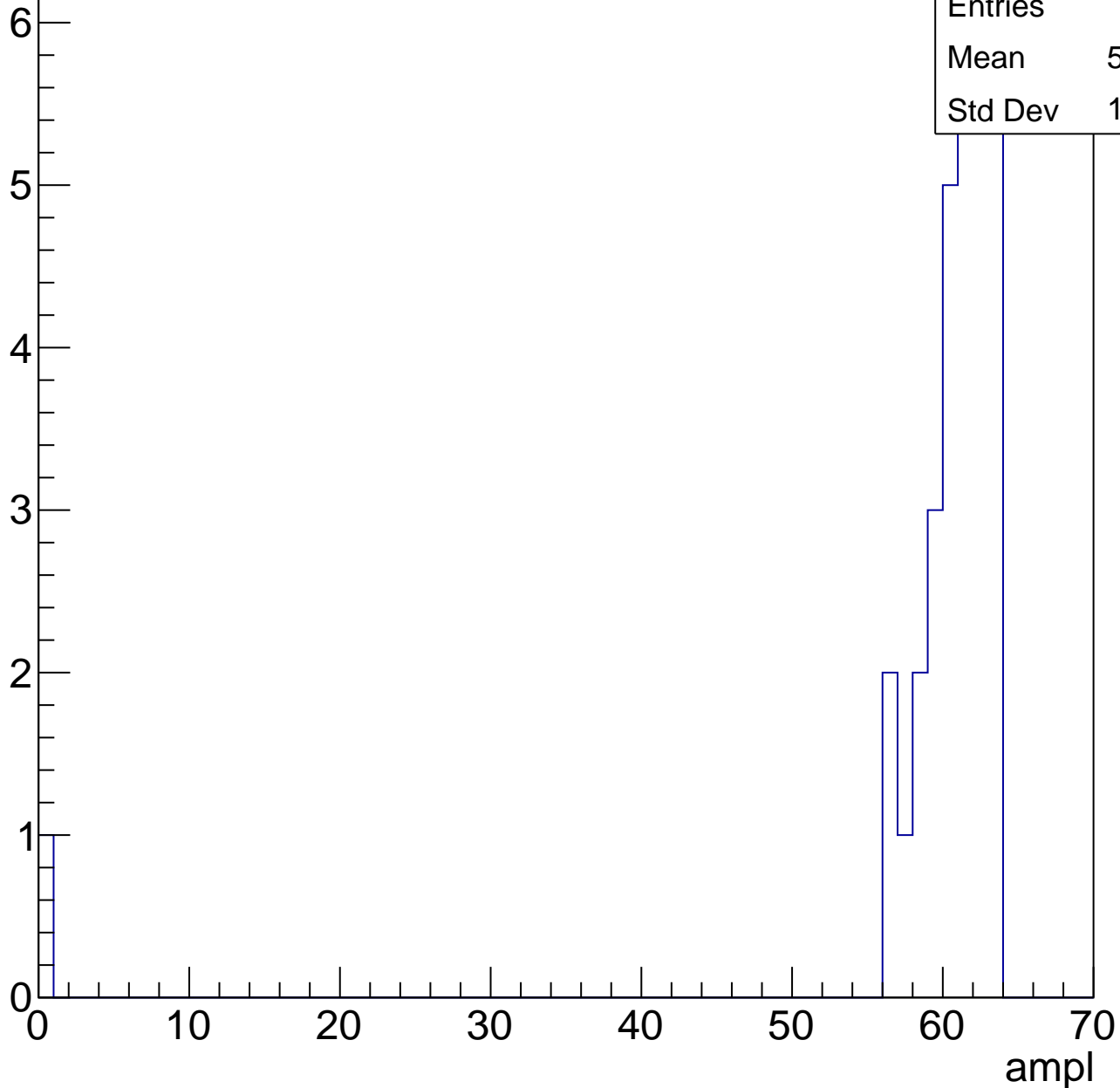


Entries	57
Mean	57.28
Std Dev	3.155

B1L100S, U12-ch93, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch94, adc0

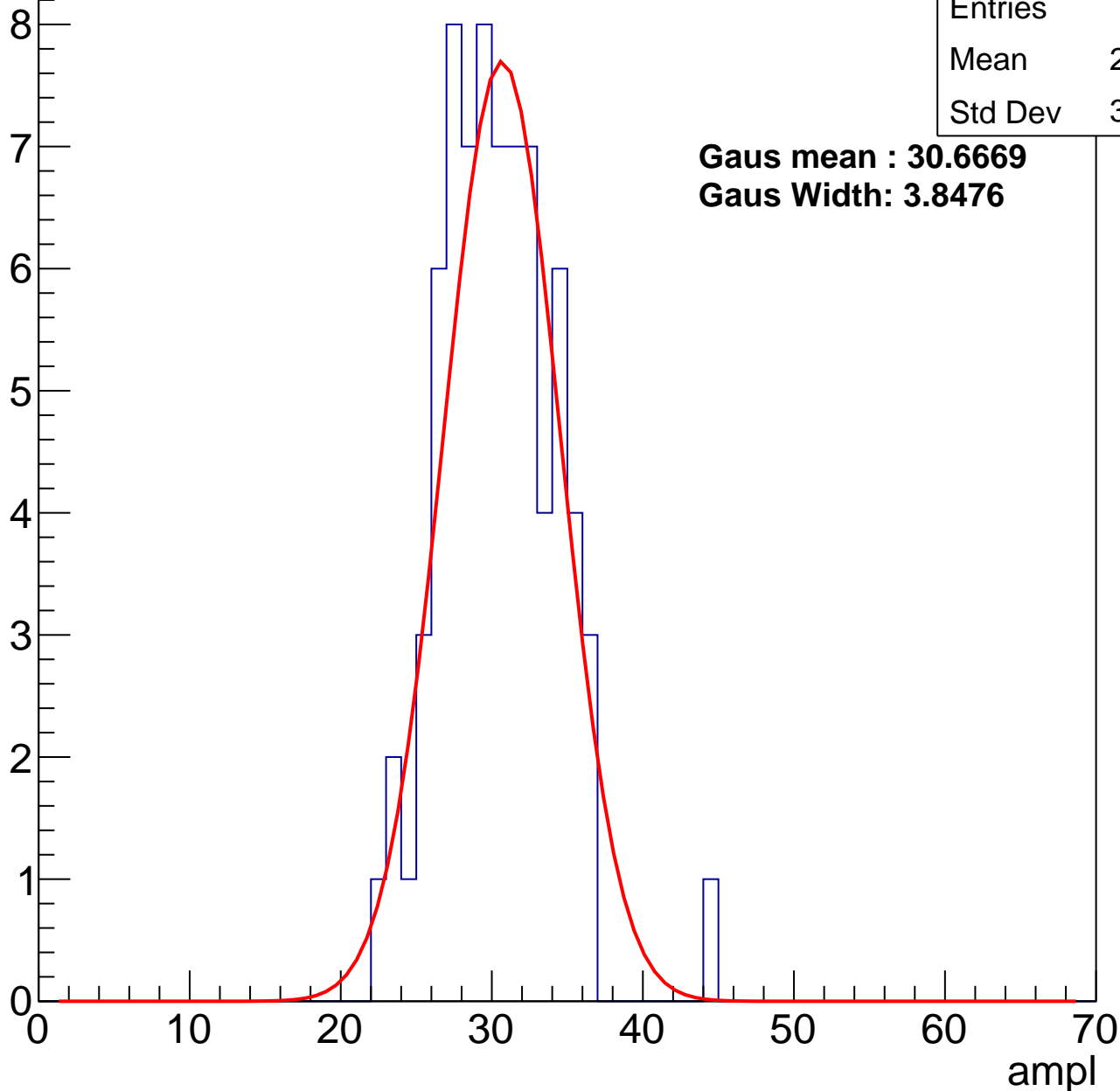
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.95
Std Dev	3.738

Gaus mean : 30.6669

Gaus Width: 3.8476



B1L100S, U12-ch94, adc1

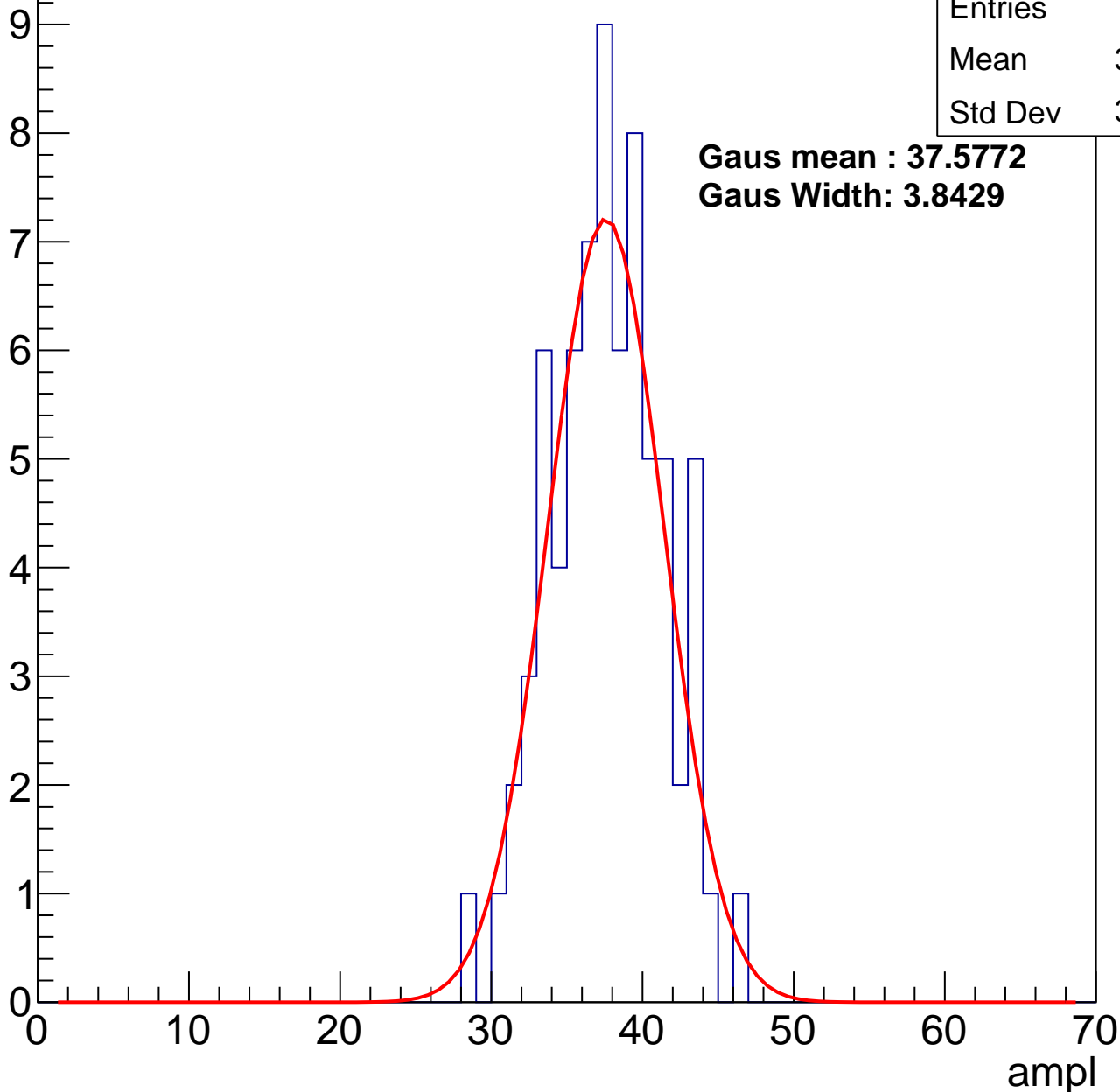
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.21
Std Dev	3.651

Gaus mean : 37.5772

Gaus Width: 3.8429



B1L100S, U12-ch94, adc2

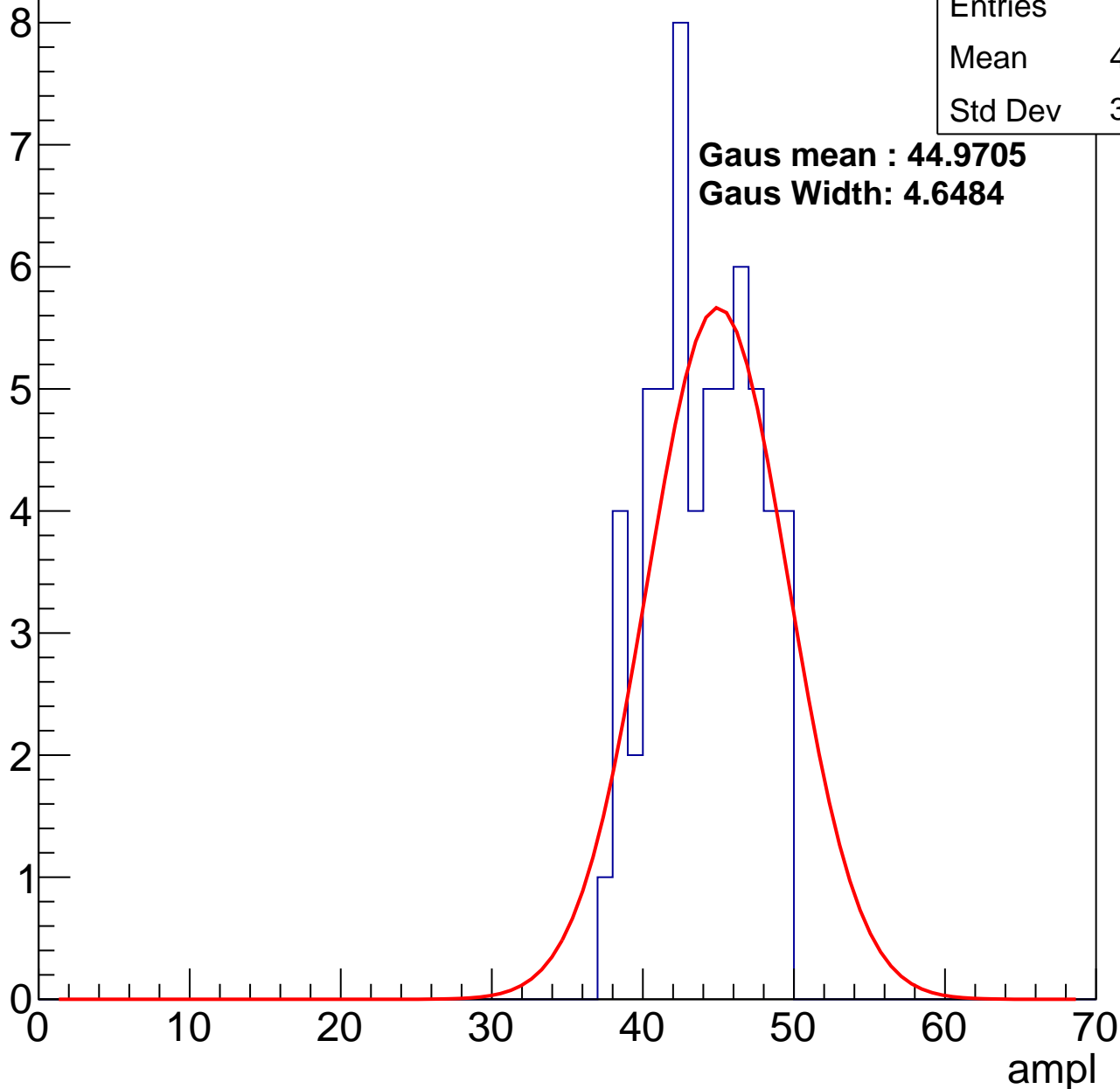
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	43.52
Std Dev	3.292

Gaus mean : 44.9705

Gaus Width: 4.6484



B1L100S, U12-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	50.63
Std Dev	3.752

Entry

10

8

6

4

2

0

0

10

20

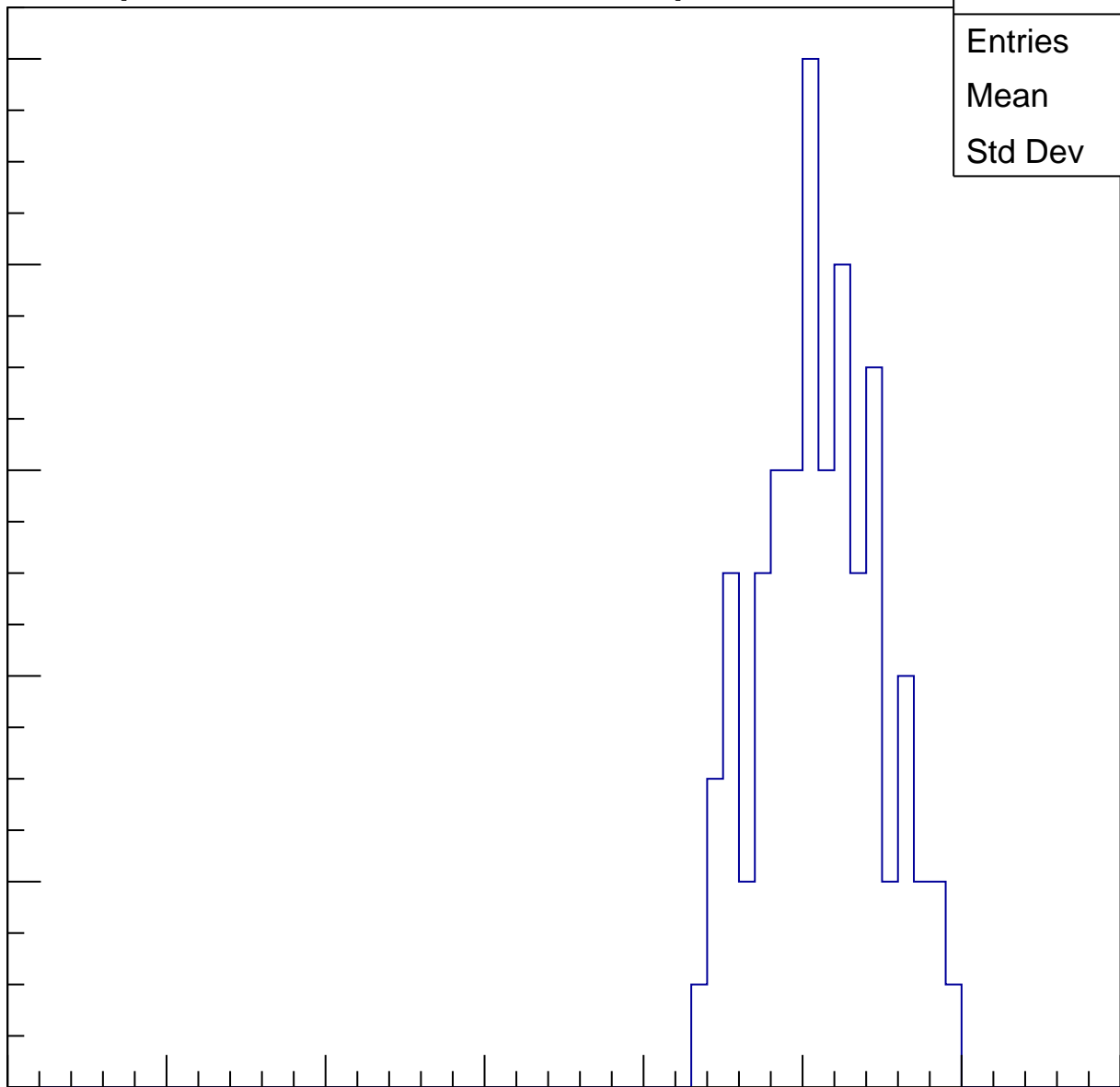
30

40

50

60

ampl

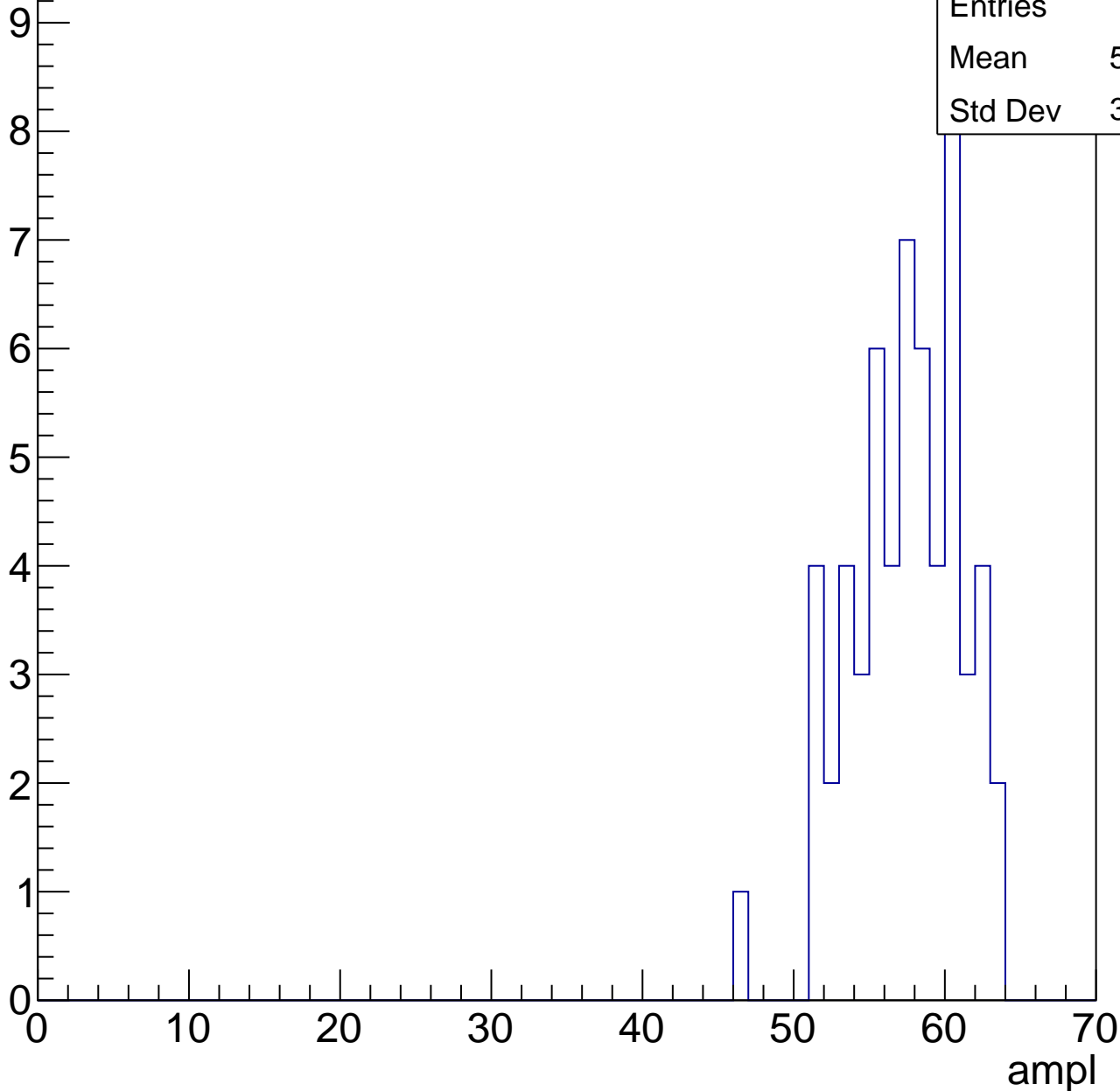


B1L100S, U12-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.98
Std Dev	3.582

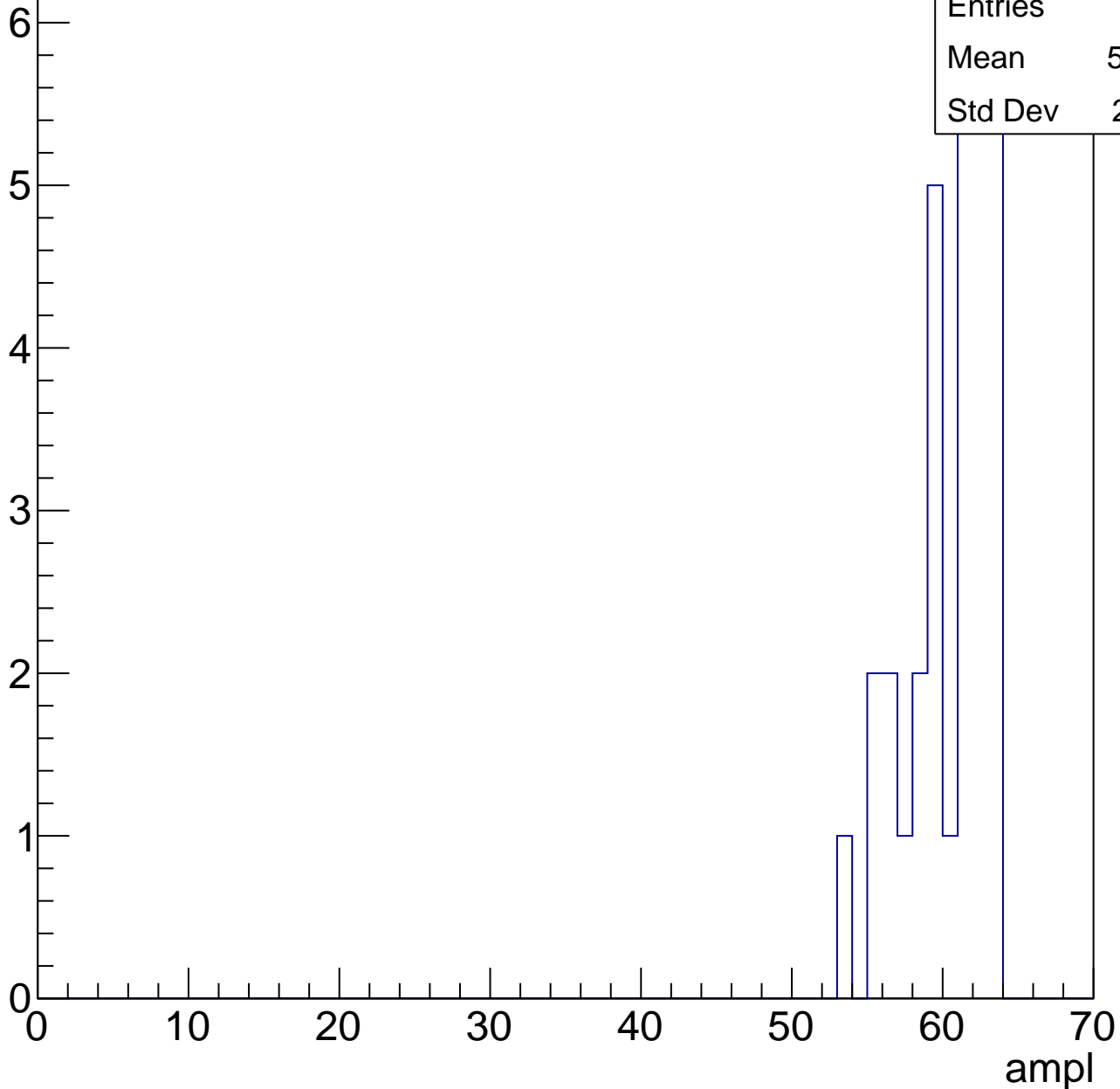


B1L100S, U12-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

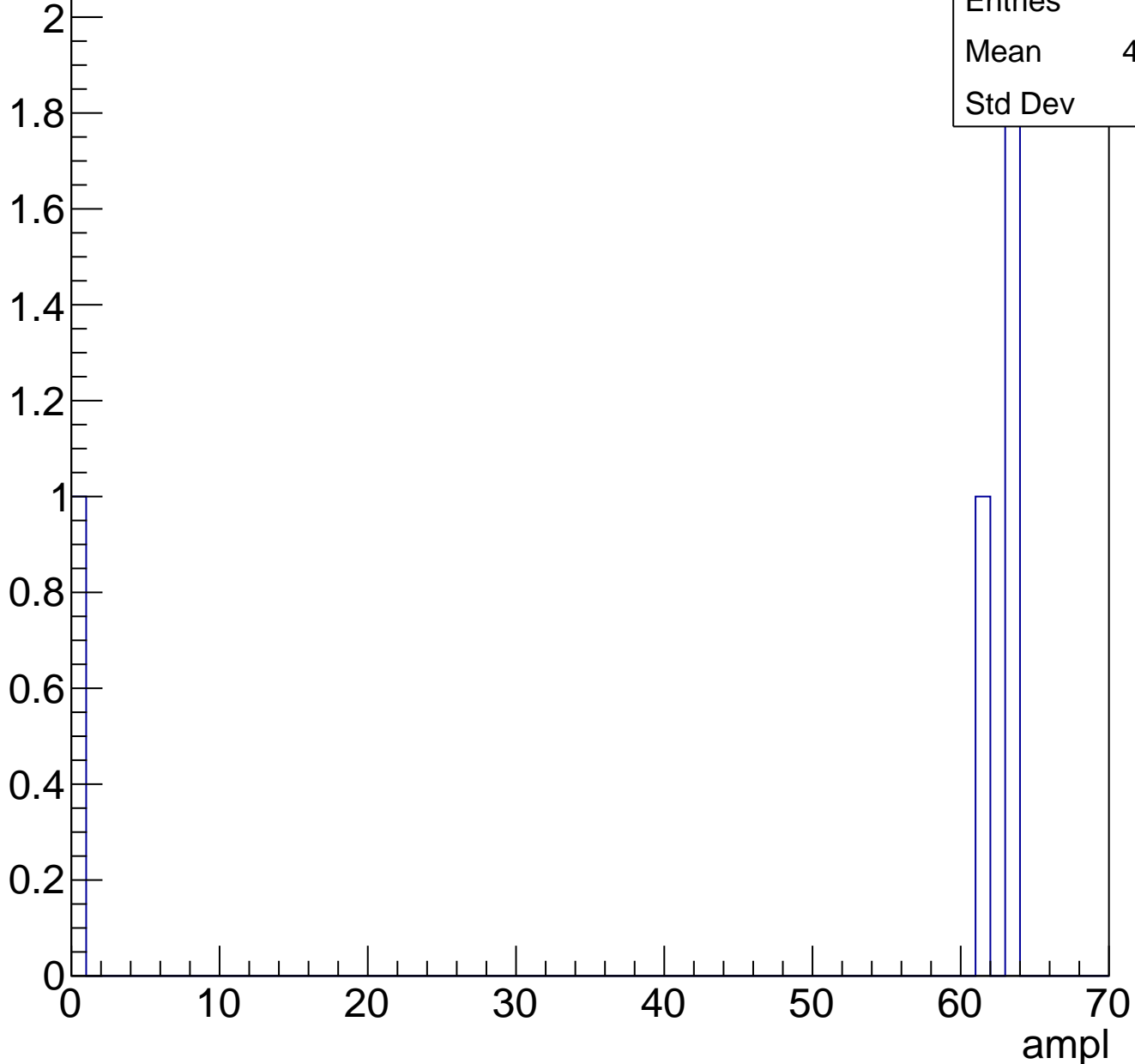
Entries	32
Mean	59.97
Std Dev	2.721



B1L100S, U12-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

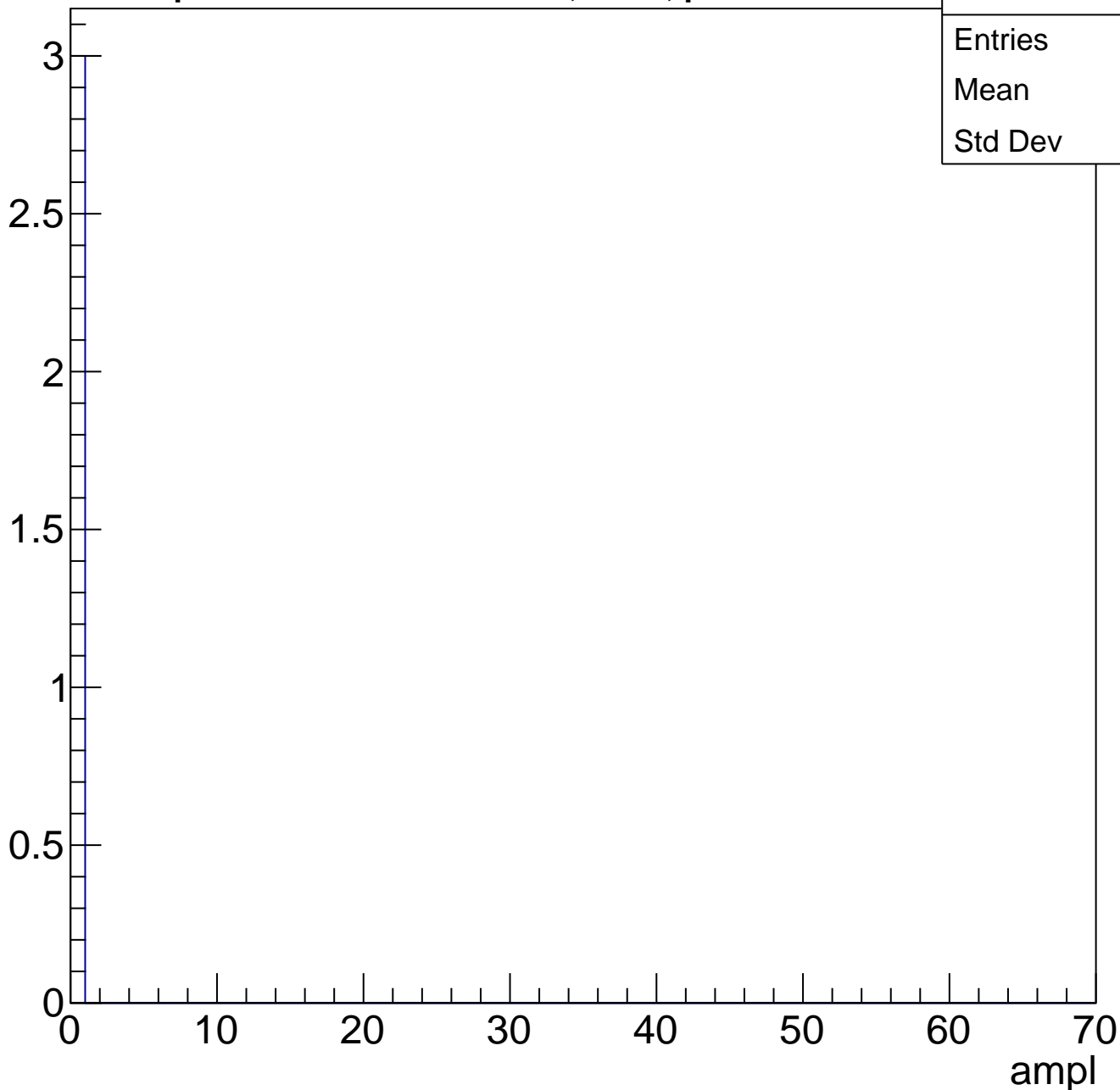


Entries	4
Mean	46.75
Std Dev	27

B1L100S, U12-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L100S, U12-ch95, adc0

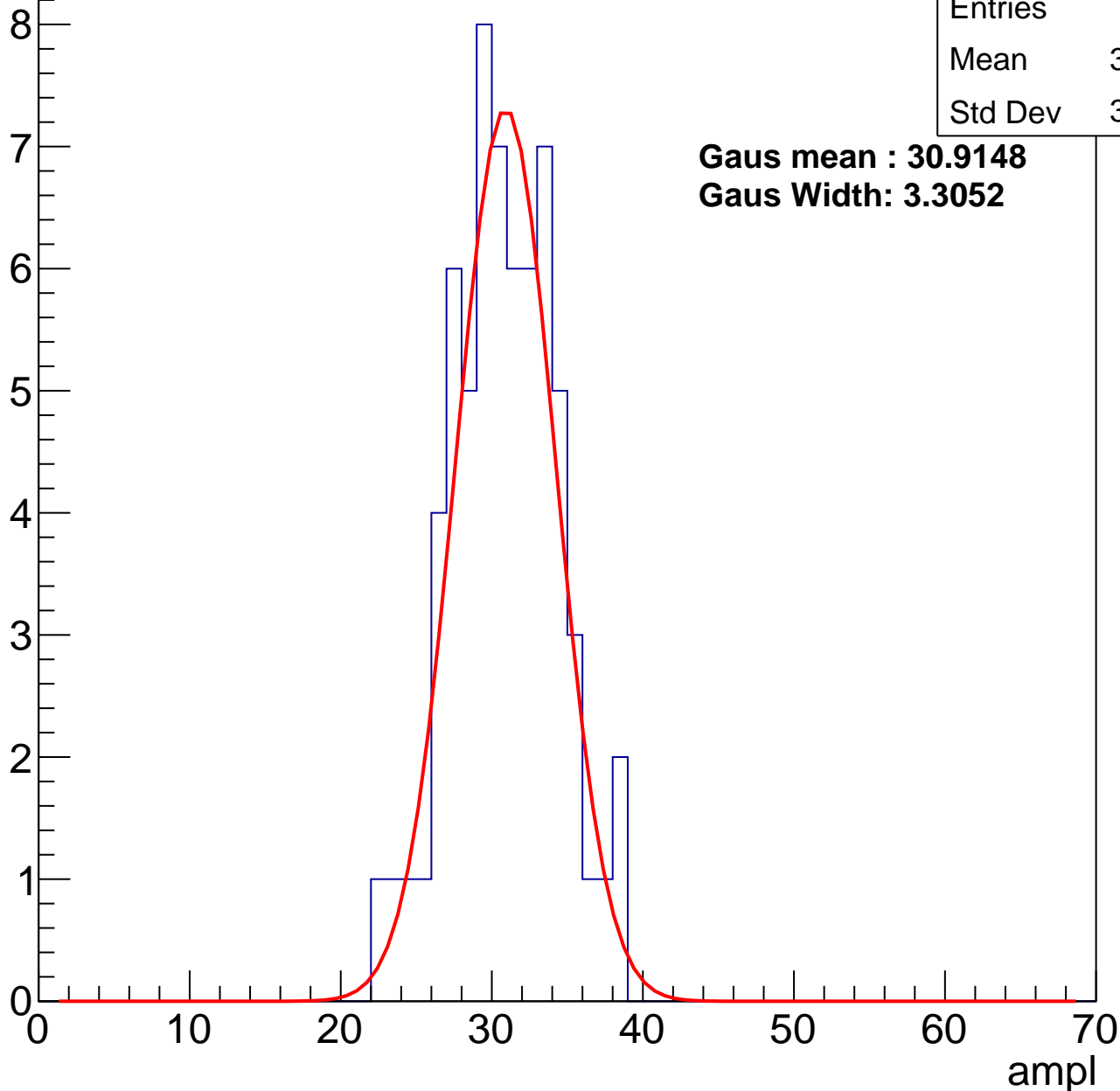
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	30.38
Std Dev	3.445

Gaus mean : 30.9148

Gaus Width: 3.3052



B1L100S, U12-ch95, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	37.63
Std Dev	3.779

Gaus mean : 37.9795

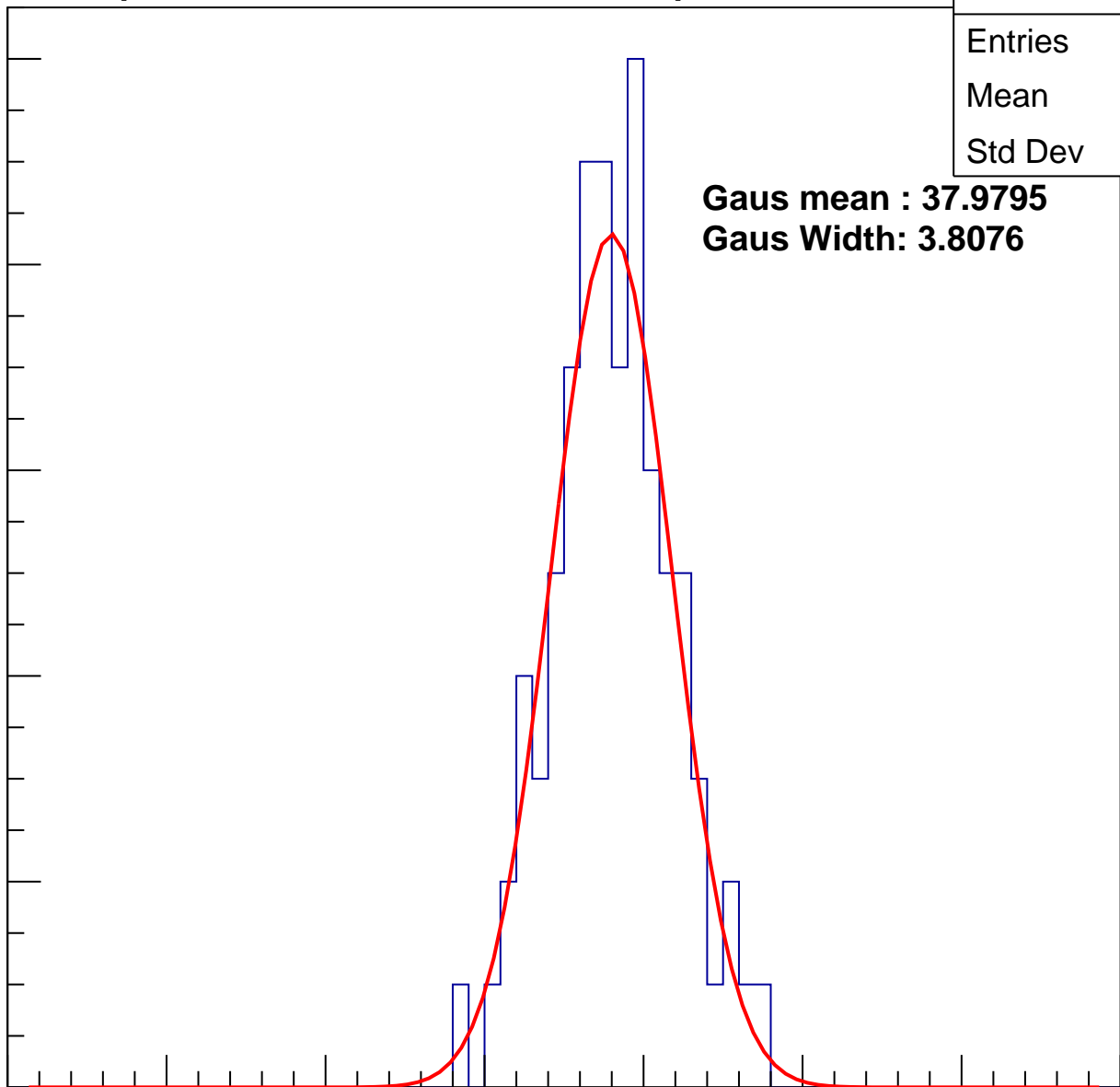
Gaus Width: 3.8076

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch95, adc2

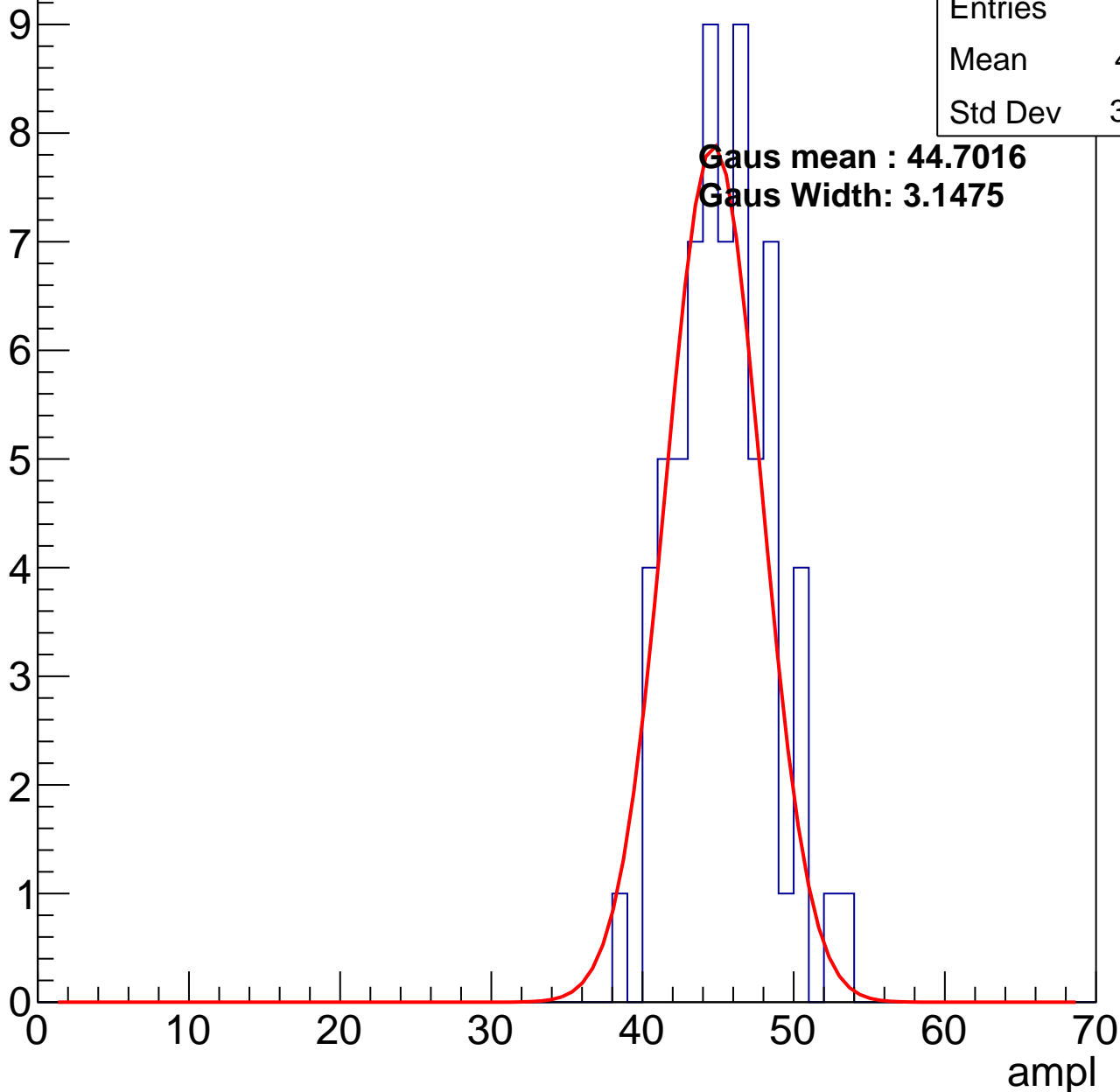
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	44.91
Std Dev	3.093

Gaus mean : 44.7016

Gaus Width: 3.1475



B1L100S, U12-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	51.75
Std Dev	3.06

Entry

10

8

6

4

2

0

0

10

20

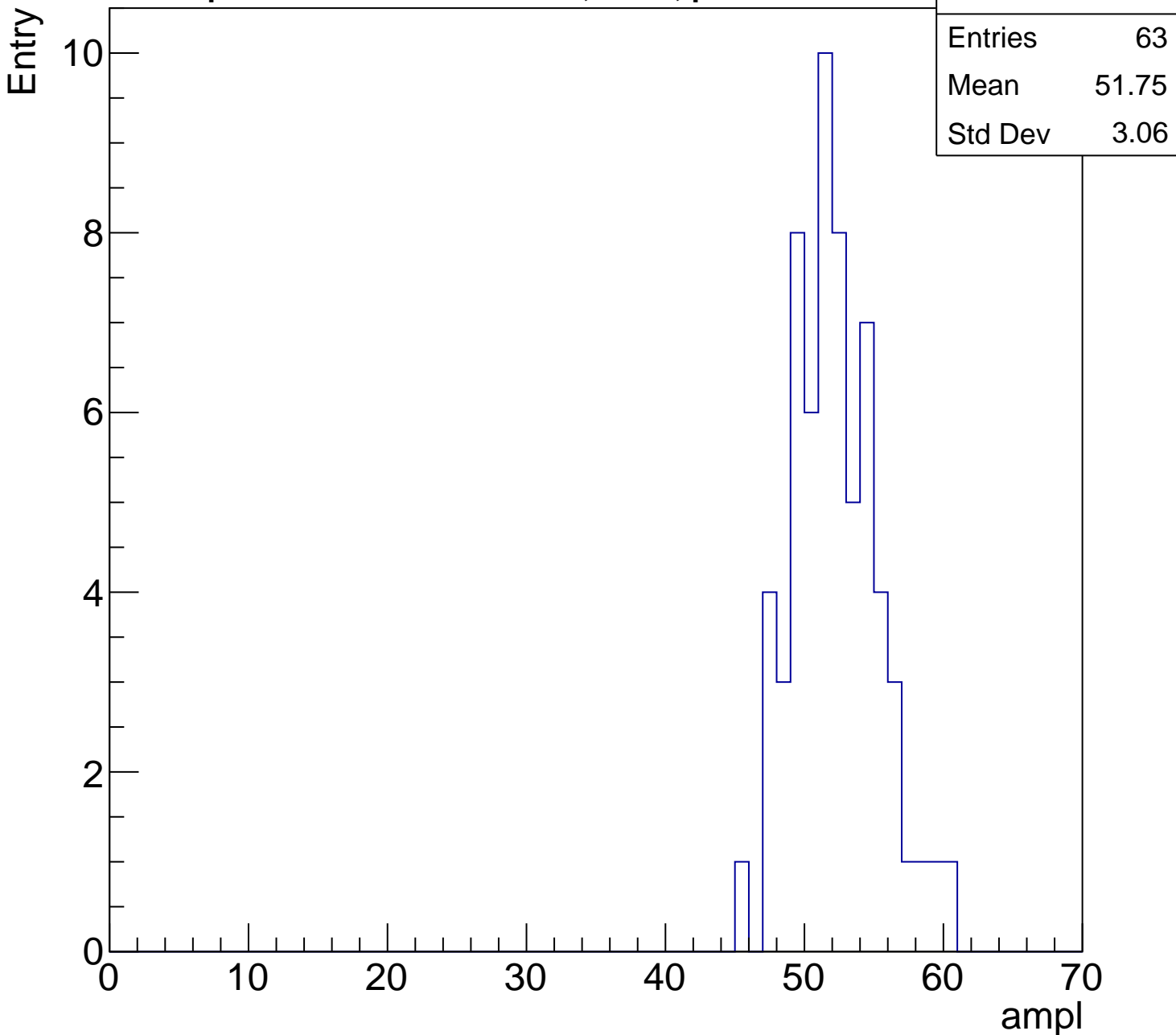
30

40

50

60

ampl

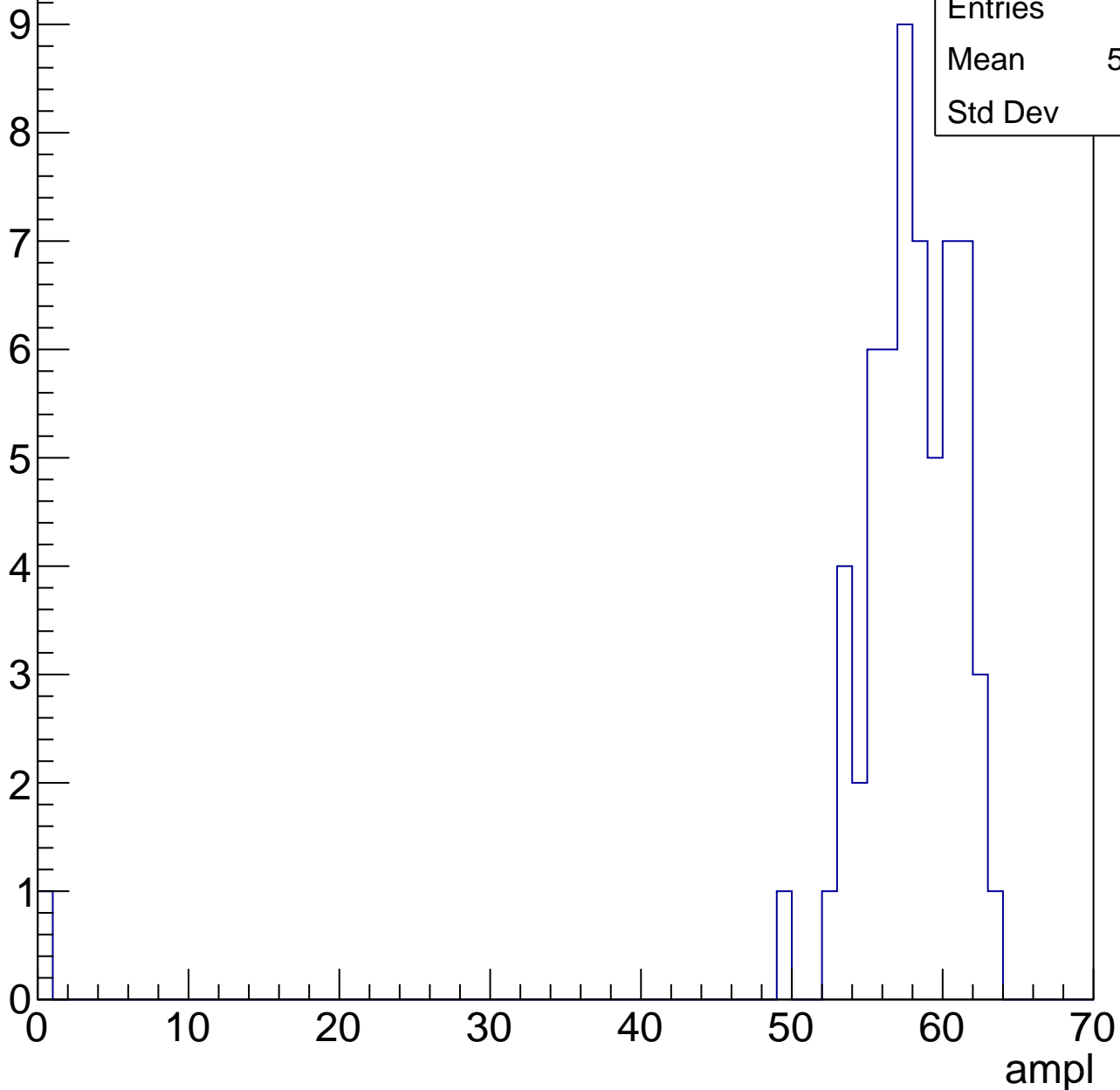


B1L100S, U12-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	56.62
Std Dev	7.91

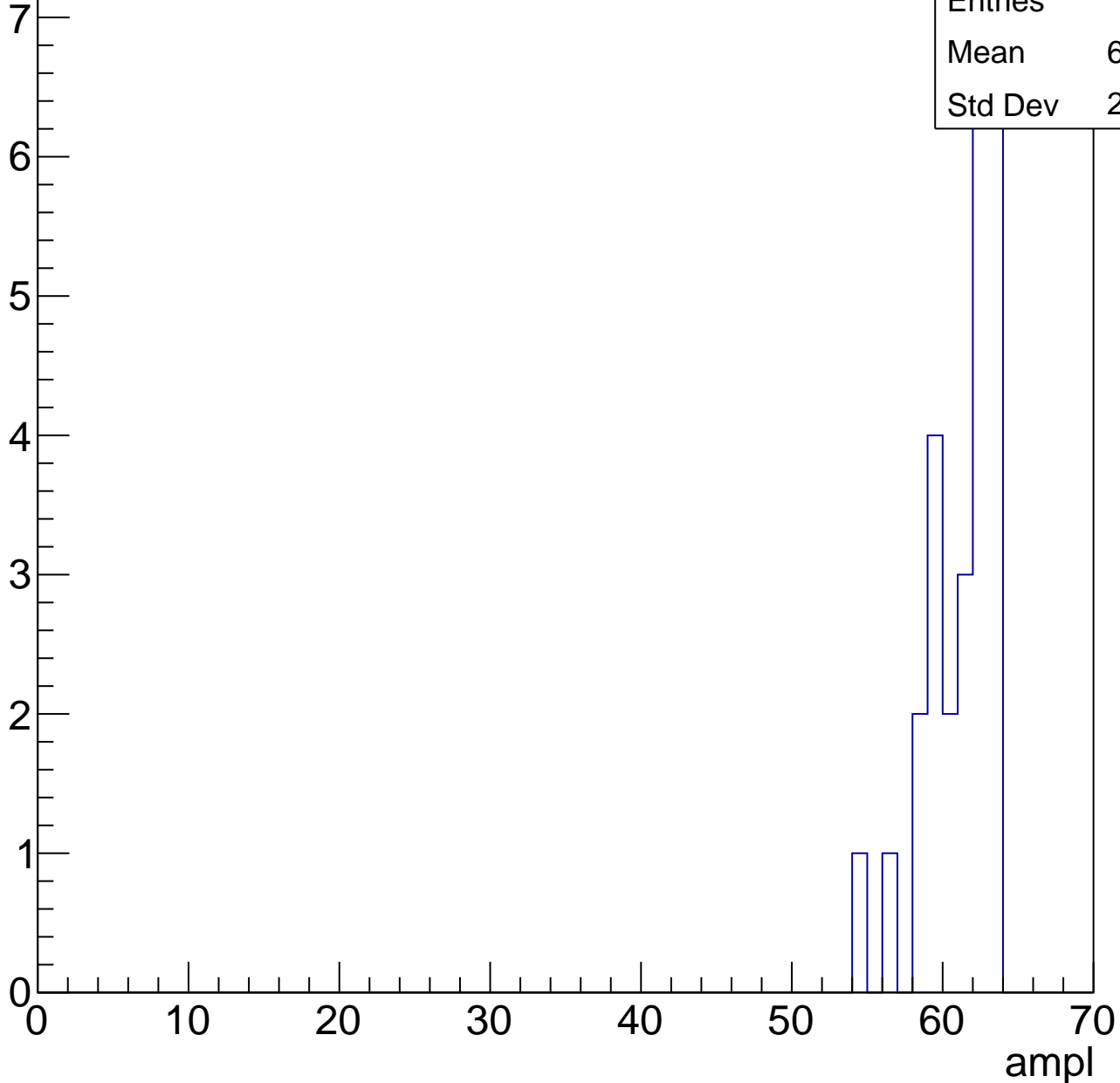


B1L100S, U12-ch95, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	60.74
Std Dev	2.303



B1L100S, U12-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

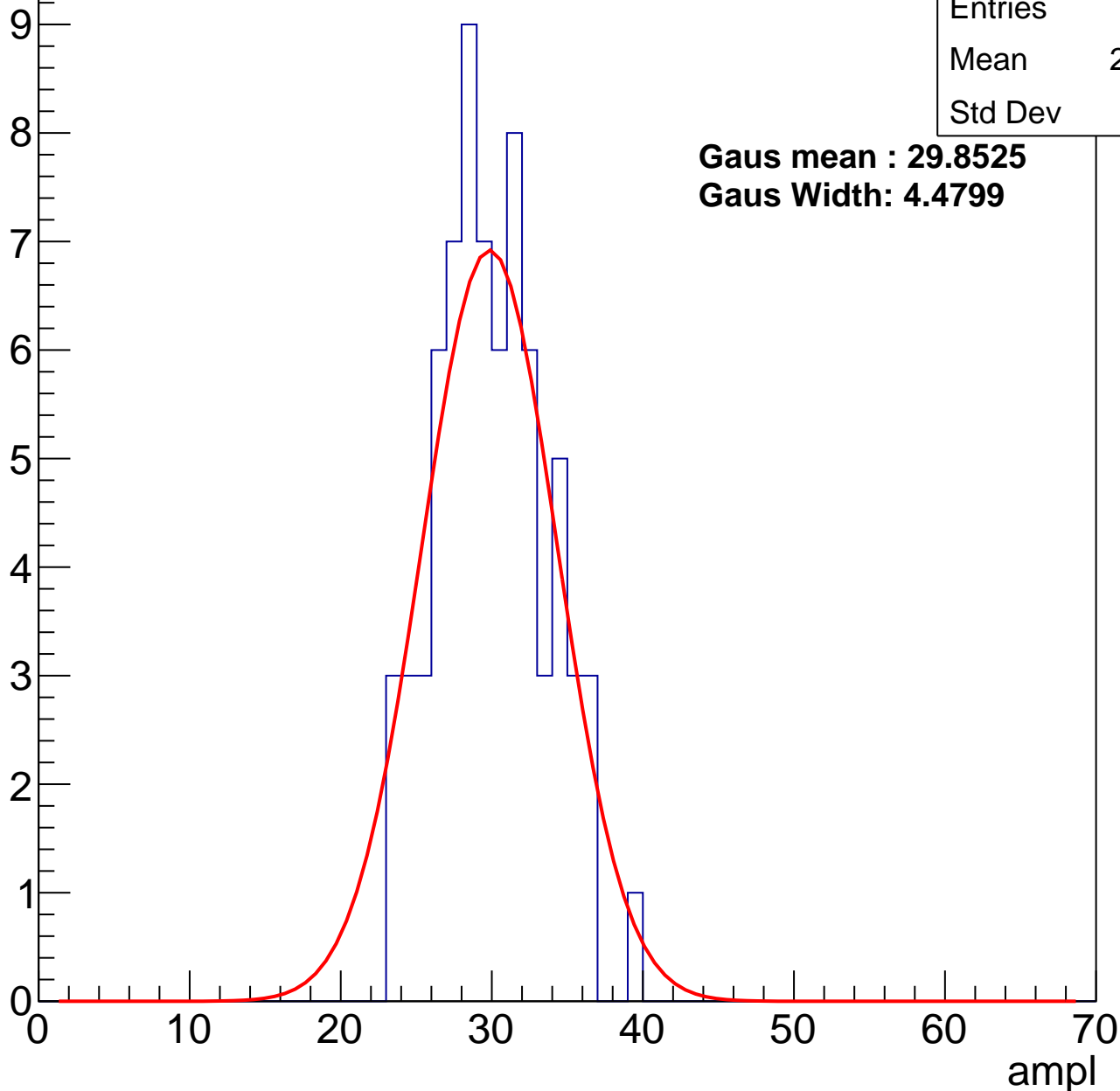
Entry



B1L100S, U12-ch96, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch96, adc1

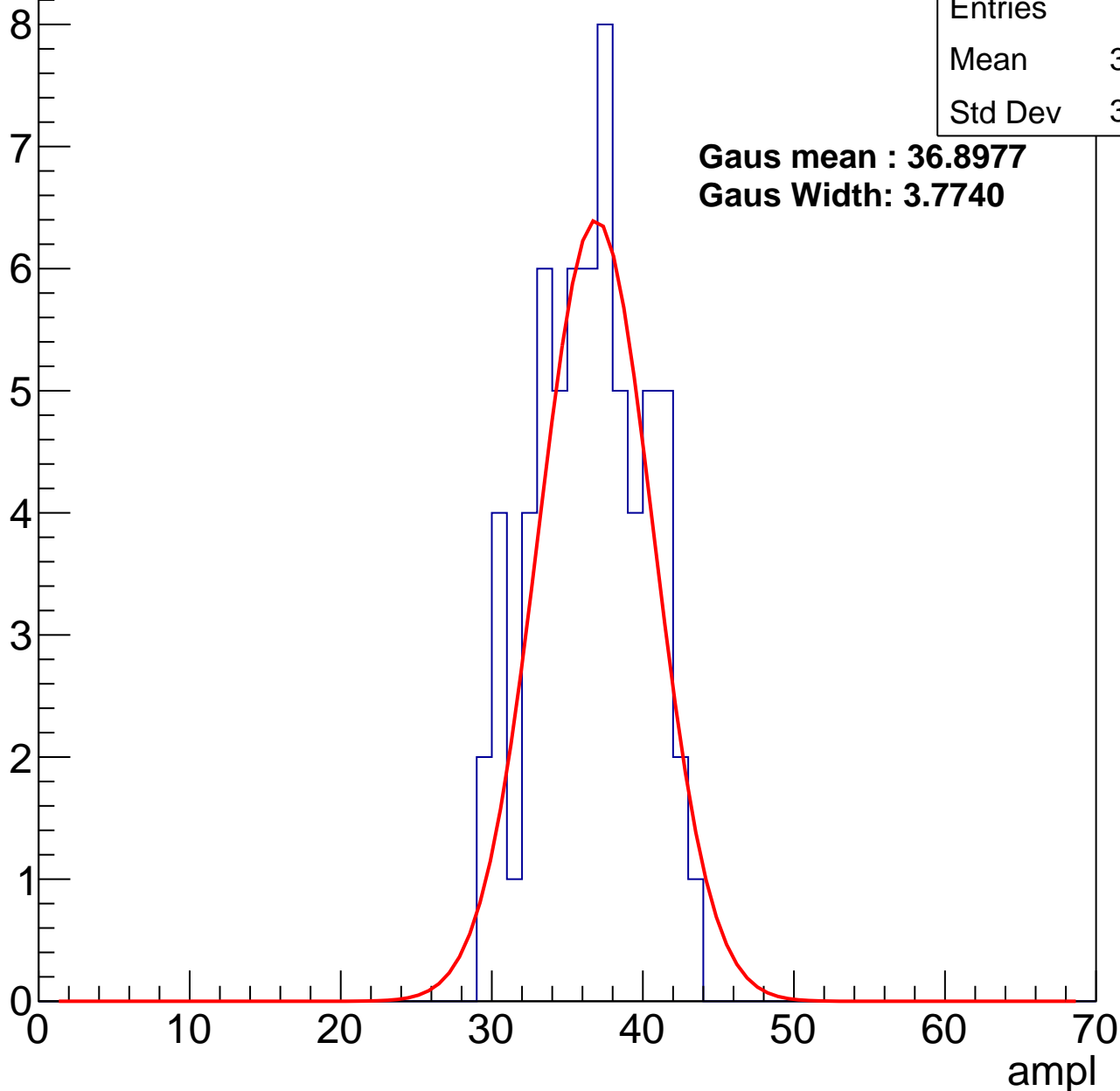
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.02
Std Dev	3.538

Gaus mean : 36.8977

Gaus Width: 3.7740



B1L100S, U12-ch96, adc2

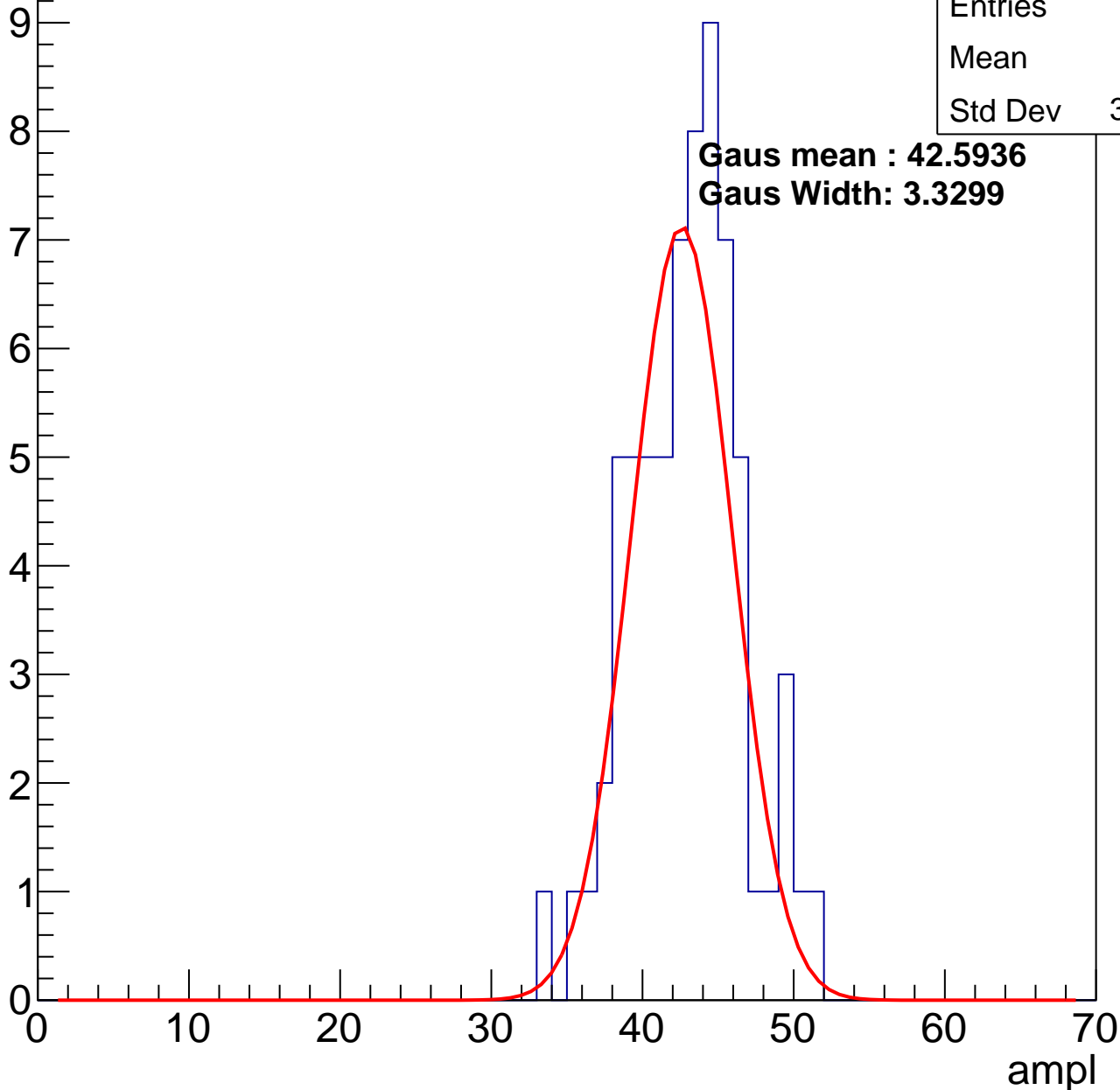
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.5
Std Dev	3.616

Gaus mean : 42.5936

Gaus Width: 3.3299

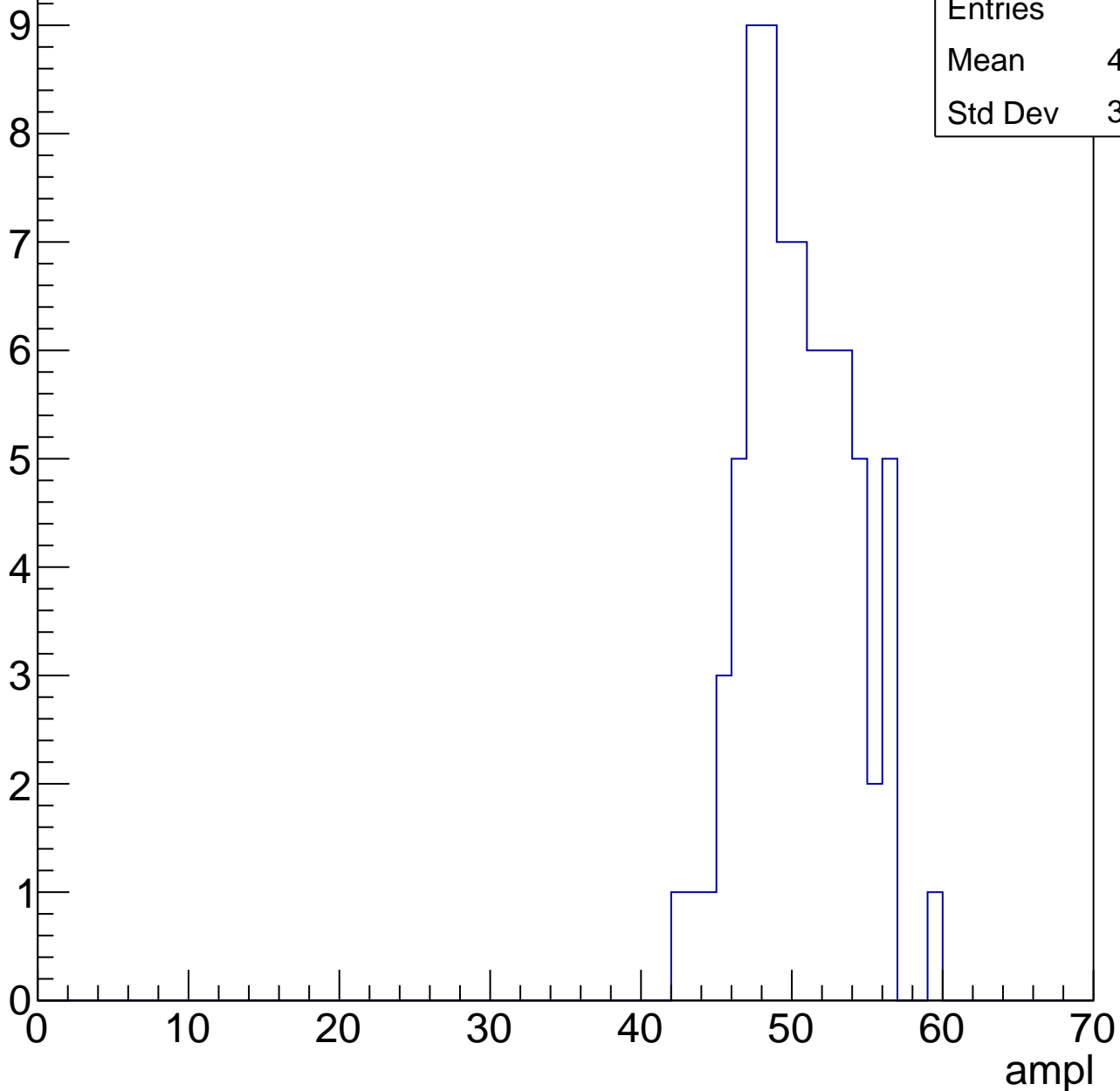


B1L100S, U12-ch96, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	49.96
Std Dev	3.493

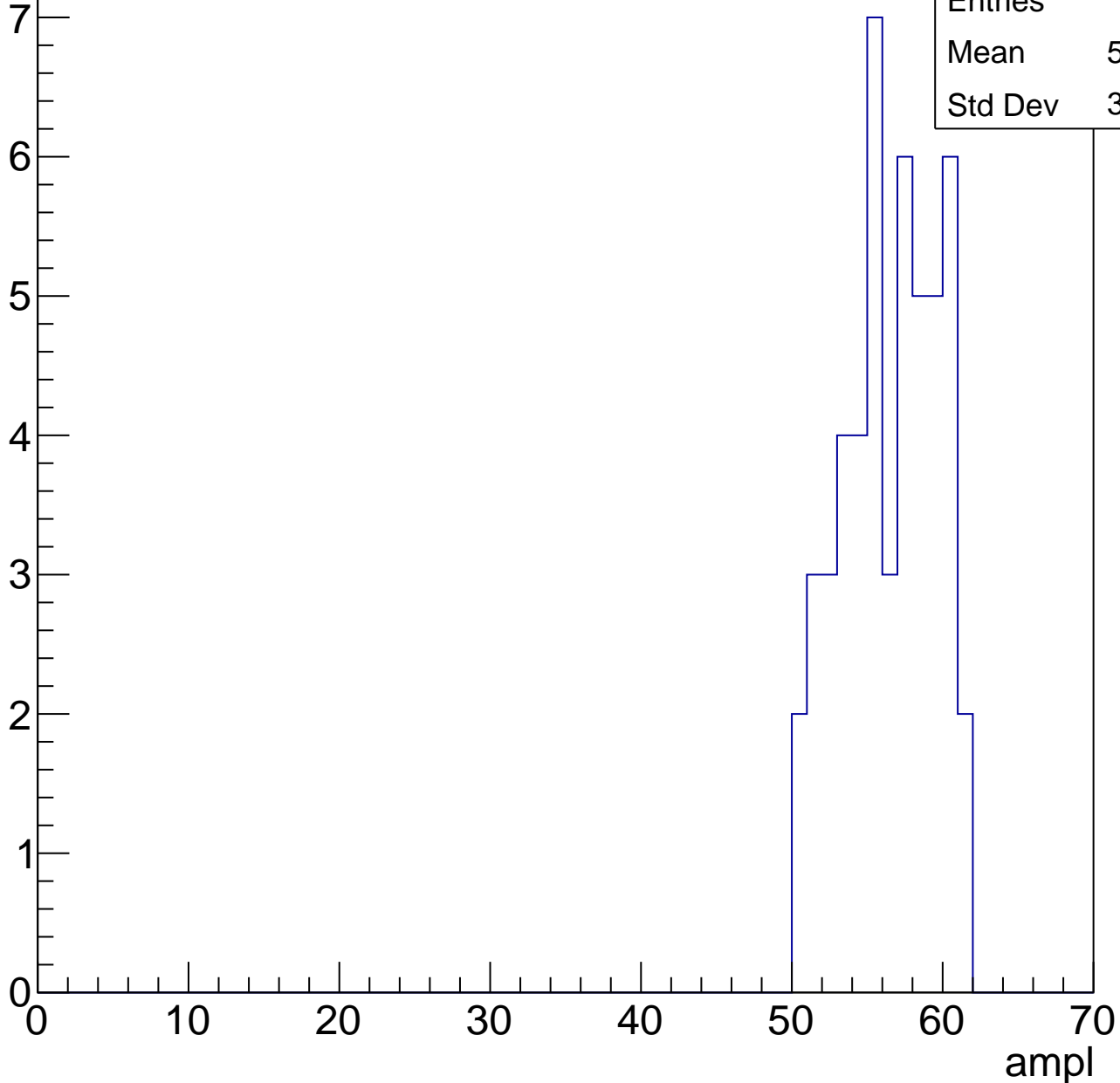


B1L100S, U12-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	55.98
Std Dev	3.069

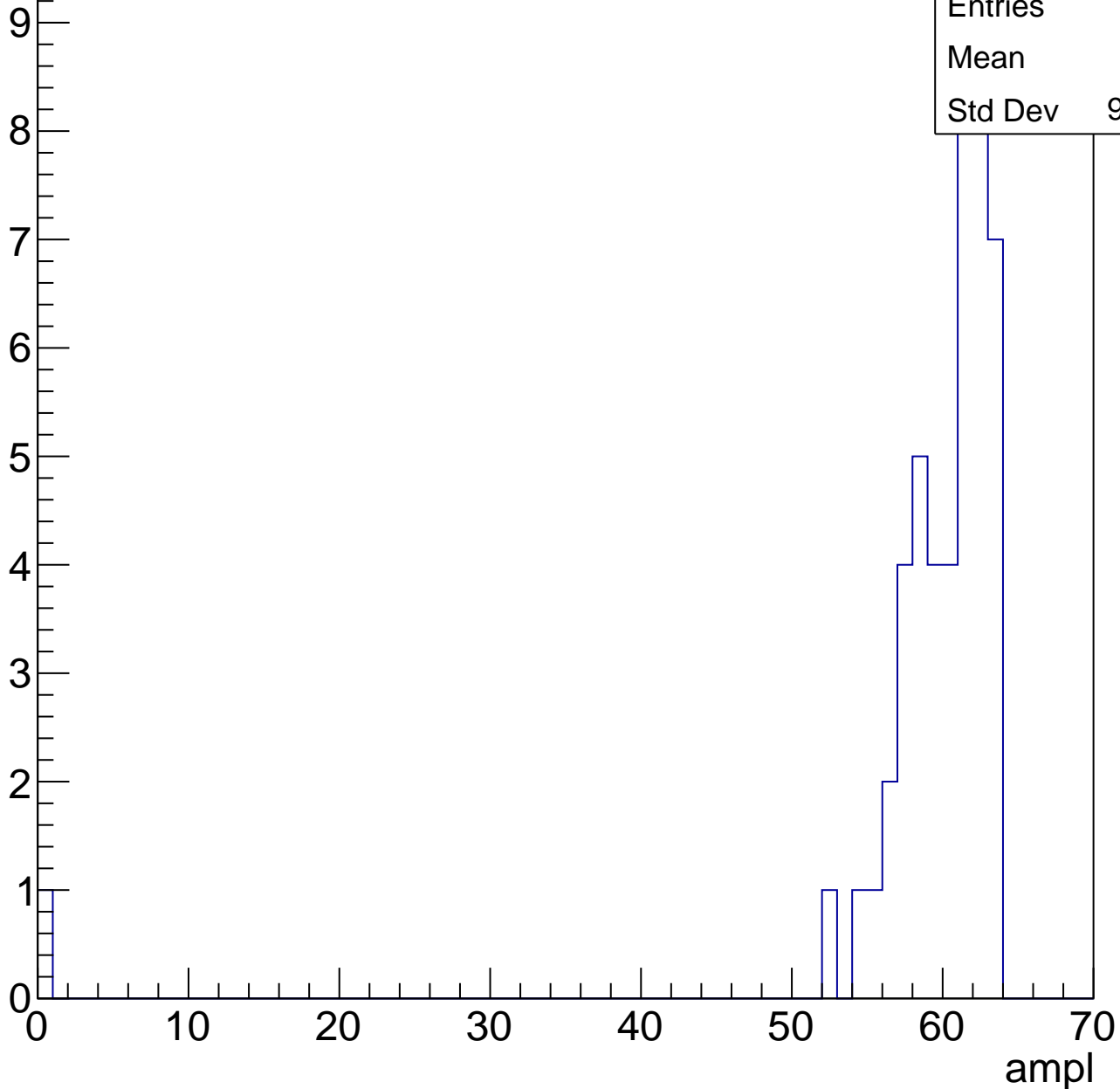


B1L100S, U12-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.6
Std Dev	9.029



B1L100S, U12-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch97, adc0

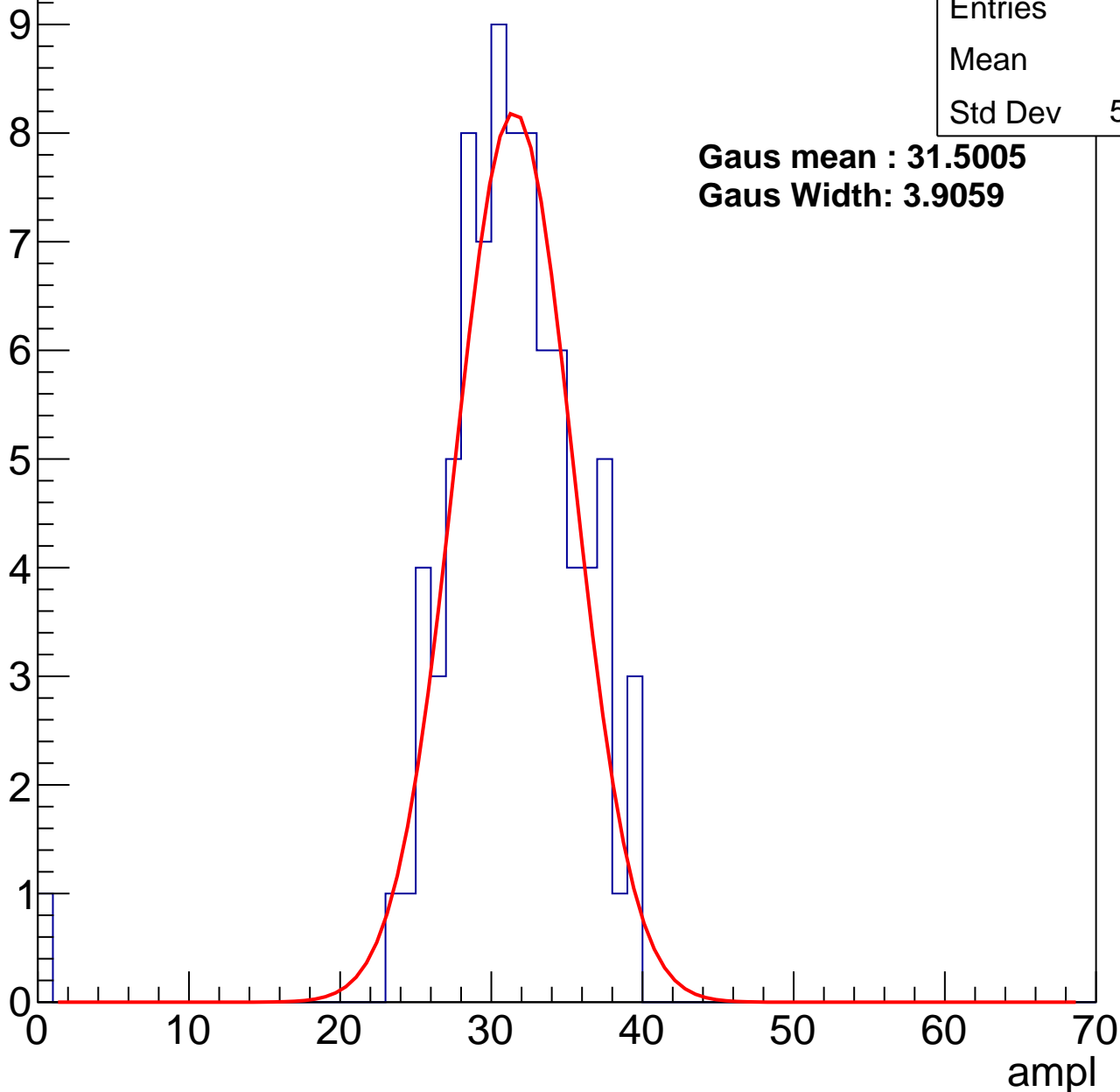
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	30.8
Std Dev	5.059

Gaus mean : 31.5005

Gaus Width: 3.9059



B1L100S, U12-ch97, adc1

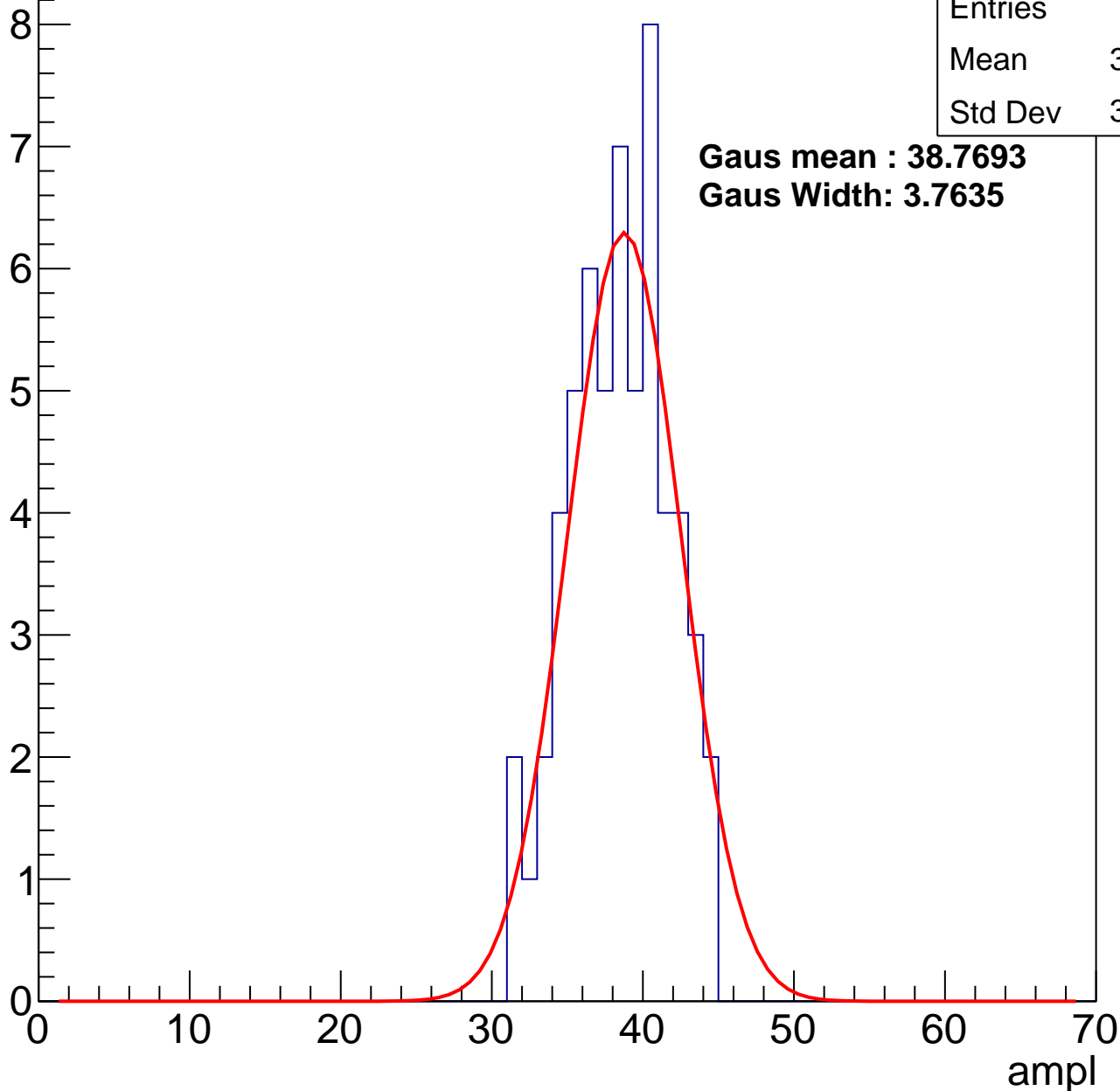
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	37.97
Std Dev	3.232

Gaus mean : 38.7693

Gaus Width: 3.7635



B1L100S, U12-ch97, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	44.5
Std Dev	3.67

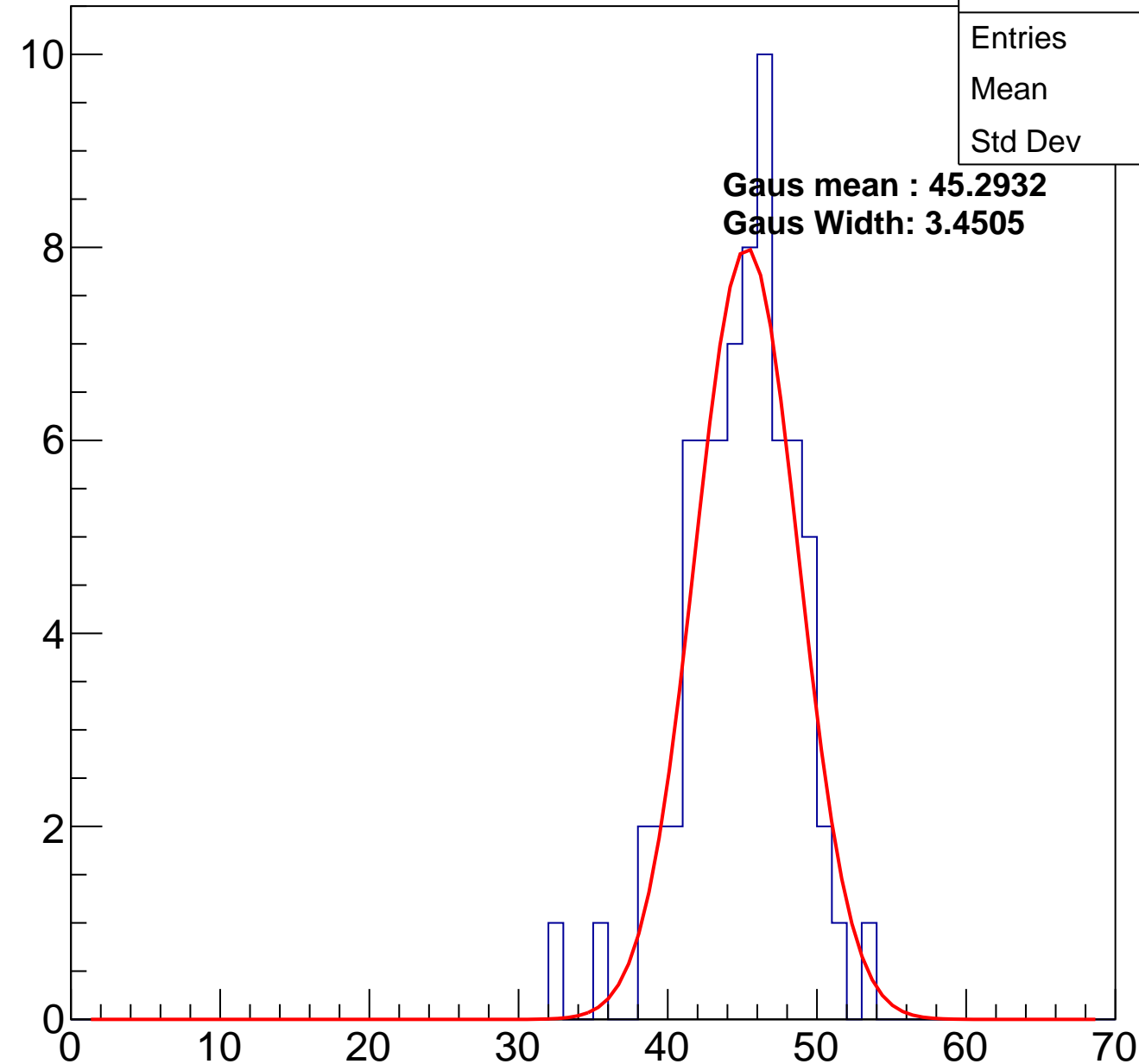
Gaus mean : 45.2932

Gaus Width: 3.4505

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

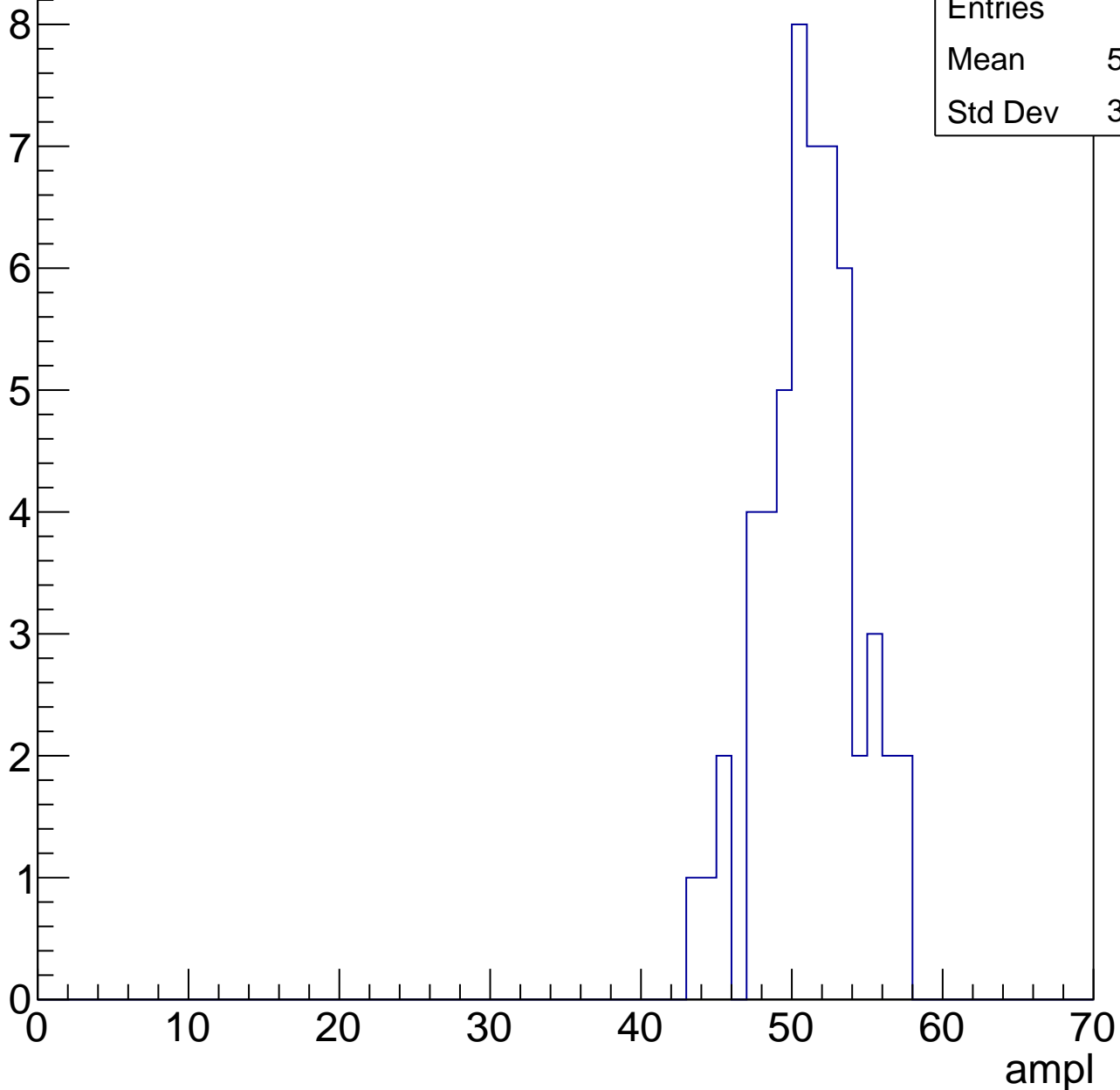


B1L100S, U12-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	50.74
Std Dev	3.128

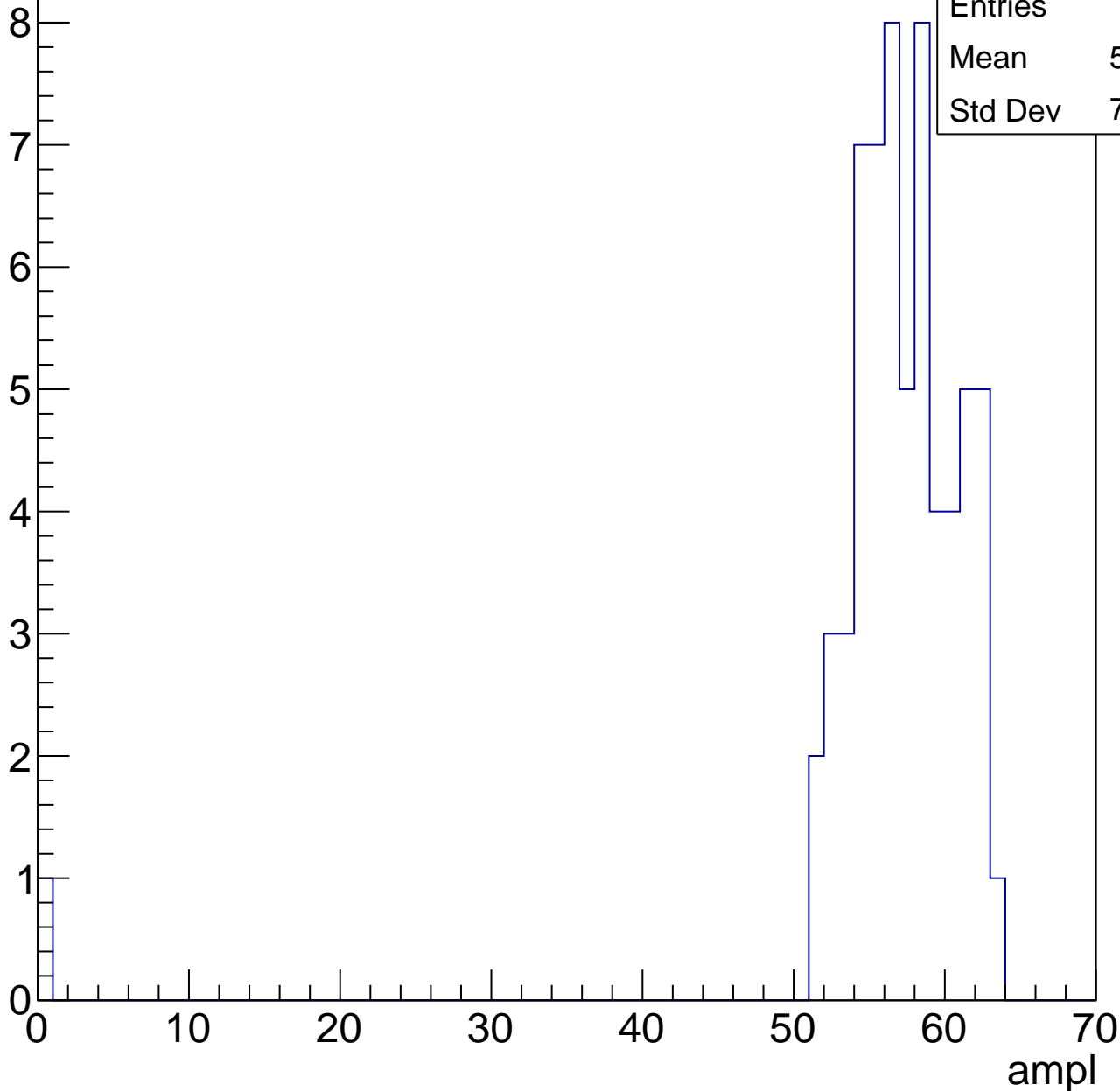


B1L100S, U12-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	56.05
Std Dev	7.753

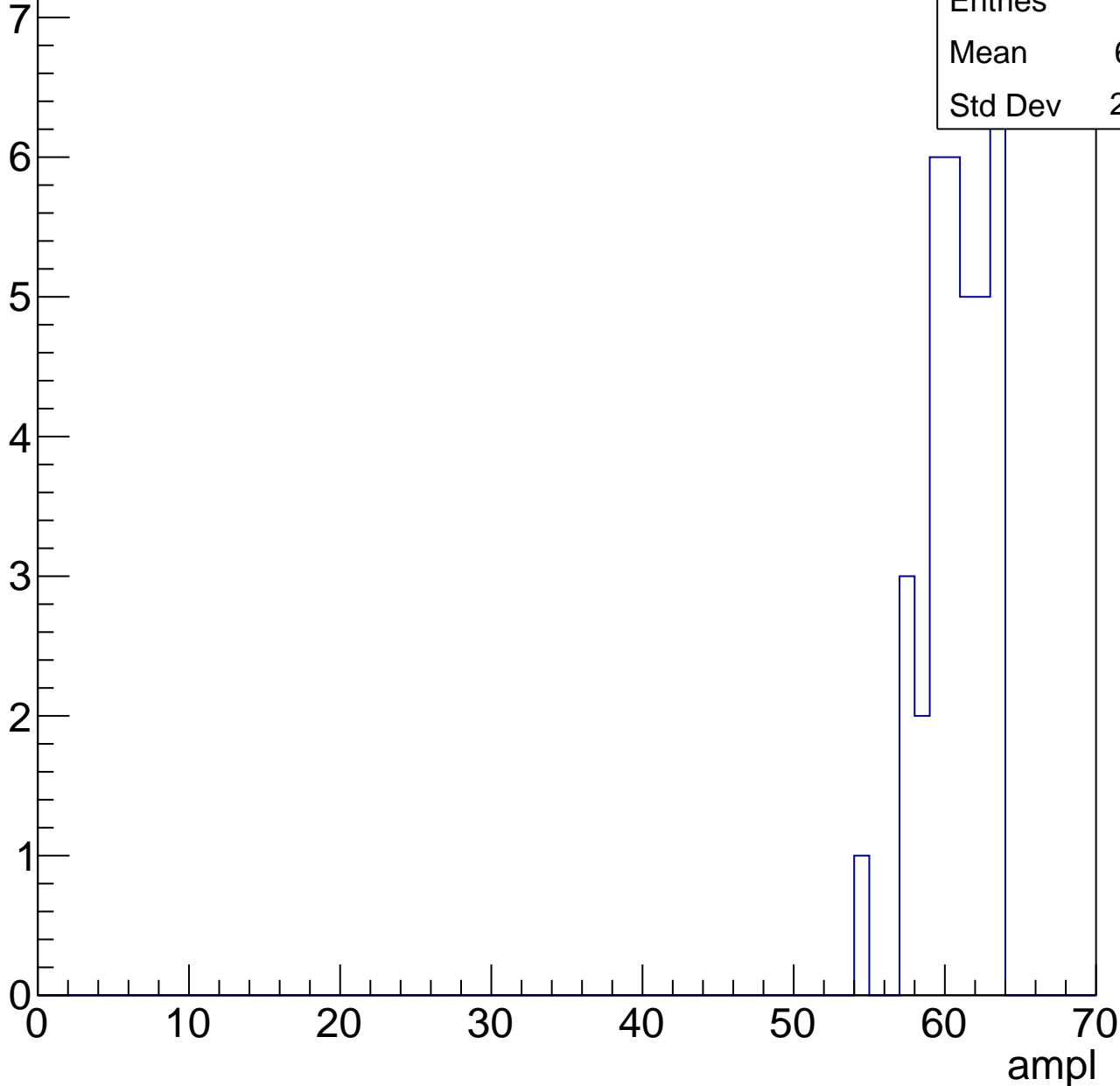


B1L100S, U12-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	60.31
Std Dev	2.148



B1L100S, U12-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch98, adc0

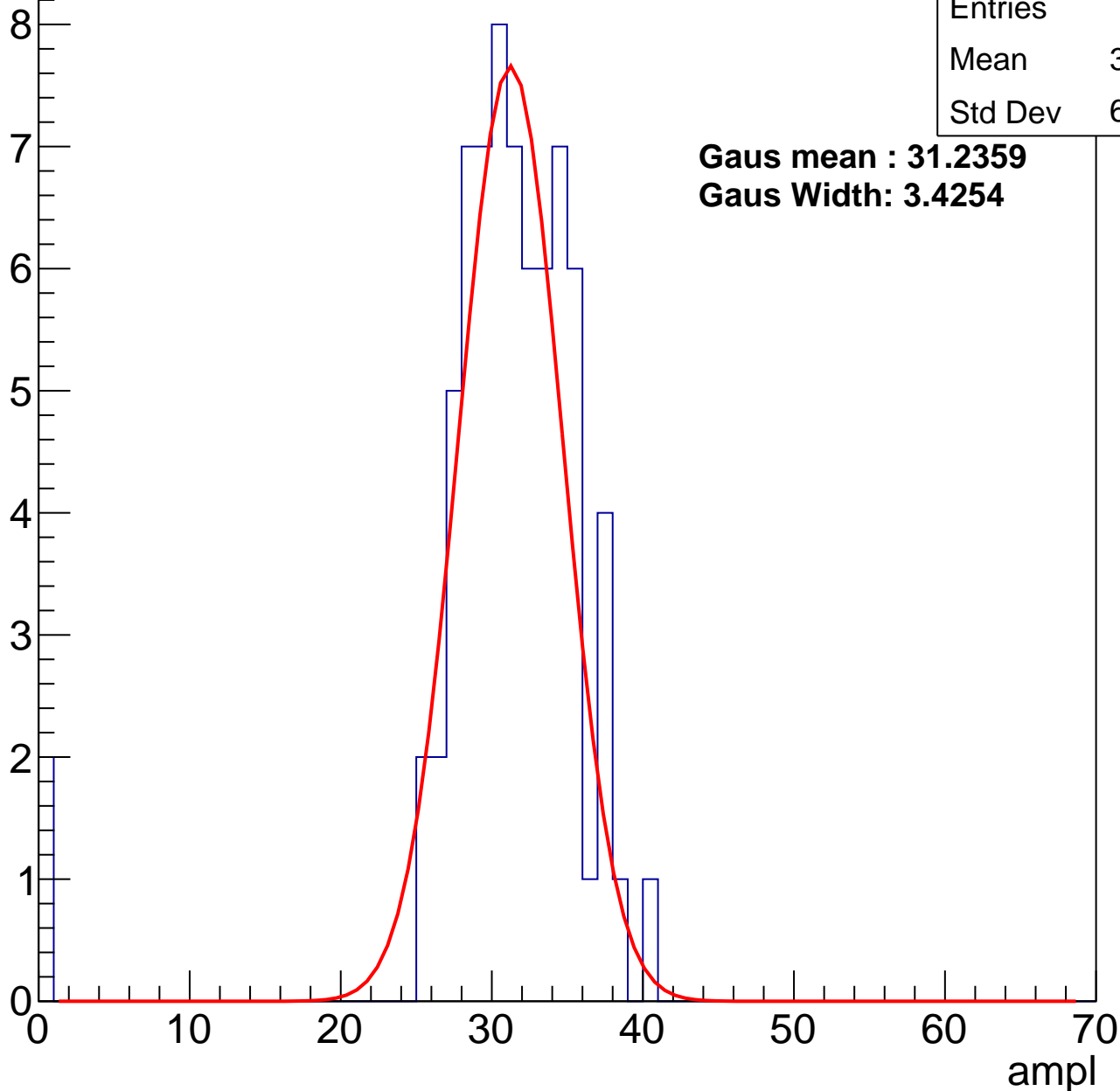
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.46
Std Dev	6.112

Gaus mean : 31.2359

Gaus Width: 3.4254



B1L100S, U12-ch98, adc1

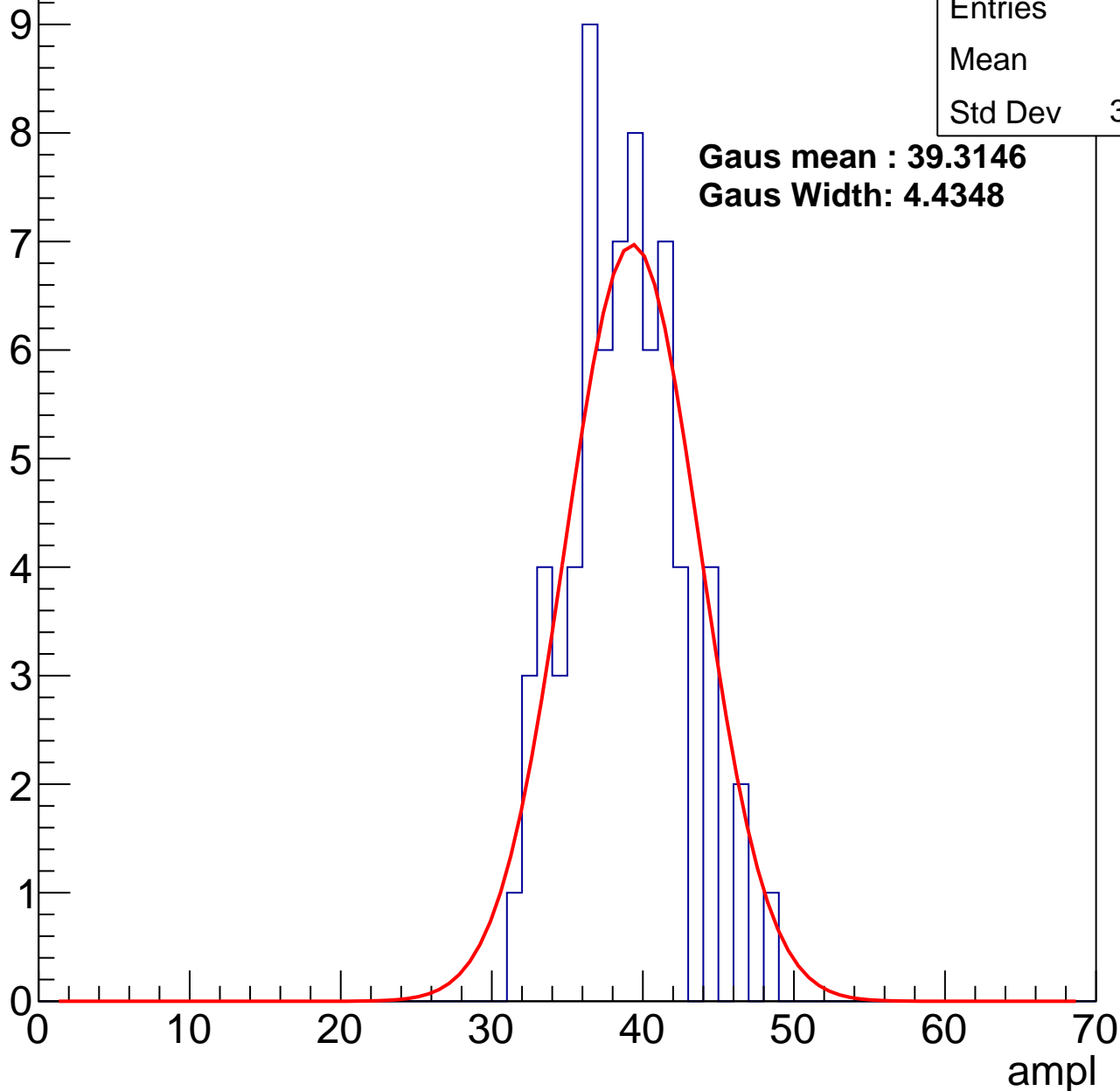
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	38.2
Std Dev	3.634

Gaus mean : 39.3146

Gaus Width: 4.4348



B1L100S, U12-ch98, adc2

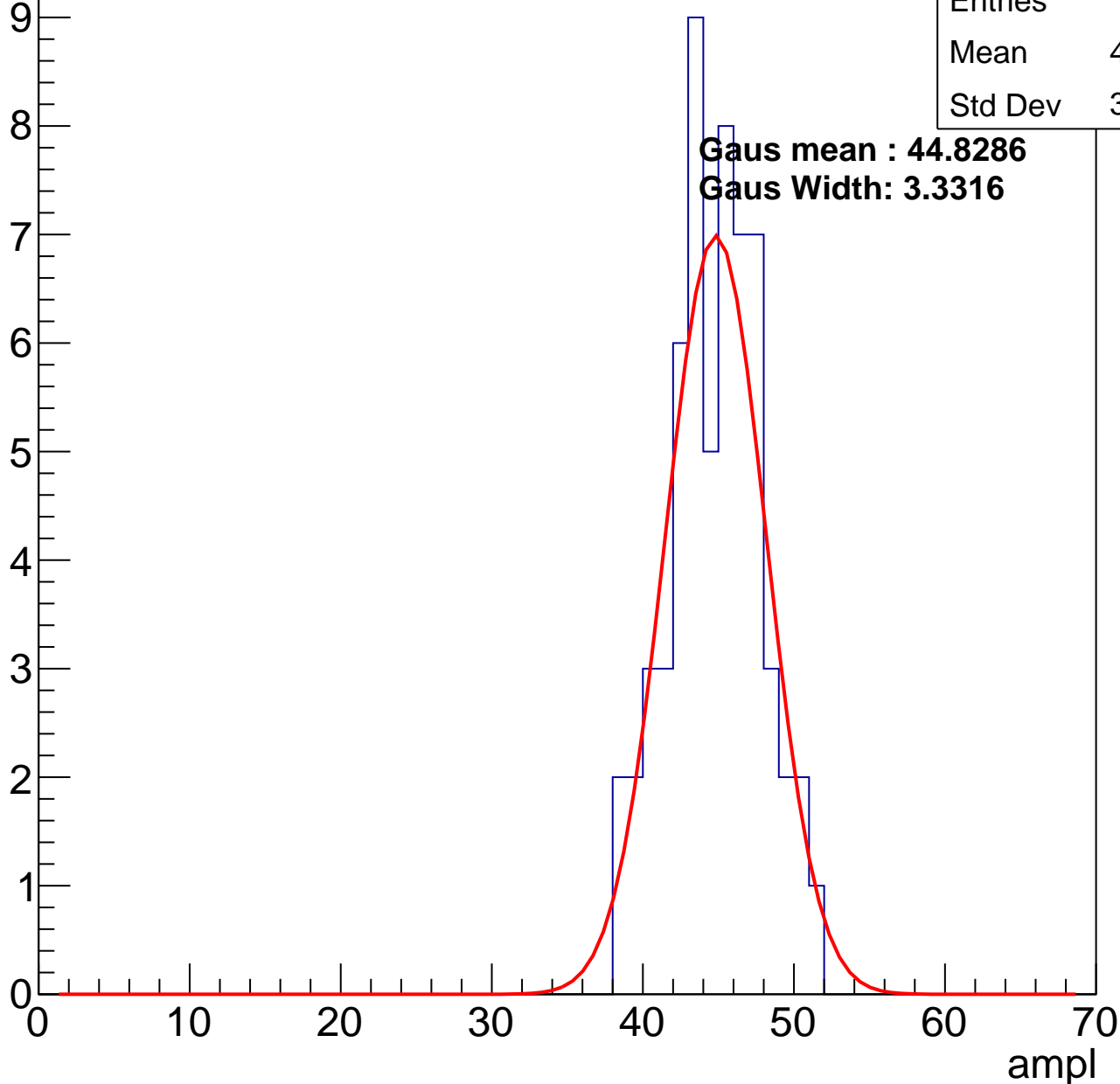
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.33
Std Dev	3.004

Gaus mean : 44.8286

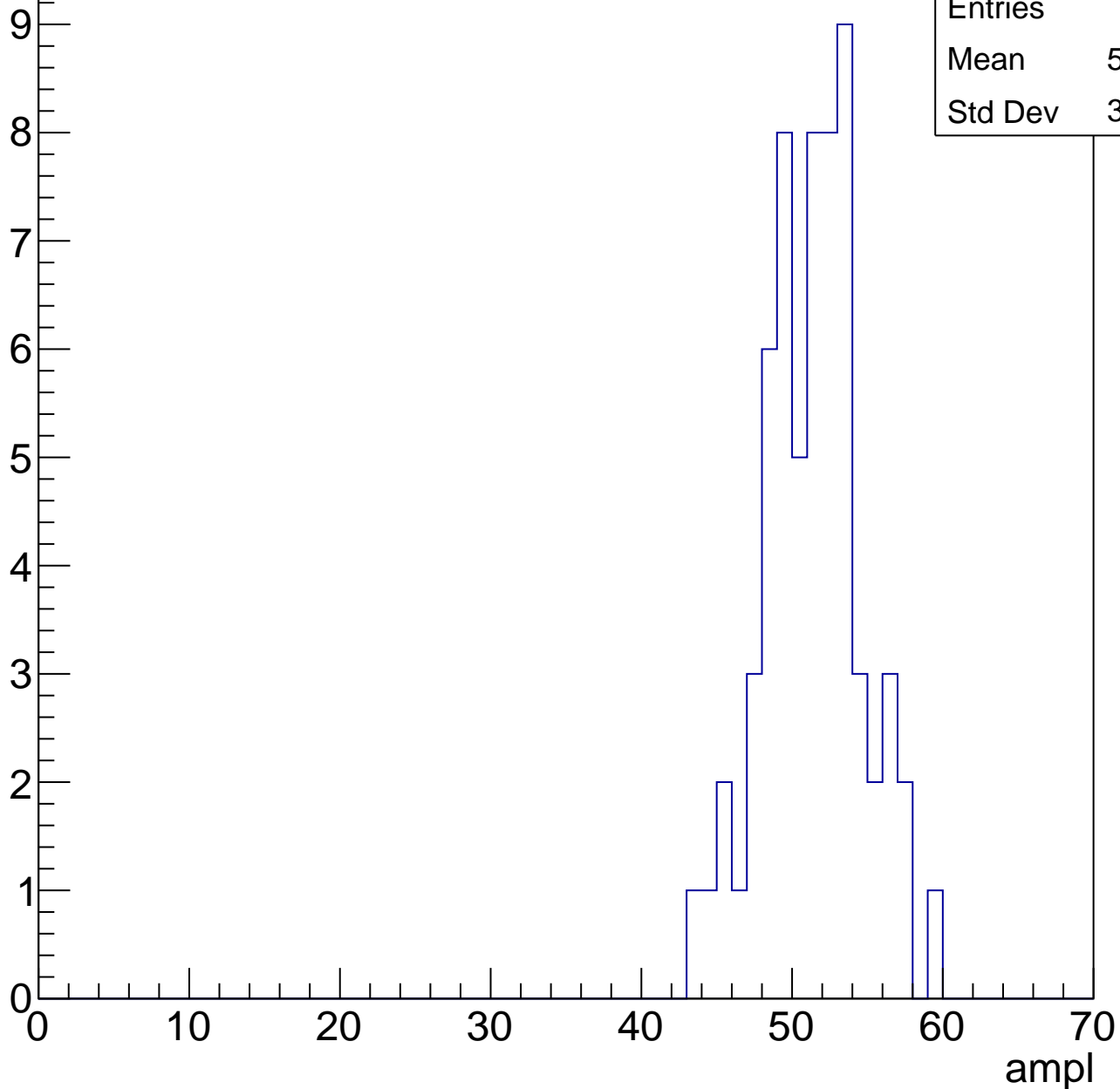
Gaus Width: 3.3316



B1L100S, U12-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

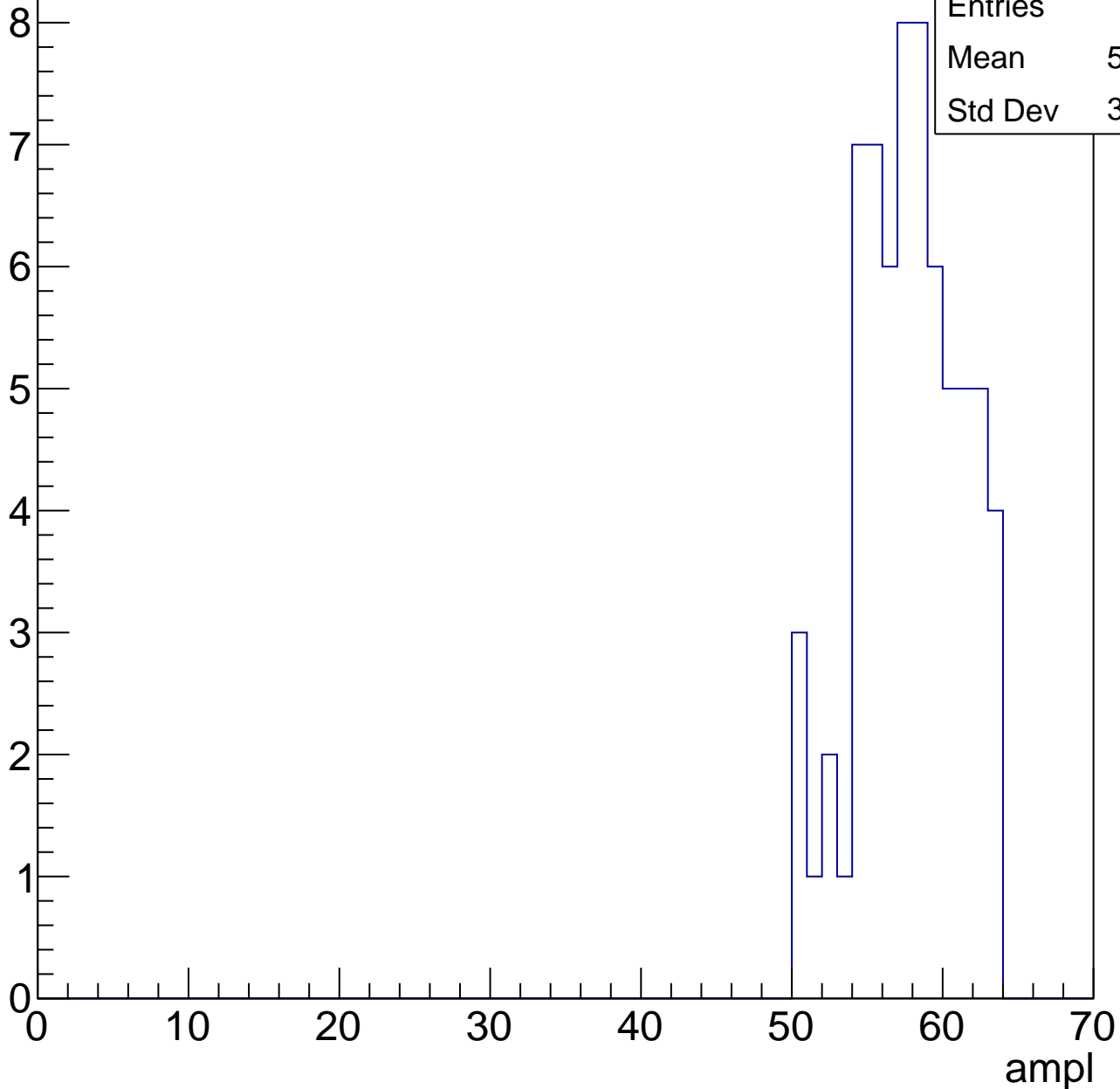


B1L100S, U12-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	57.32
Std Dev	3.354

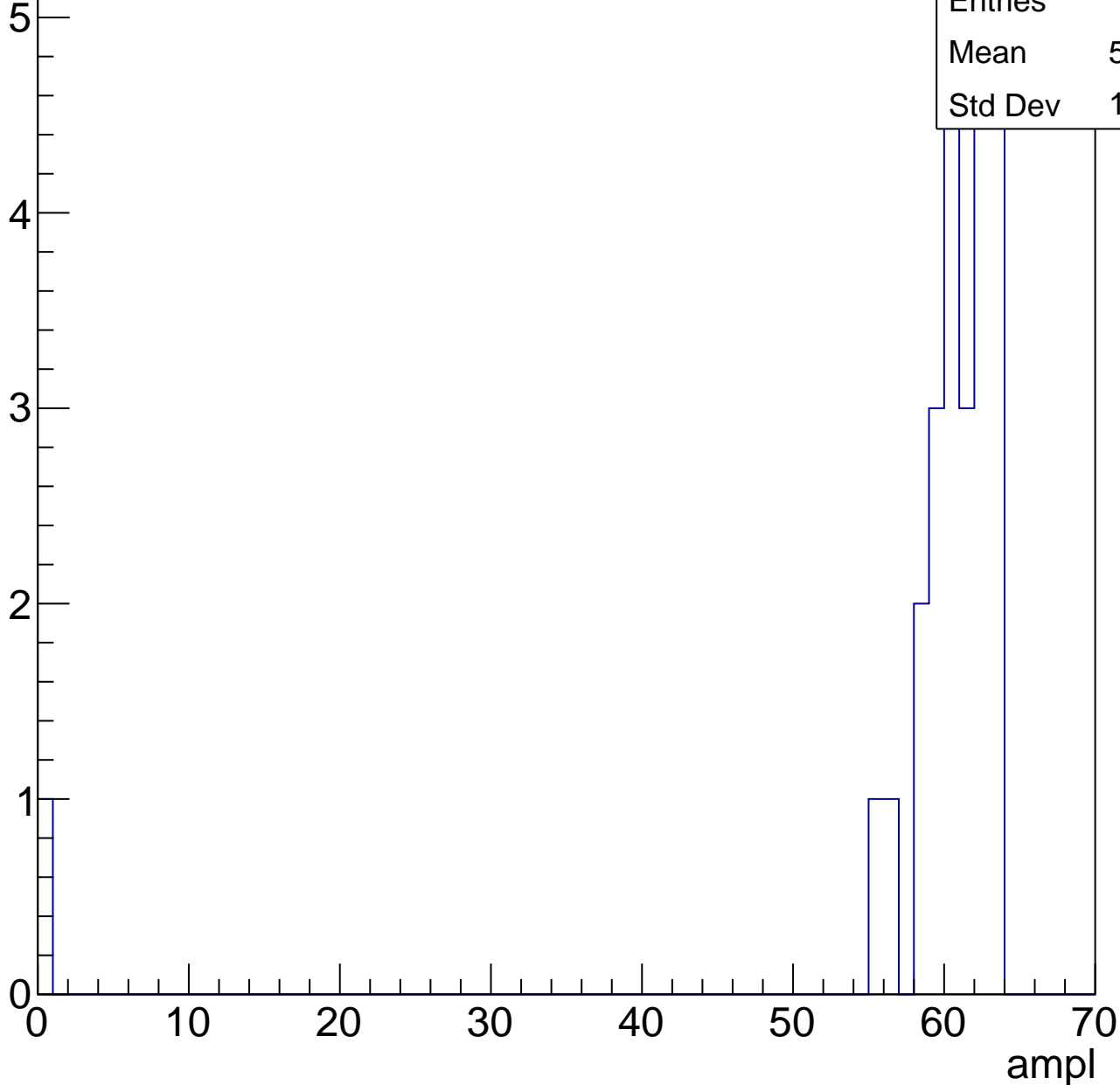


B1L100S, U12-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	26
Mean	58.15
Std Dev	11.82



B1L100S, U12-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

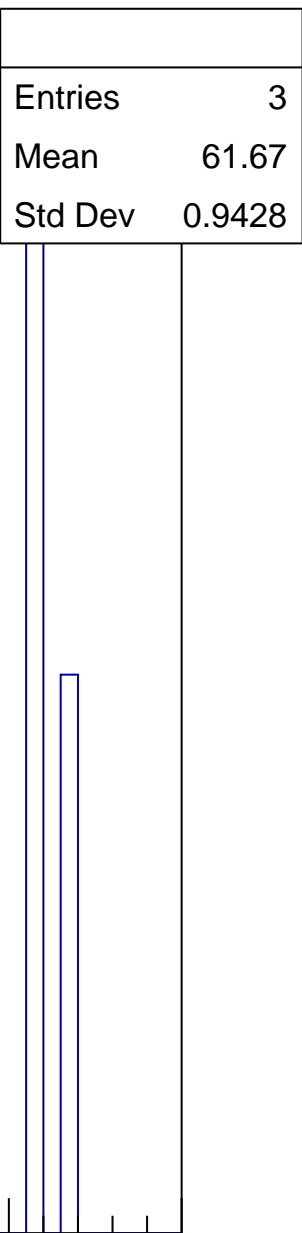
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L100S, U12-ch99, adc0

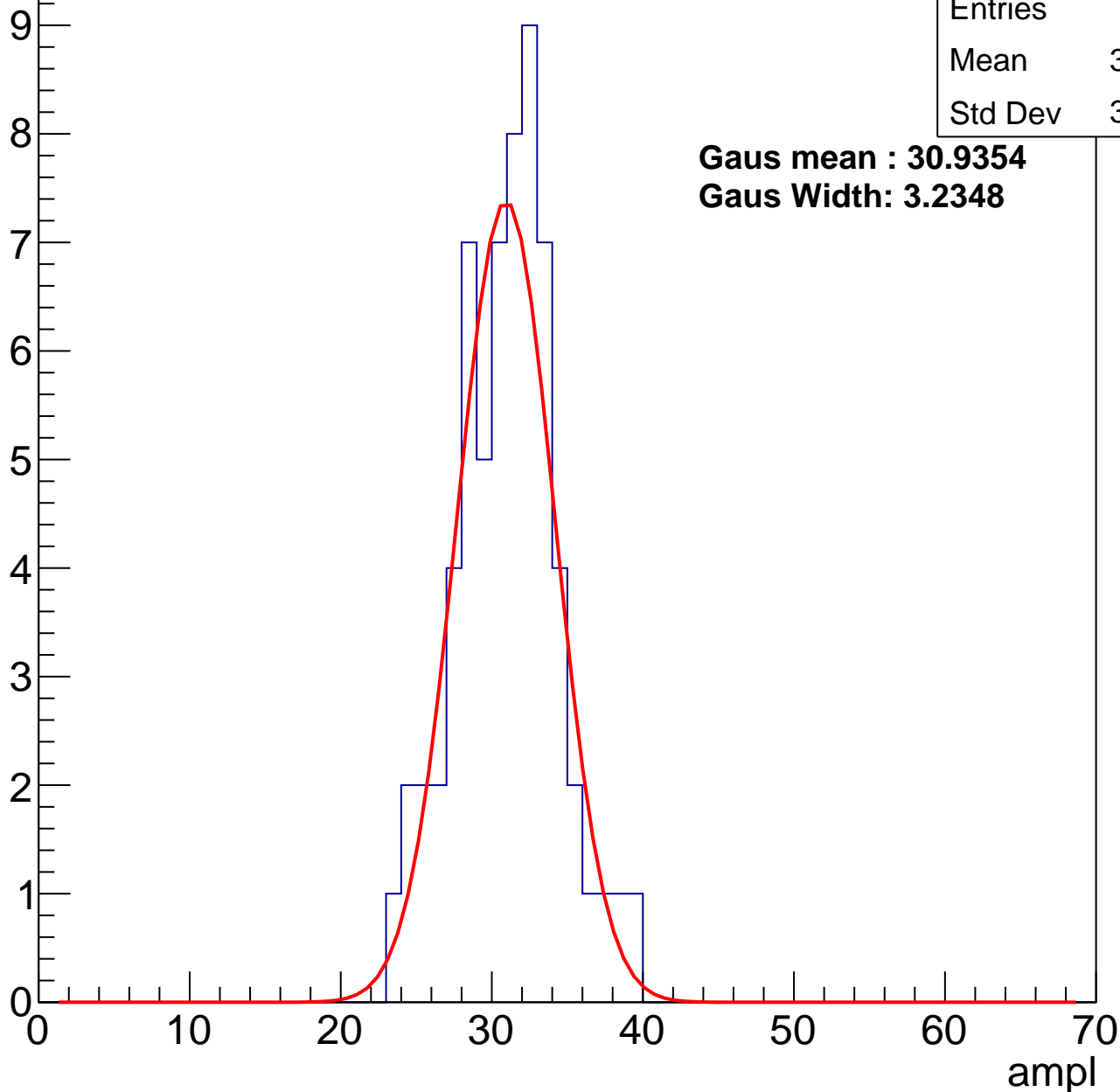
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	30.55
Std Dev	3.316

Gaus mean : 30.9354

Gaus Width: 3.2348



B1L100S, U12-ch99, adc1

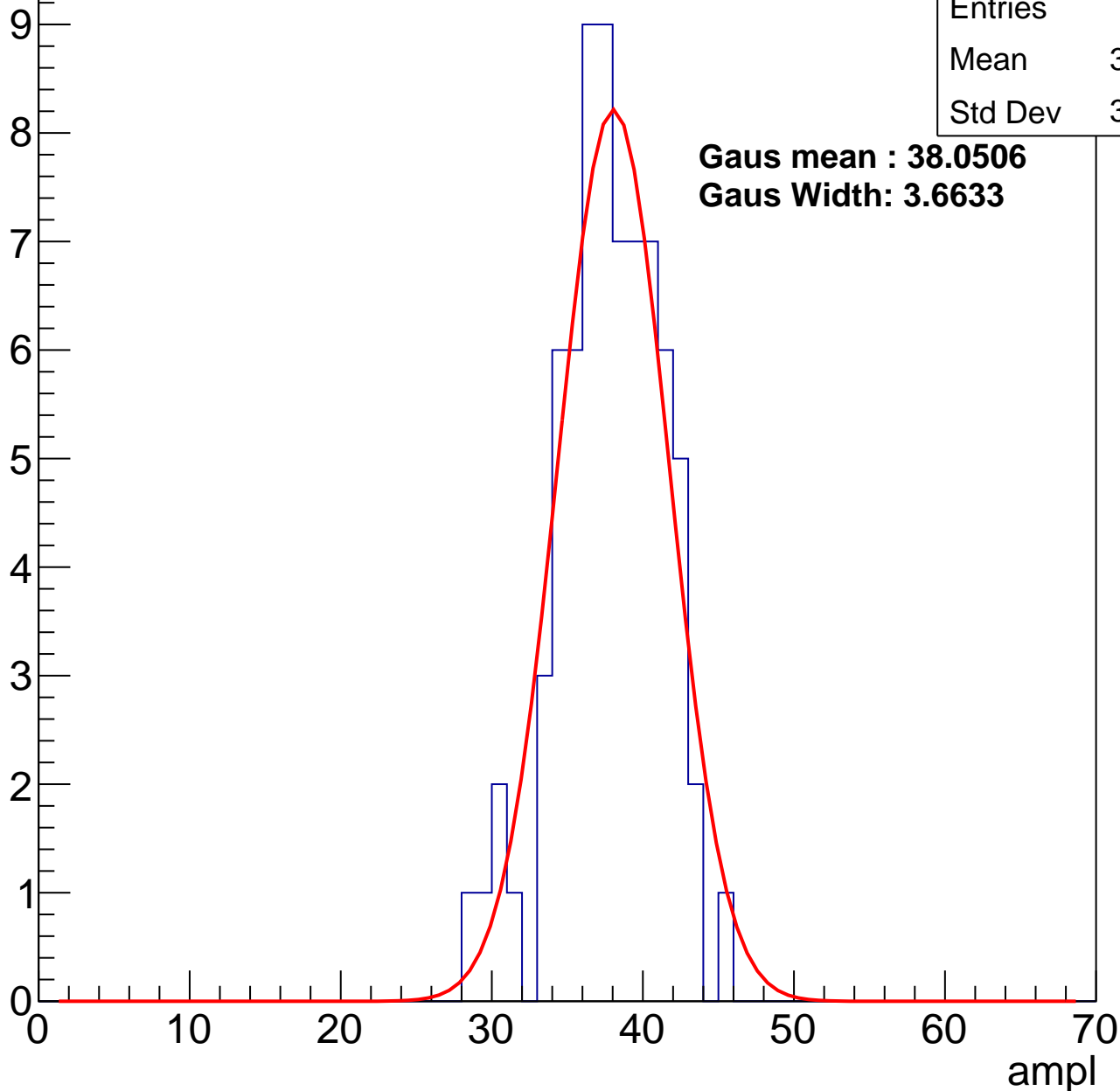
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	37.32
Std Dev	3.436

Gaus mean : 38.0506

Gaus Width: 3.6633



B1L100S, U12-ch99, adc2

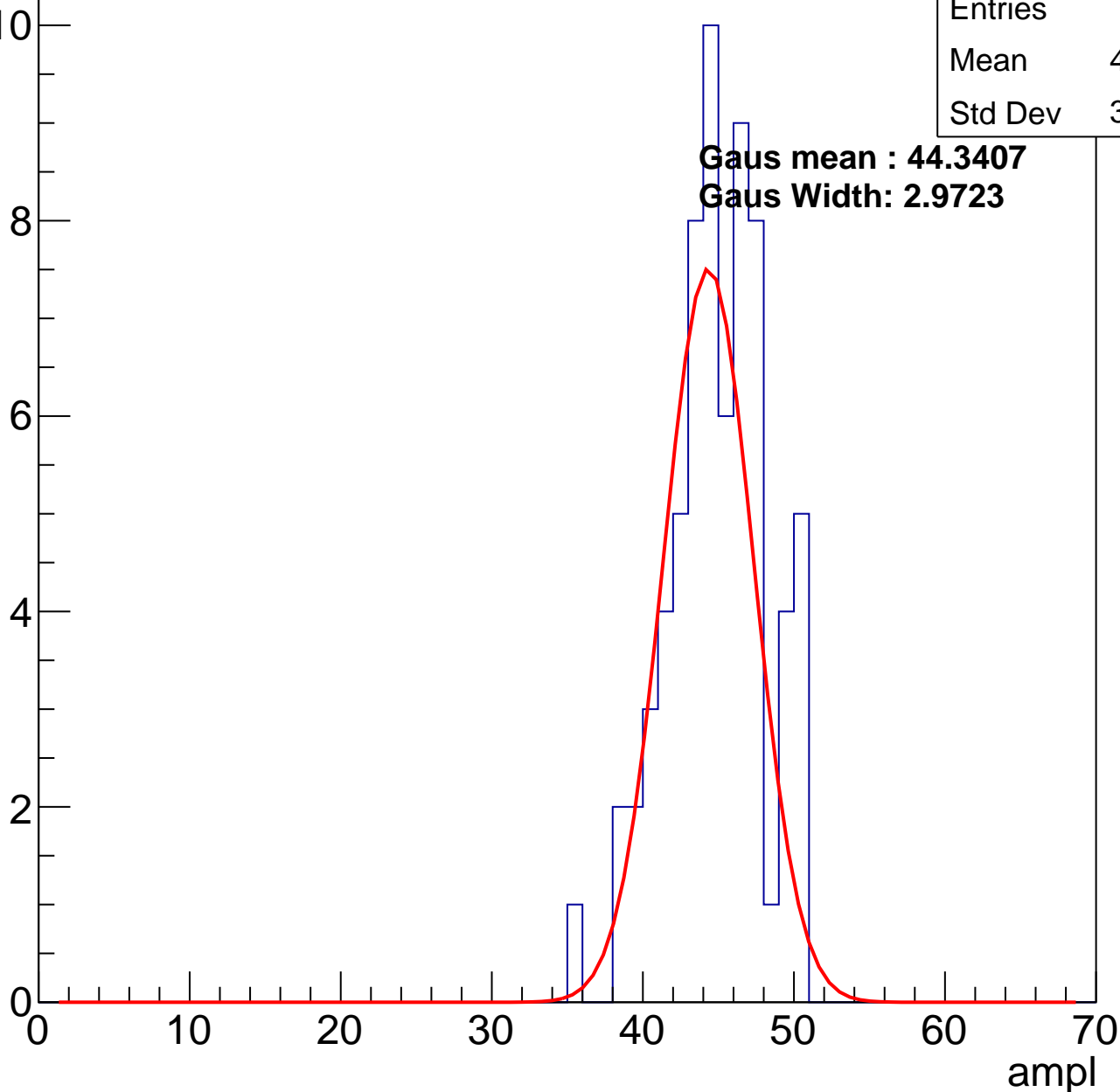
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.43
Std Dev	3.237

Gaus mean : 44.3407

Gaus Width: 2.9723

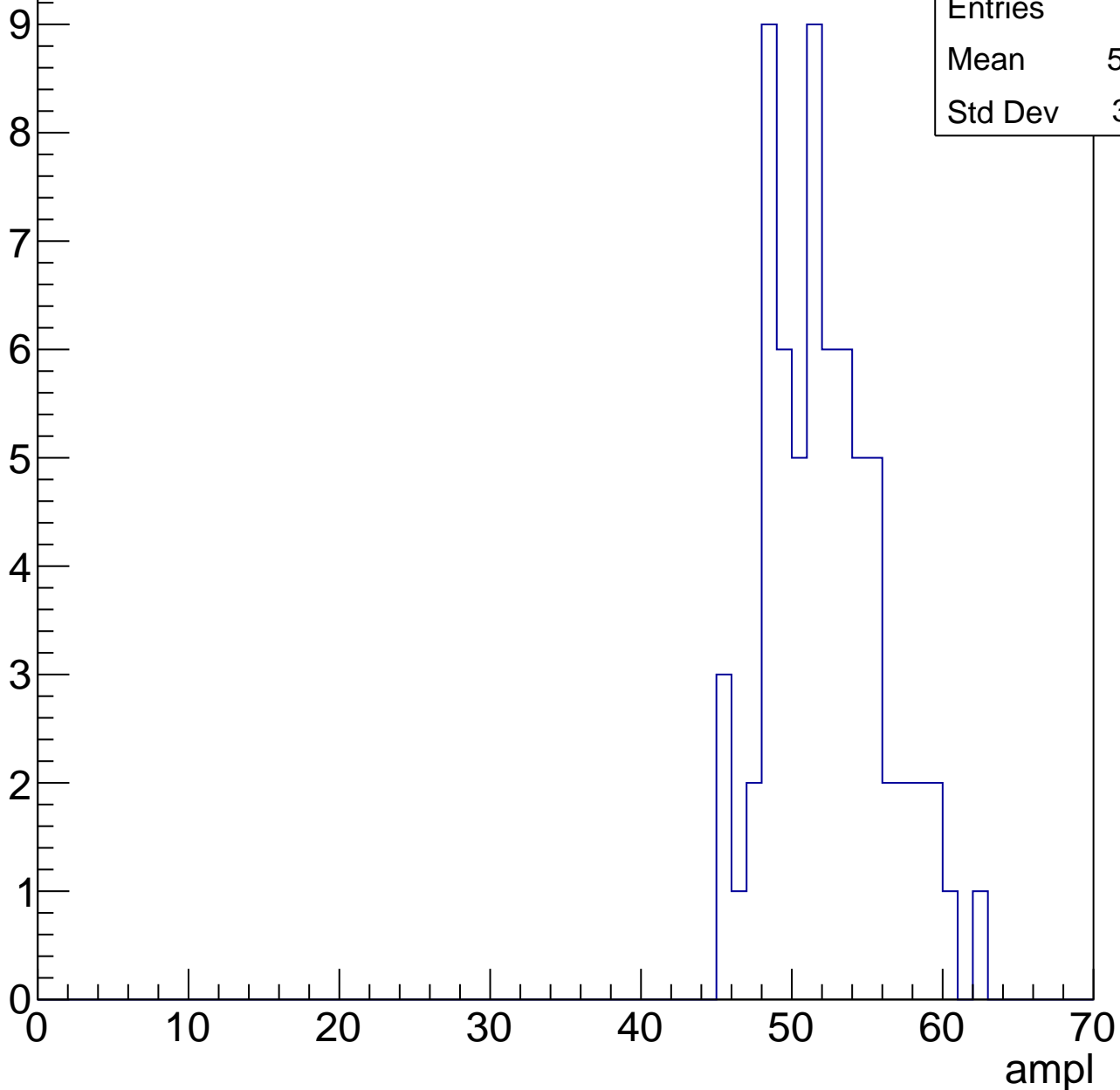


B1L100S, U12-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	51.75
Std Dev	3.751

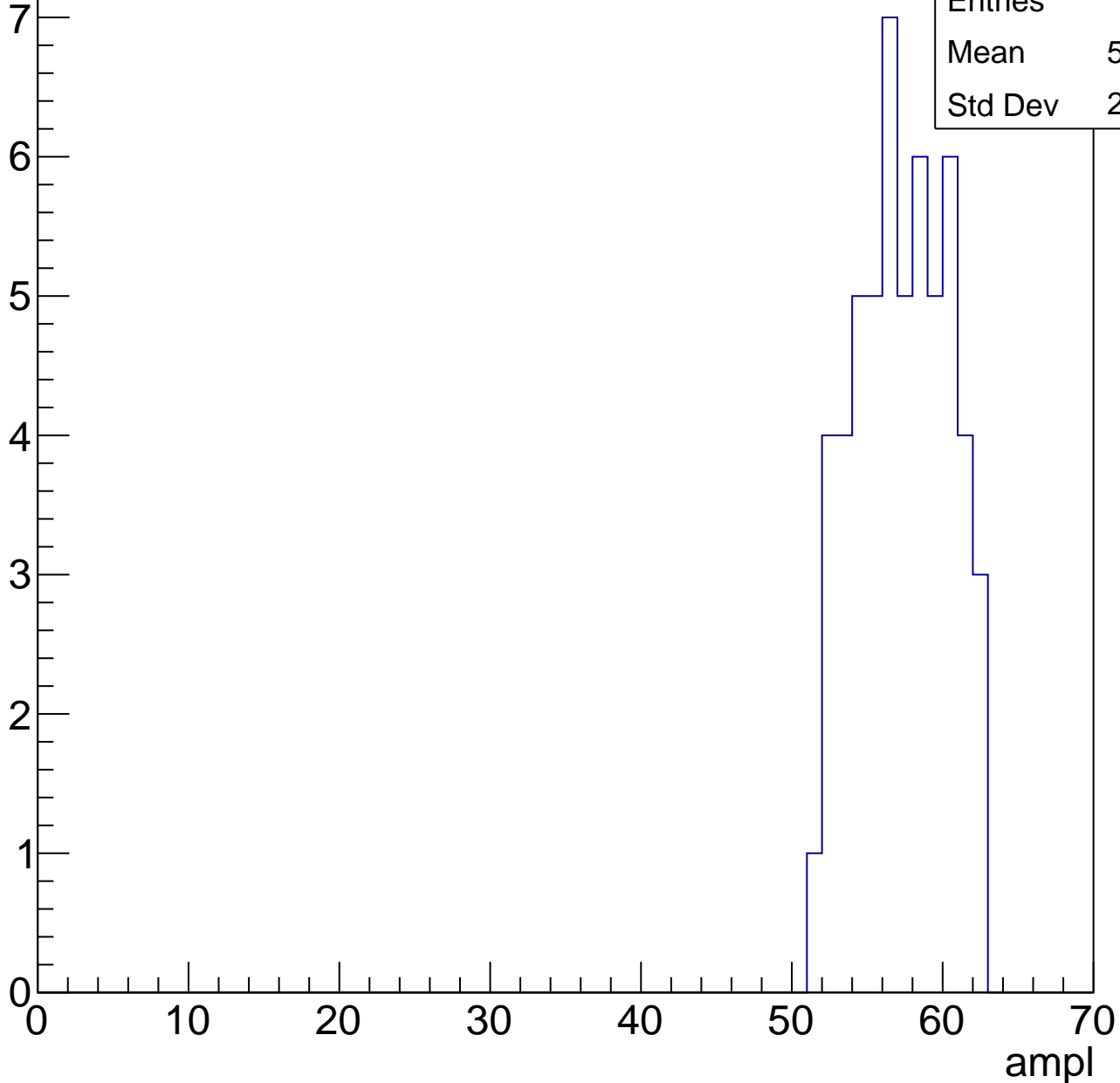


B1L100S, U12-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	56.84
Std Dev	2.983

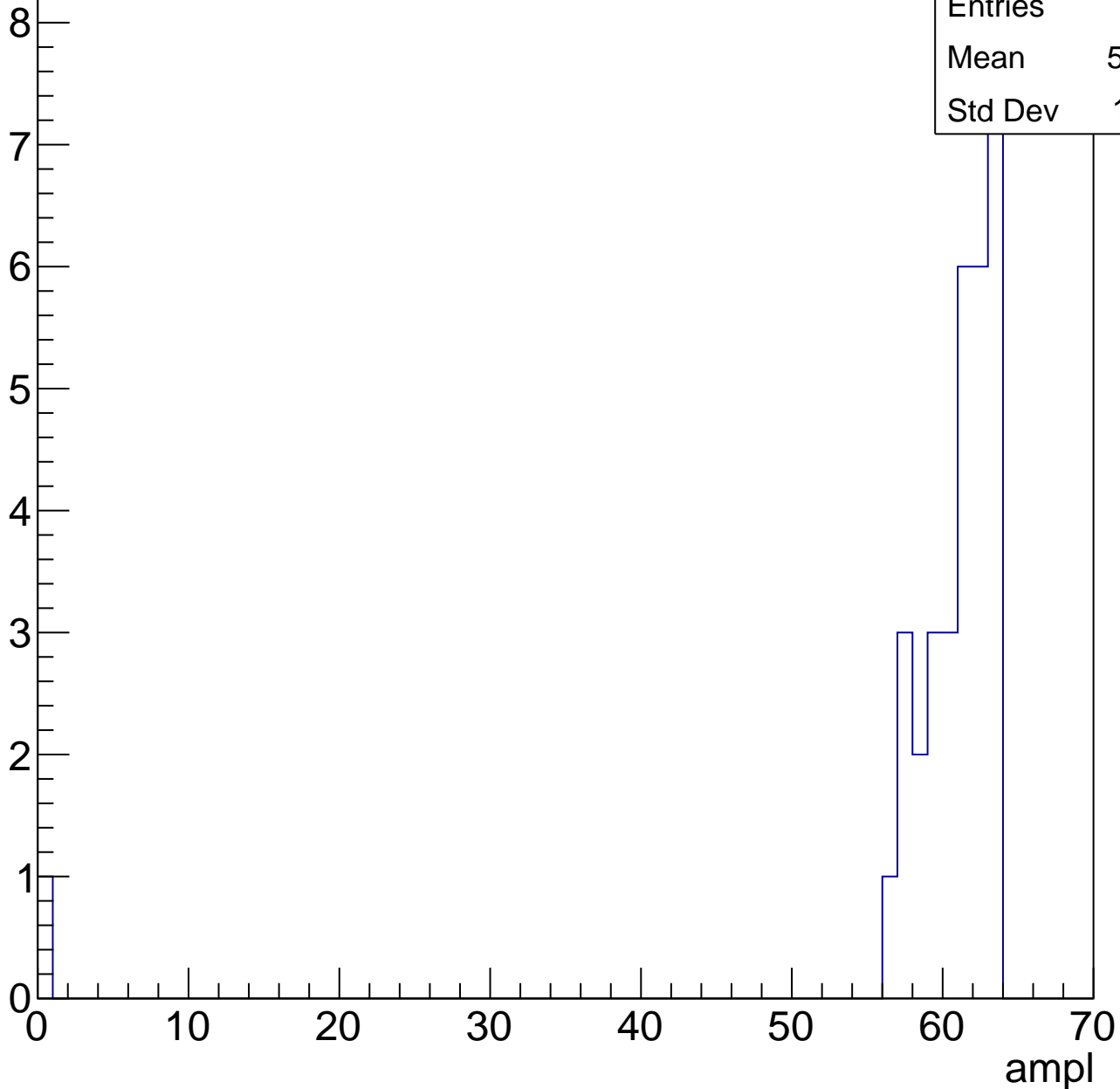


B1L100S, U12-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.85
Std Dev	10.61



B1L100S, U12-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

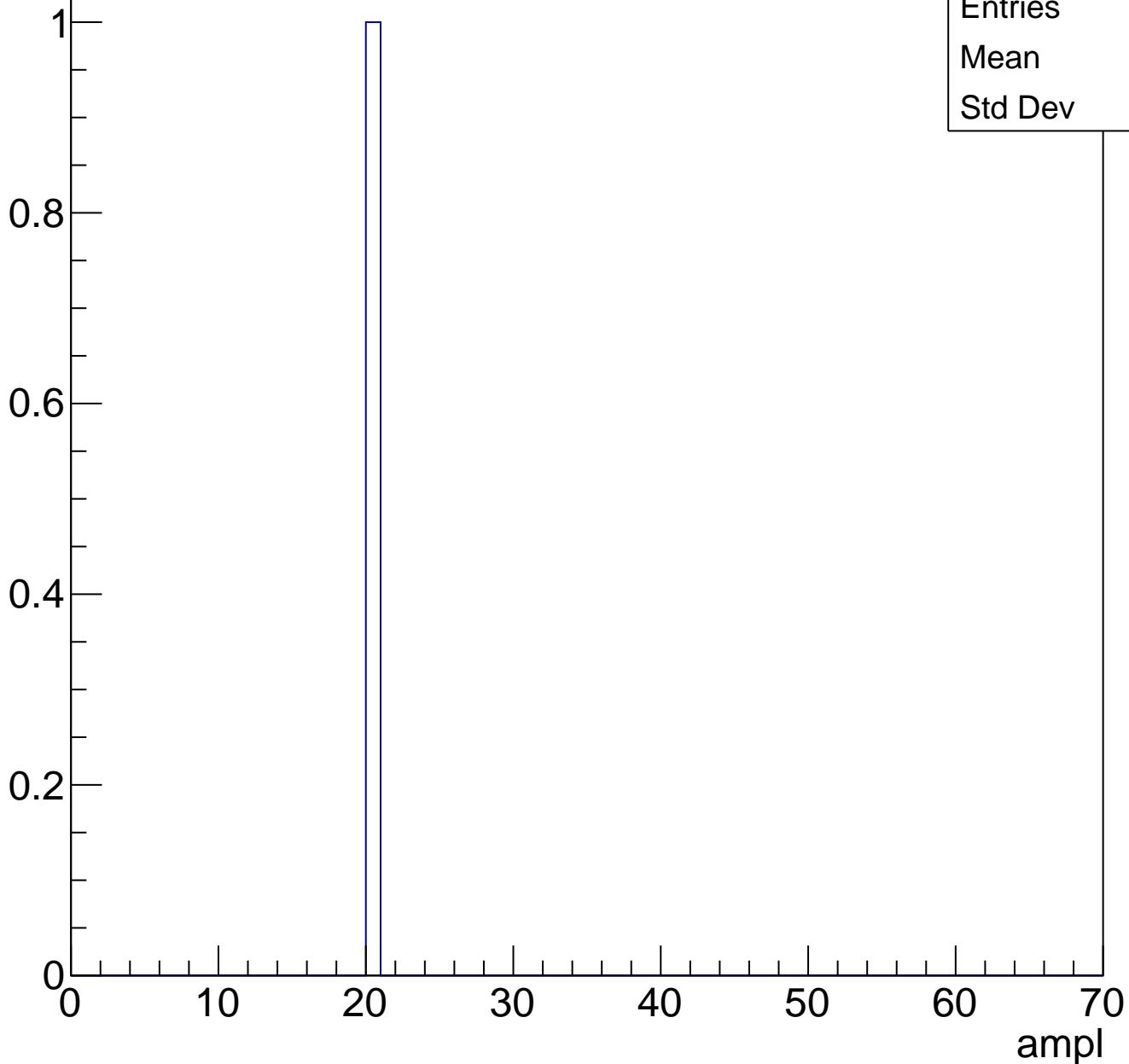
Entry



B1L100S, U12-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch100, adc0

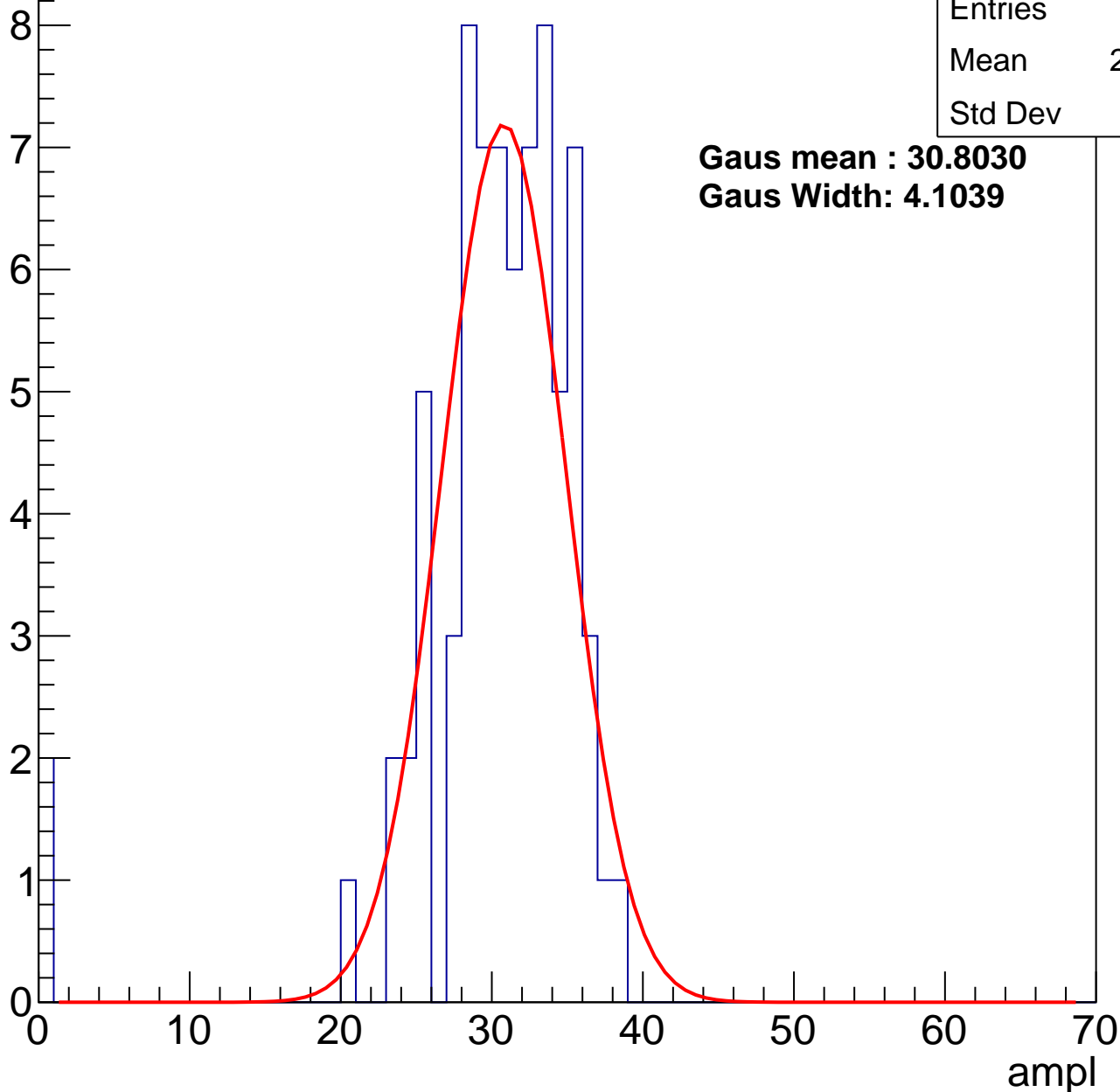
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	29.72
Std Dev	6.15

Gaus mean : 30.8030

Gaus Width: 4.1039



B1L100S, U12-ch100, adc1

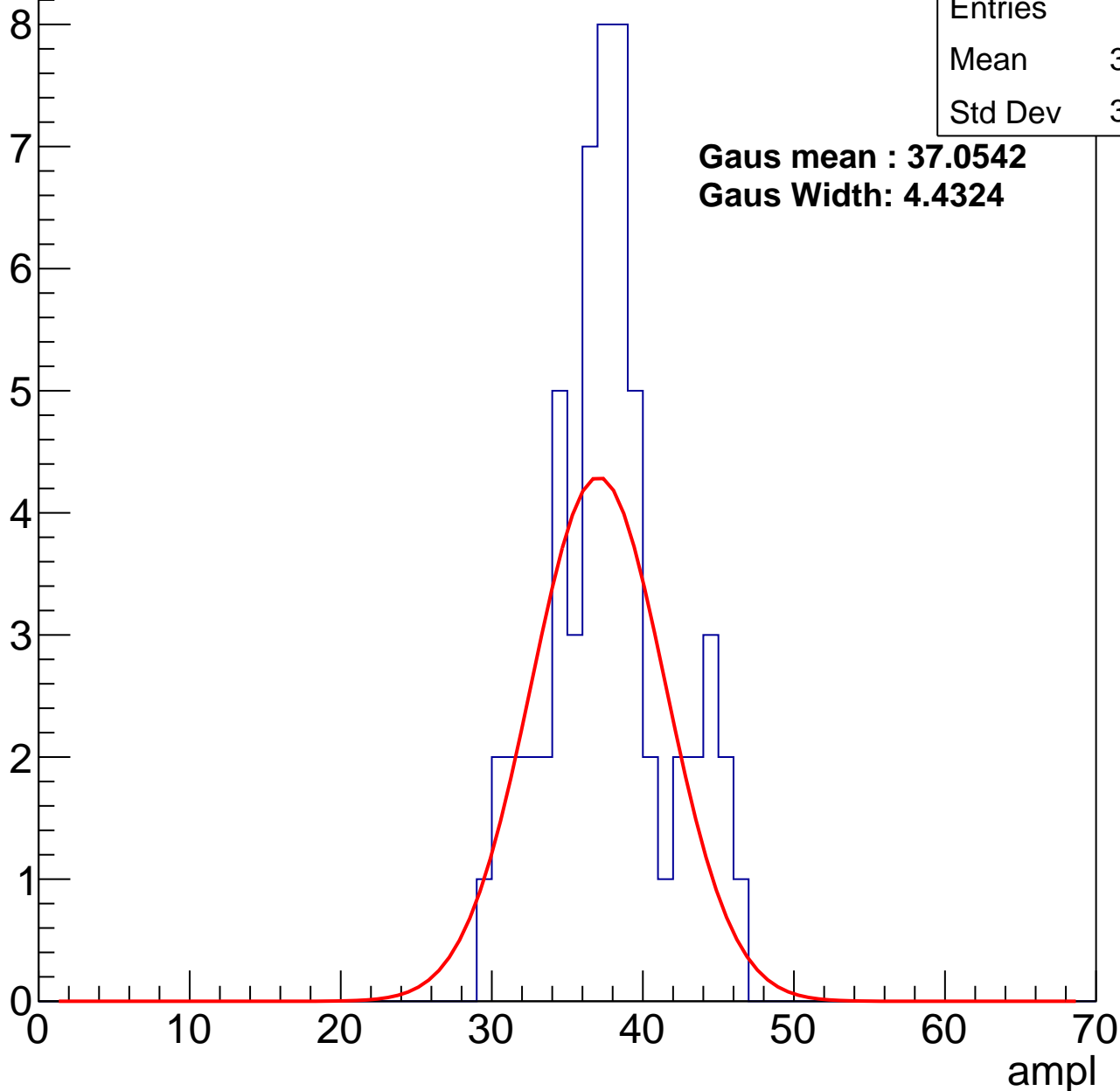
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	37.28
Std Dev	3.973

Gaus mean : 37.0542

Gaus Width: 4.4324



B1L100S, U12-ch100, adc2

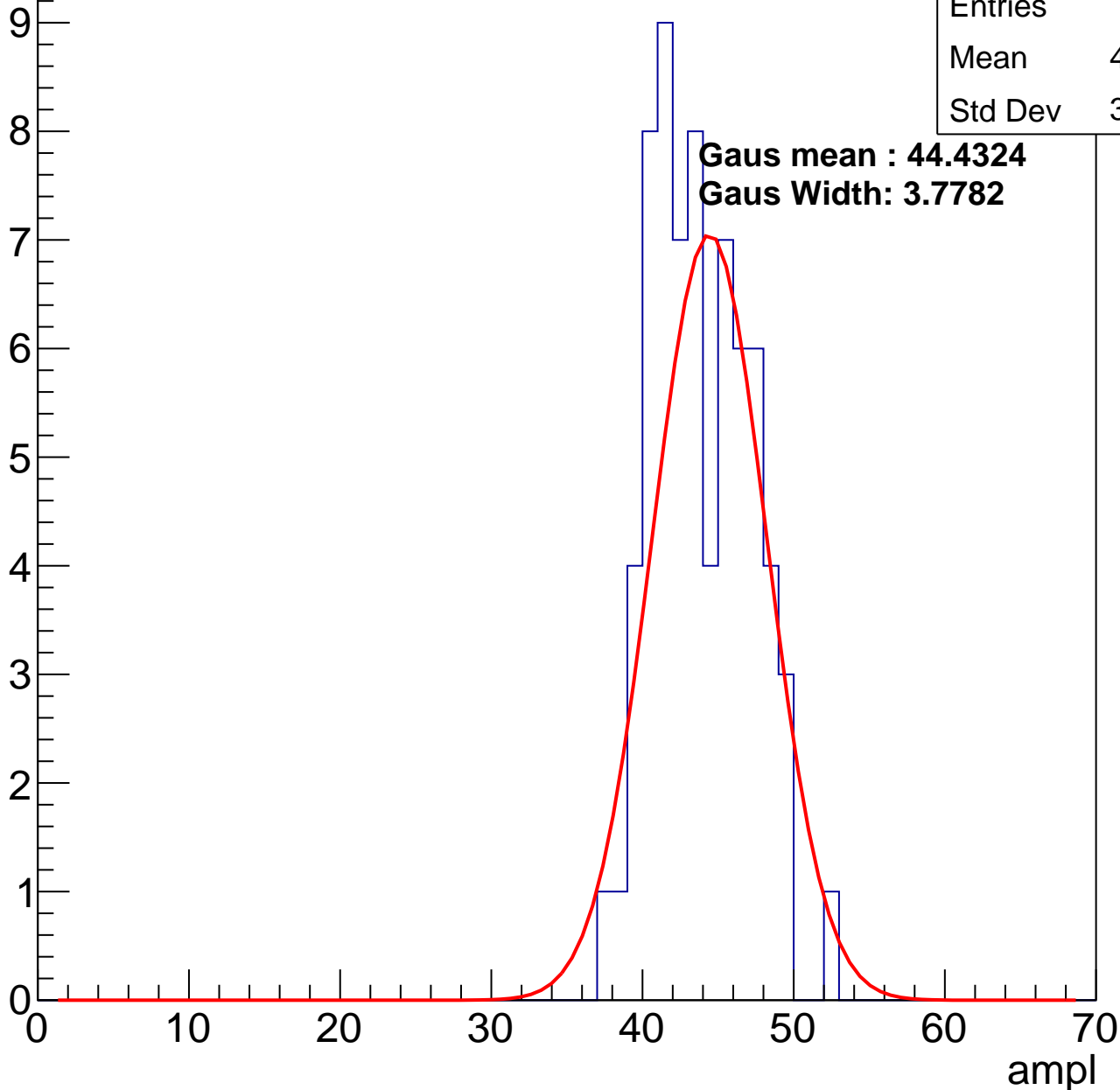
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	43.45
Std Dev	3.178

Gaus mean : 44.4324

Gaus Width: 3.7782



B1L100S, U12-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

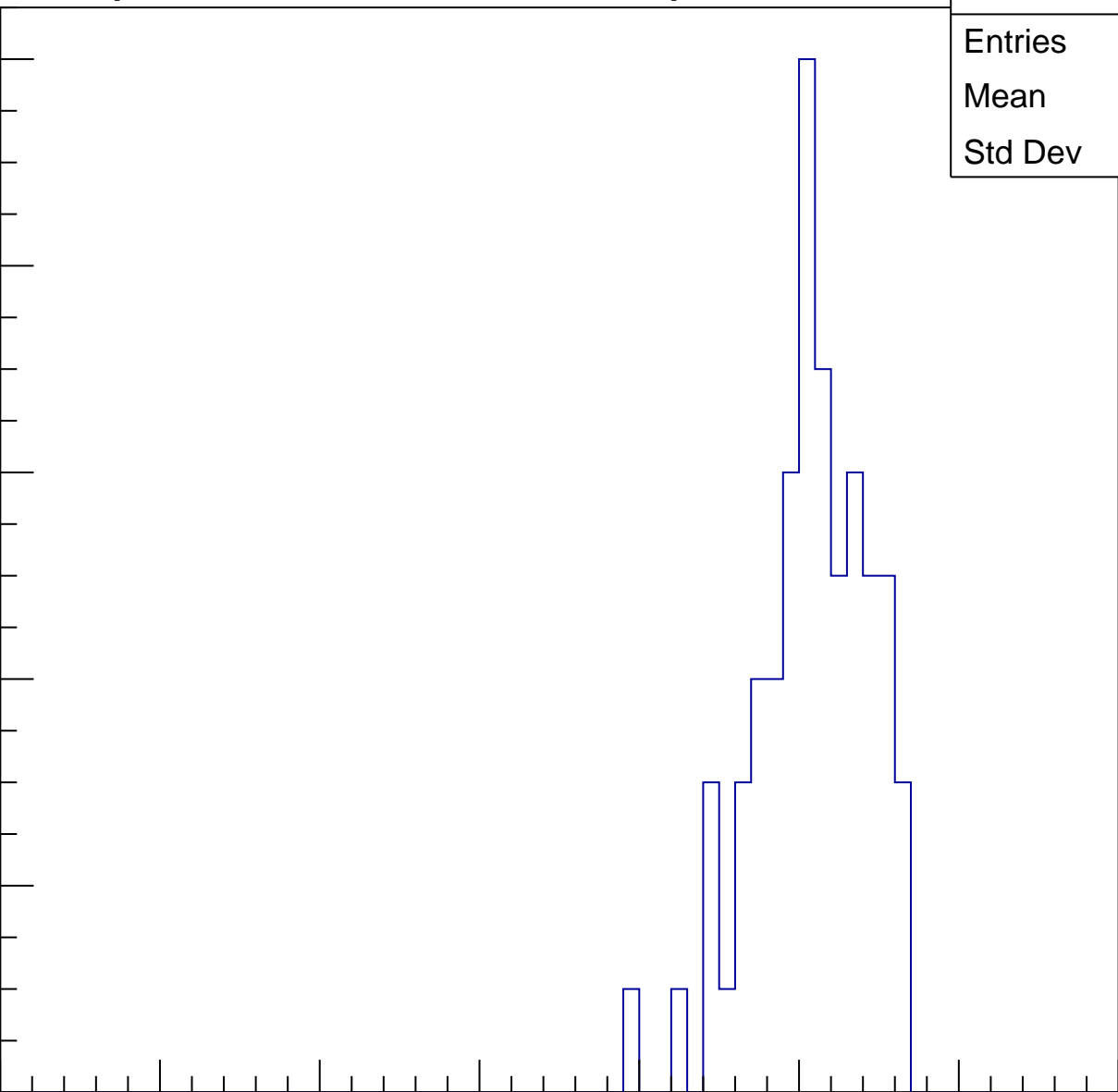
Entries	64
Mean	50.28
Std Dev	3.564

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch100, adc4

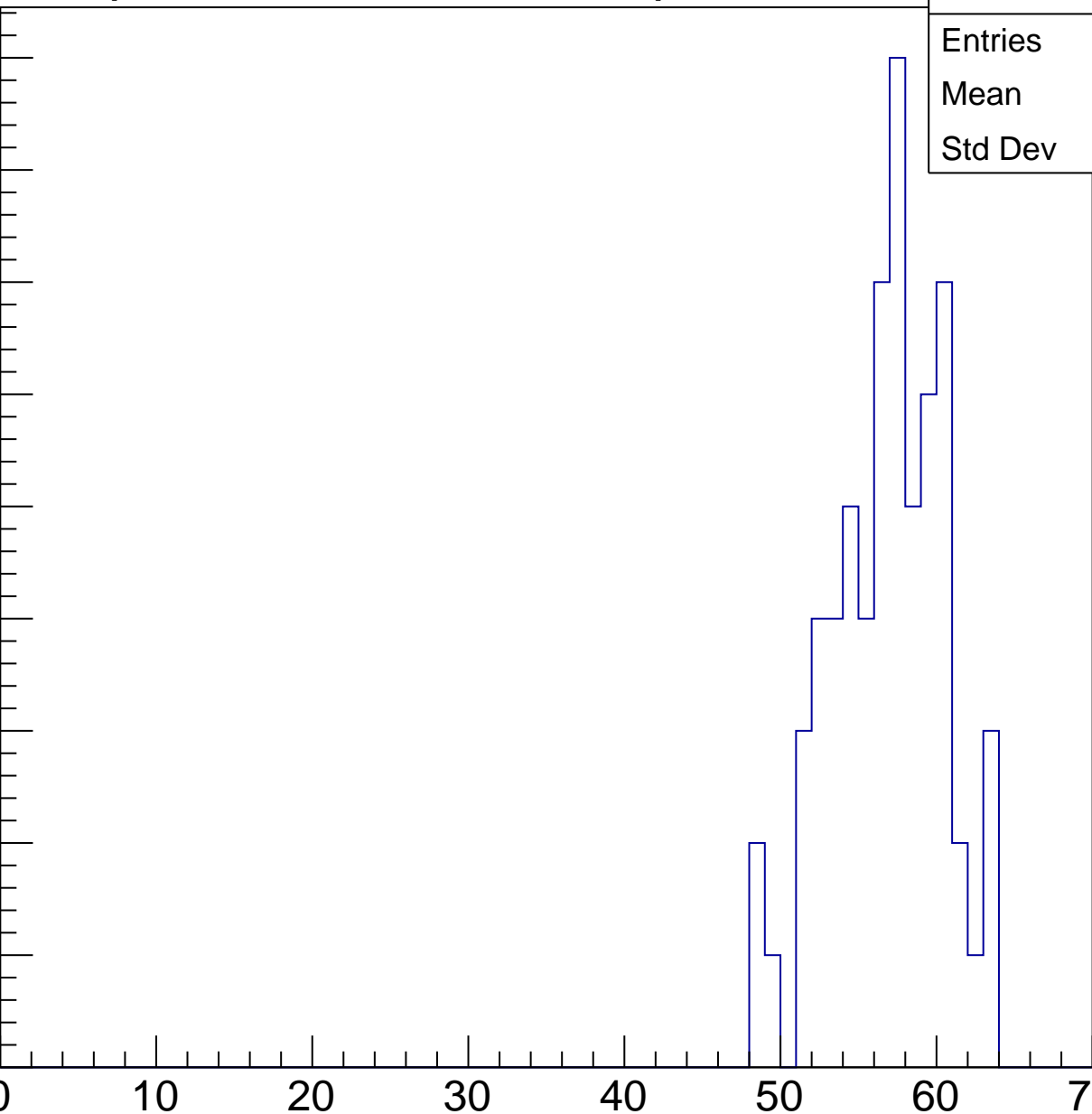
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	56.35
Std Dev	3.555

ampl

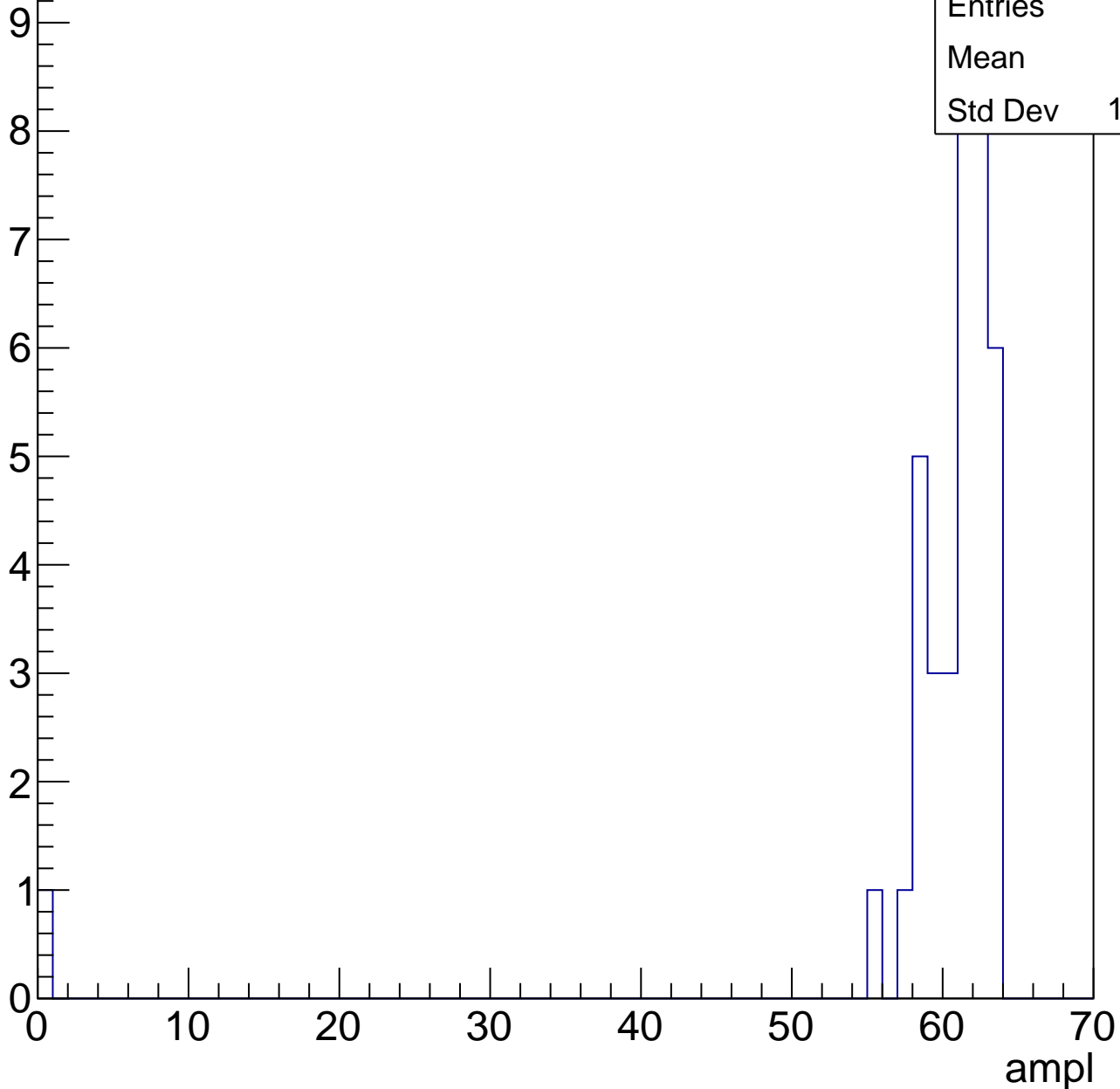


B1L100S, U12-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	37
Mean	59
Std Dev	10.02



B1L100S, U12-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	61
Std Dev	2

ampl

B1L100S, U12-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch101, adc0

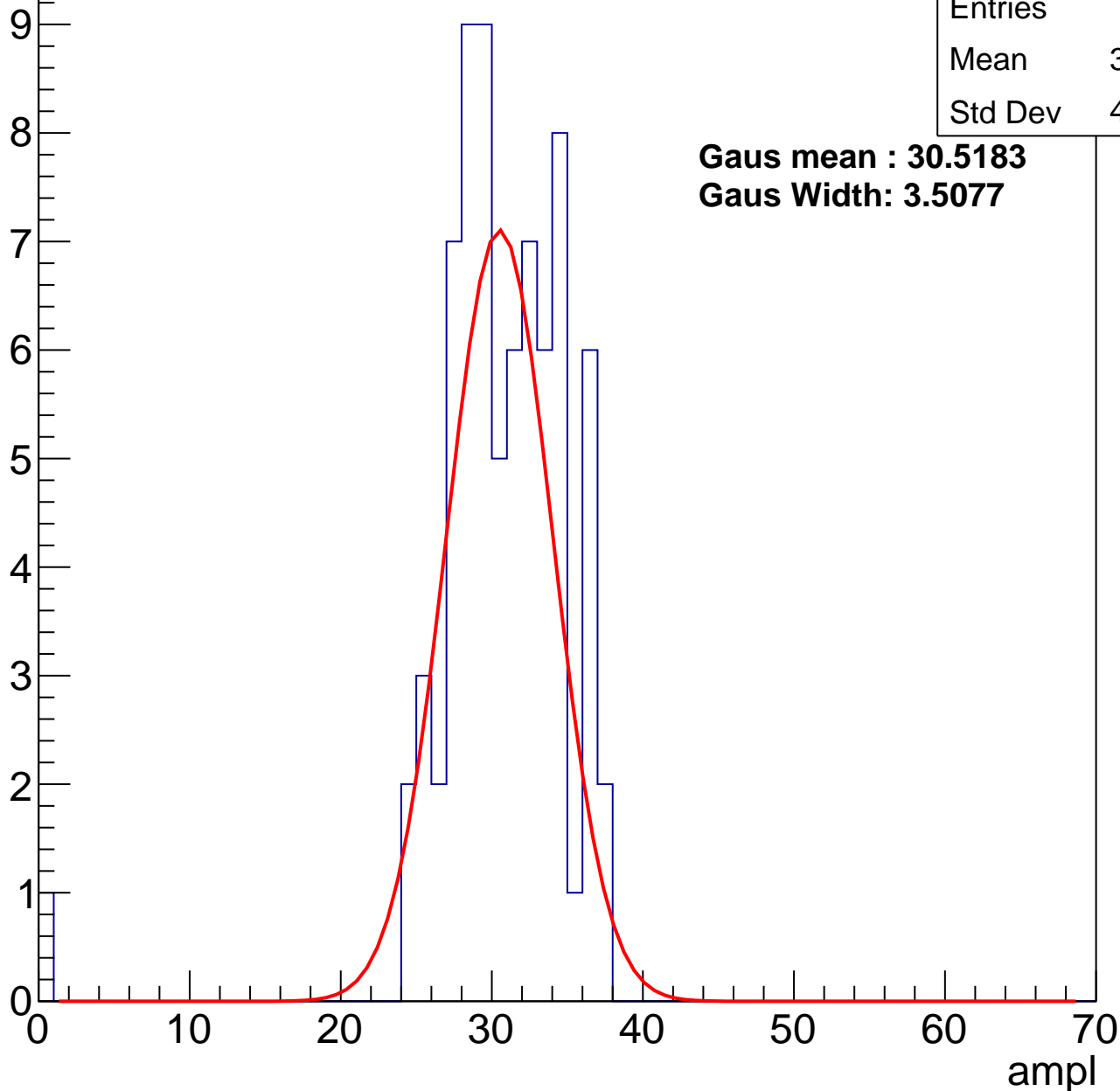
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	30.16
Std Dev	4.849

Gaus mean : 30.5183

Gaus Width: 3.5077



B1L100S, U12-ch101, adc1

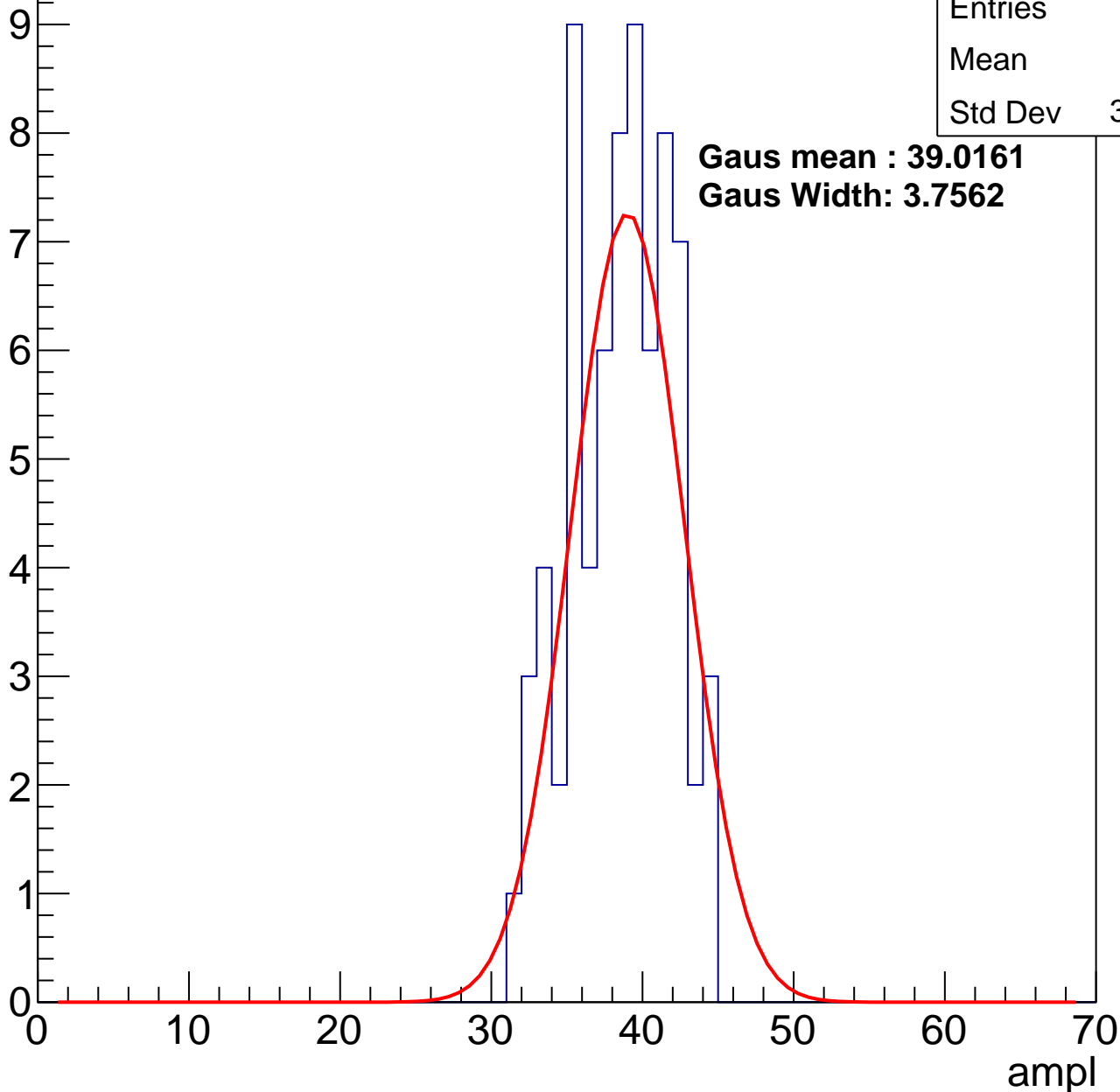
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	38.1
Std Dev	3.262

Gaus mean : 39.0161

Gaus Width: 3.7562



B1L100S, U12-ch101, adc2

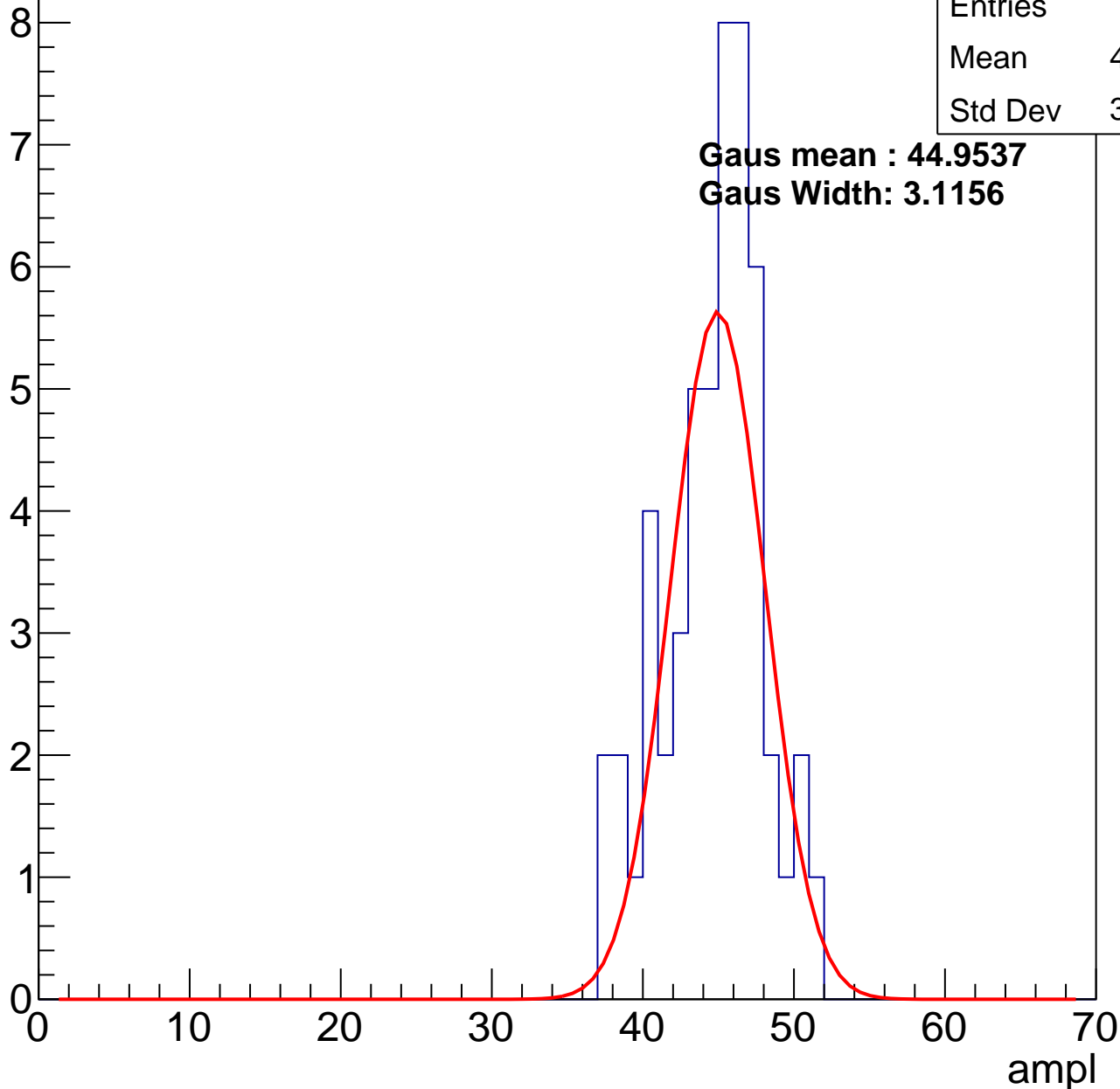
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	44.19
Std Dev	3.294

Gaus mean : 44.9537

Gaus Width: 3.1156

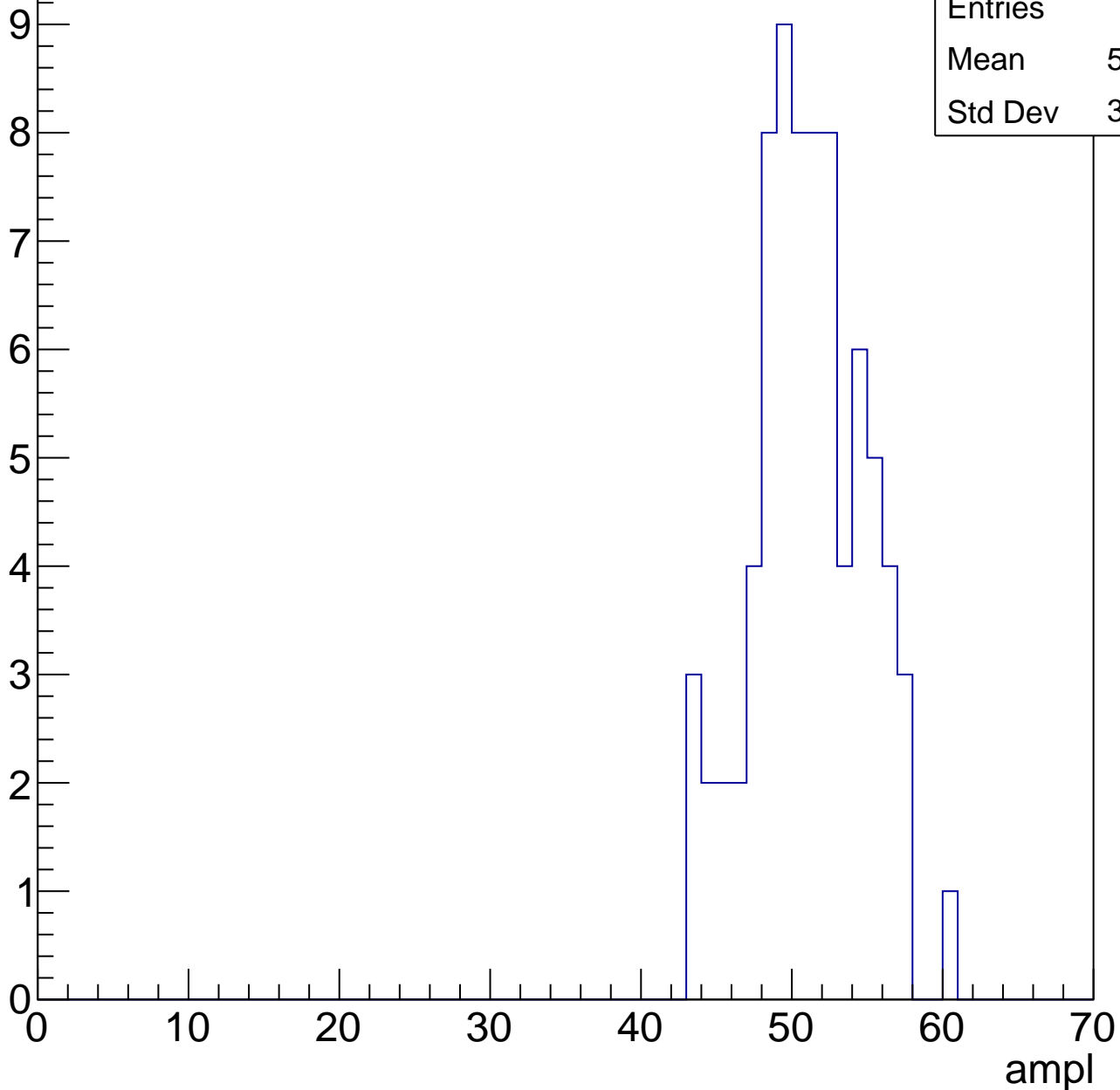


B1L100S, U12-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	50.68
Std Dev	3.659

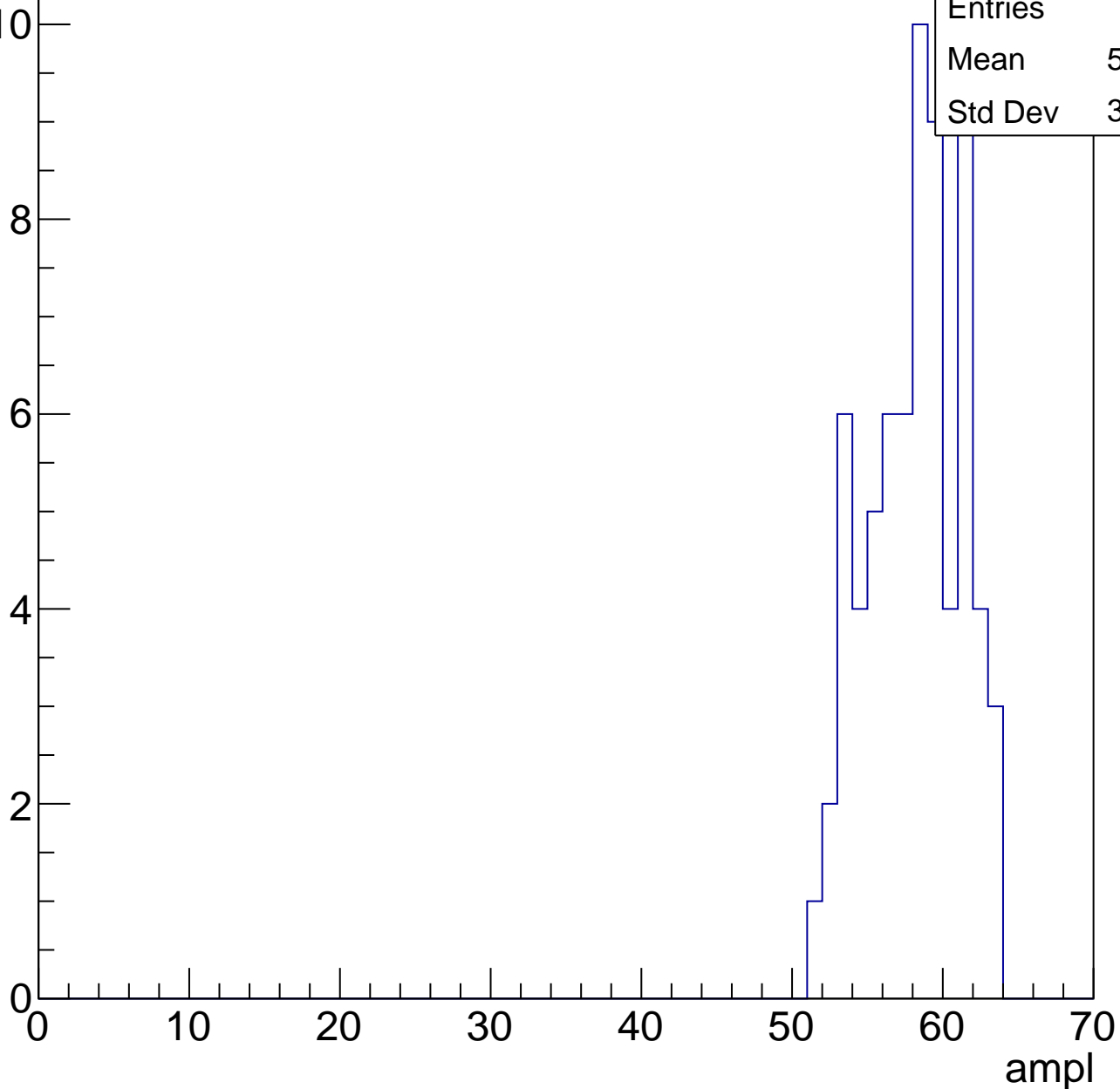


B1L100S, U12-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	57.67
Std Dev	3.063

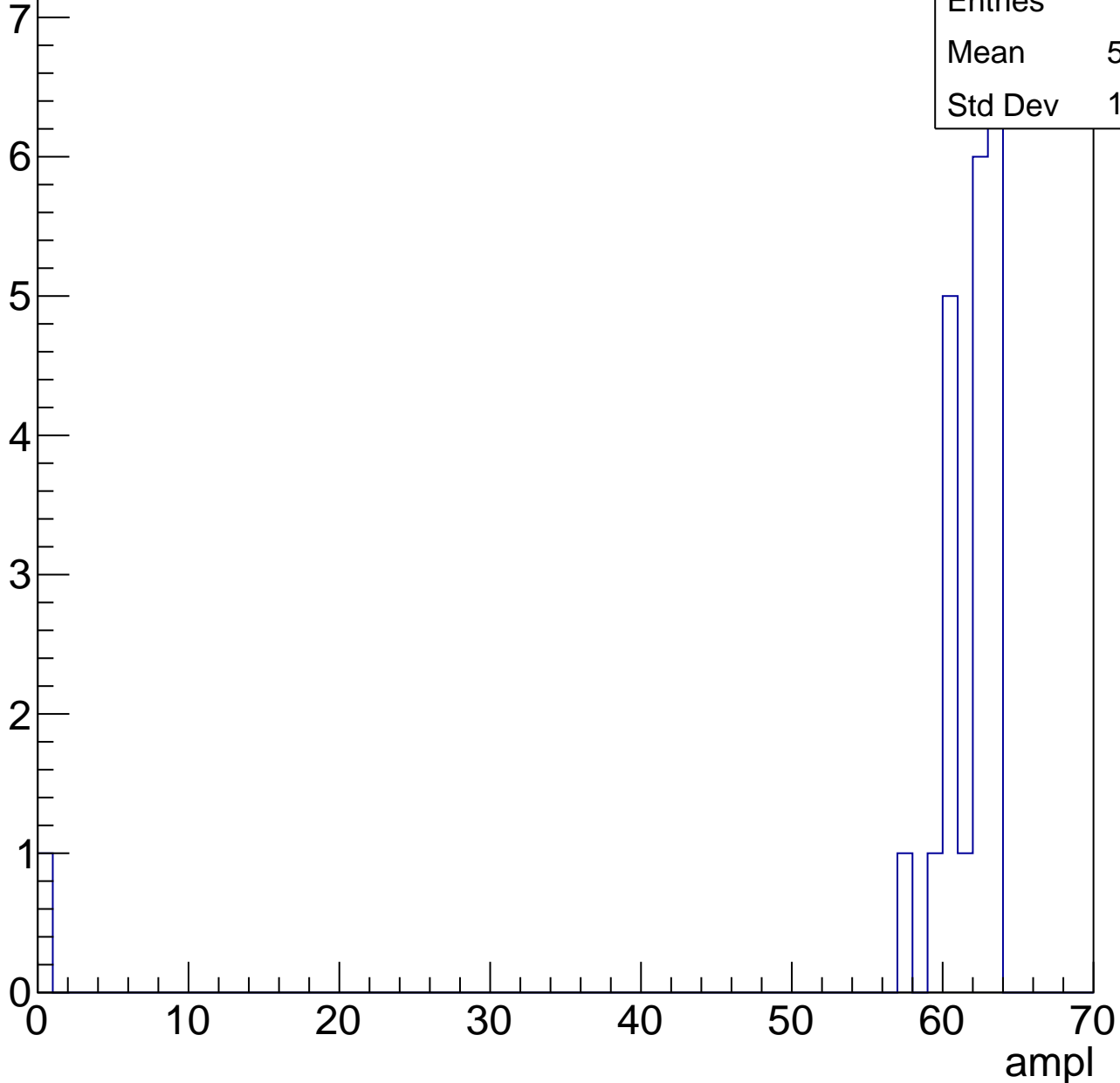


B1L100S, U12-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.64
Std Dev	12.89



B1L100S, U12-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	24
Std Dev	0

B1L100S, U12-ch102, adc0

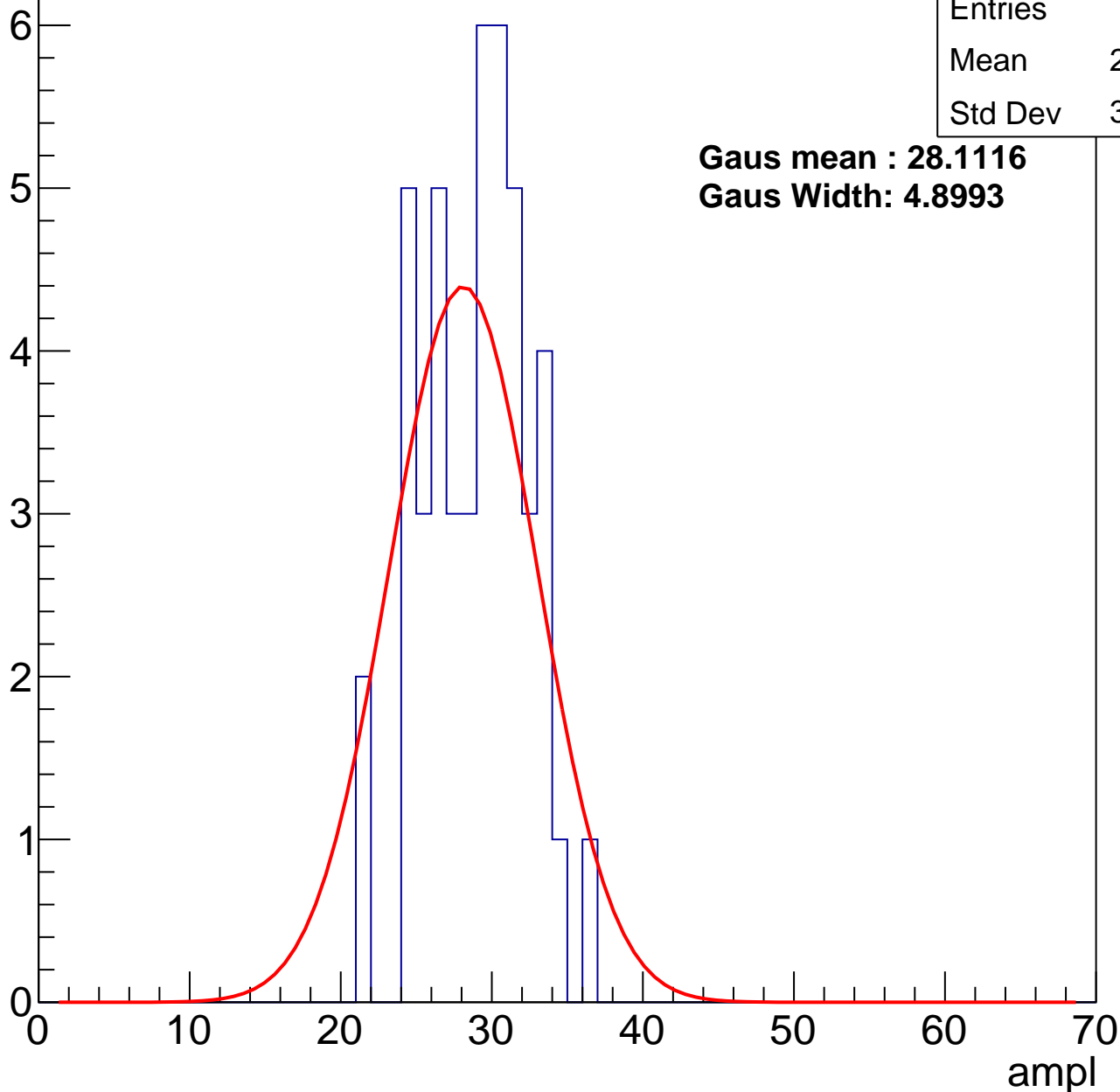
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	28.49
Std Dev	3.389

Gaus mean : 28.1116

Gaus Width: 4.8993



B1L100S, U12-ch102, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	35.54
Std Dev	3.775

Gaus mean : 36.0188

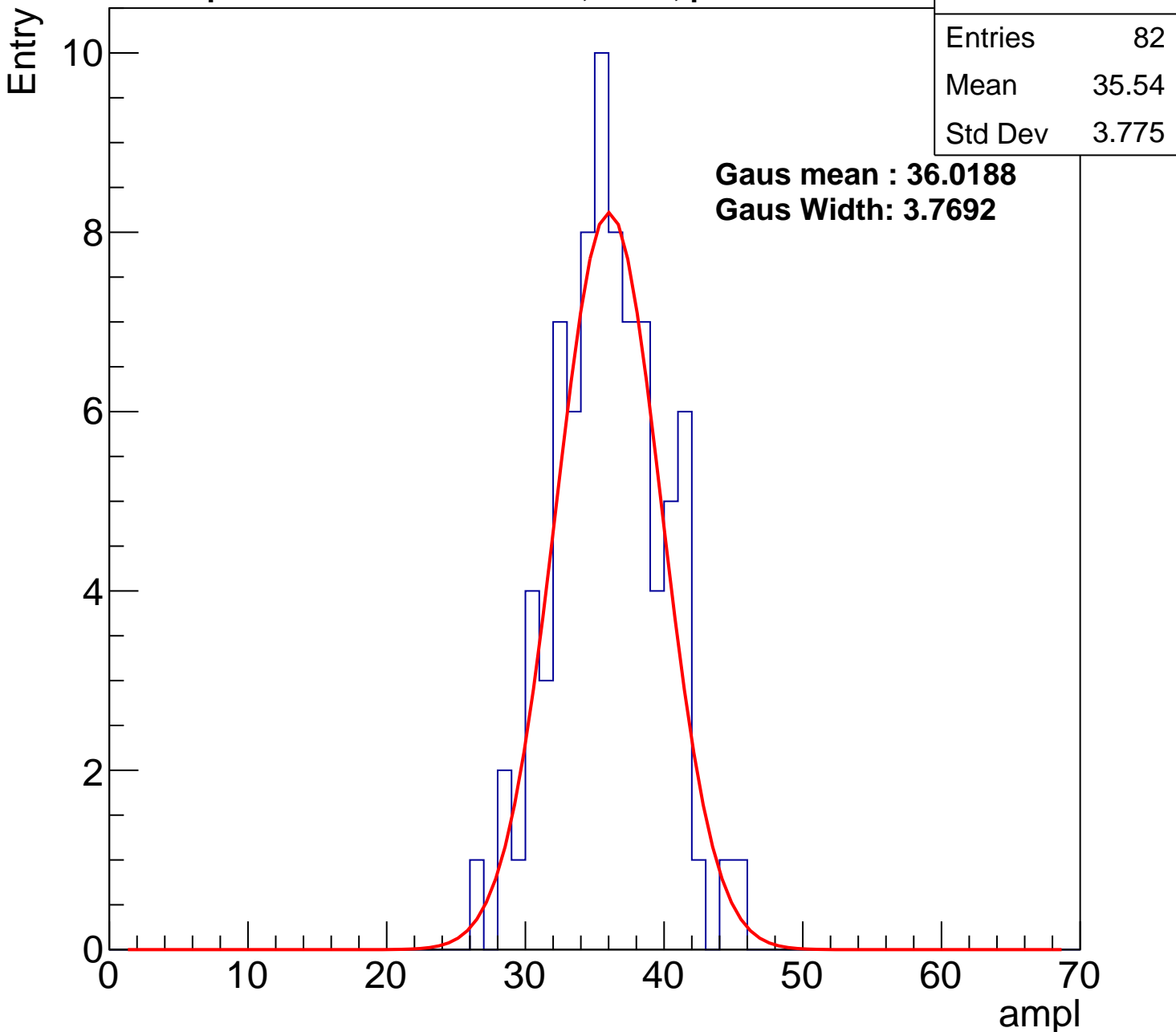
Gaus Width: 3.7692

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch102, adc2

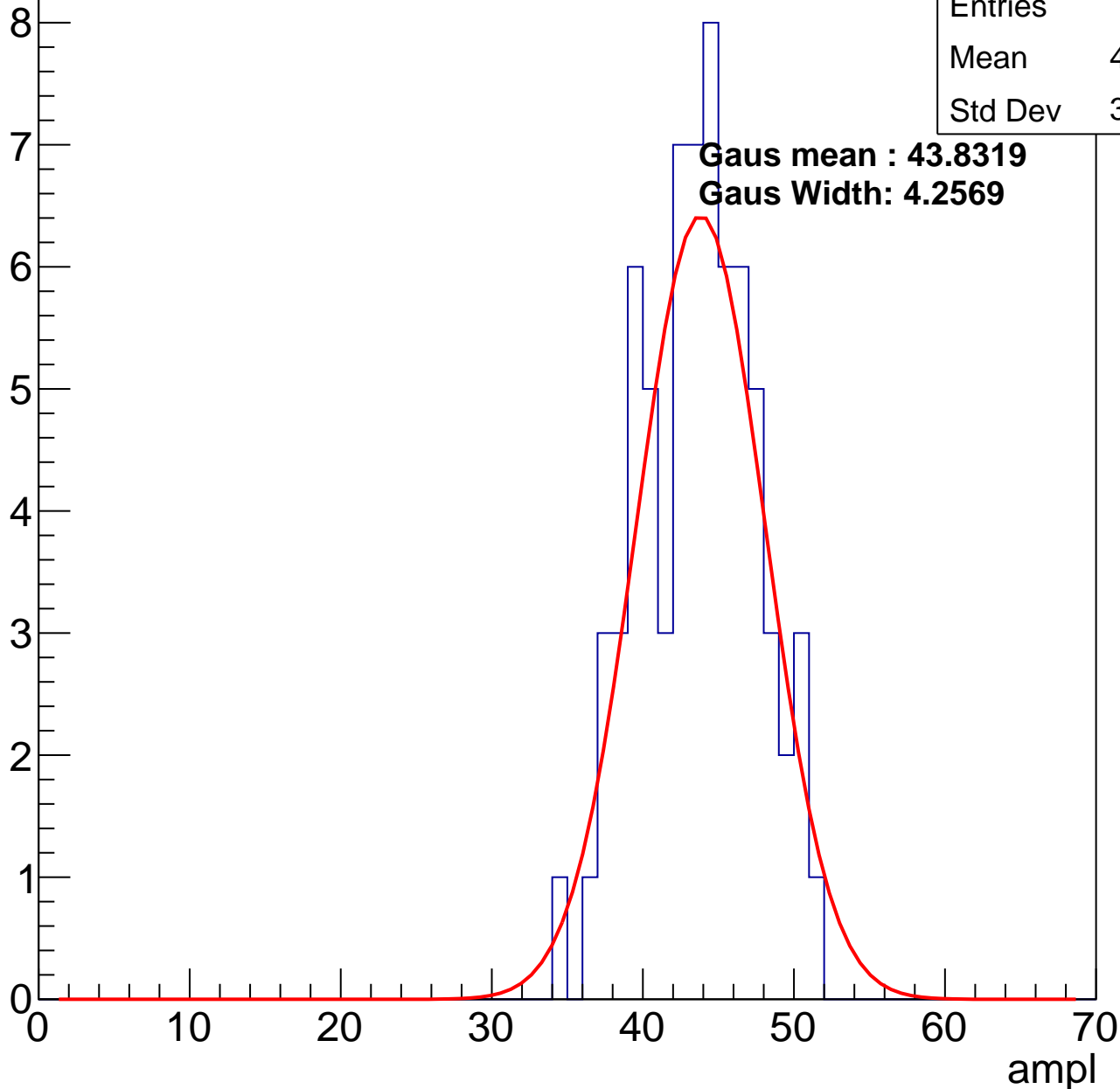
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	43.19
Std Dev	3.769

Gaus mean : 43.8319

Gaus Width: 4.2569

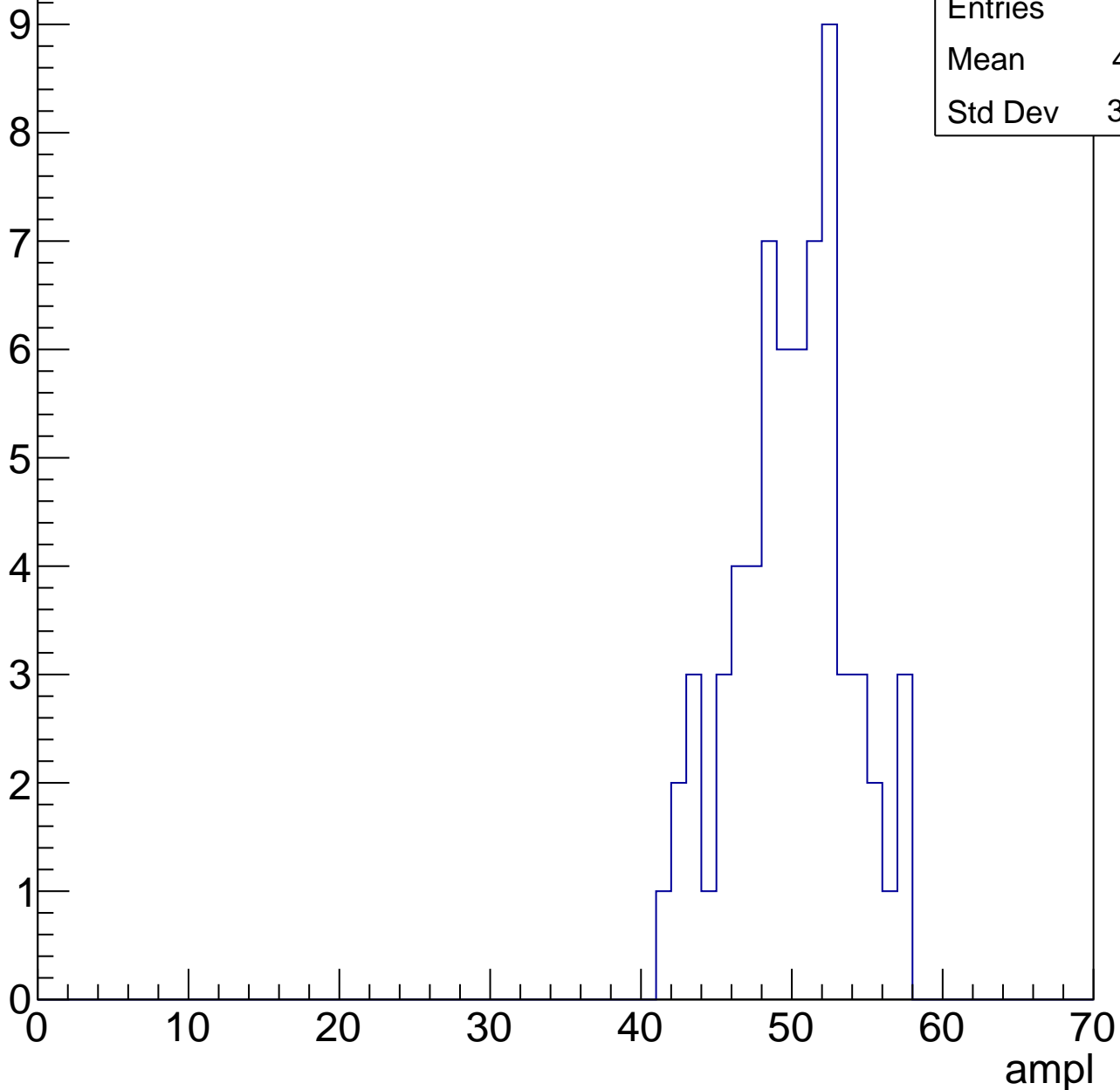


B1L100S, U12-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.51
Std Dev	3.795

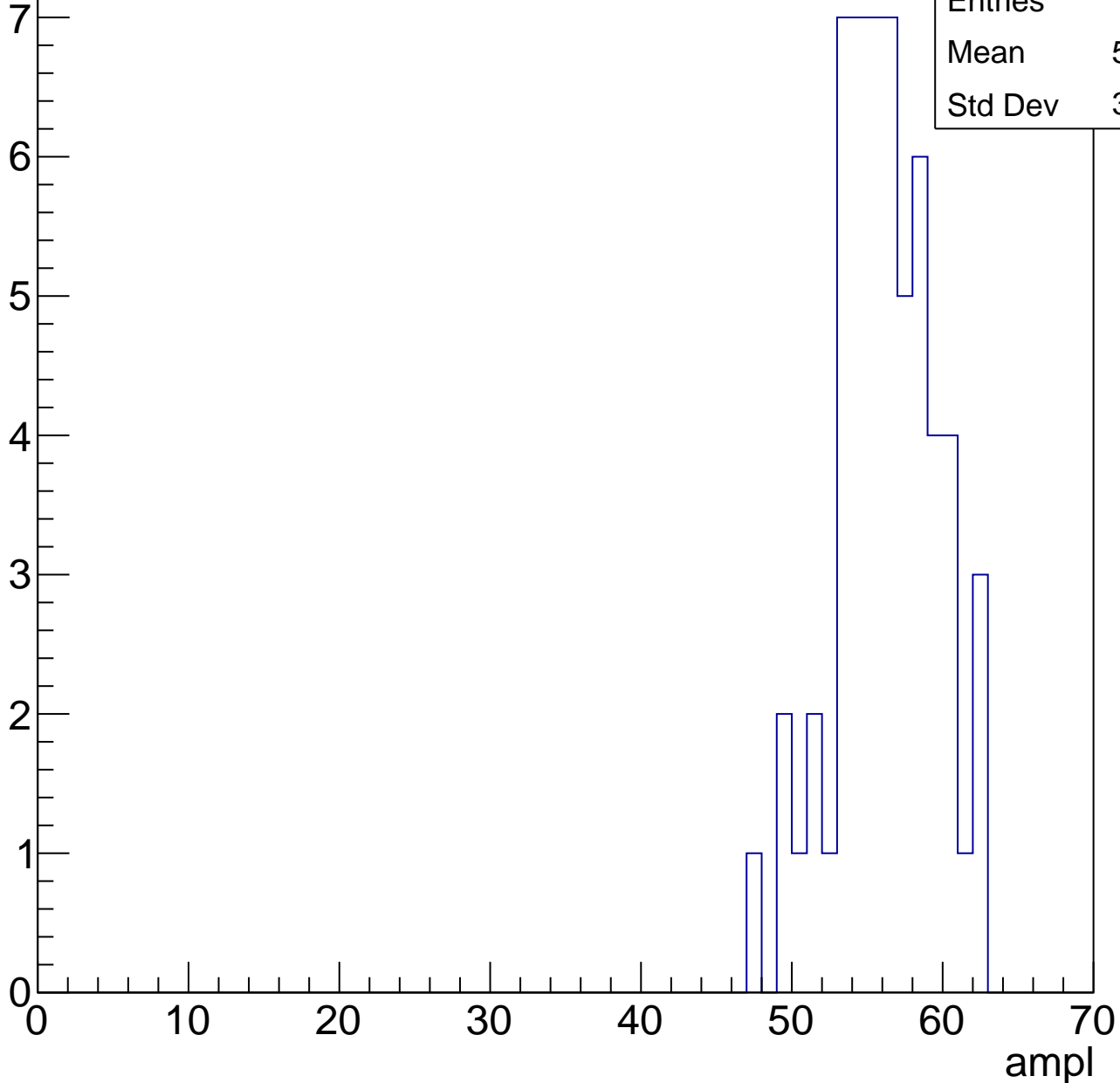


B1L100S, U12-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	55.71
Std Dev	3.311

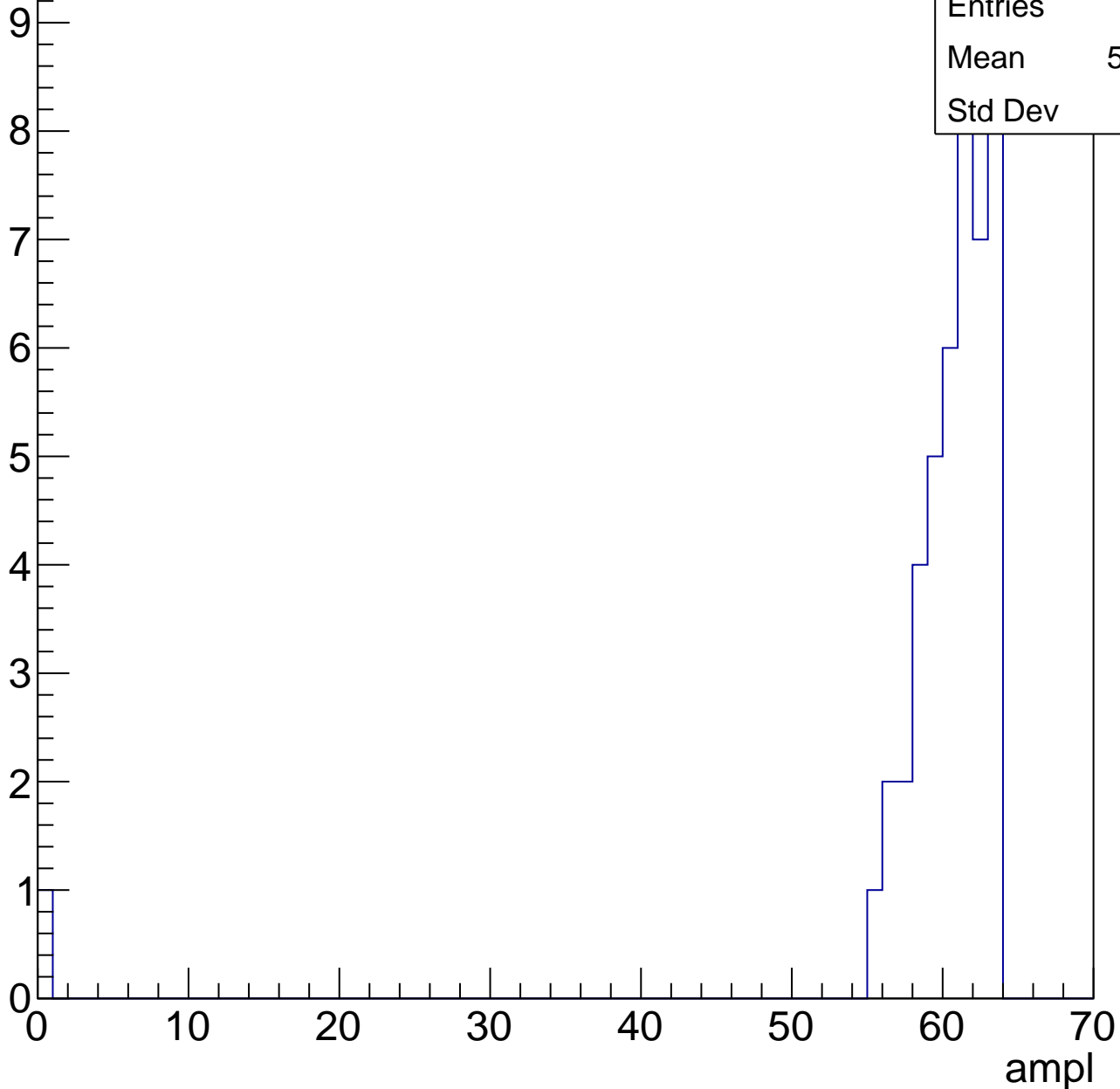


B1L100S, U12-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	59.09
Std Dev	9.06



B1L100S, U12-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

ampl

Entries	1
Mean	59
Std Dev	0

B1L100S, U12-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch103, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	52
Mean	28.29
Std Dev	6.271

Gaus mean : 30.0558

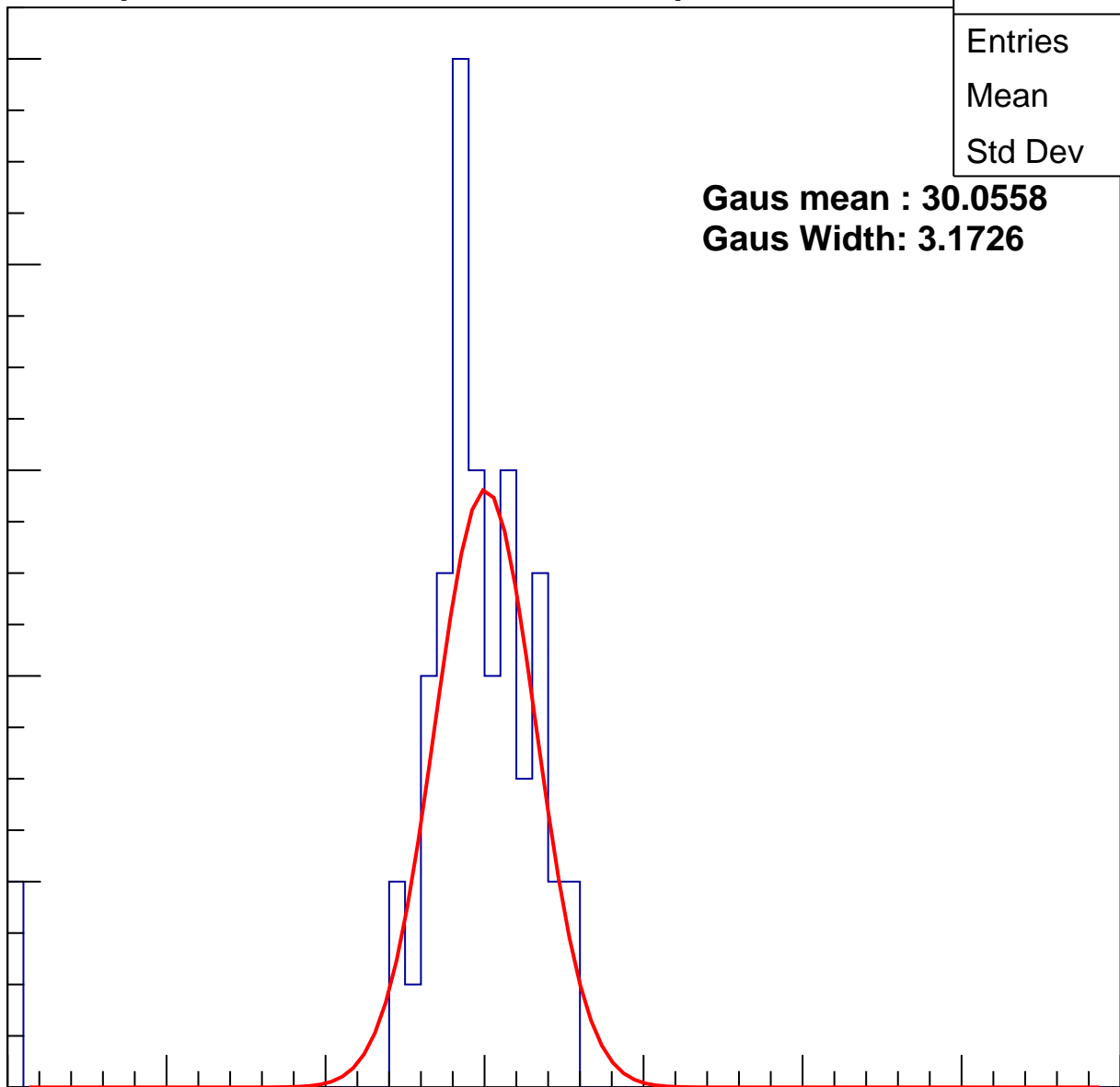
Gaus Width: 3.1726

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	36.16
Std Dev	3.702

Gaus mean : 36.3384

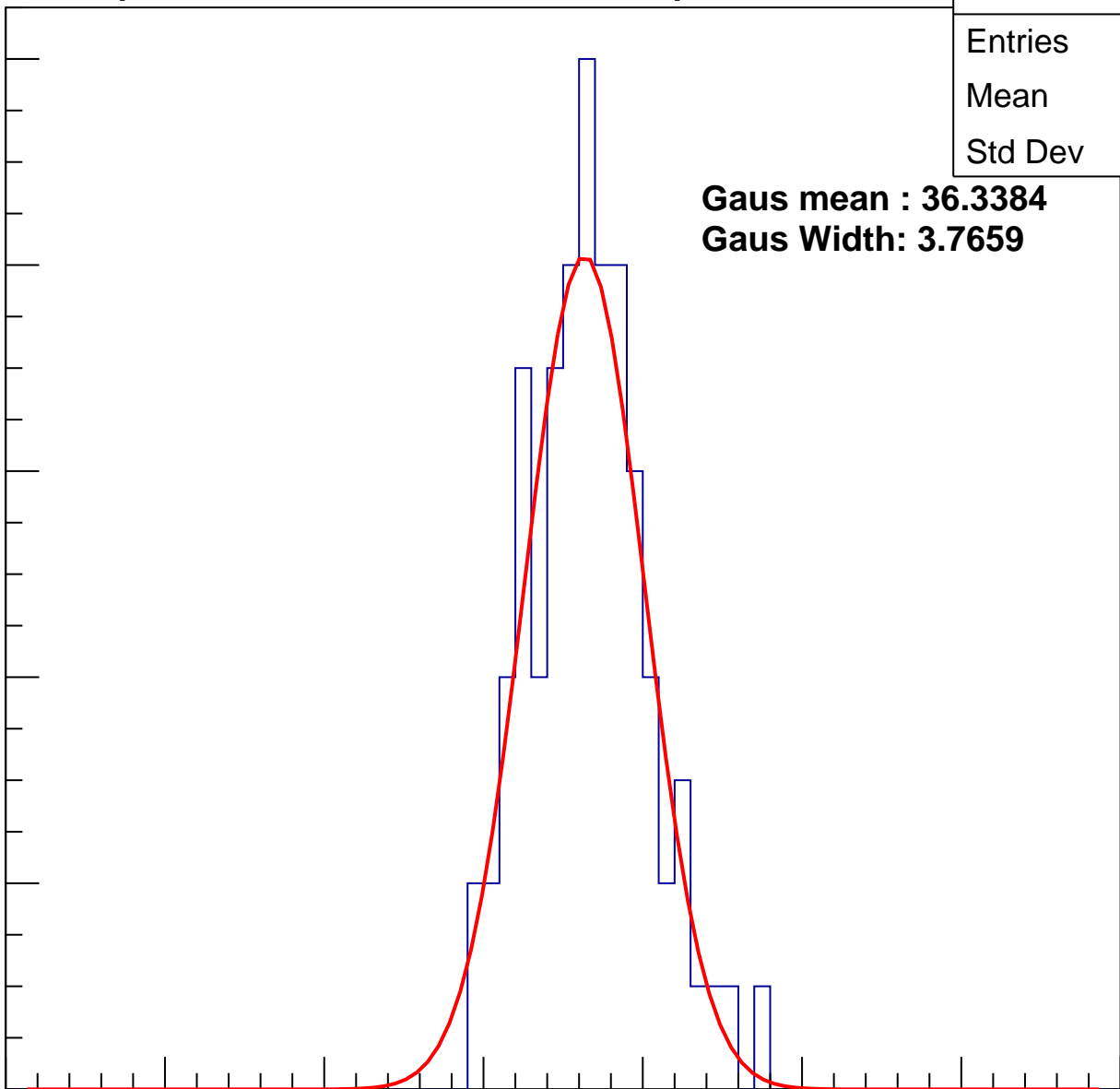
Gaus Width: 3.7659

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch103, adc2

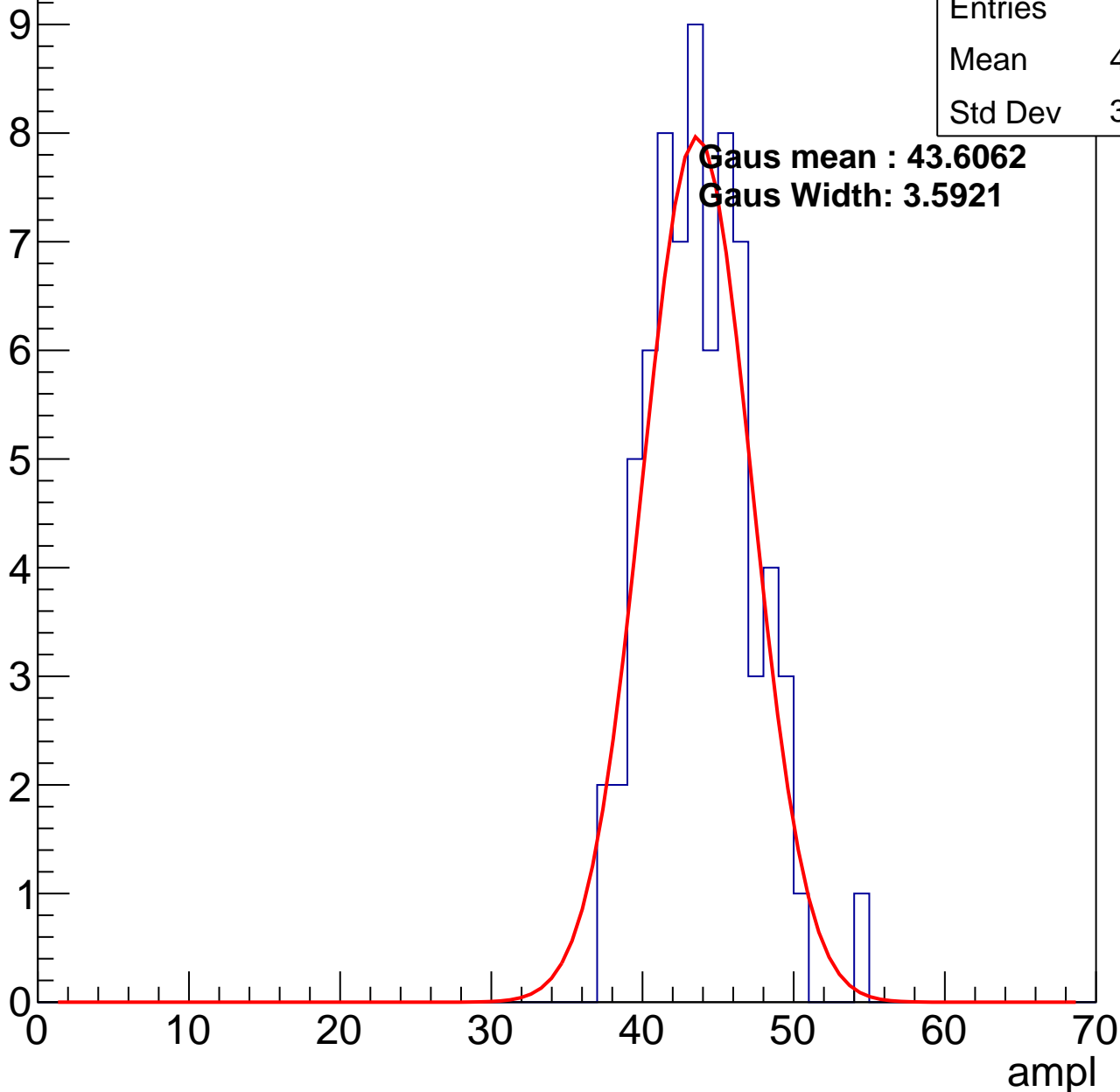
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.39
Std Dev	3.356

Gaus mean : 43.6062

Gaus Width: 3.5921



B1L100S, U12-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

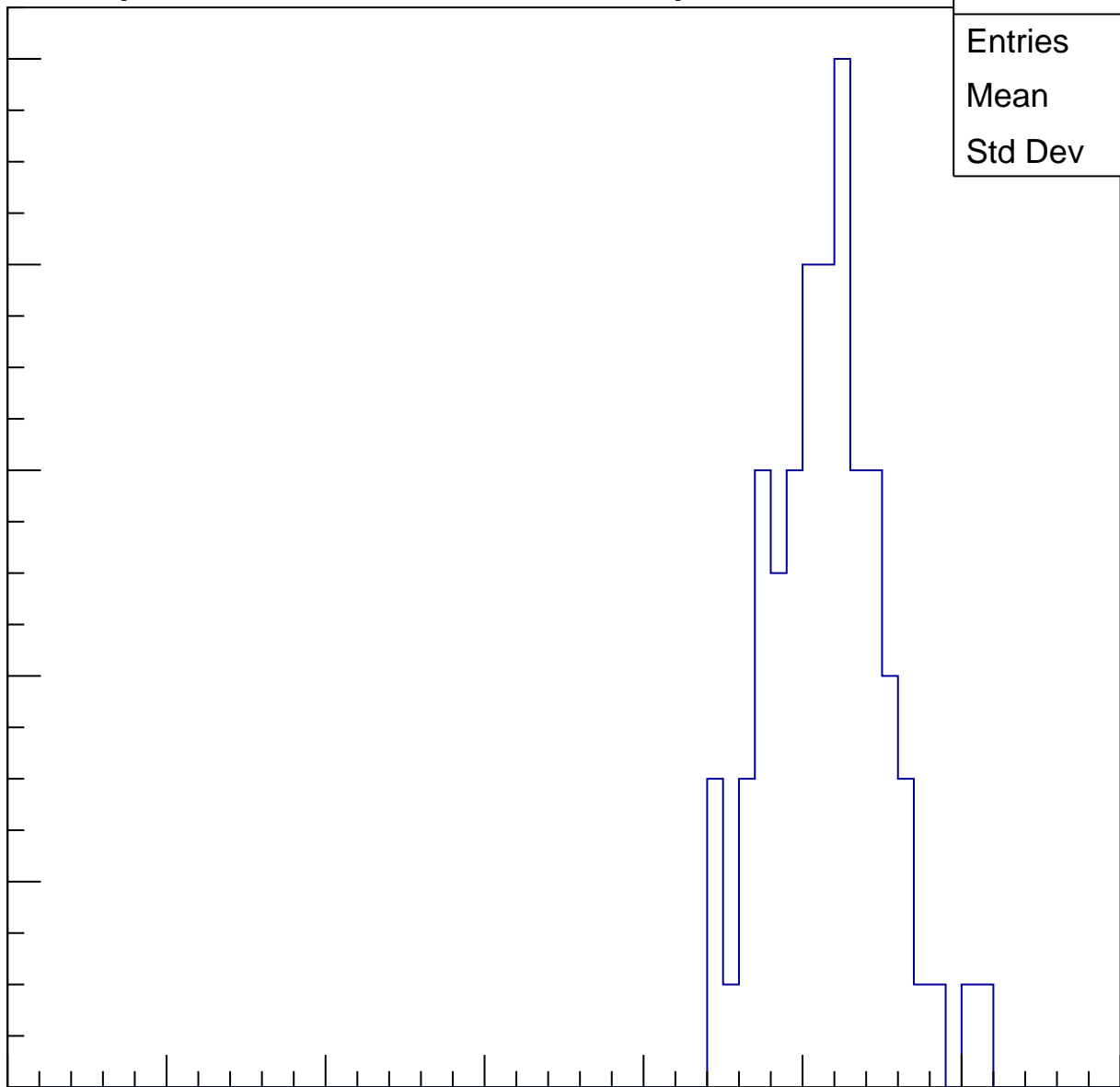
Entries	73
Mean	51.03
Std Dev	3.558

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

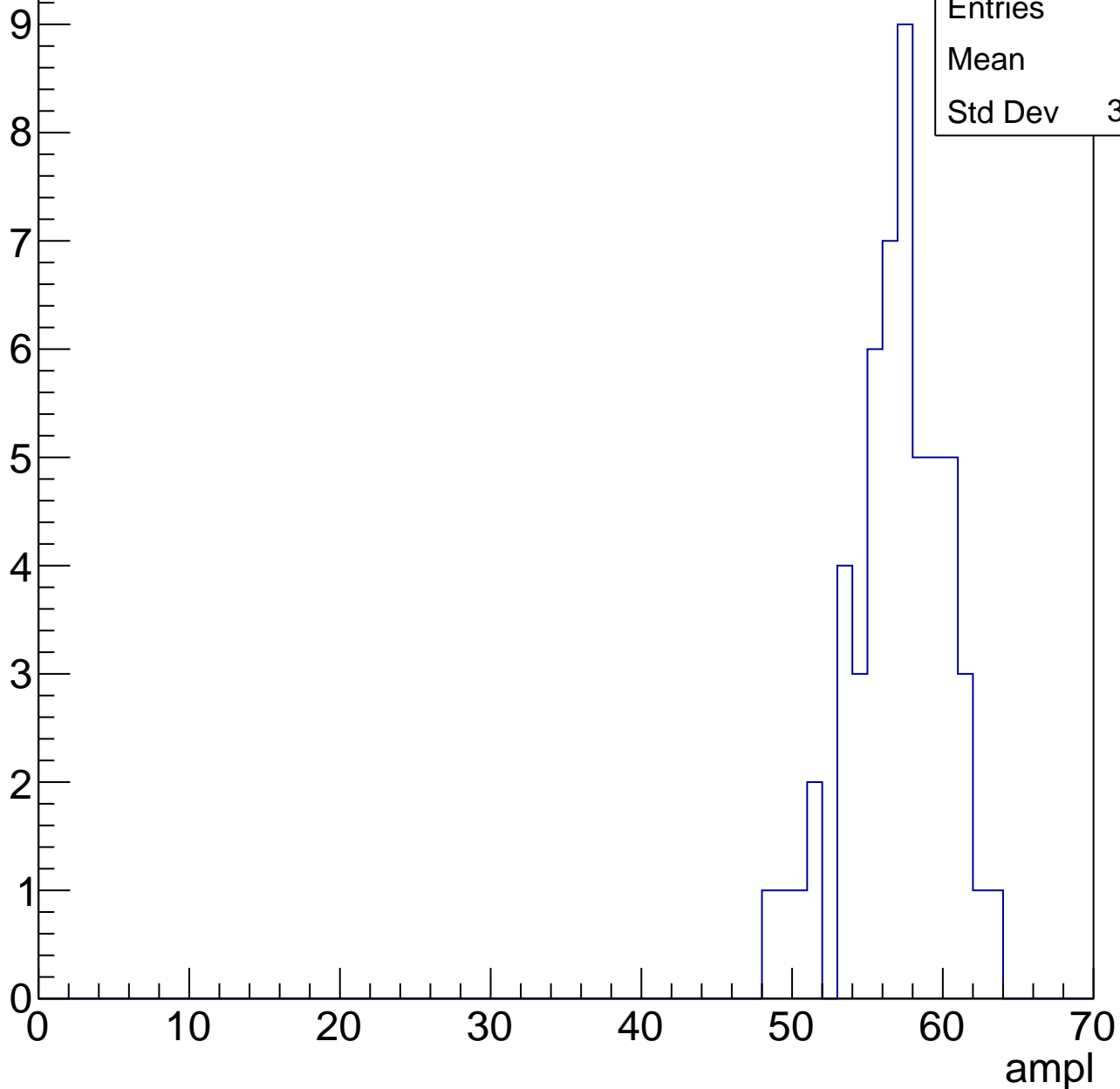
ampl



B1L100S, U12-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



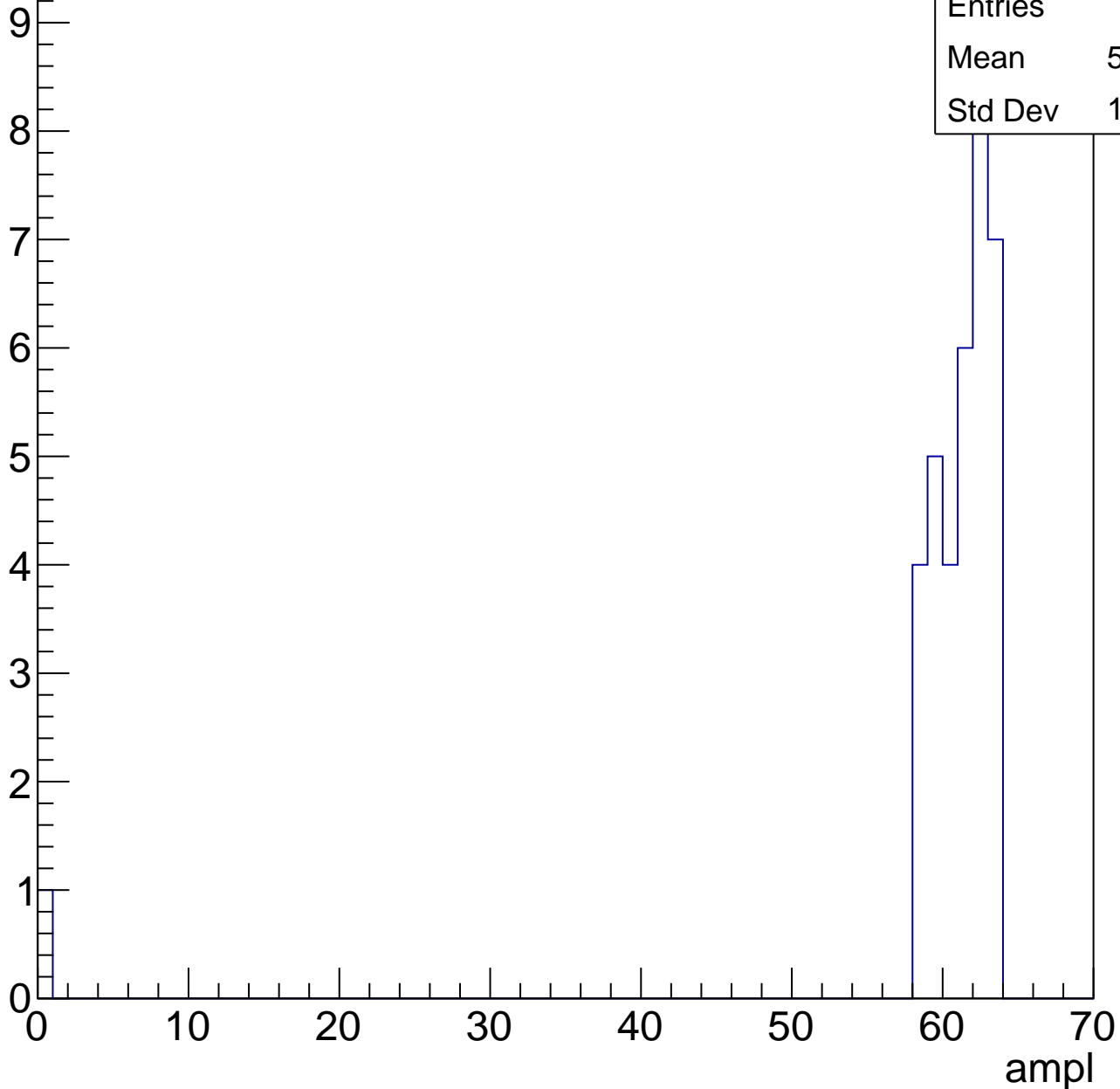
Entries	54
Mean	56.5
Std Dev	3.202

B1L100S, U12-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	59.22
Std Dev	10.14



B1L100S, U12-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L100S, U12-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch104, adc0

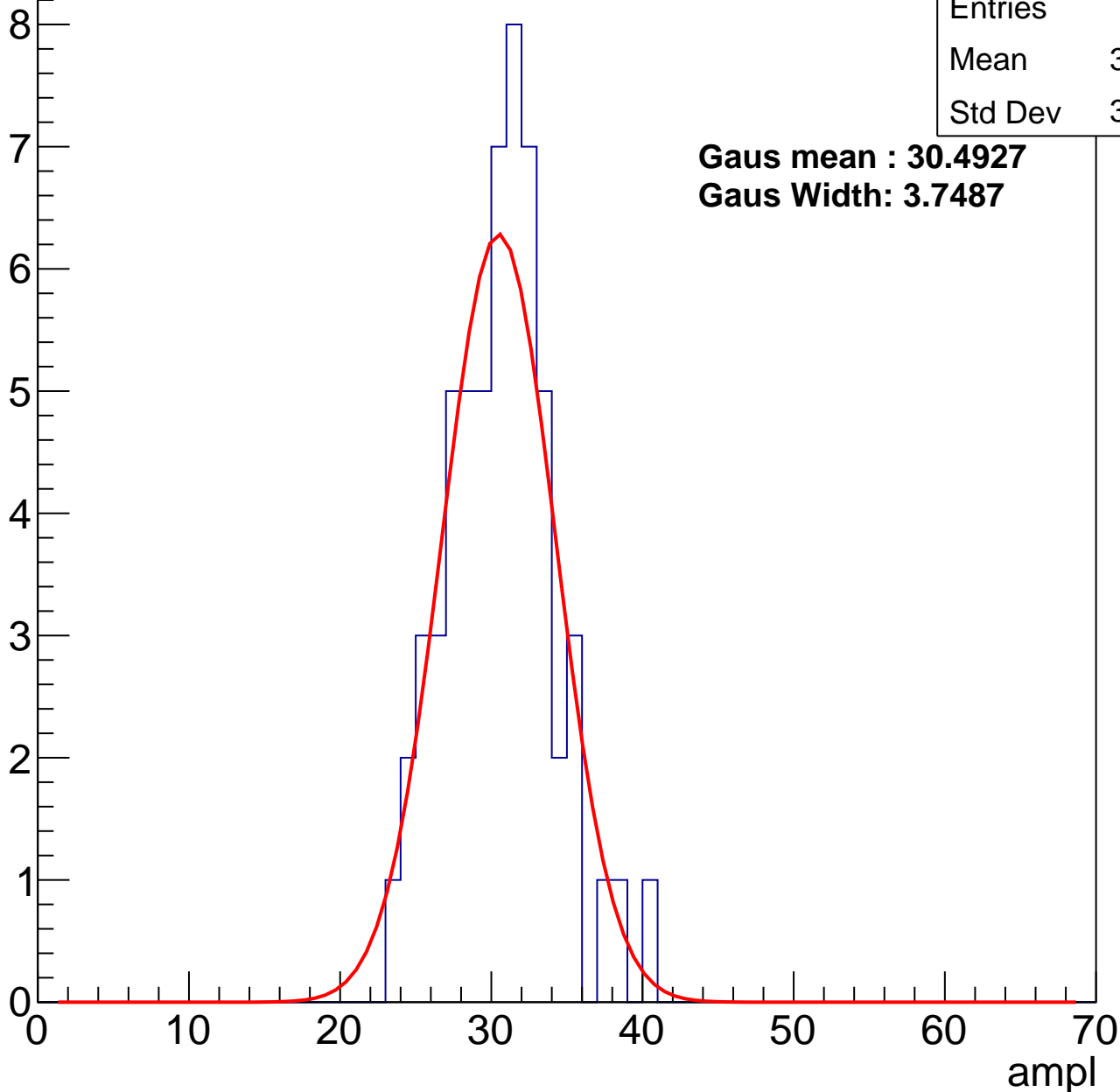
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	30.15
Std Dev	3.478

Gaus mean : 30.4927

Gaus Width: 3.7487



B1L100S, U12-ch104, adc1

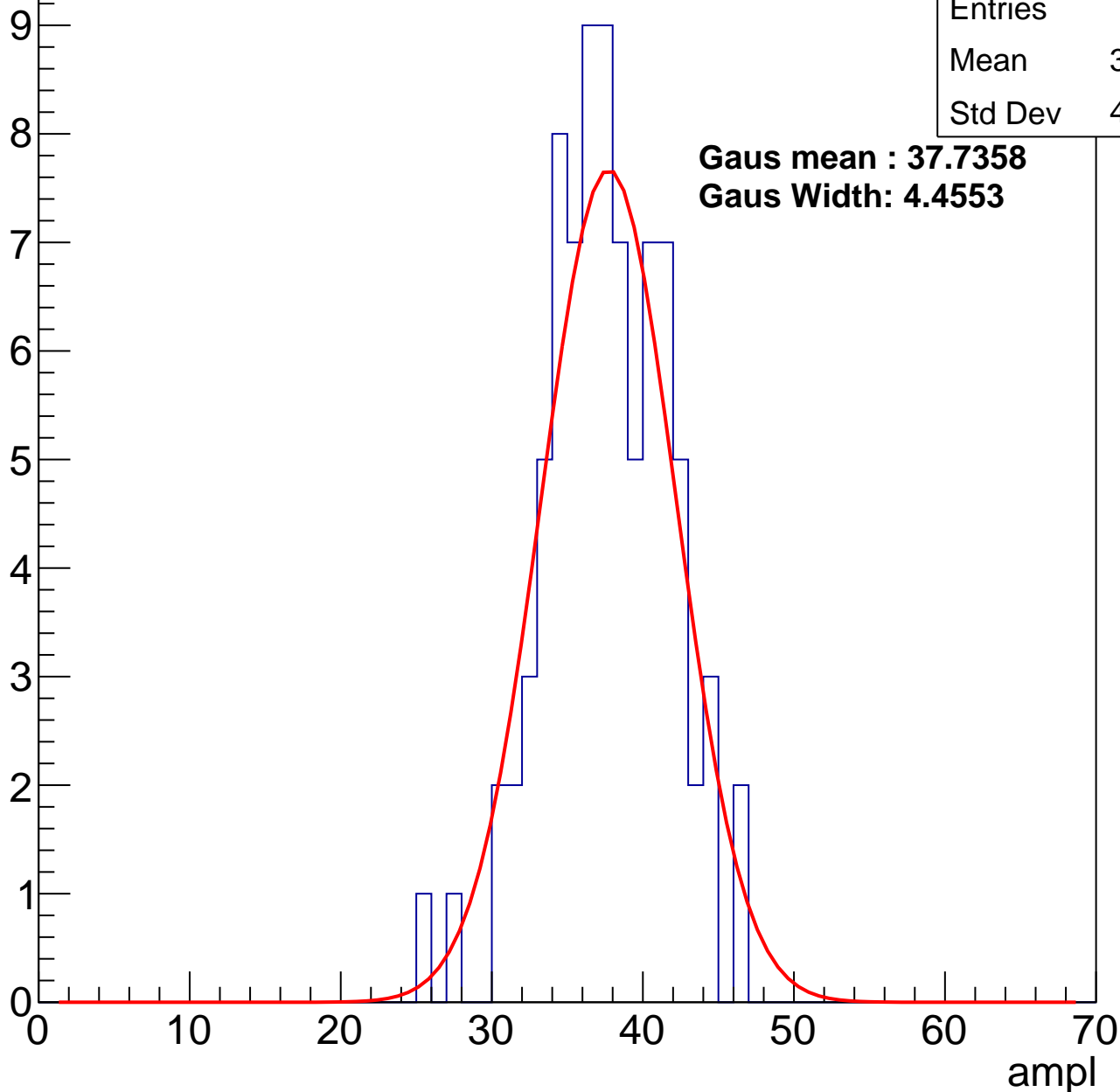
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	37.14
Std Dev	4.036

Gaus mean : 37.7358

Gaus Width: 4.4553



B1L100S, U12-ch104, adc2

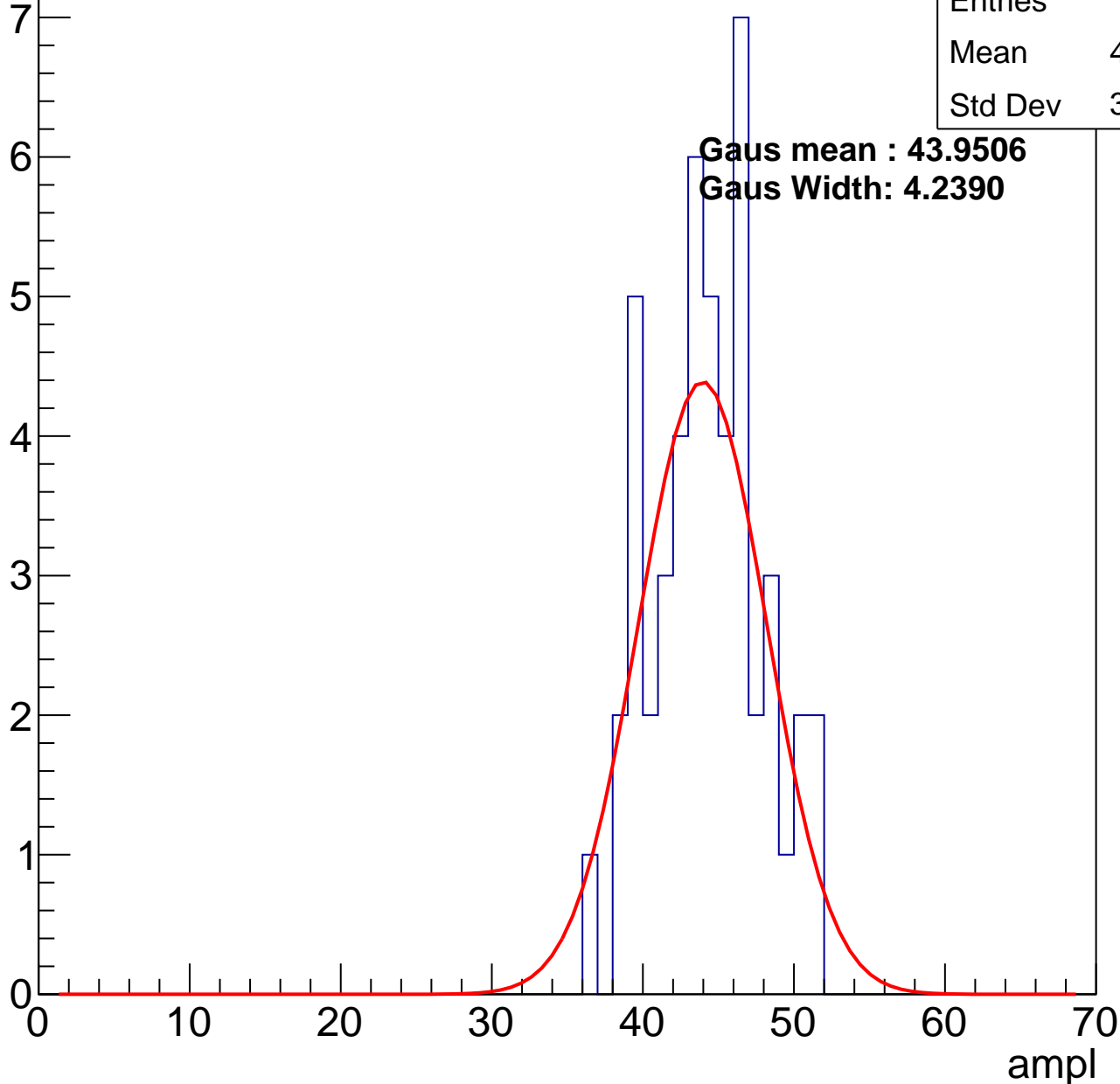
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	43.82
Std Dev	3.595

Gaus mean : 43.9506

Gaus Width: 4.2390



B1L100S, U12-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

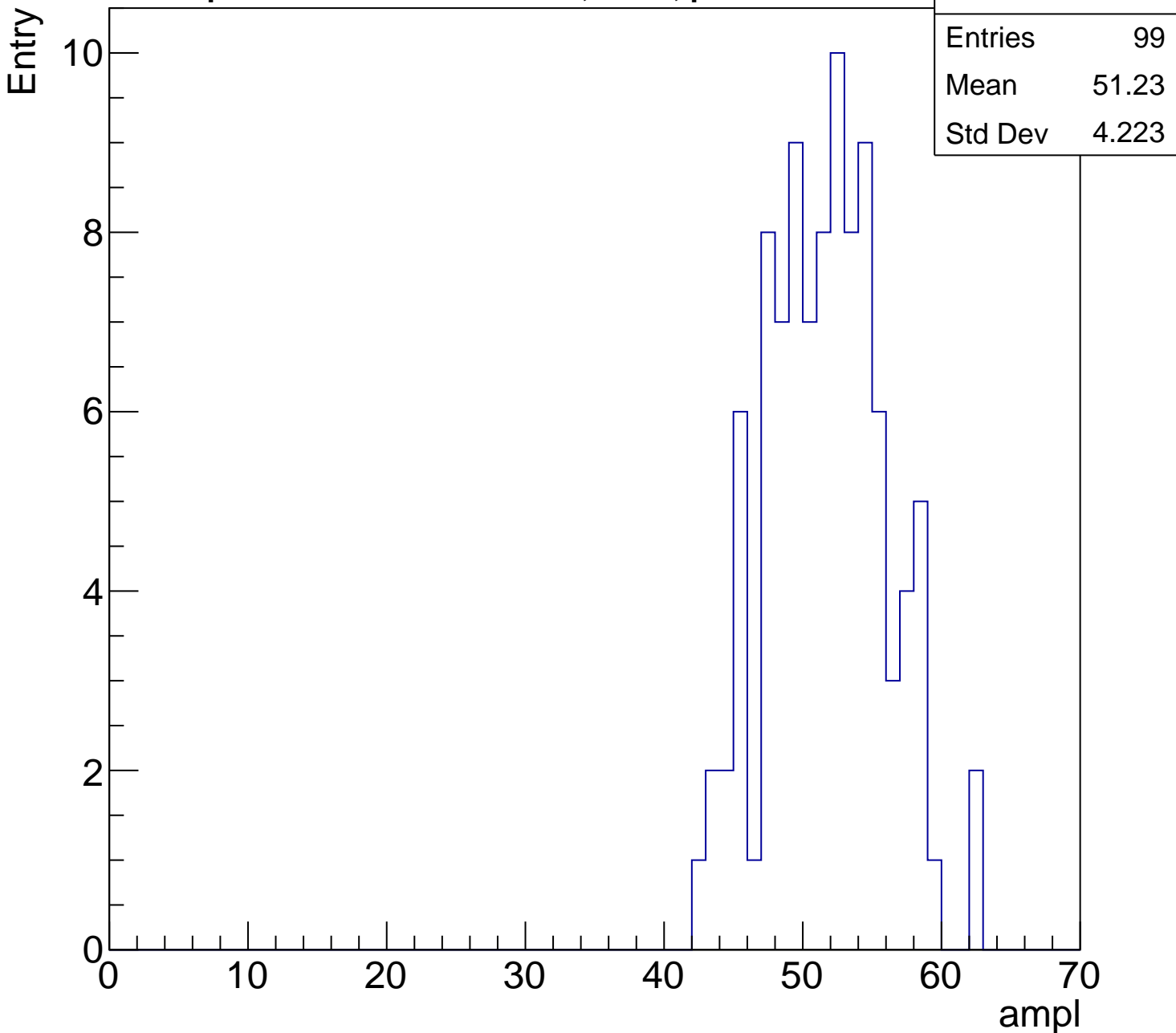
Entries	99
Mean	51.23
Std Dev	4.223

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

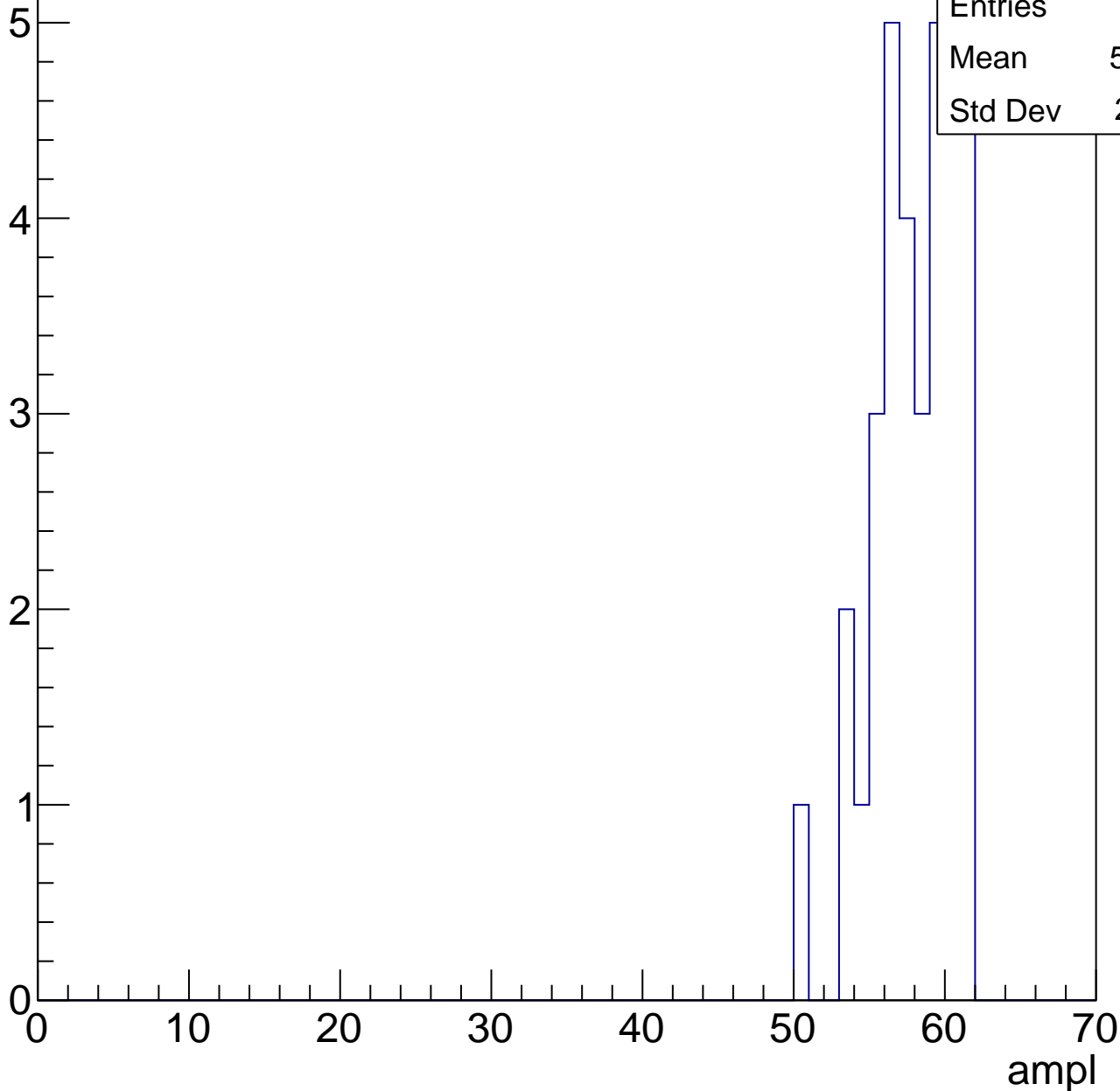


B1L100S, U12-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

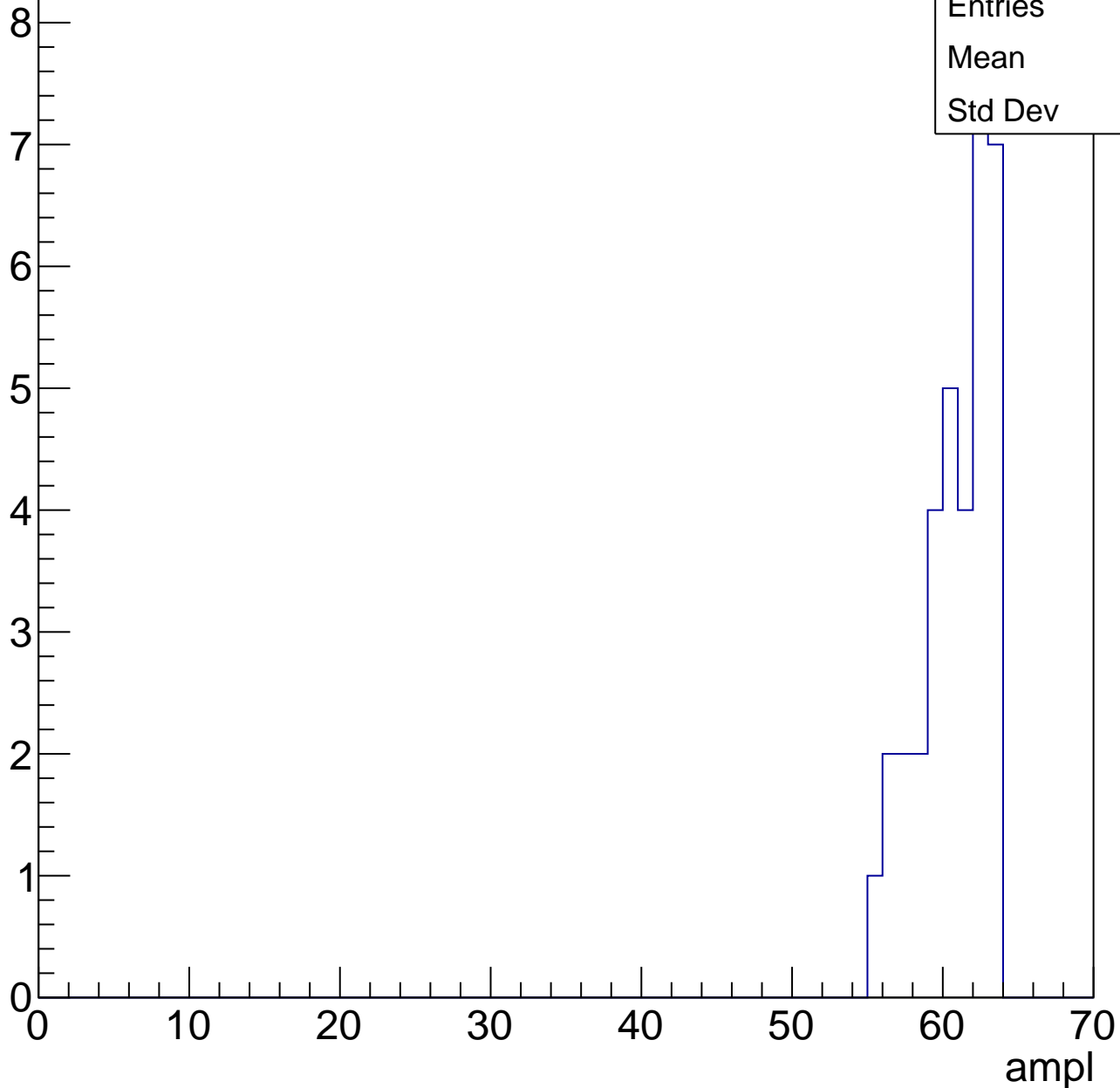
Entries	34
Mean	57.56
Std Dev	2.681



B1L100S, U12-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

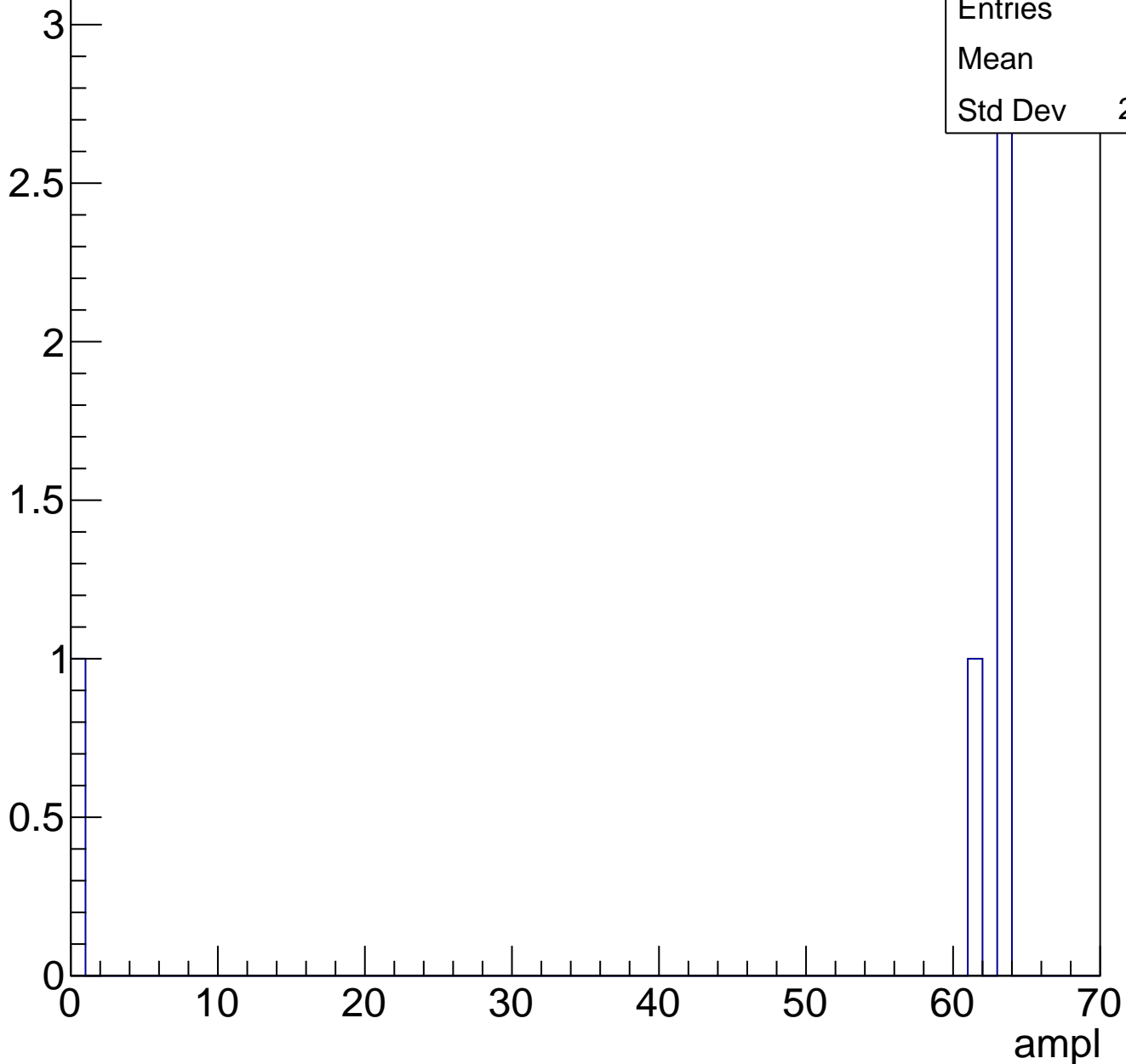
Entry



B1L100S, U12-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch105, adc0

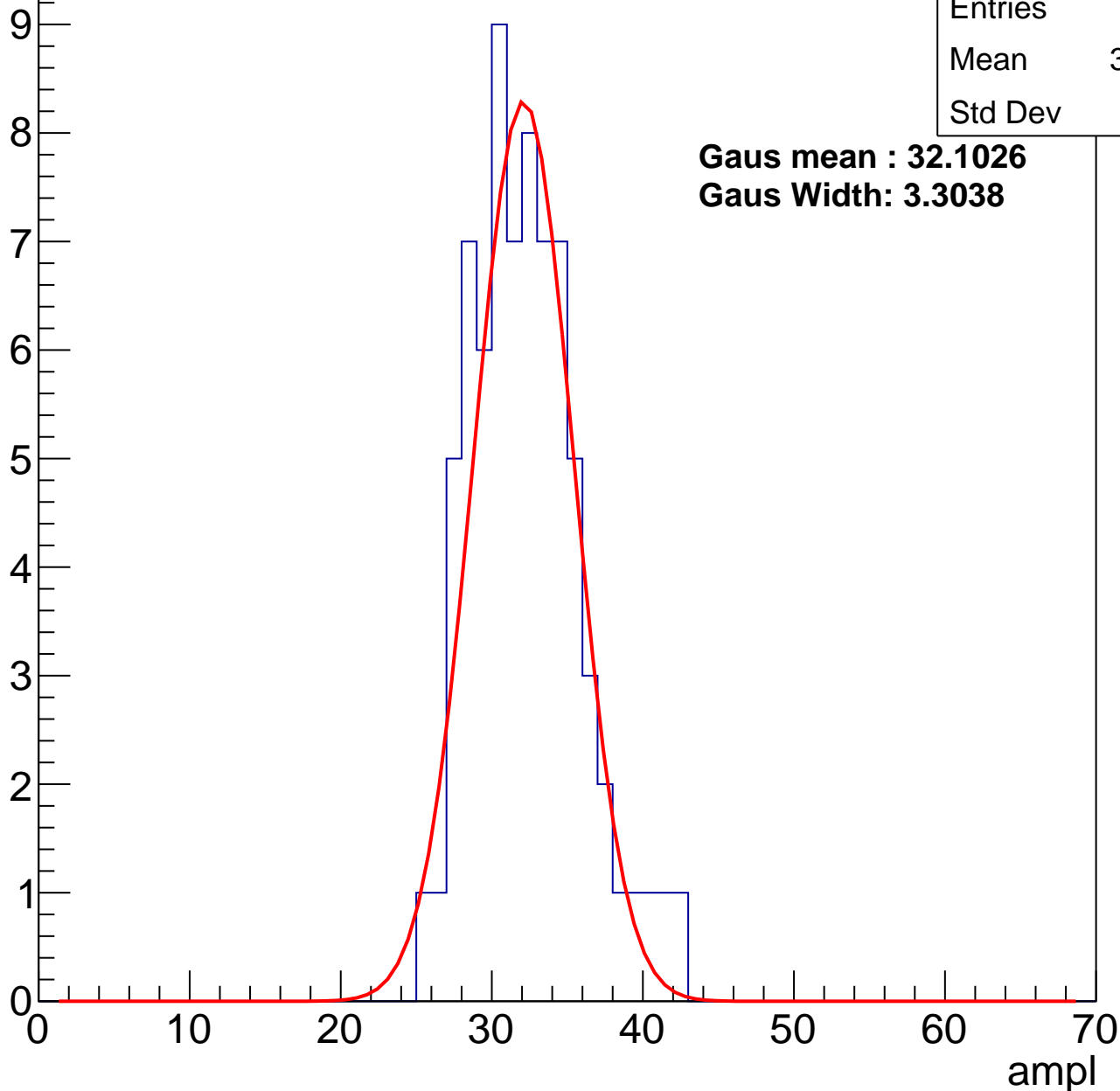
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	31.85
Std Dev	3.56

Gaus mean : 32.1026

Gaus Width: 3.3038



B1L100S, U12-ch105, adc1

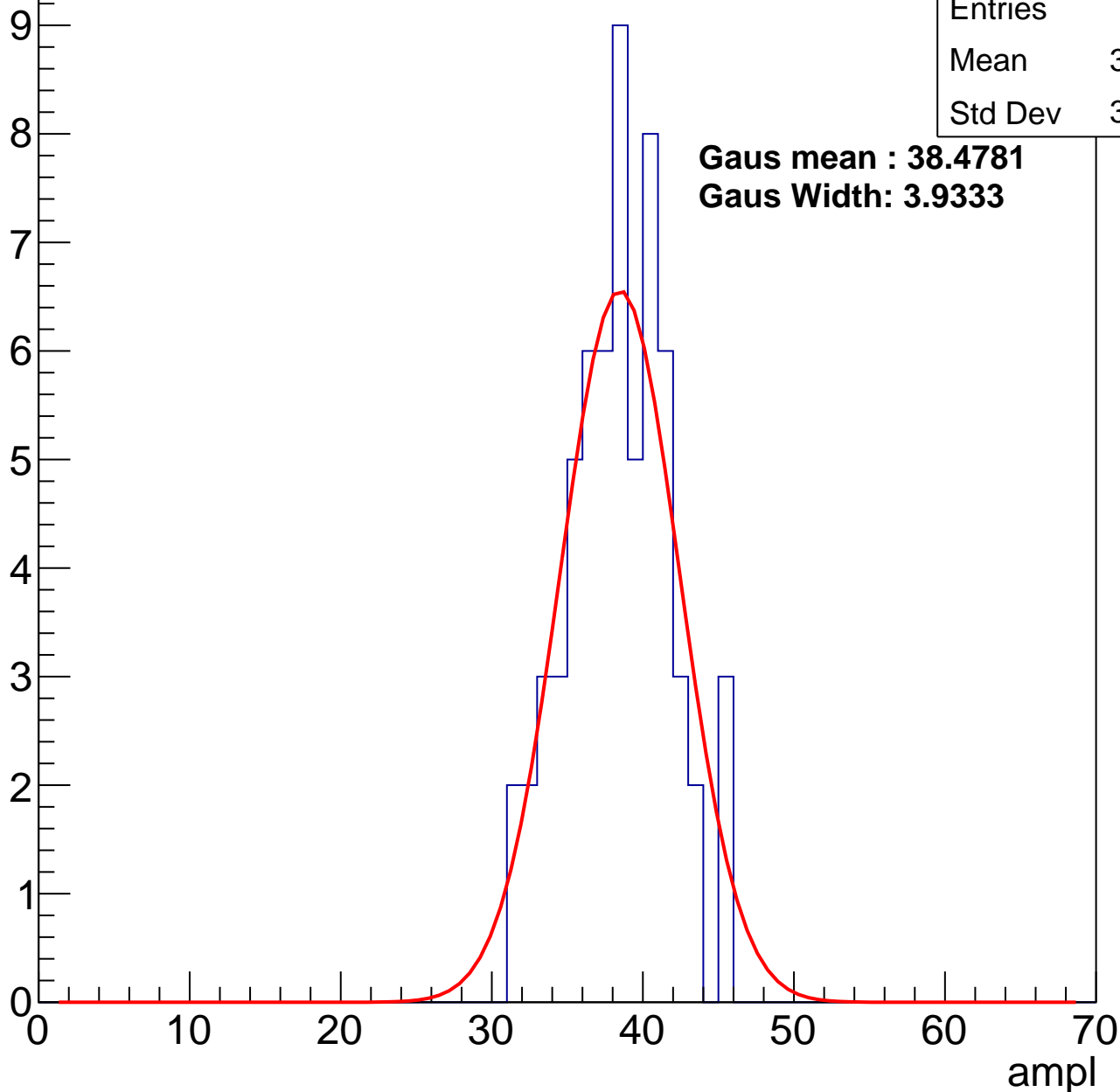
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	37.94
Std Dev	3.342

Gaus mean : 38.4781

Gaus Width: 3.9333



B1L100S, U12-ch105, adc2

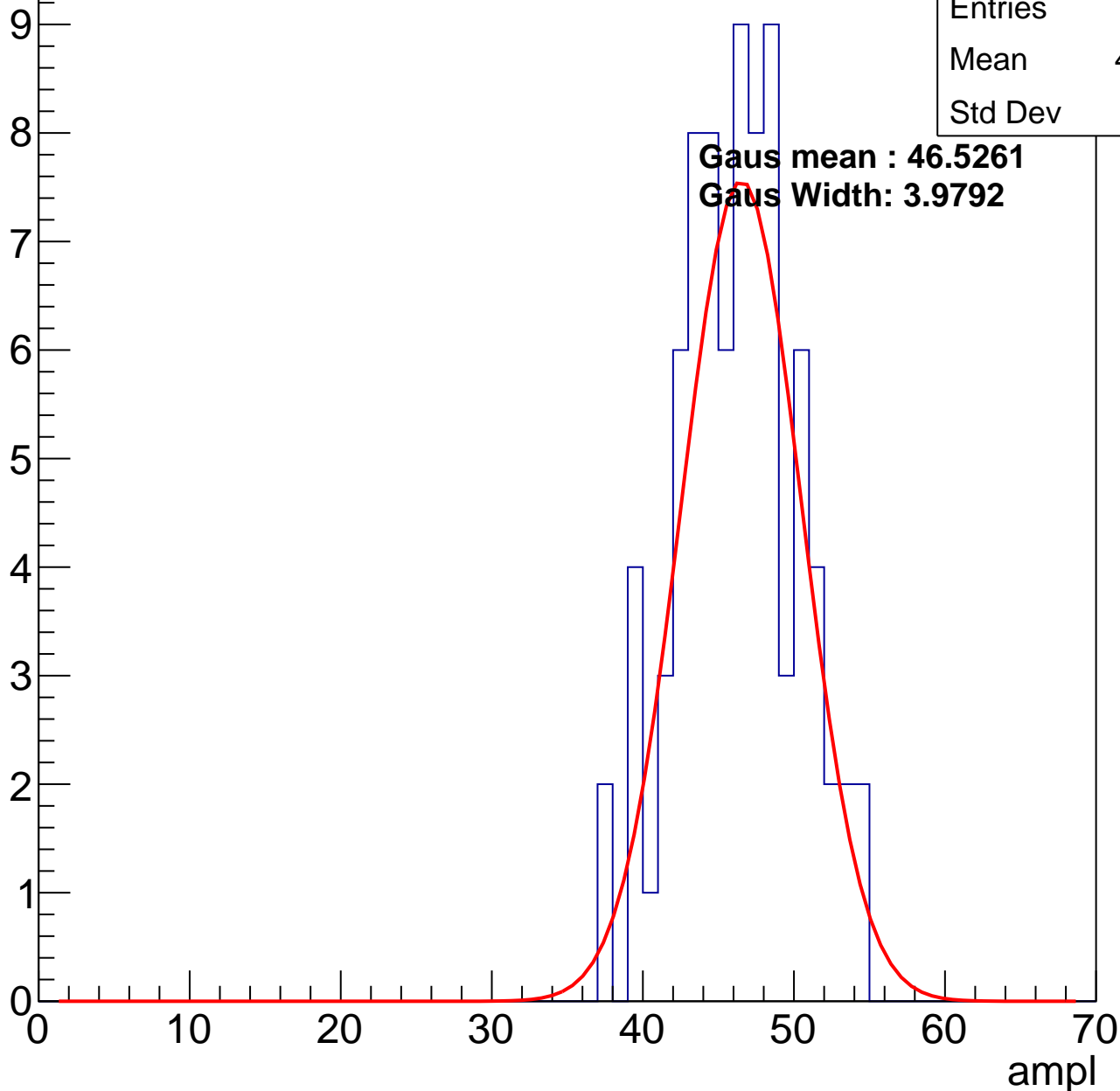
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	45.81
Std Dev	3.86

Gaus mean : 46.5261

Gaus Width: 3.9792

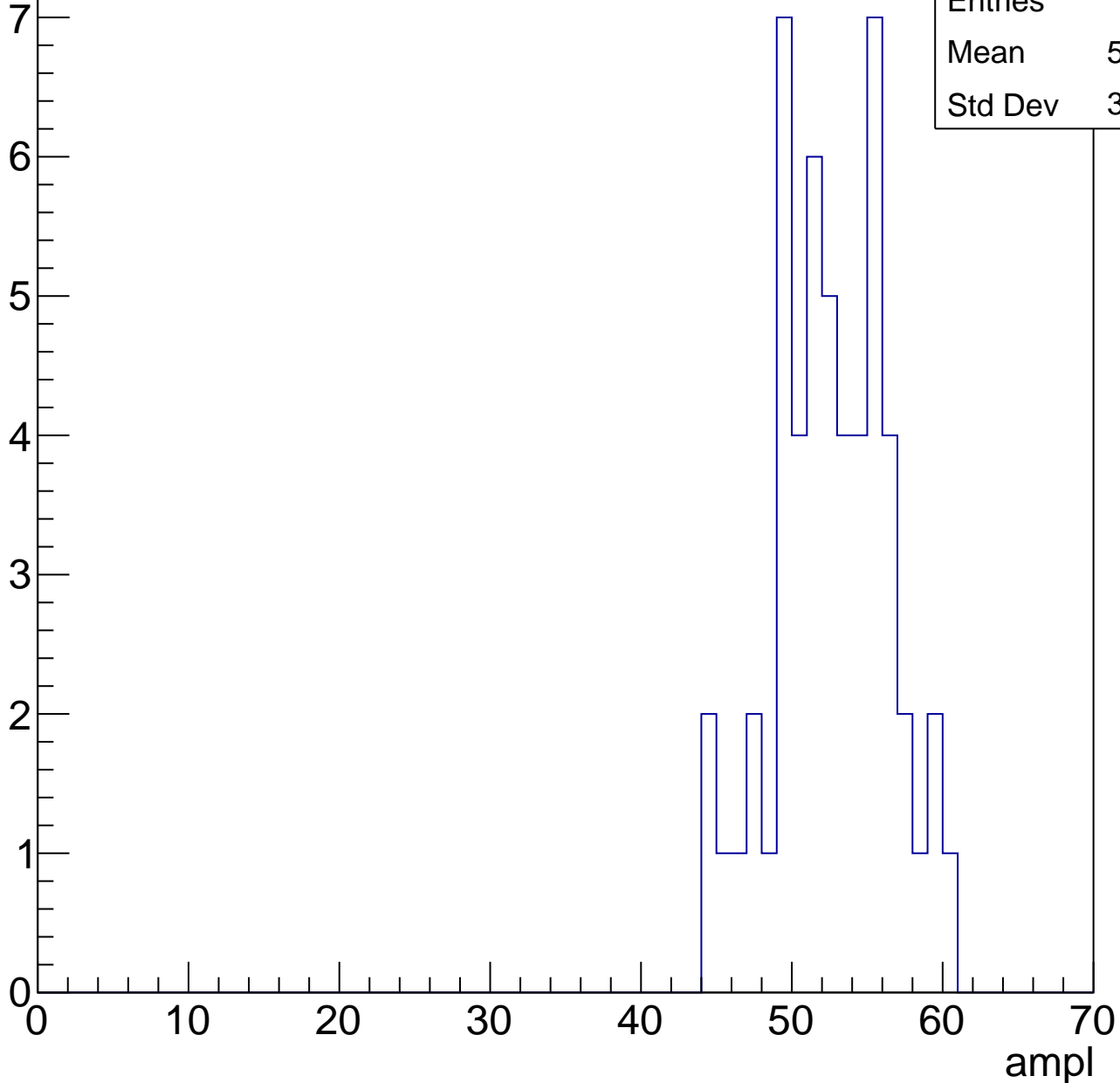


B1L100S, U12-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	52.17
Std Dev	3.745

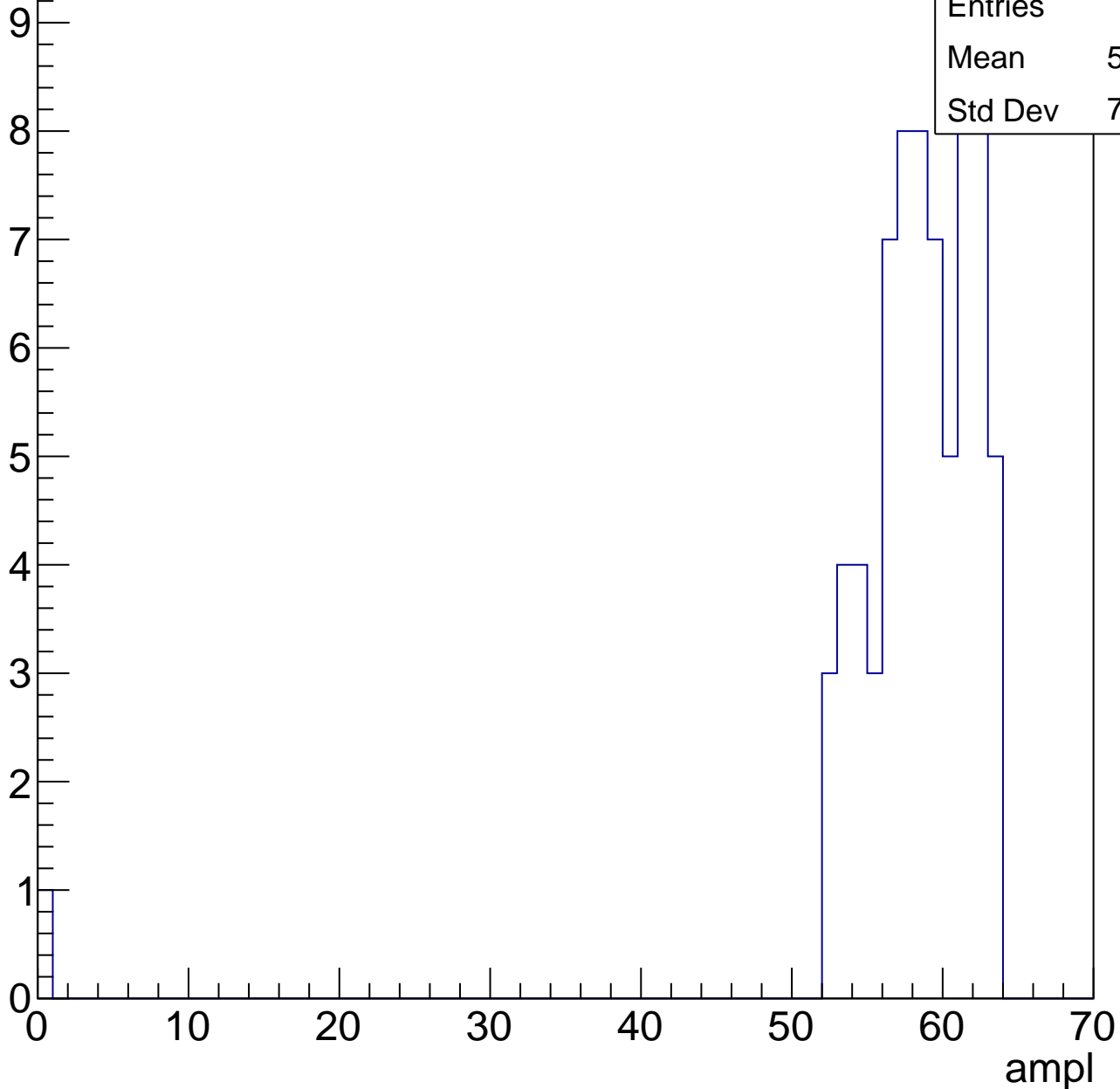


B1L100S, U12-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	57.42
Std Dev	7.487

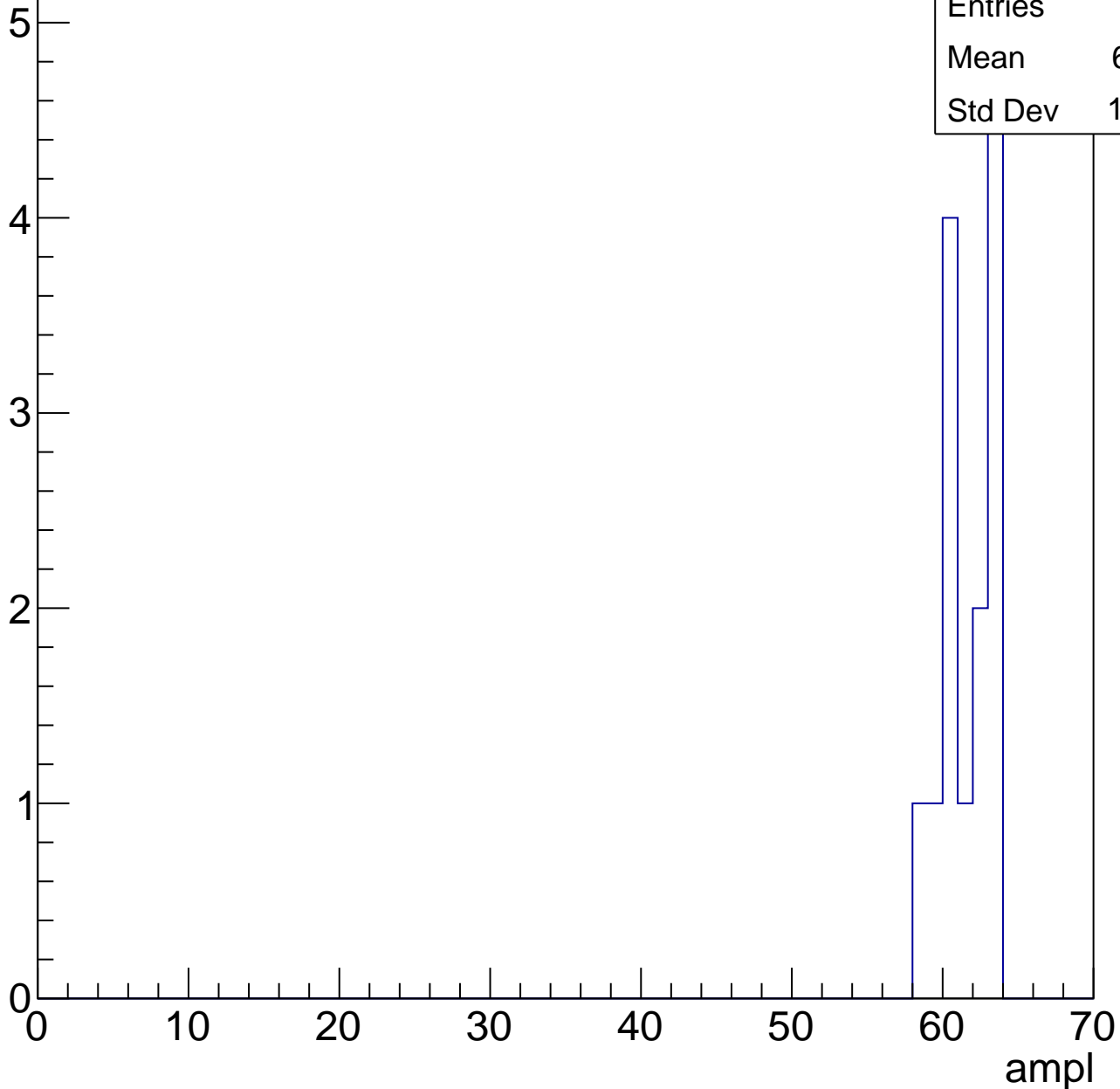


B1L100S, U12-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	61.21
Std Dev	1.655



B1L100S, U12-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch106, adc0

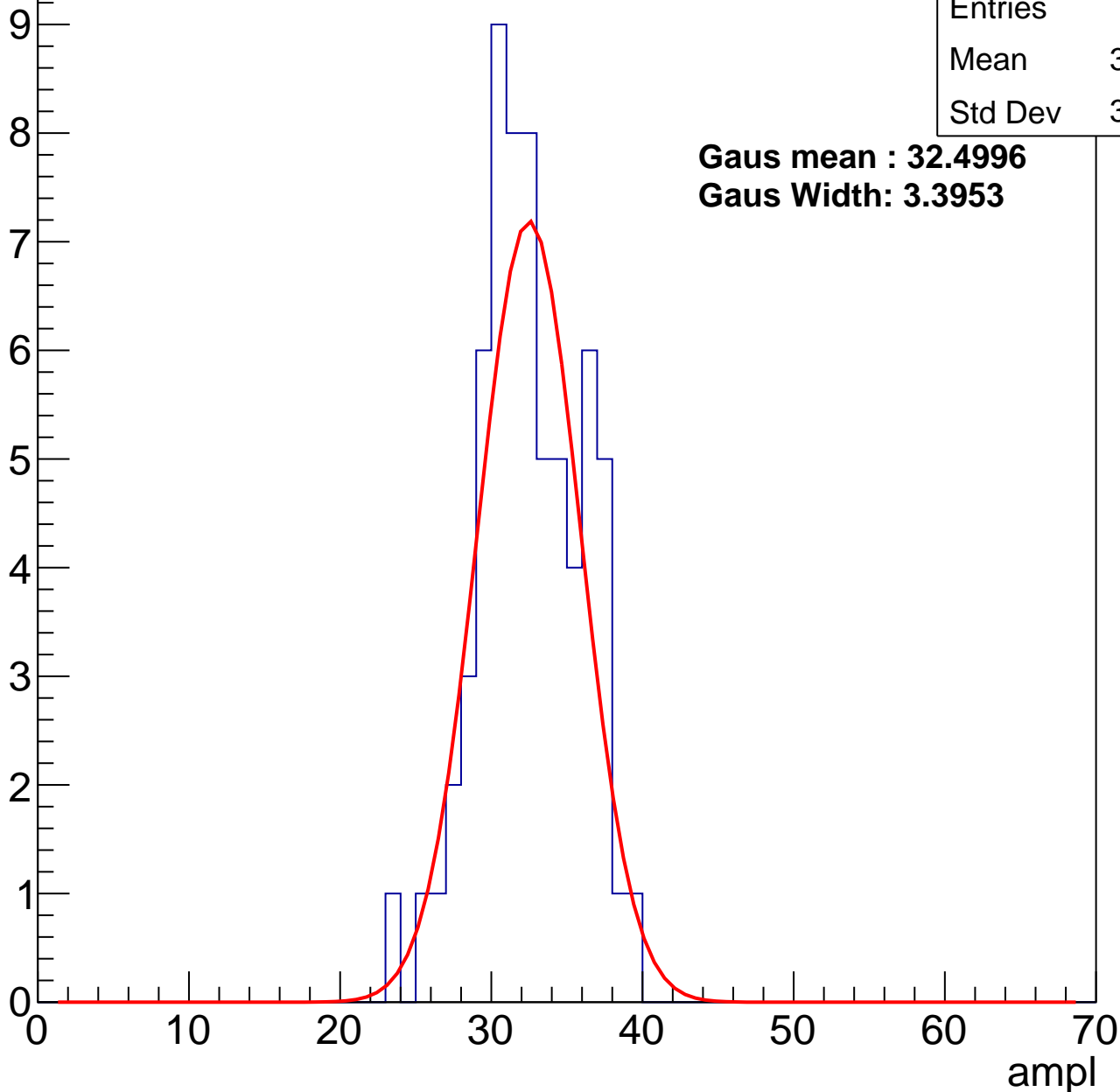
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	32.02
Std Dev	3.323

Gaus mean : 32.4996

Gaus Width: 3.3953



B1L100S, U12-ch106, adc1

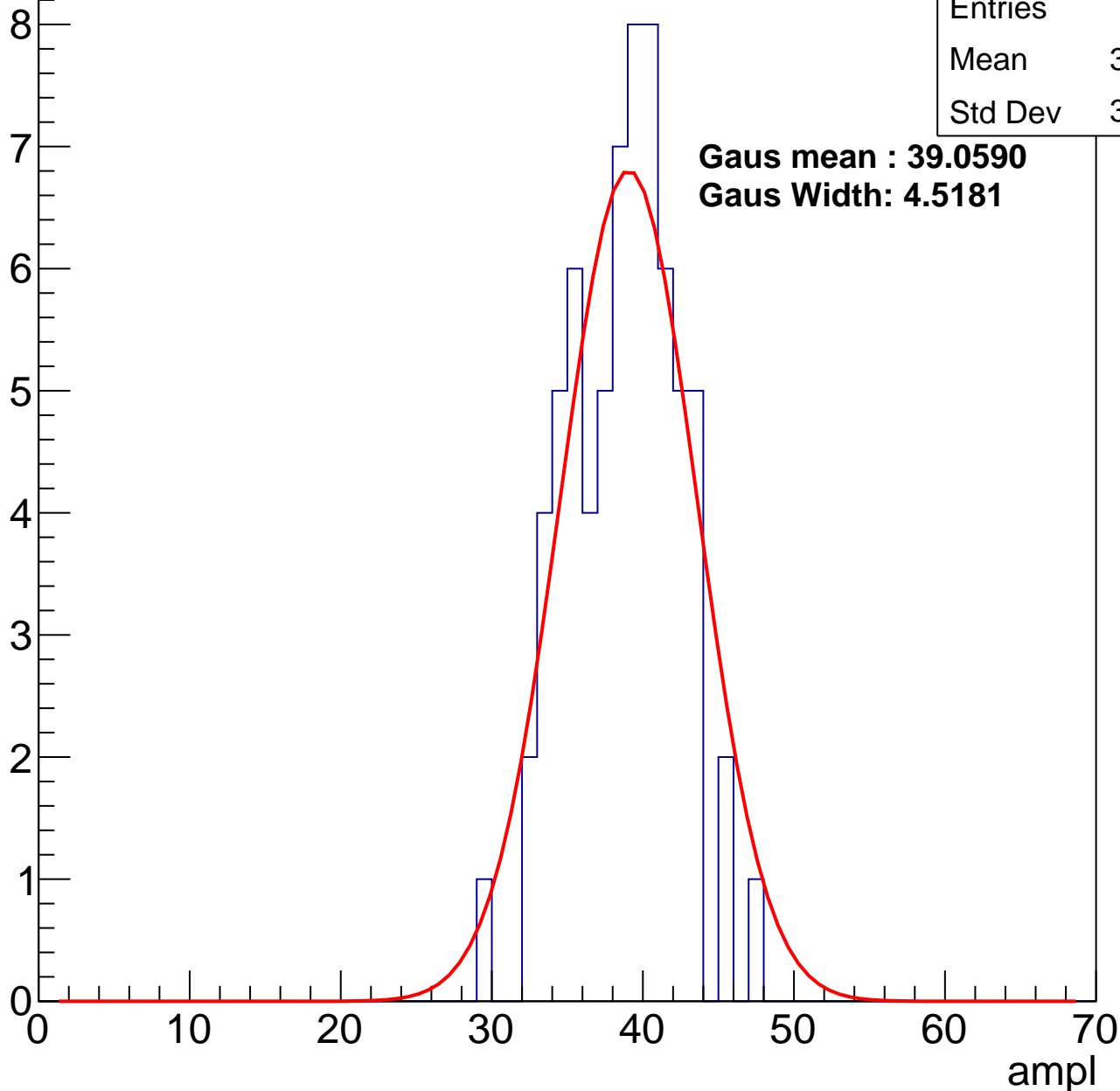
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	38.26
Std Dev	3.574

Gaus mean : 39.0590

Gaus Width: 4.5181



B1L100S, U12-ch106, adc2

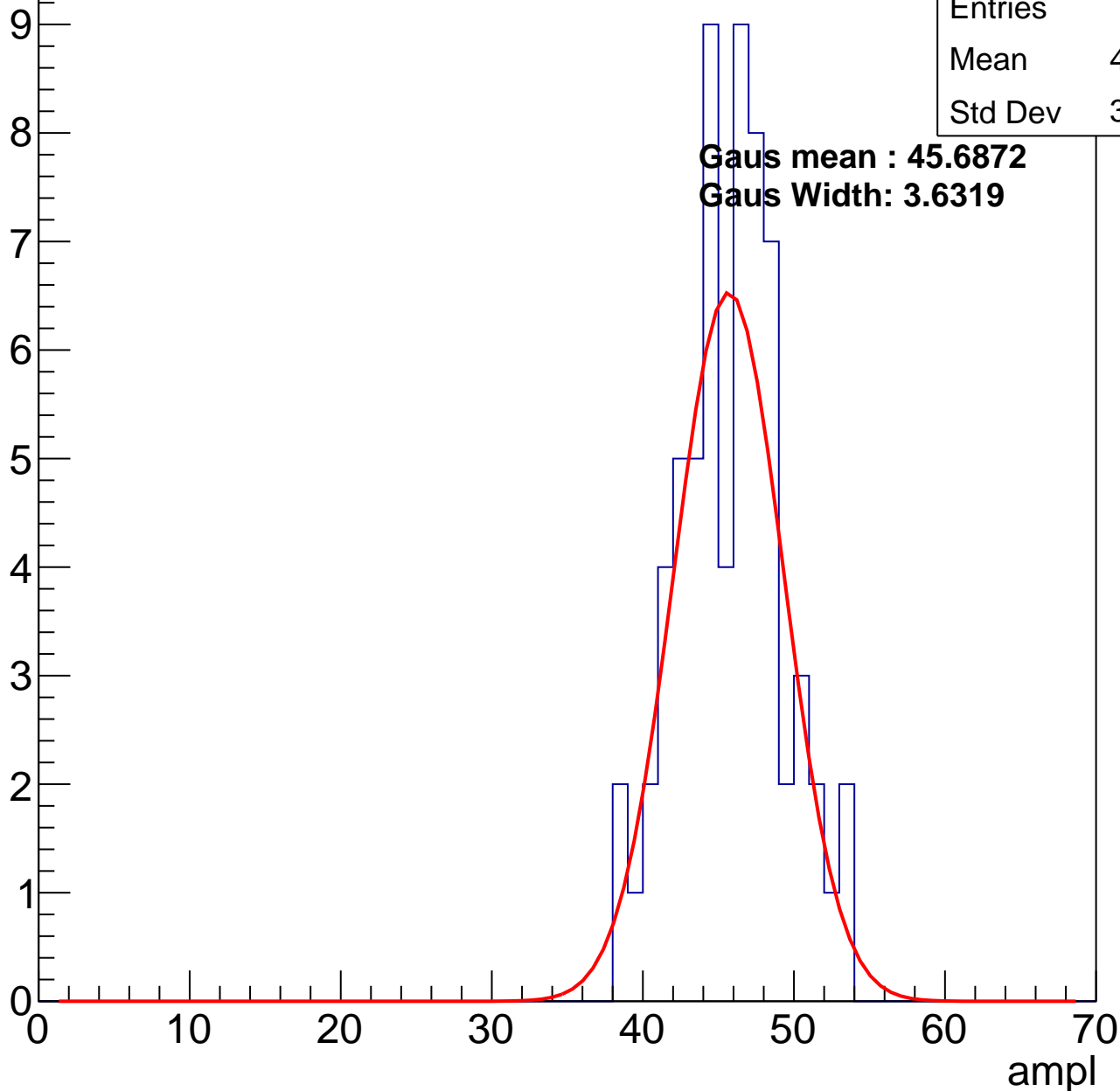
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	45.36
Std Dev	3.423

Gaus mean : 45.6872

Gaus Width: 3.6319

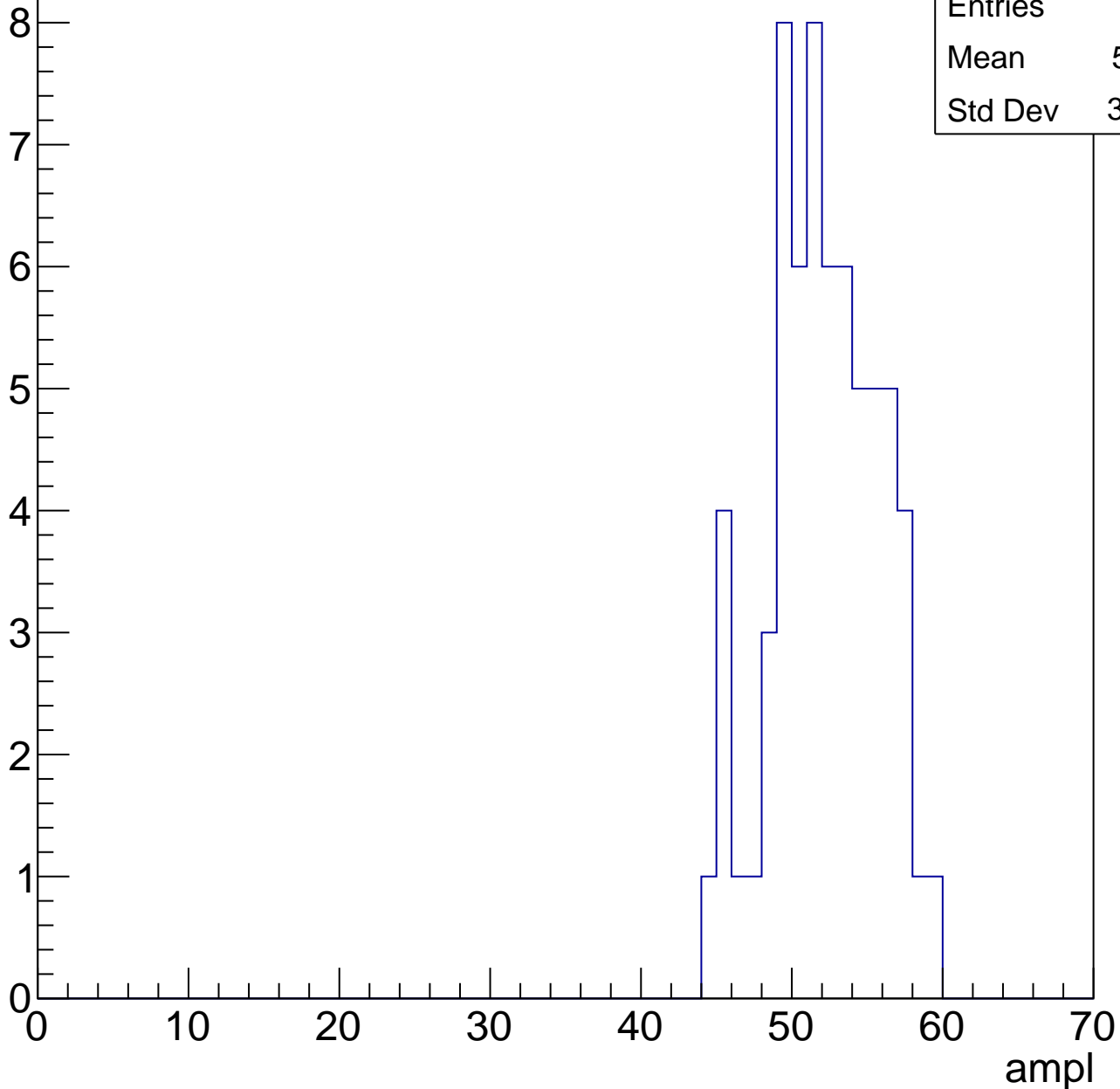


B1L100S, U12-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	51.71
Std Dev	3.516

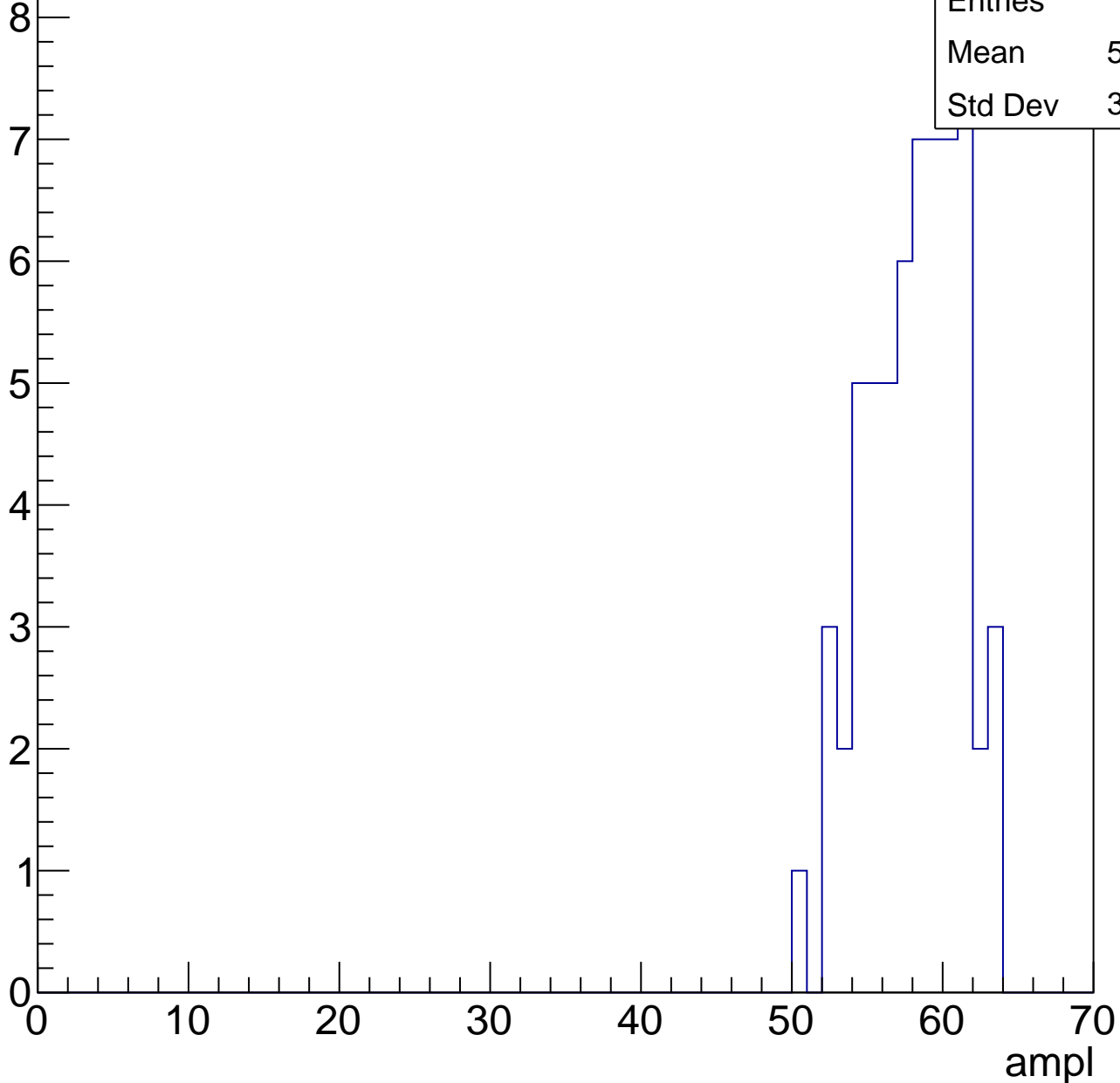


B1L100S, U12-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

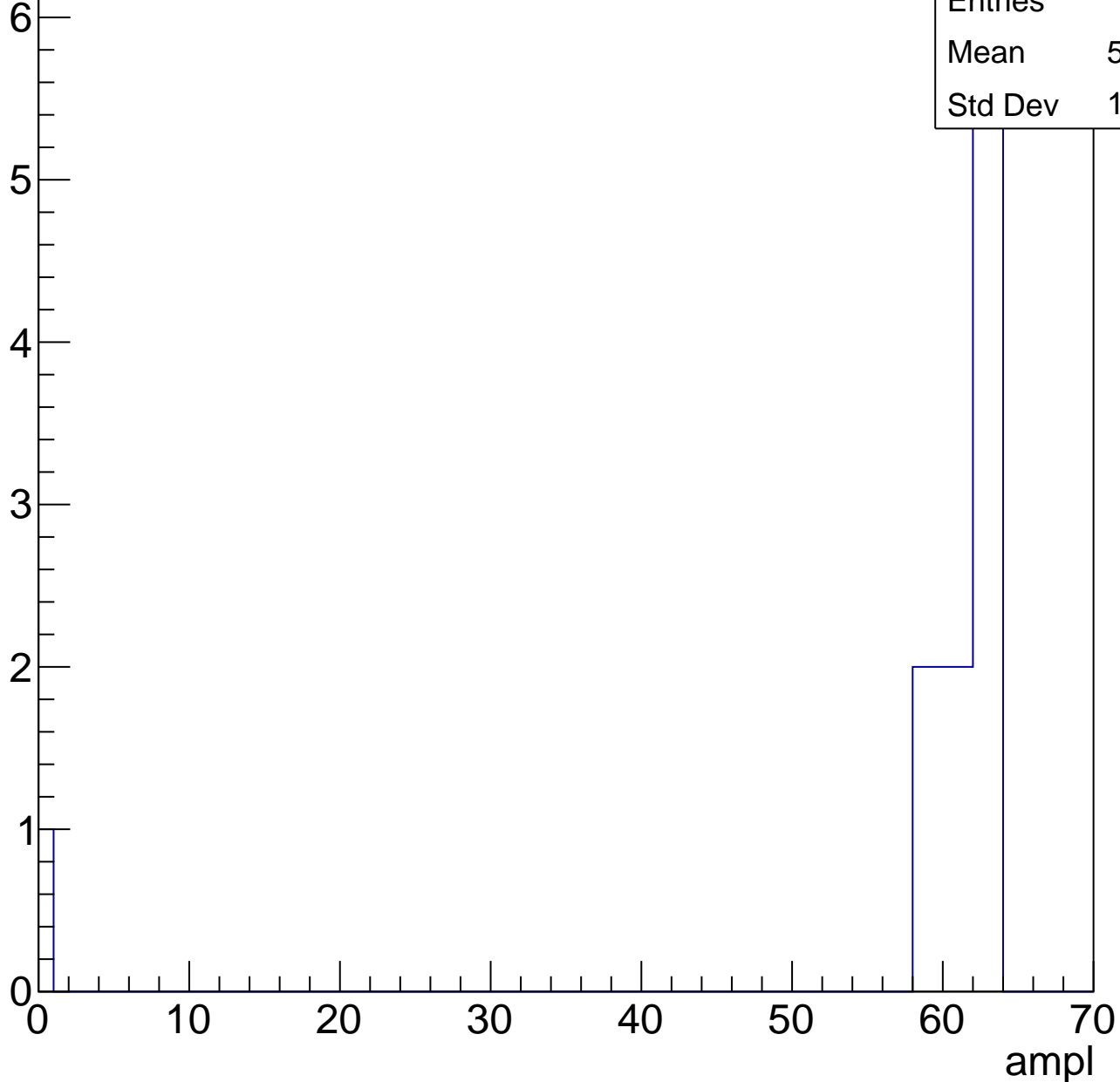
Entries	61
Mean	57.69
Std Dev	3.086



B1L100S, U12-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	21
Mean	58.38
Std Dev	13.16

B1L100S, U12-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B1L100S, U12-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch107, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	30.84
Std Dev	3.462

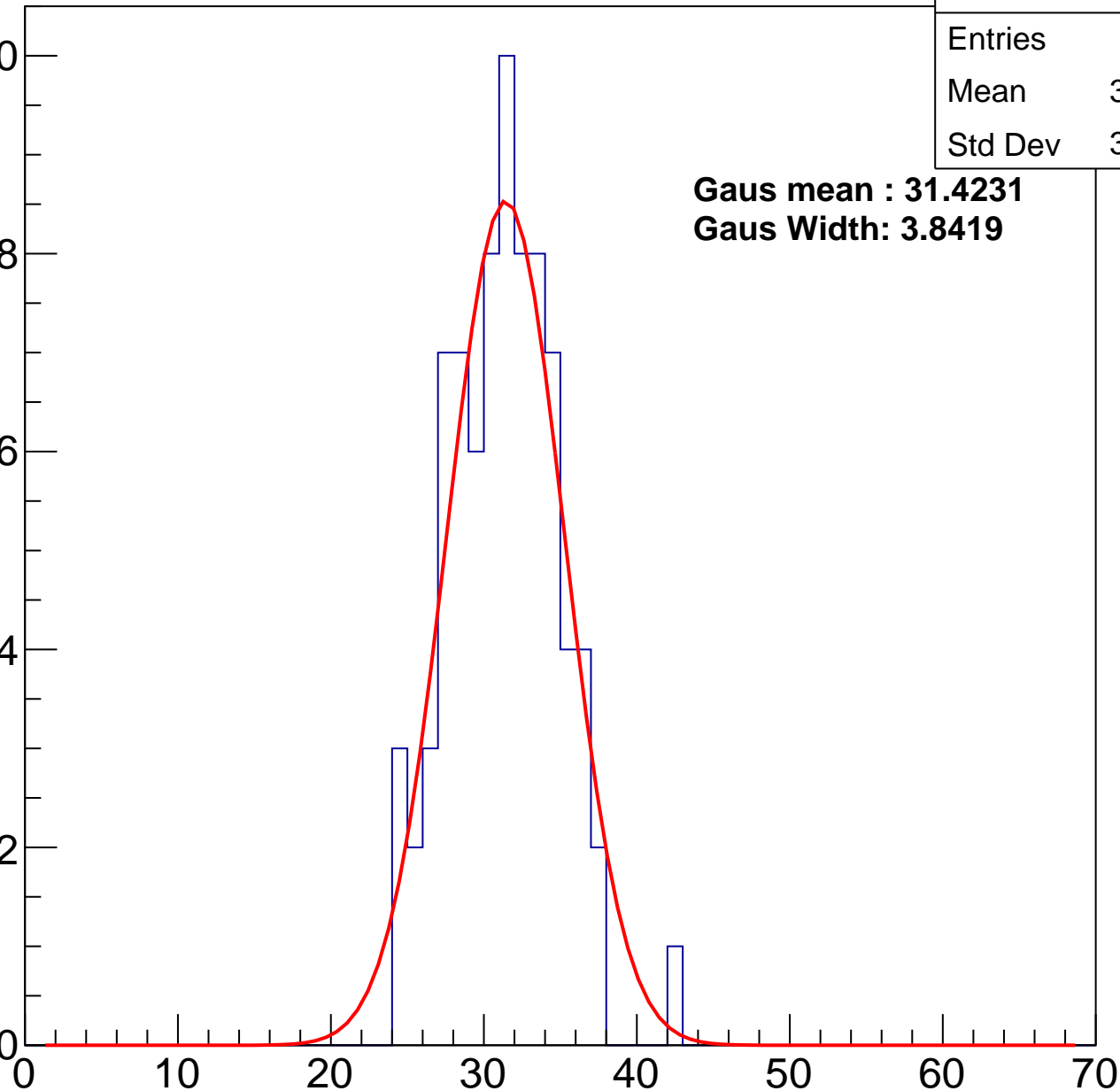
Gaus mean : 31.4231

Gaus Width: 3.8419

Entry

10
8
6
4
2
0

ampl



B1L100S, U12-ch107, adc1

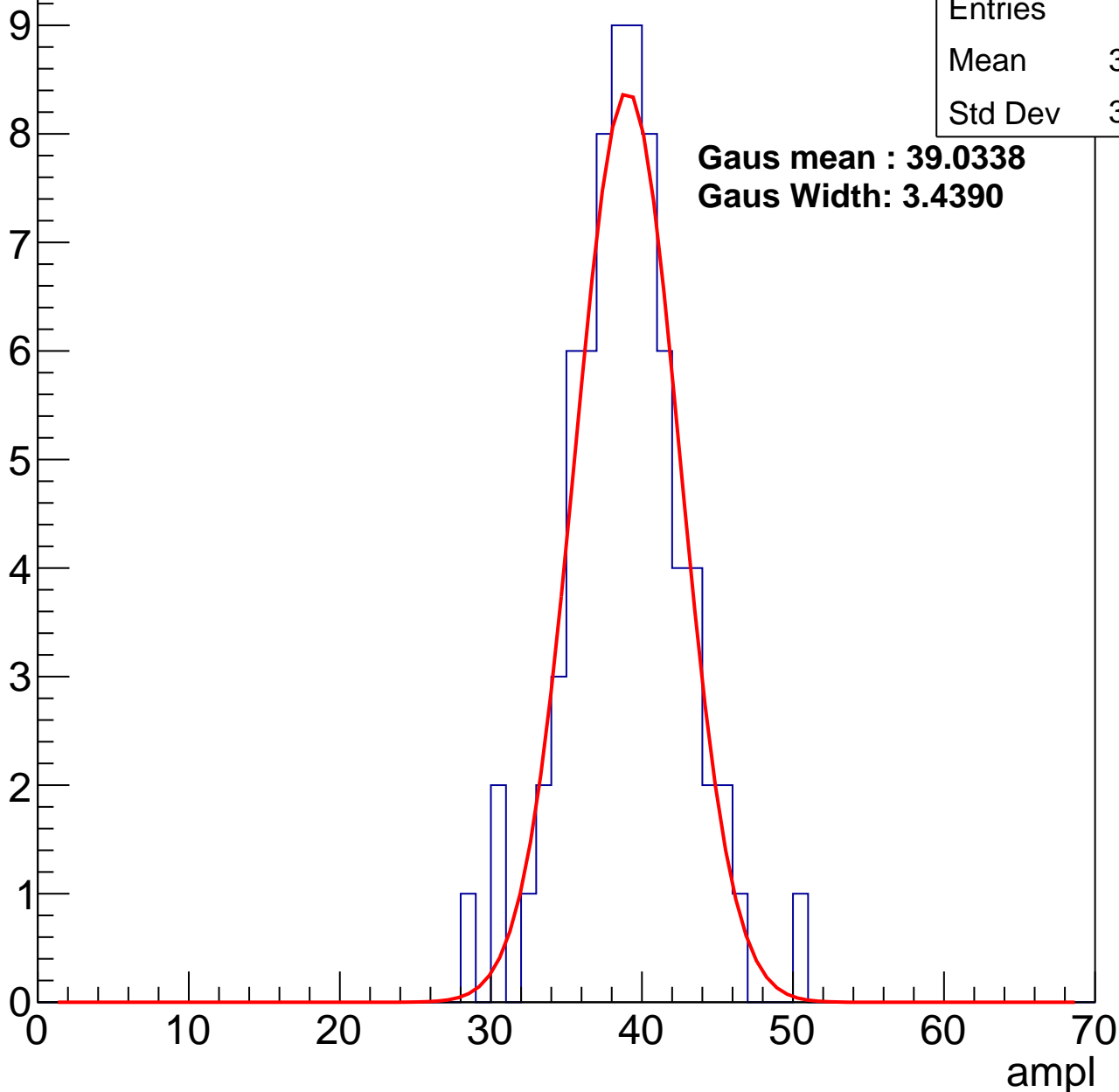
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	38.44
Std Dev	3.778

Gaus mean : 39.0338

Gaus Width: 3.4390



B1L100S, U12-ch107, adc2

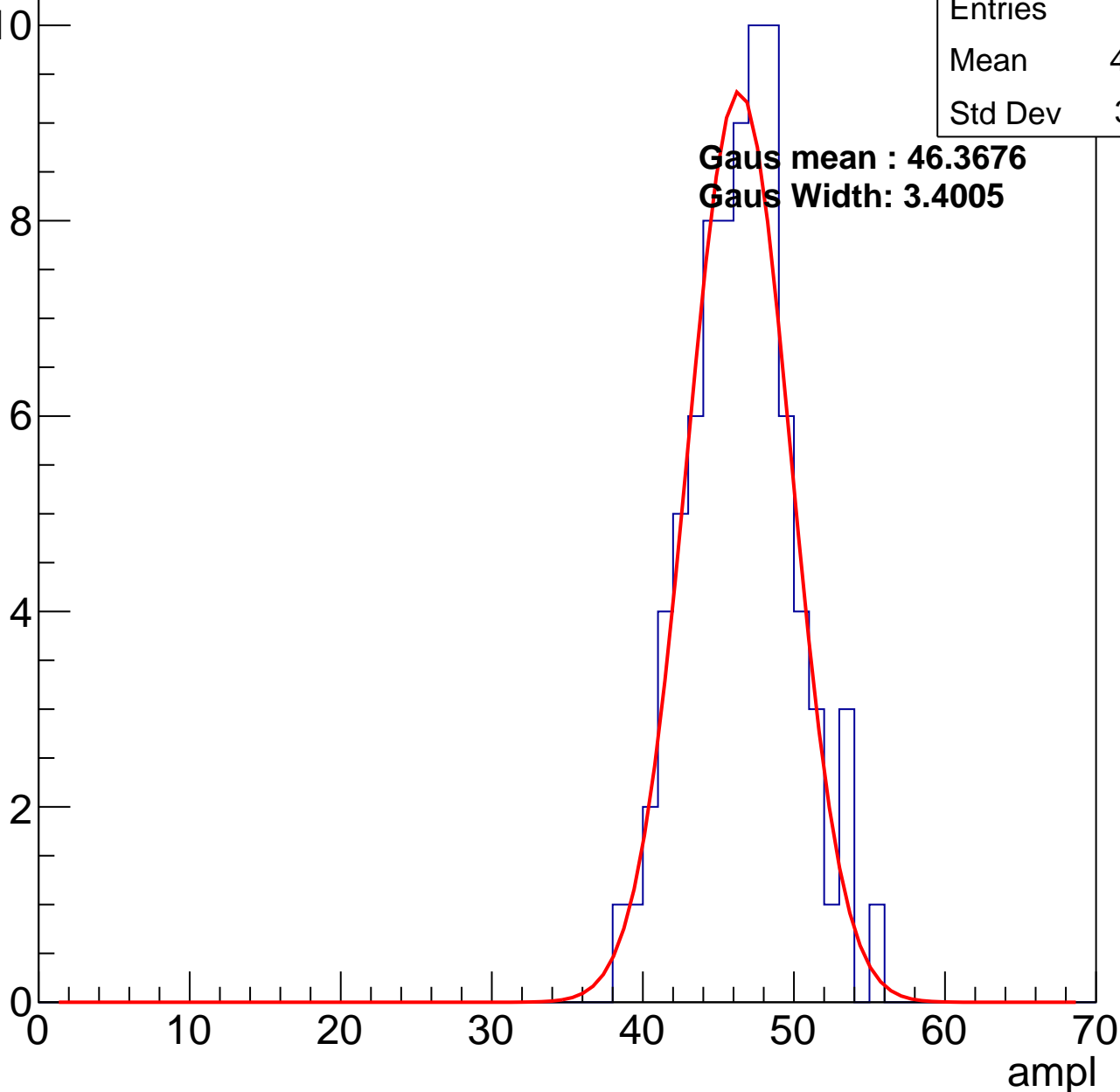
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	46.07
Std Dev	3.431

Gaus mean : 46.3676

Gaus Width: 3.4005

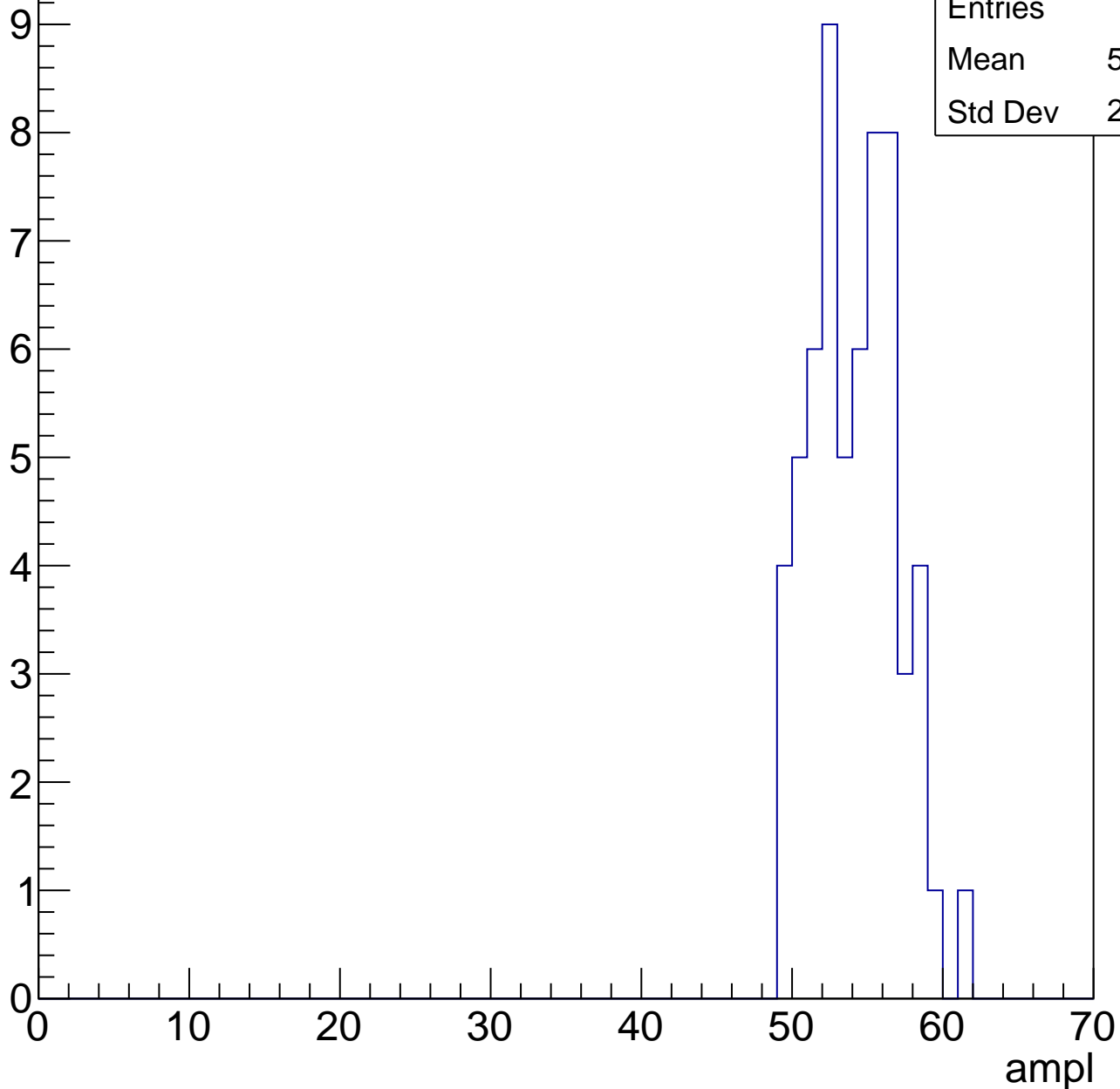


B1L100S, U12-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	53.67
Std Dev	2.809

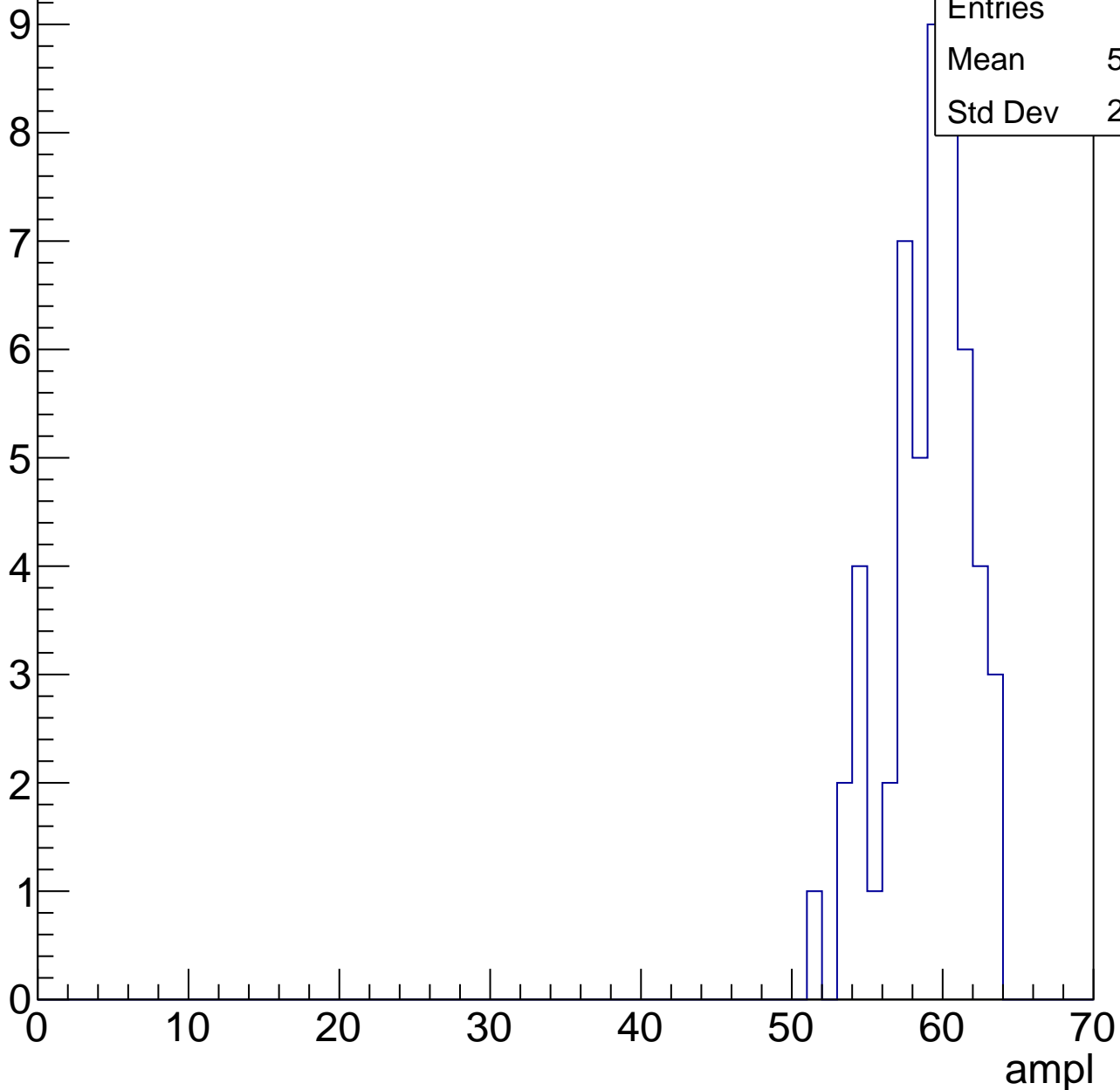


B1L100S, U12-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	58.52
Std Dev	2.798

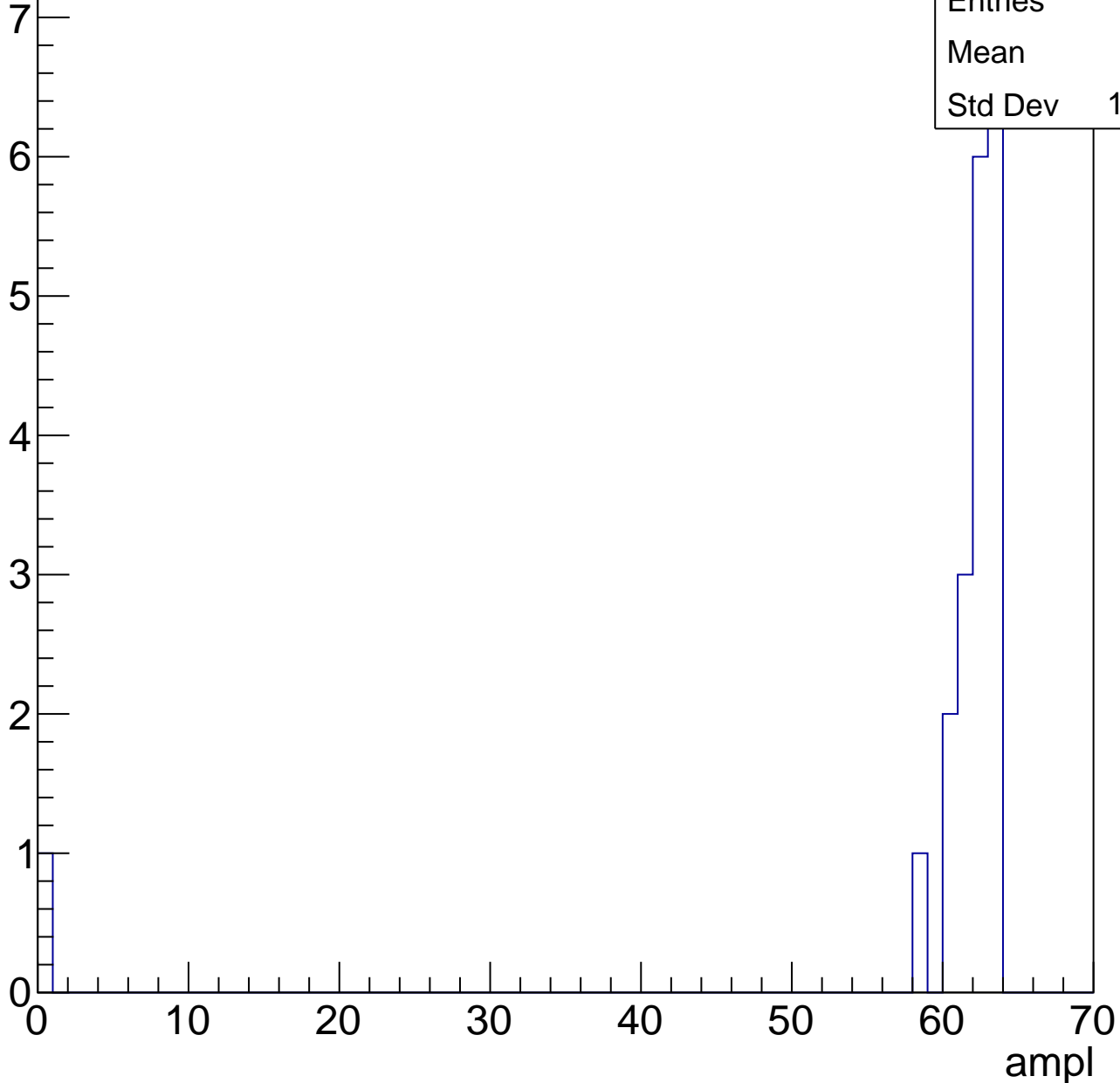


B1L100S, U12-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	58.7
Std Dev	13.53



B1L100S, U12-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch108, adc0

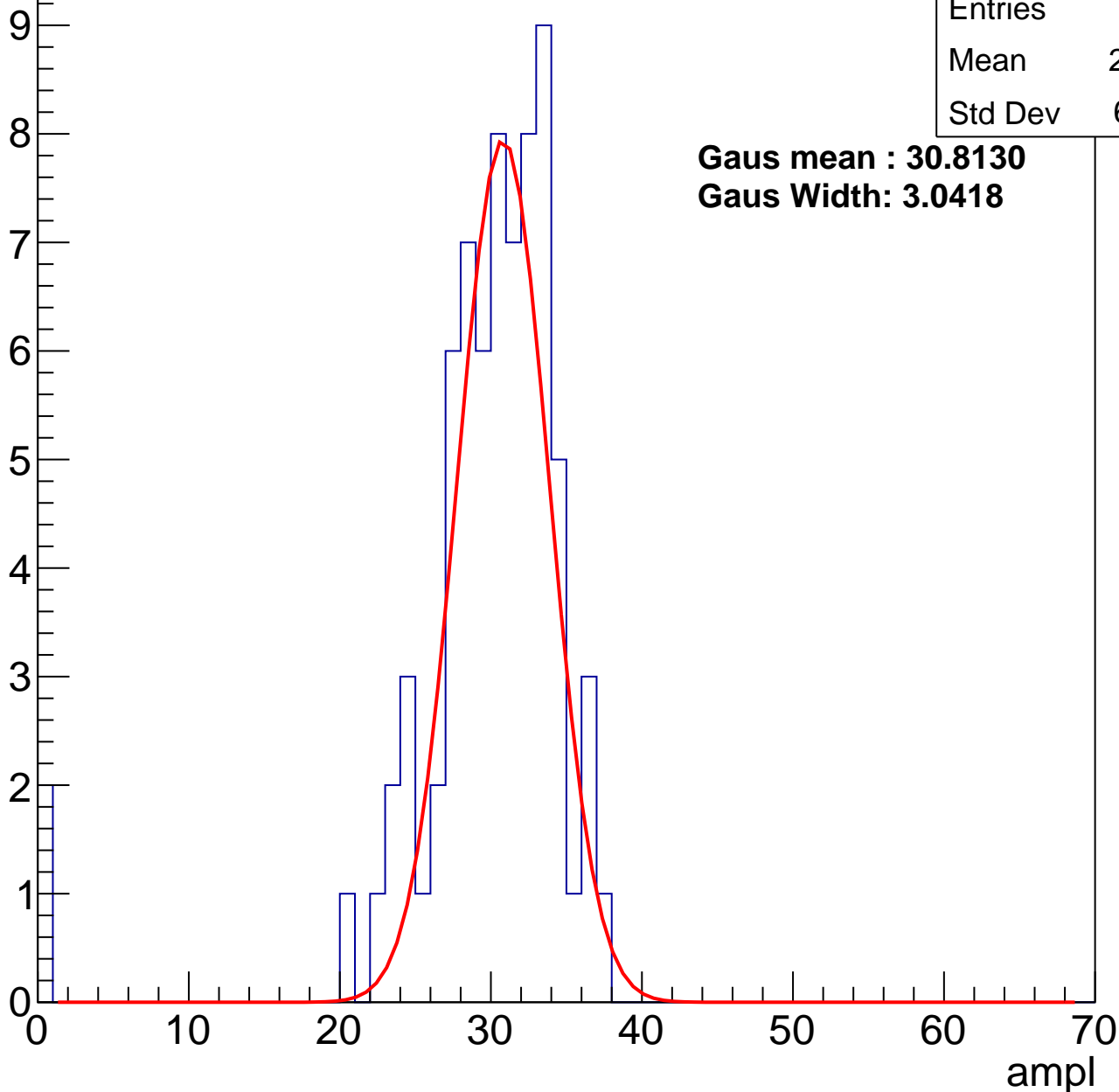
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	29.16
Std Dev	6.021

Gaus mean : 30.8130

Gaus Width: 3.0418



B1L100S, U12-ch108, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	38
Std Dev	3.916

Gaus mean : 39.4733

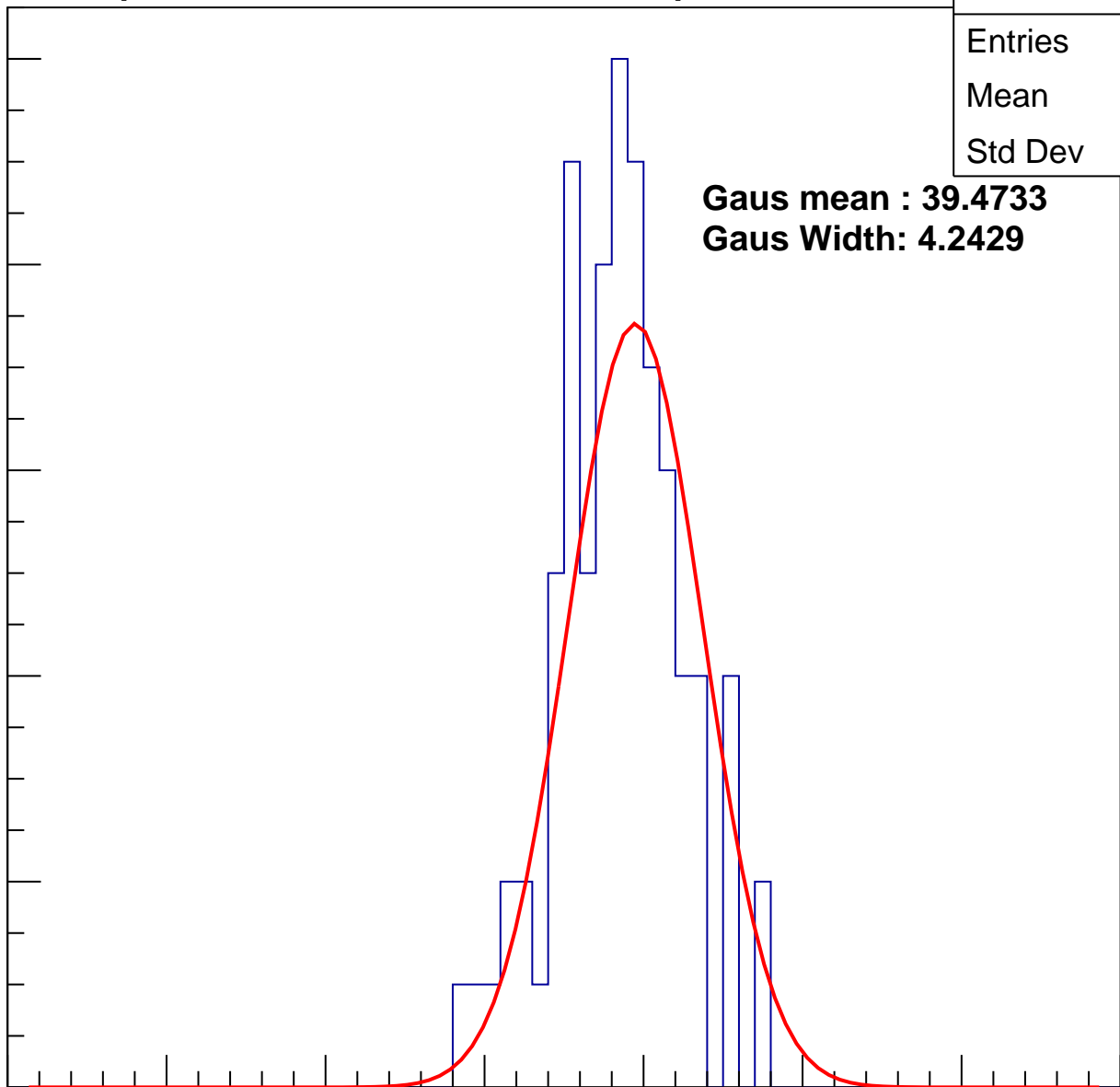
Gaus Width: 4.2429

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch108, adc2

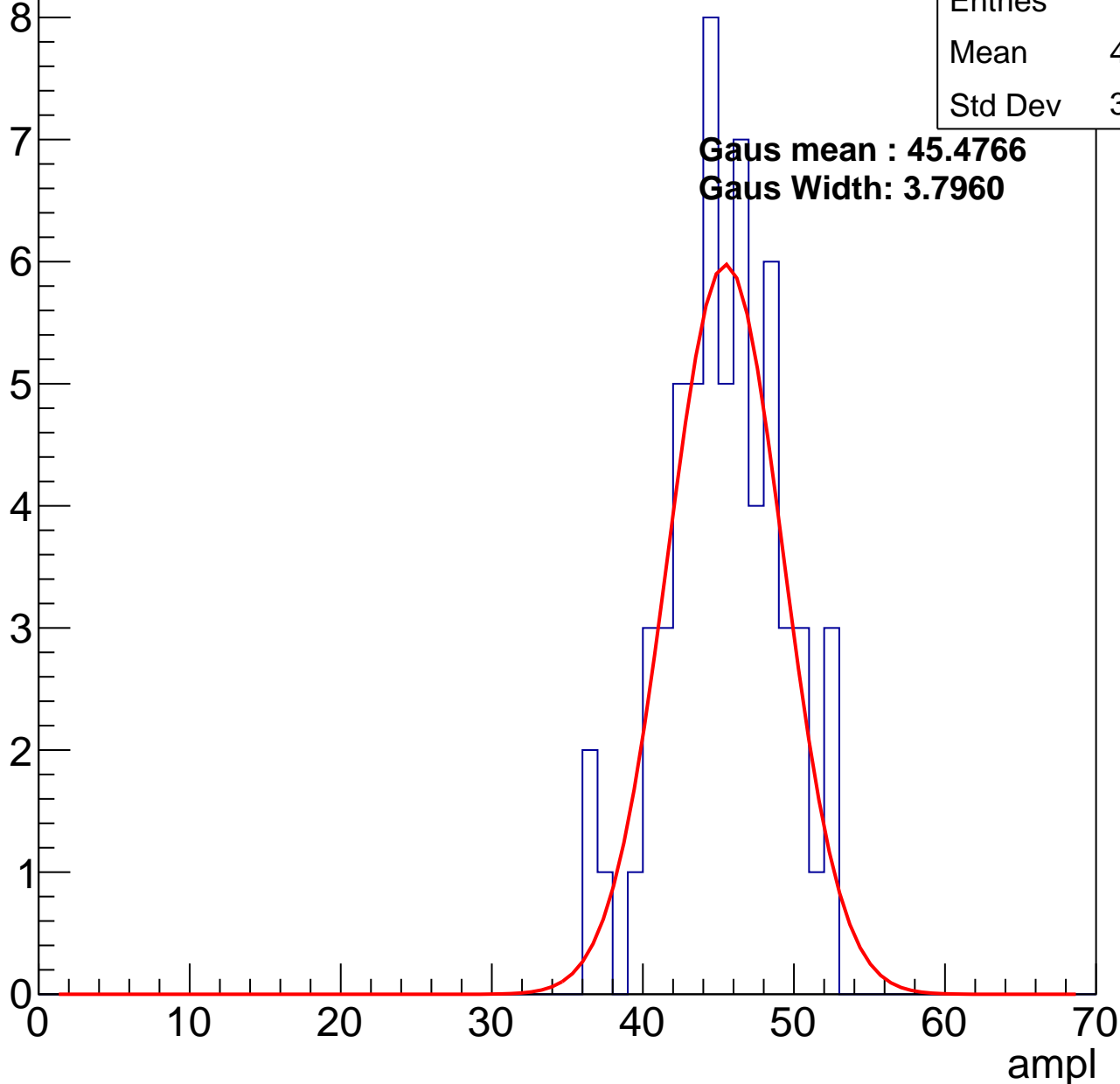
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	44.92
Std Dev	3.743

Gaus mean : 45.4766

Gaus Width: 3.7960



B1L100S, U12-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

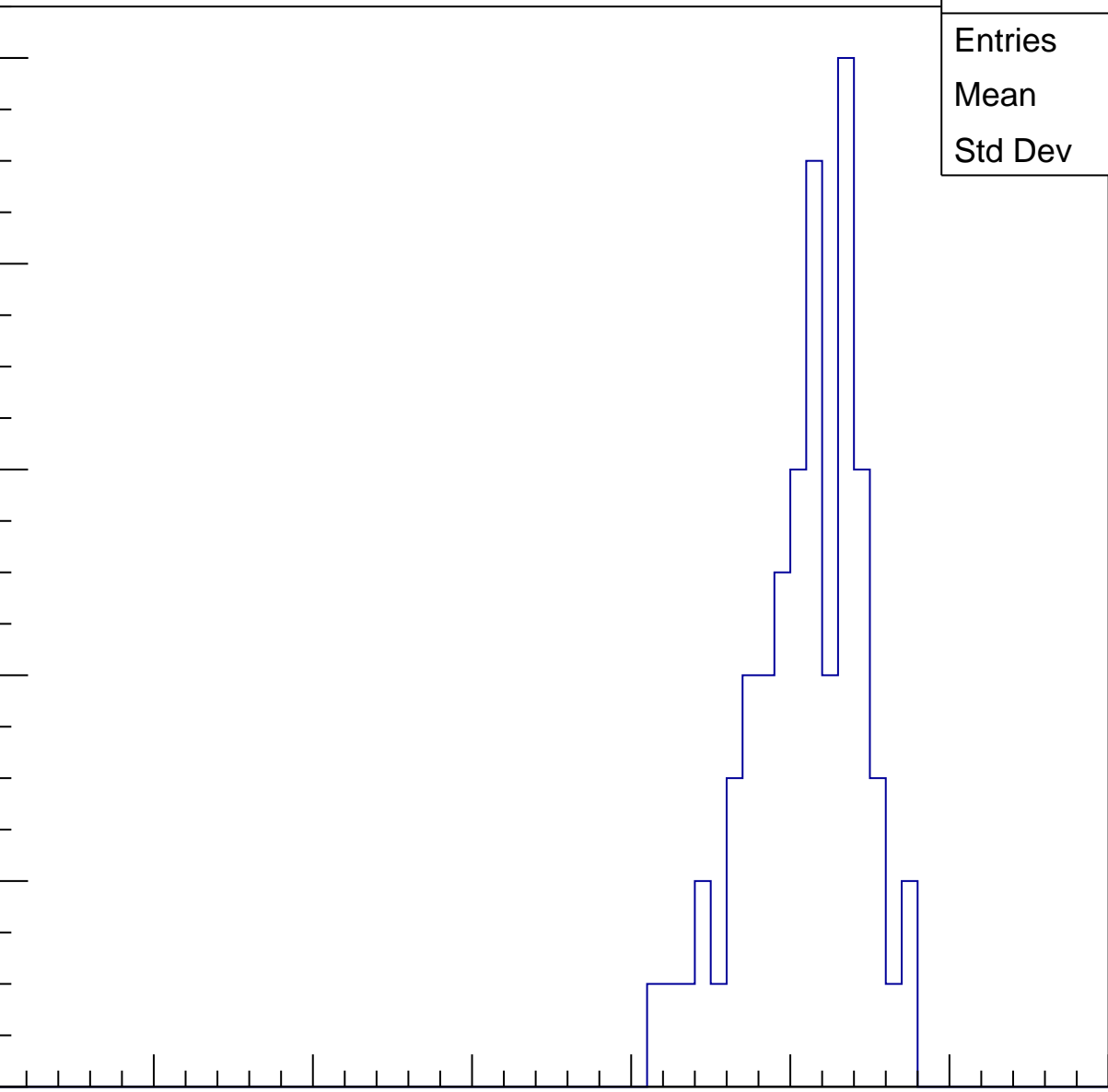
Entries	63
Mean	50.44
Std Dev	3.571

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

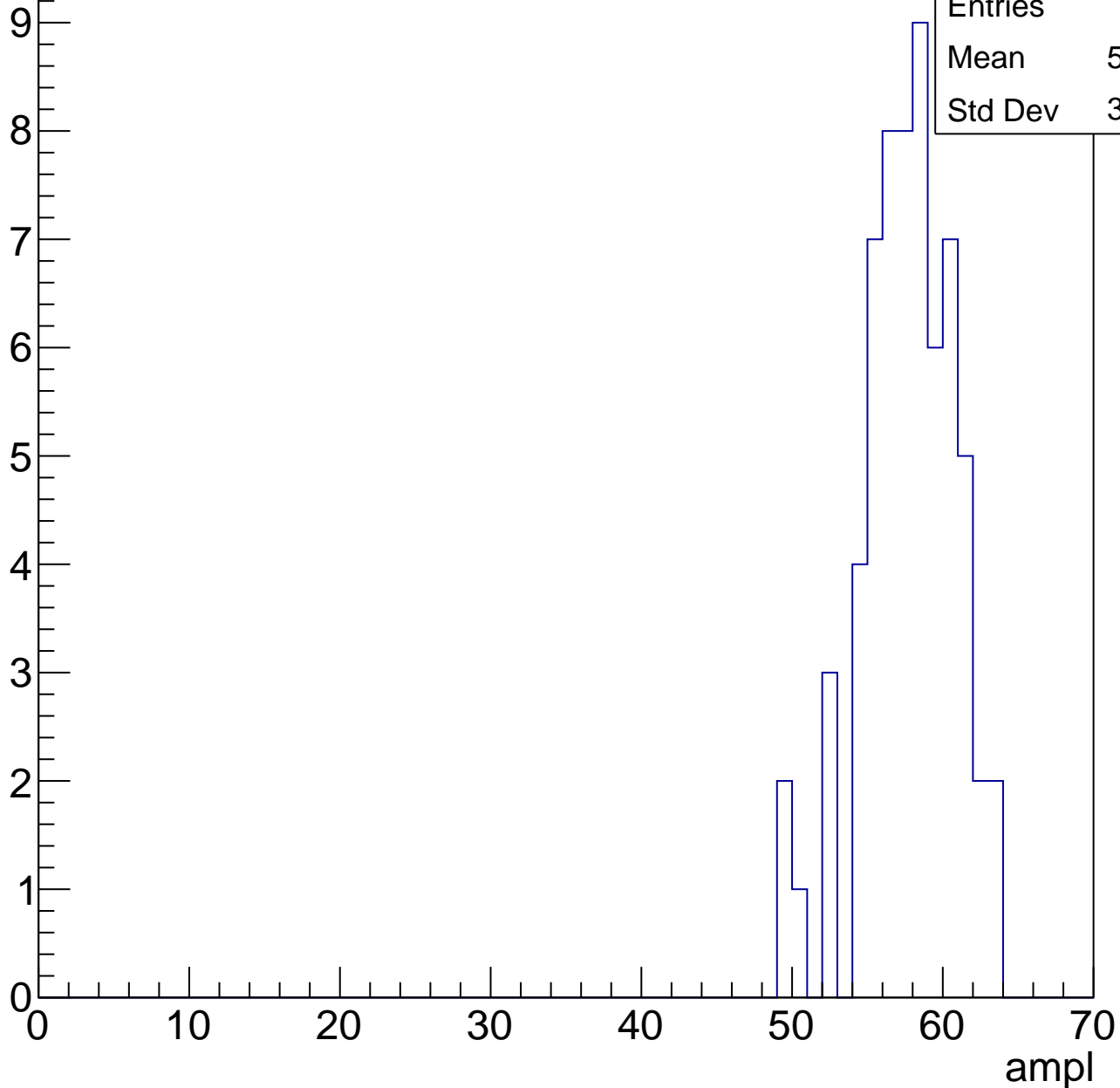


B1L100S, U12-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	57.19
Std Dev	3.112

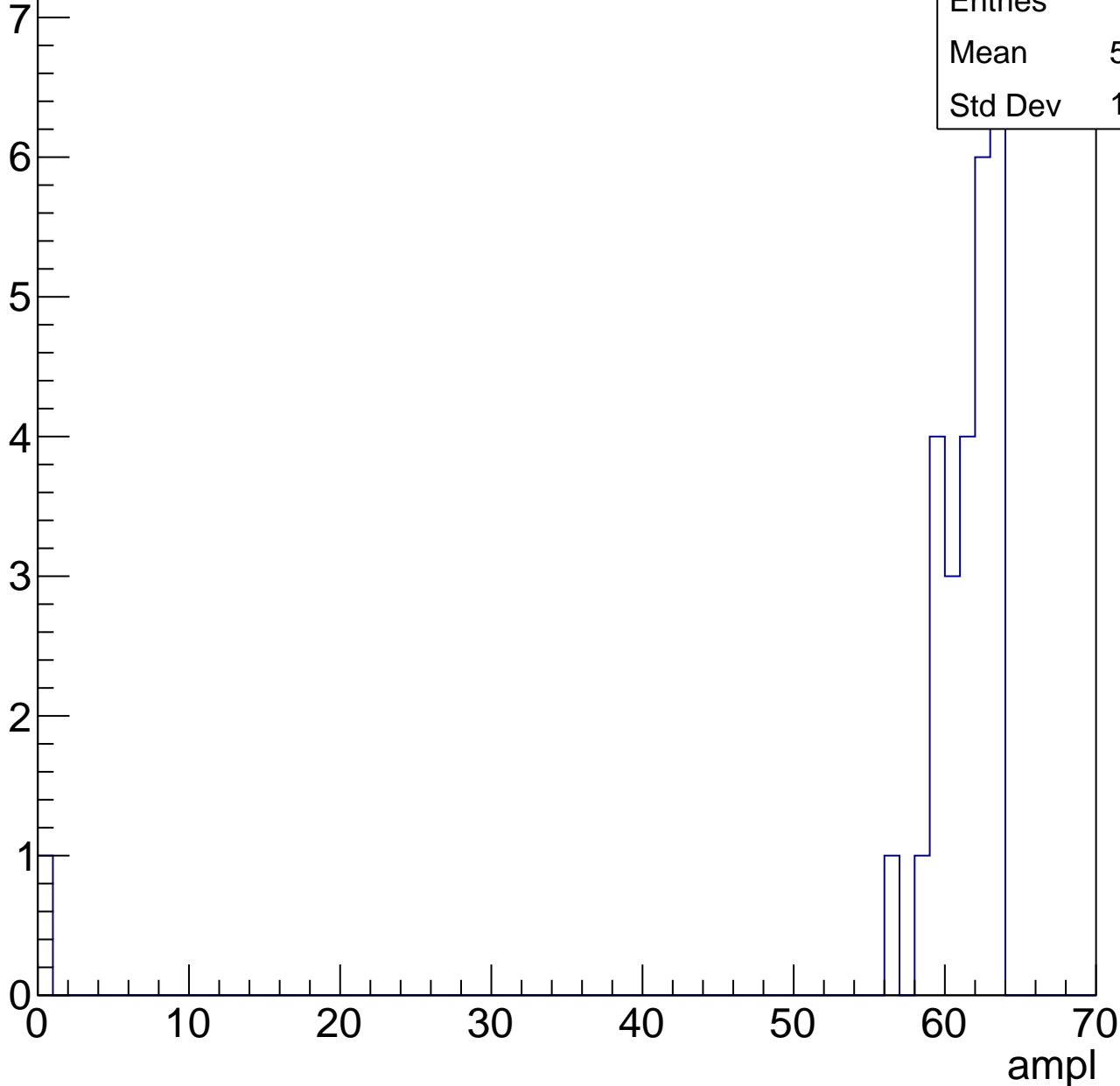


B1L100S, U12-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	27
Mean	58.78
Std Dev	11.67



B1L100S, U12-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch109, adc0

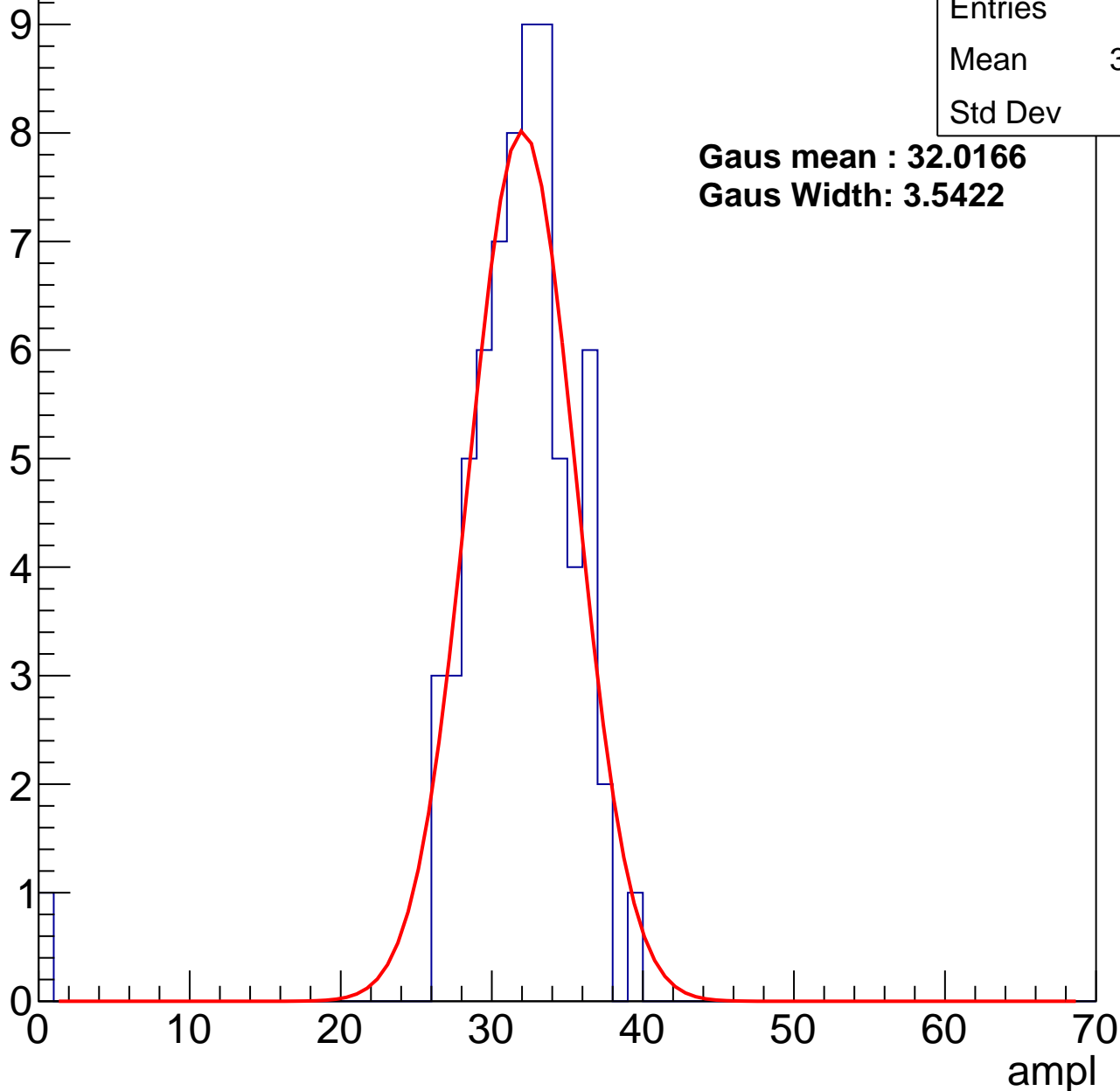
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	31.23
Std Dev	4.81

Gaus mean : 32.0166

Gaus Width: 3.5422



B1L100S, U12-ch109, adc1

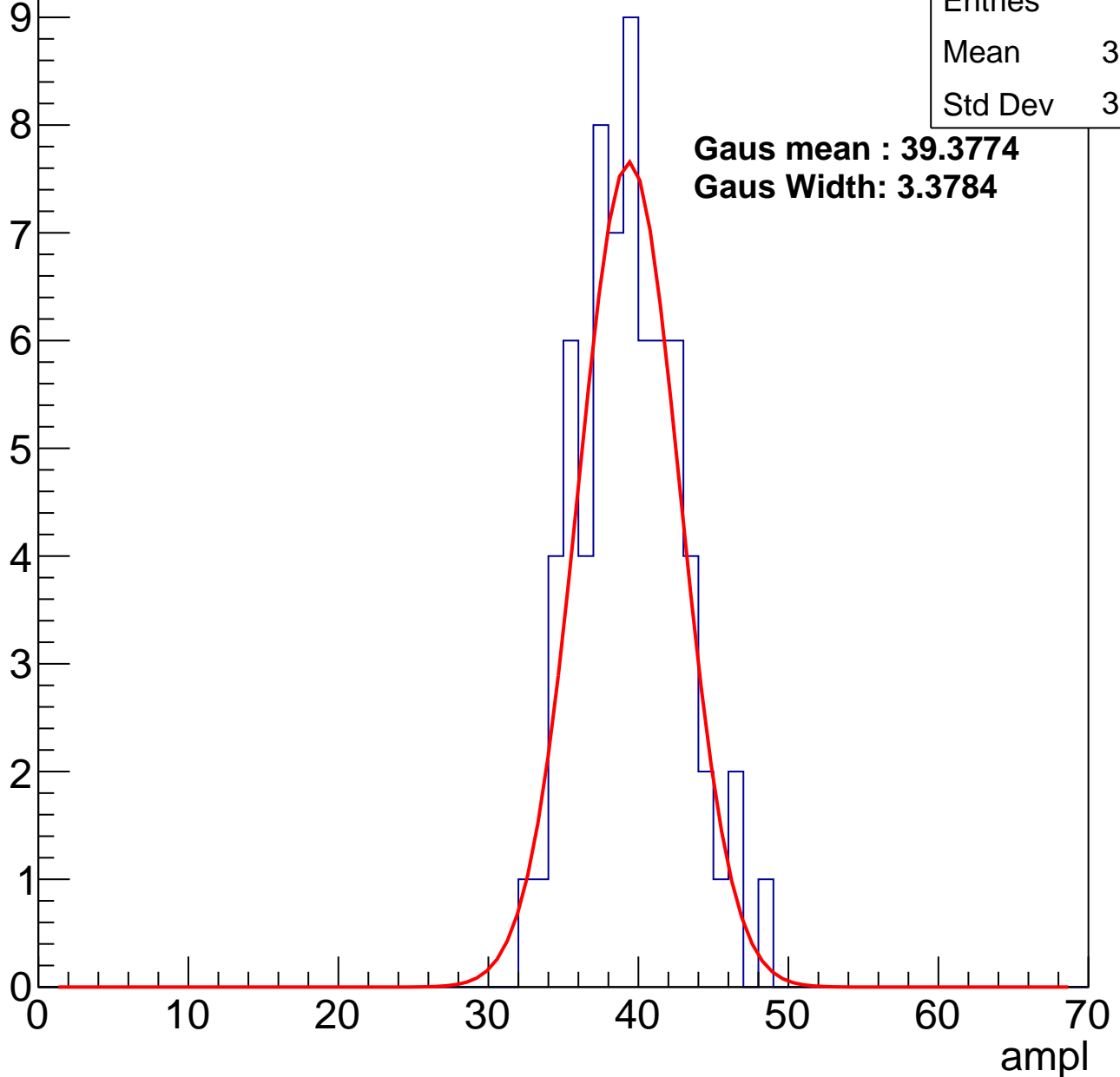
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	38.99
Std Dev	3.363

Gaus mean : 39.3774

Gaus Width: 3.3784



B1L100S, U12-ch109, adc2

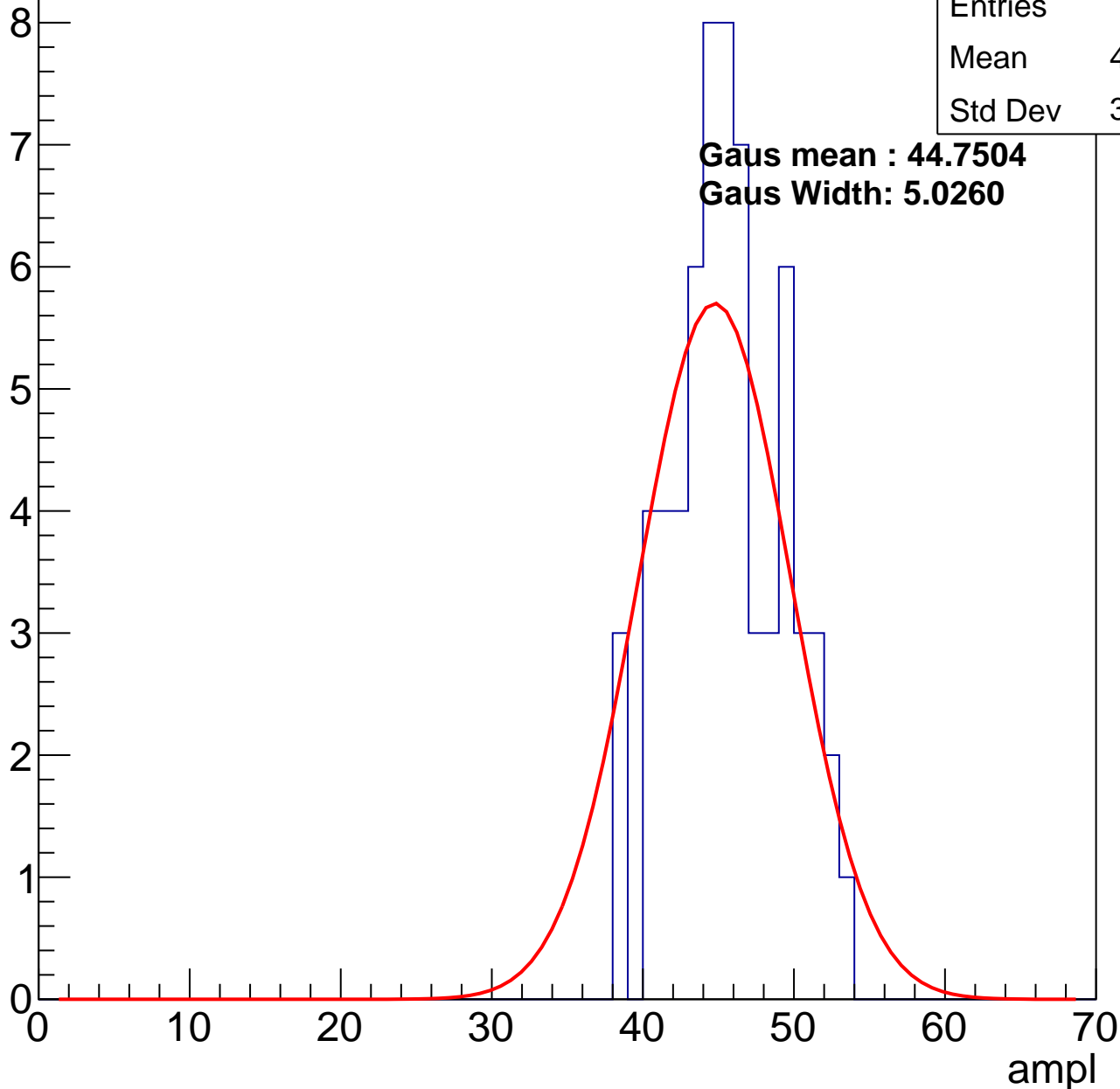
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	45.18
Std Dev	3.645

Gaus mean : 44.7504

Gaus Width: 5.0260

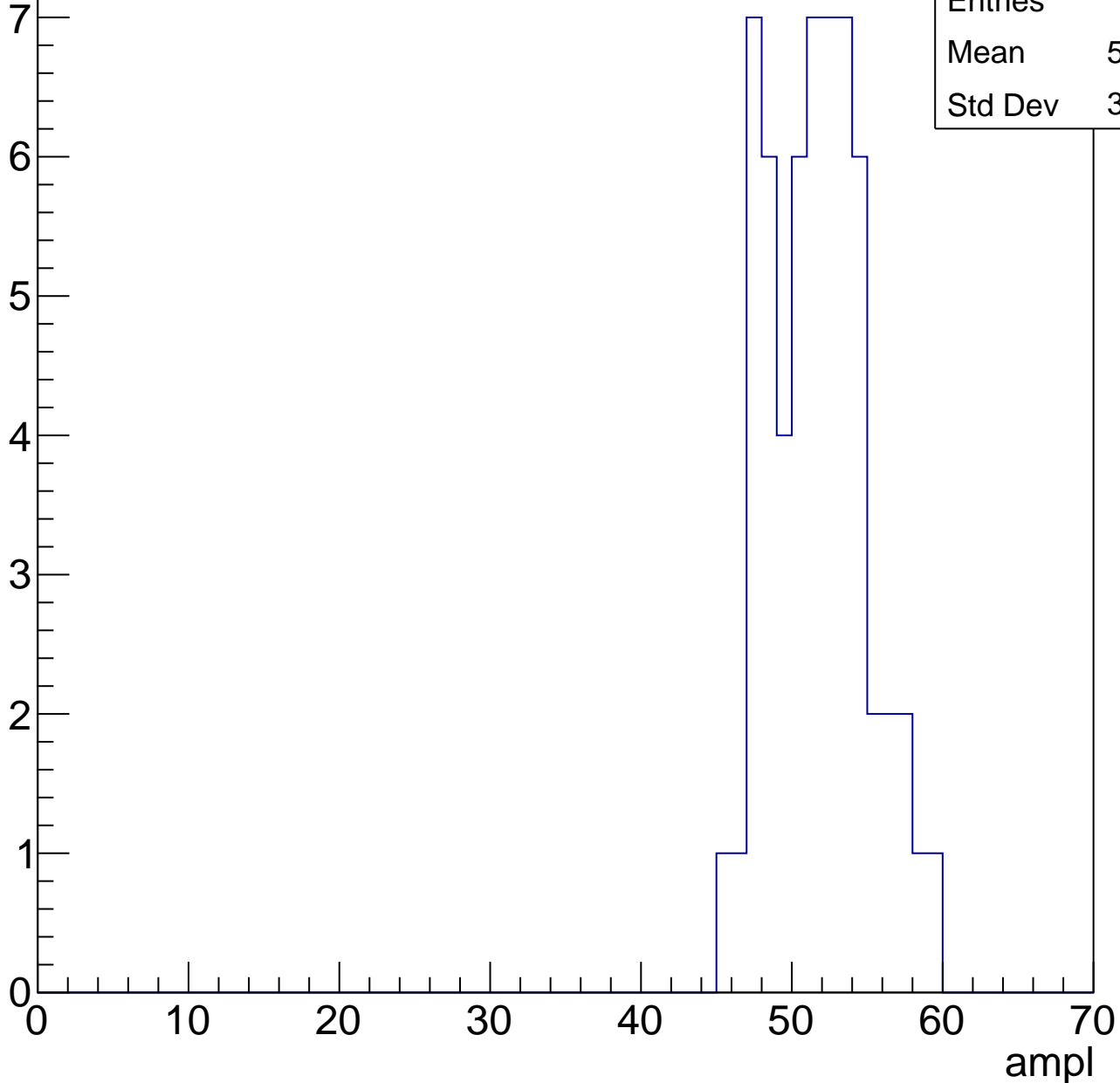


B1L100S, U12-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	51.22
Std Dev	3.168

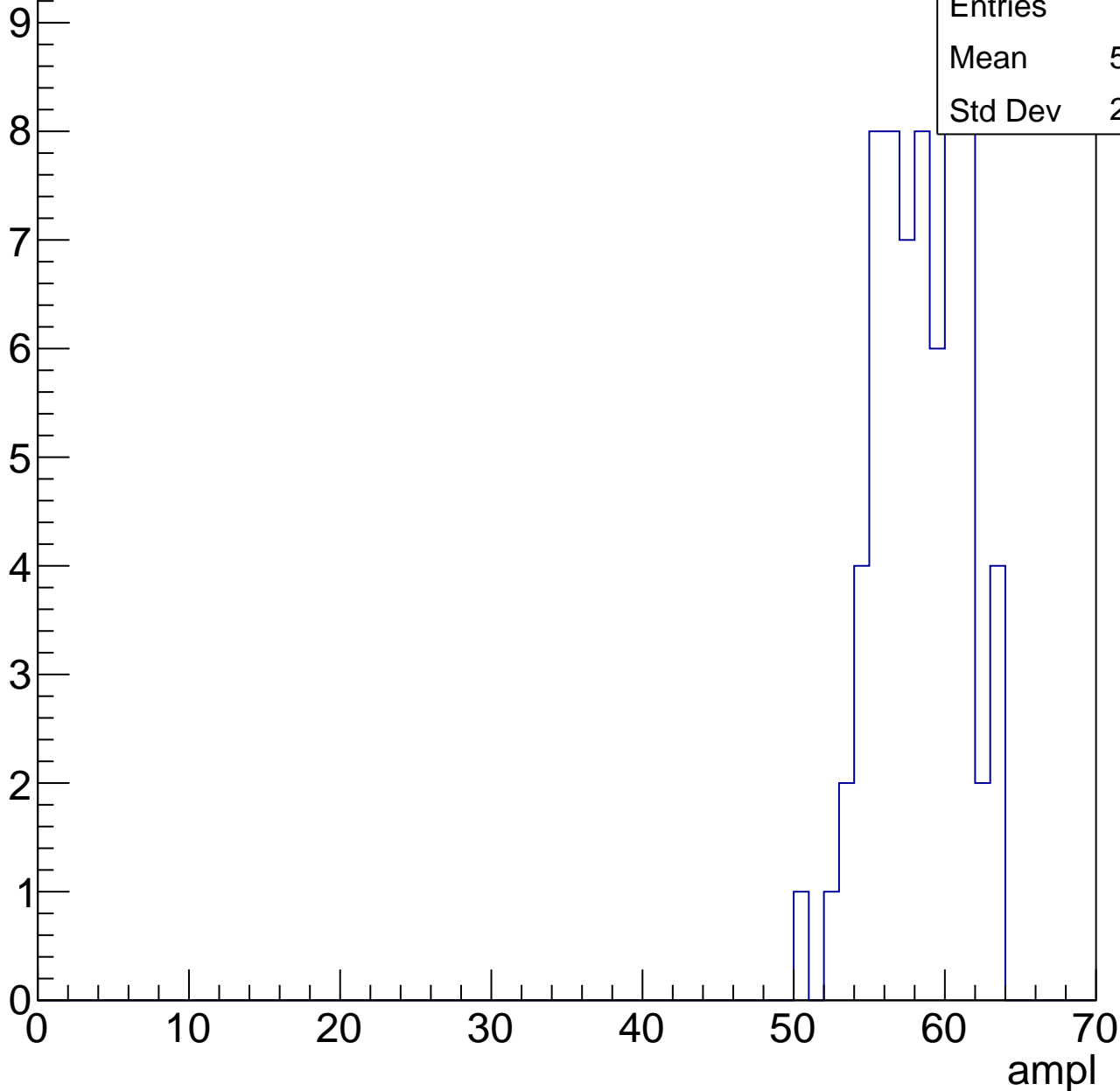


B1L100S, U12-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	57.84
Std Dev	2.893

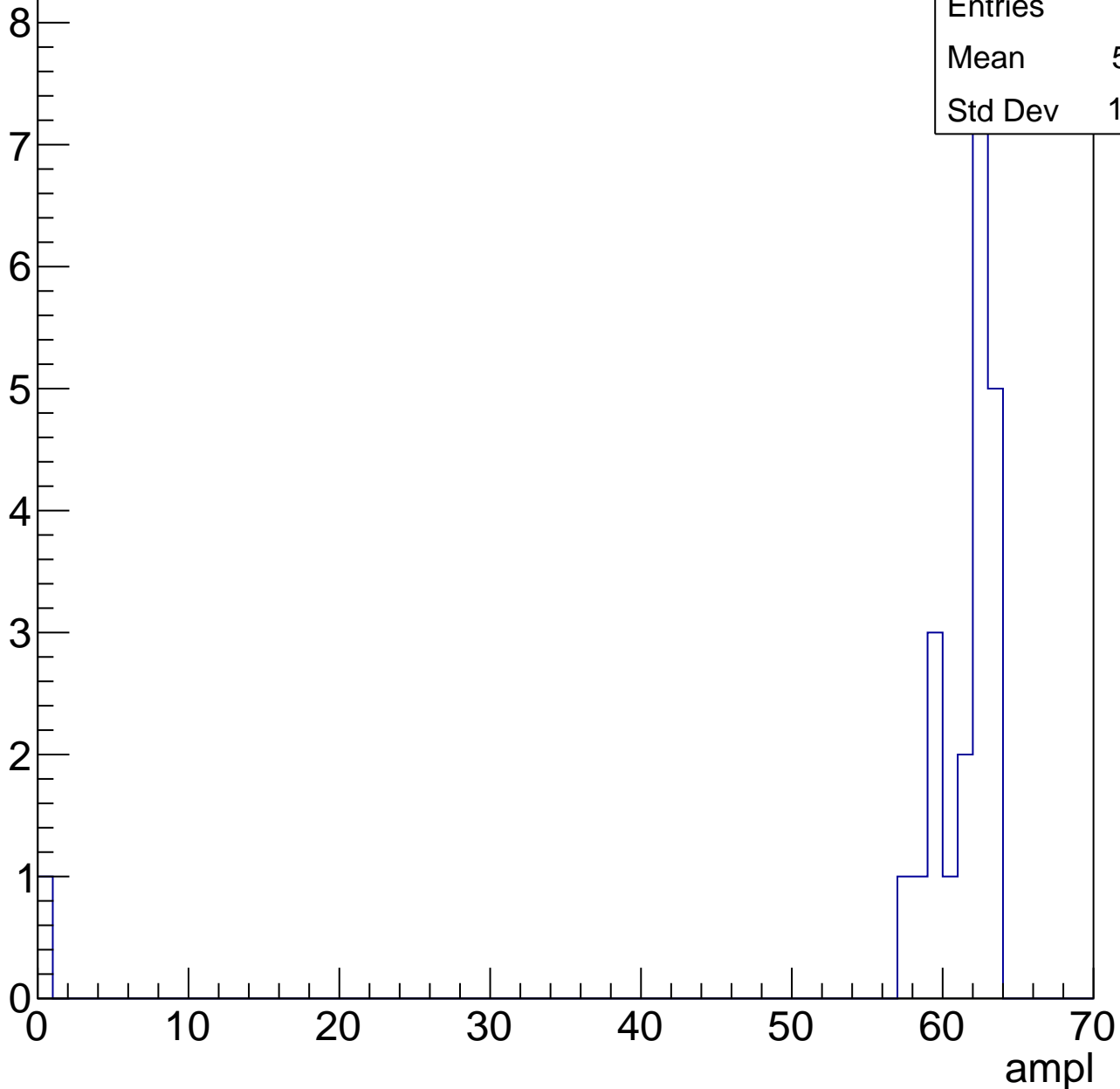


B1L100S, U12-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	22
Mean	58.41
Std Dev	12.86



B1L100S, U12-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	25
Std Dev	0

B1L100S, U12-ch110, adc0

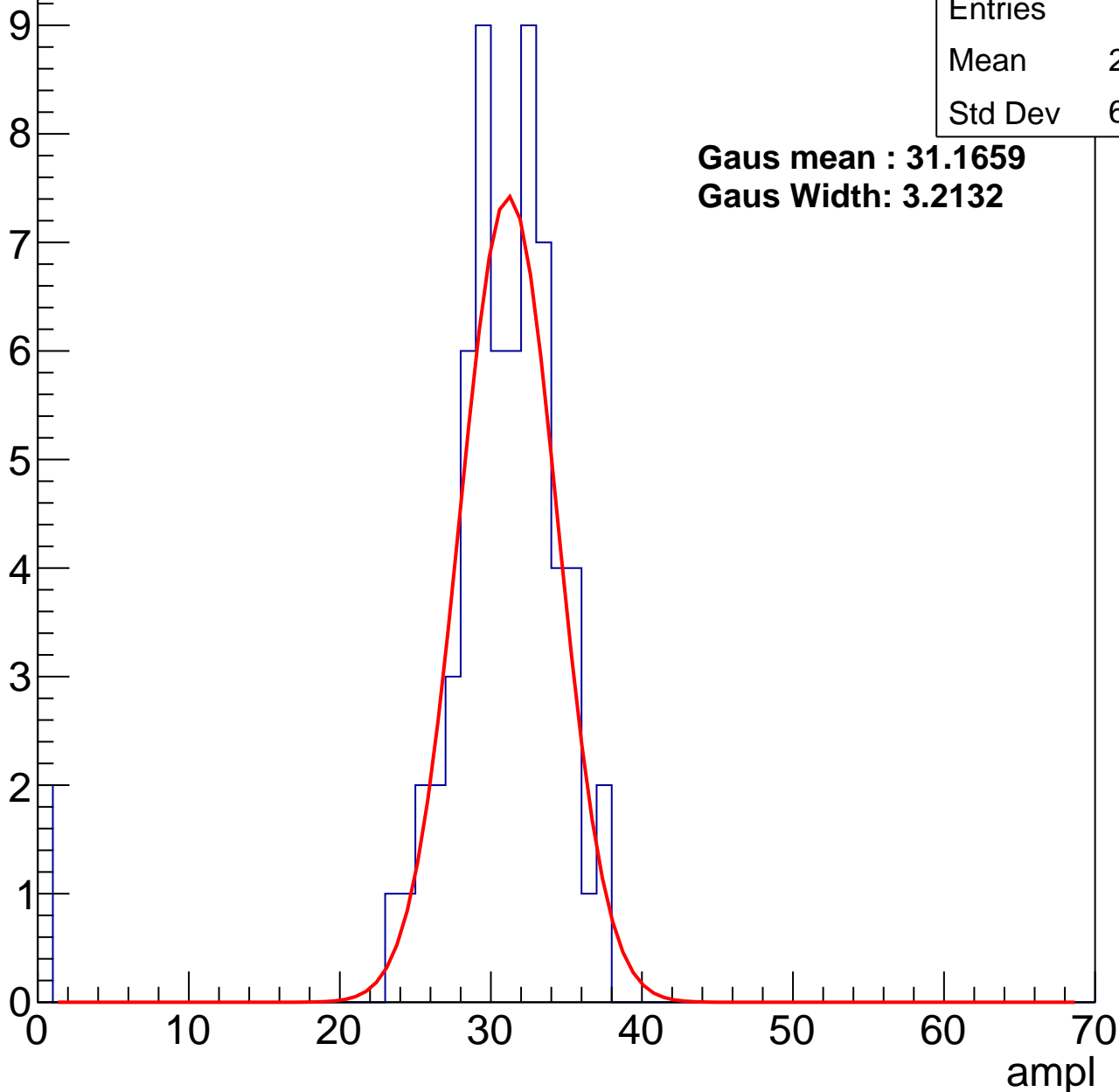
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.69
Std Dev	6.107

Gaus mean : 31.1659

Gaus Width: 3.2132



B1L100S, U12-ch110, adc1

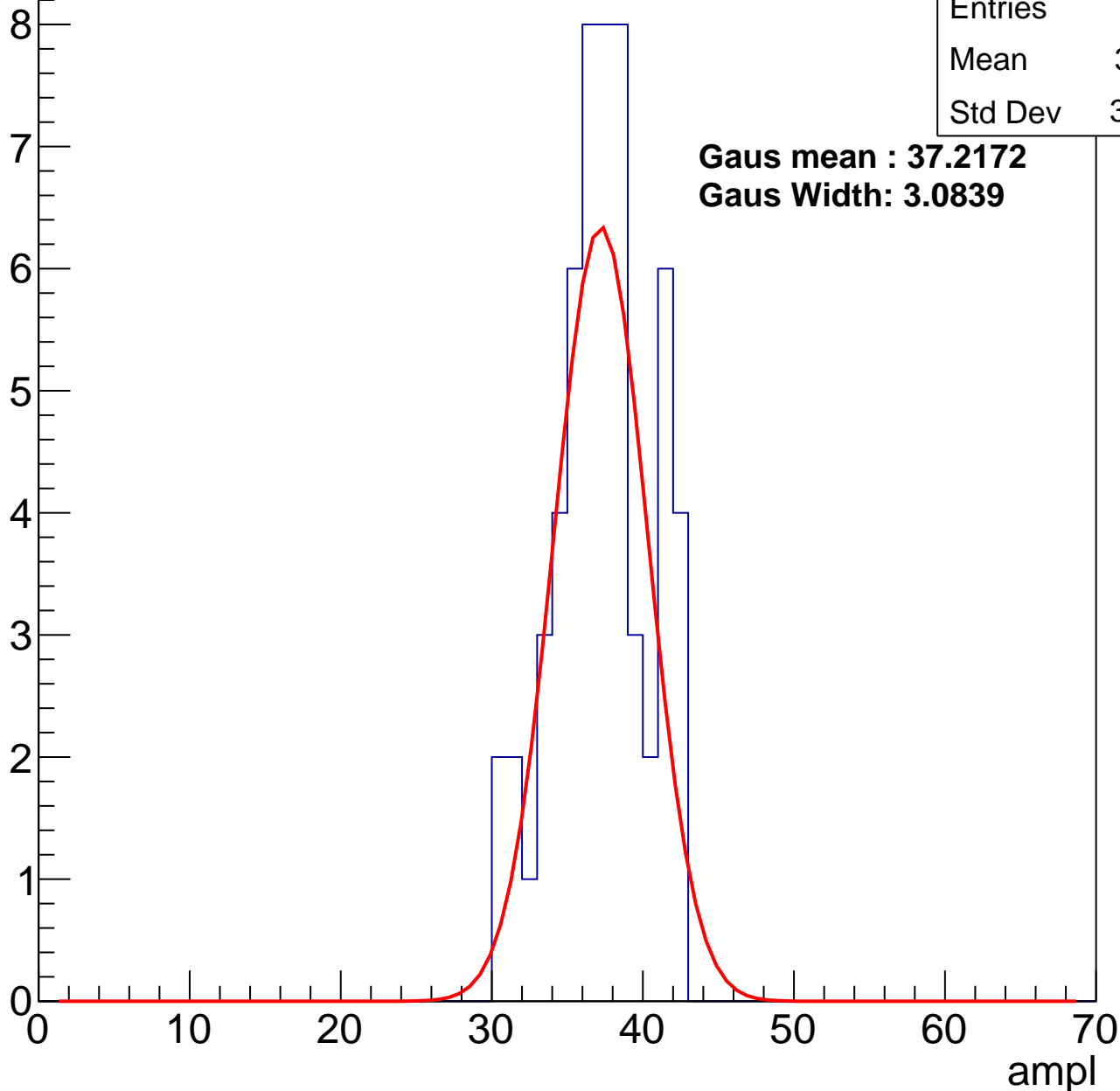
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	36.81
Std Dev	3.086

Gaus mean : 37.2172

Gaus Width: 3.0839



B1L100S, U12-ch110, adc2

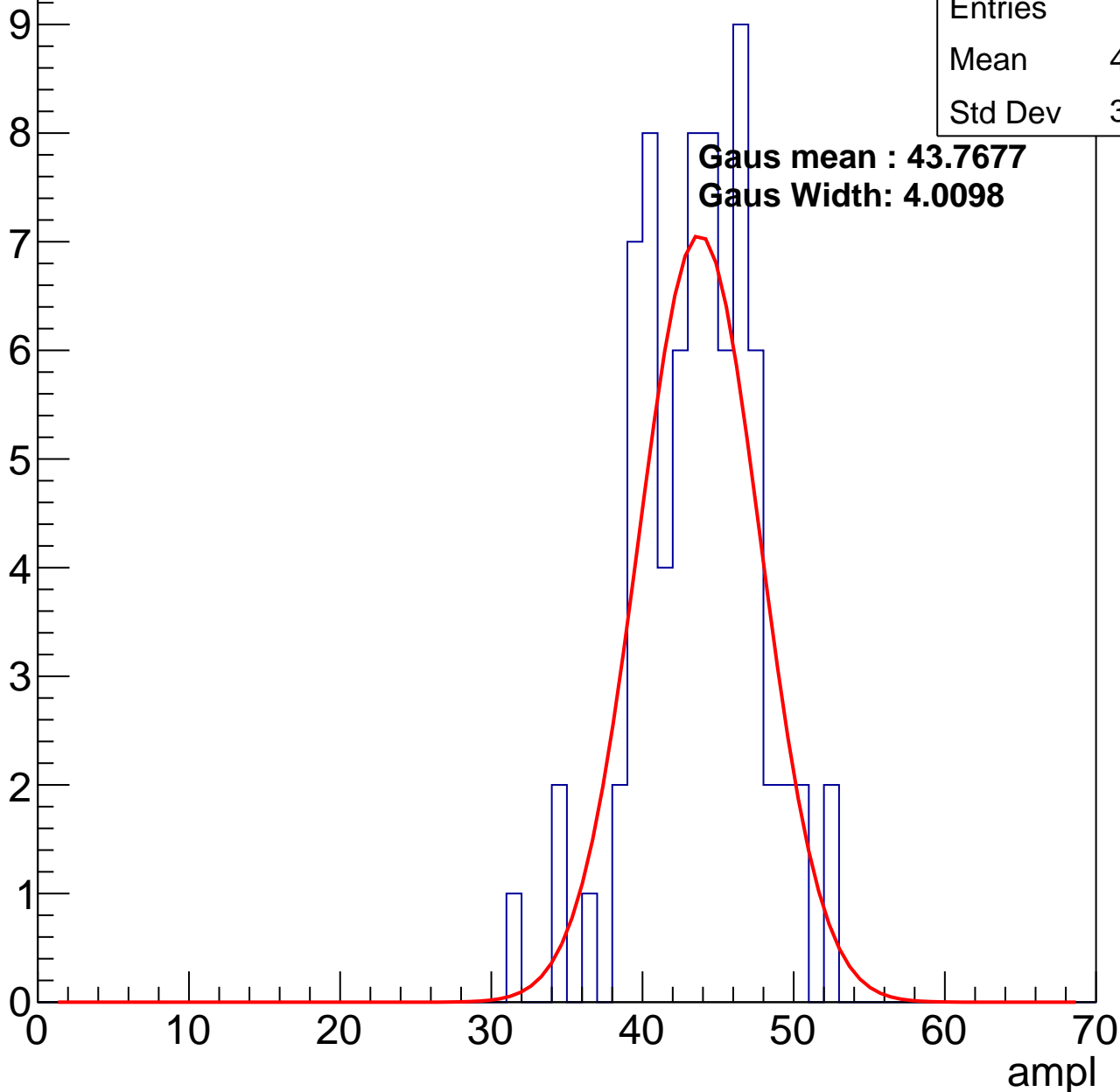
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	43.16
Std Dev	3.974

Gaus mean : 43.7677

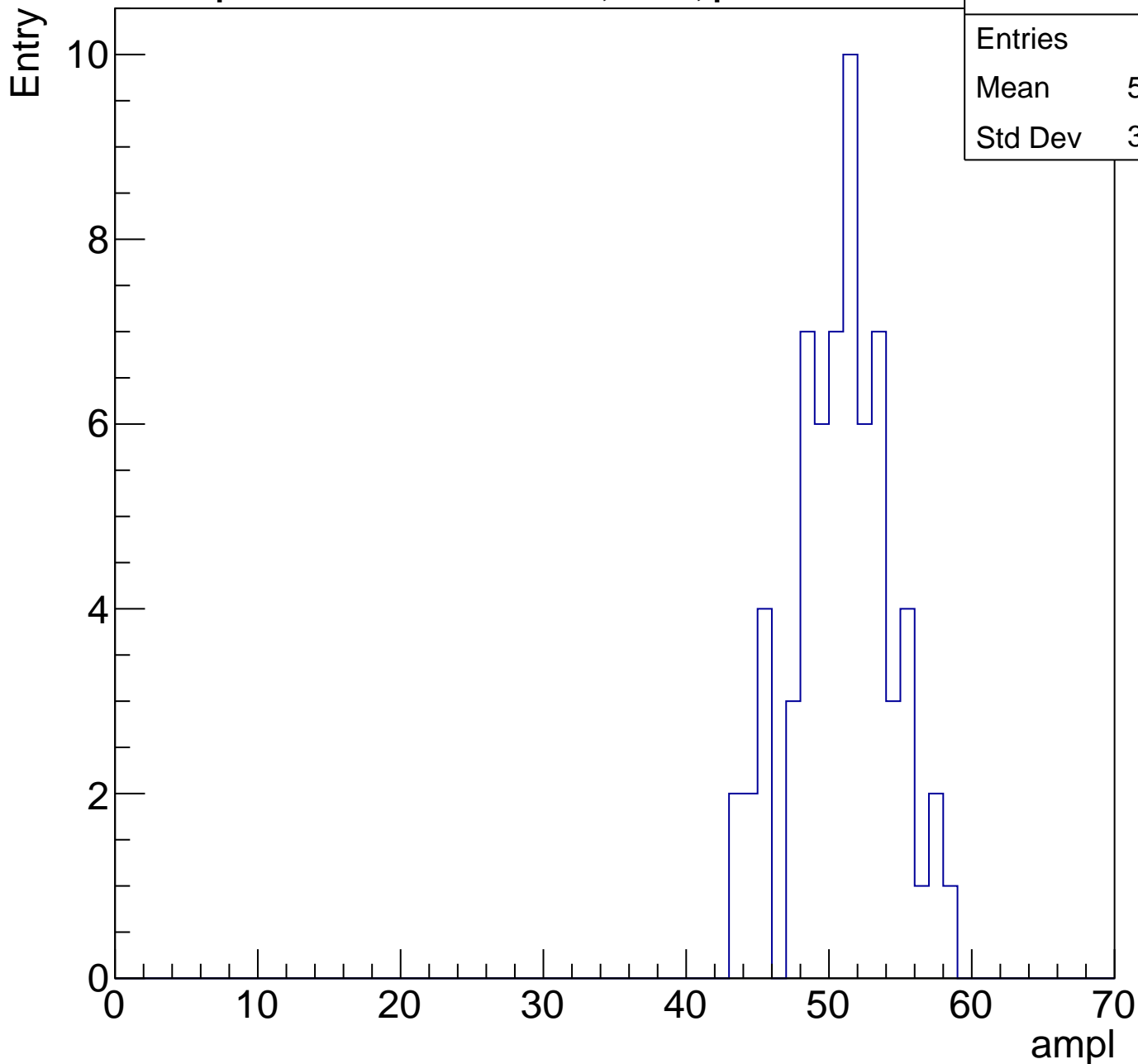
Gaus Width: 4.0098



B1L100S, U12-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	50.43
Std Dev	3.424

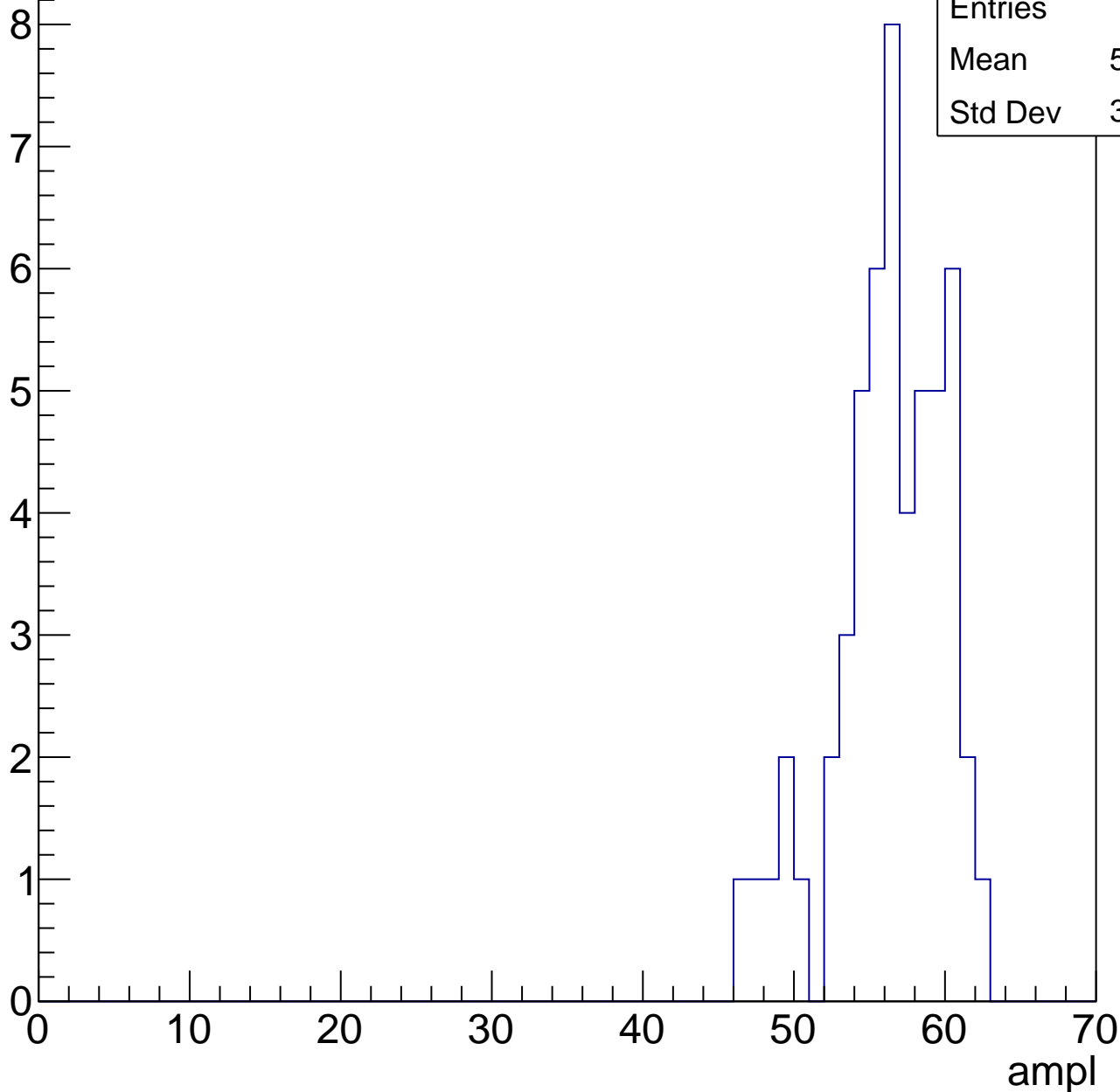


B1L100S, U12-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.79
Std Dev	3.667

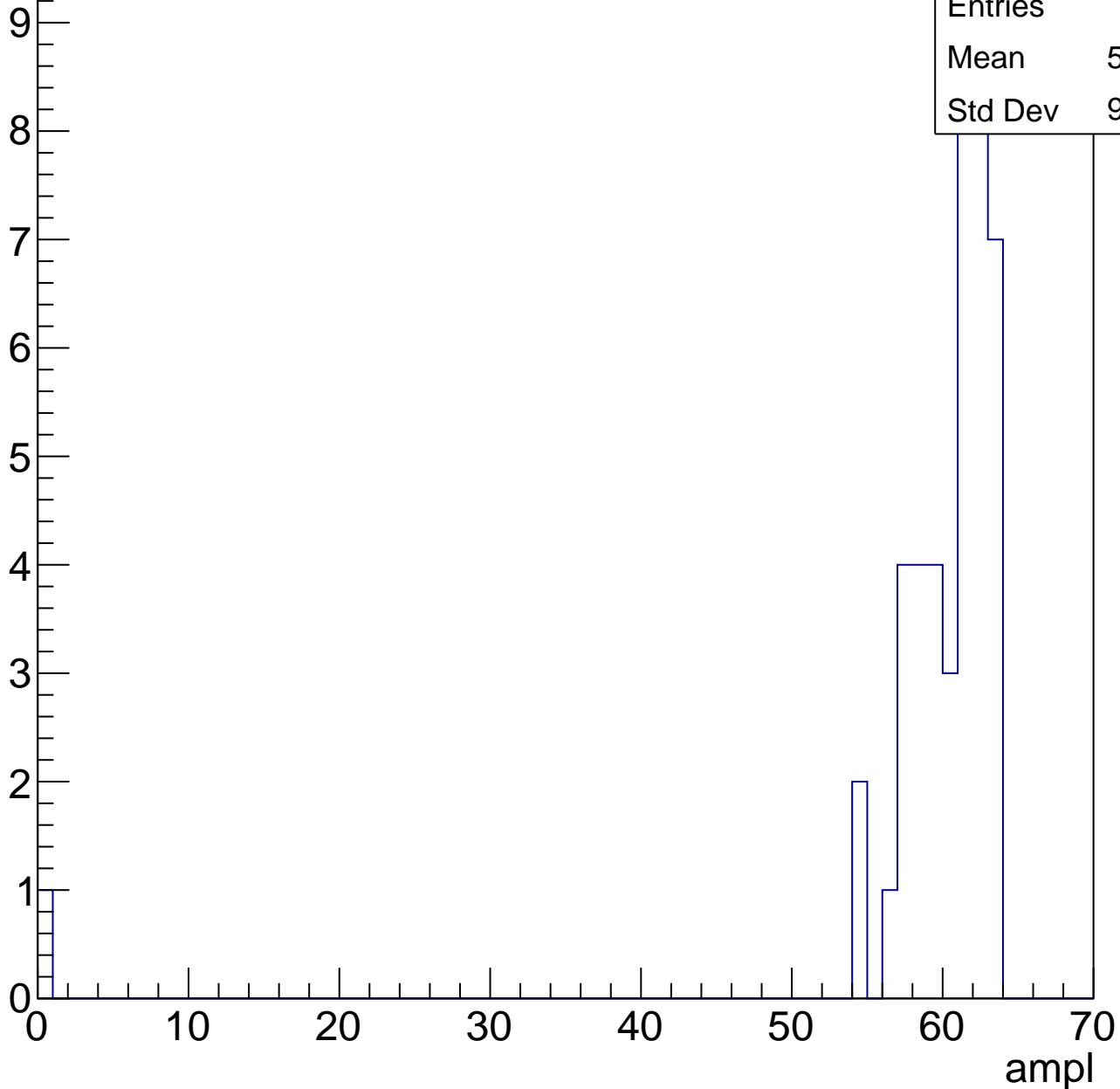


B1L100S, U12-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

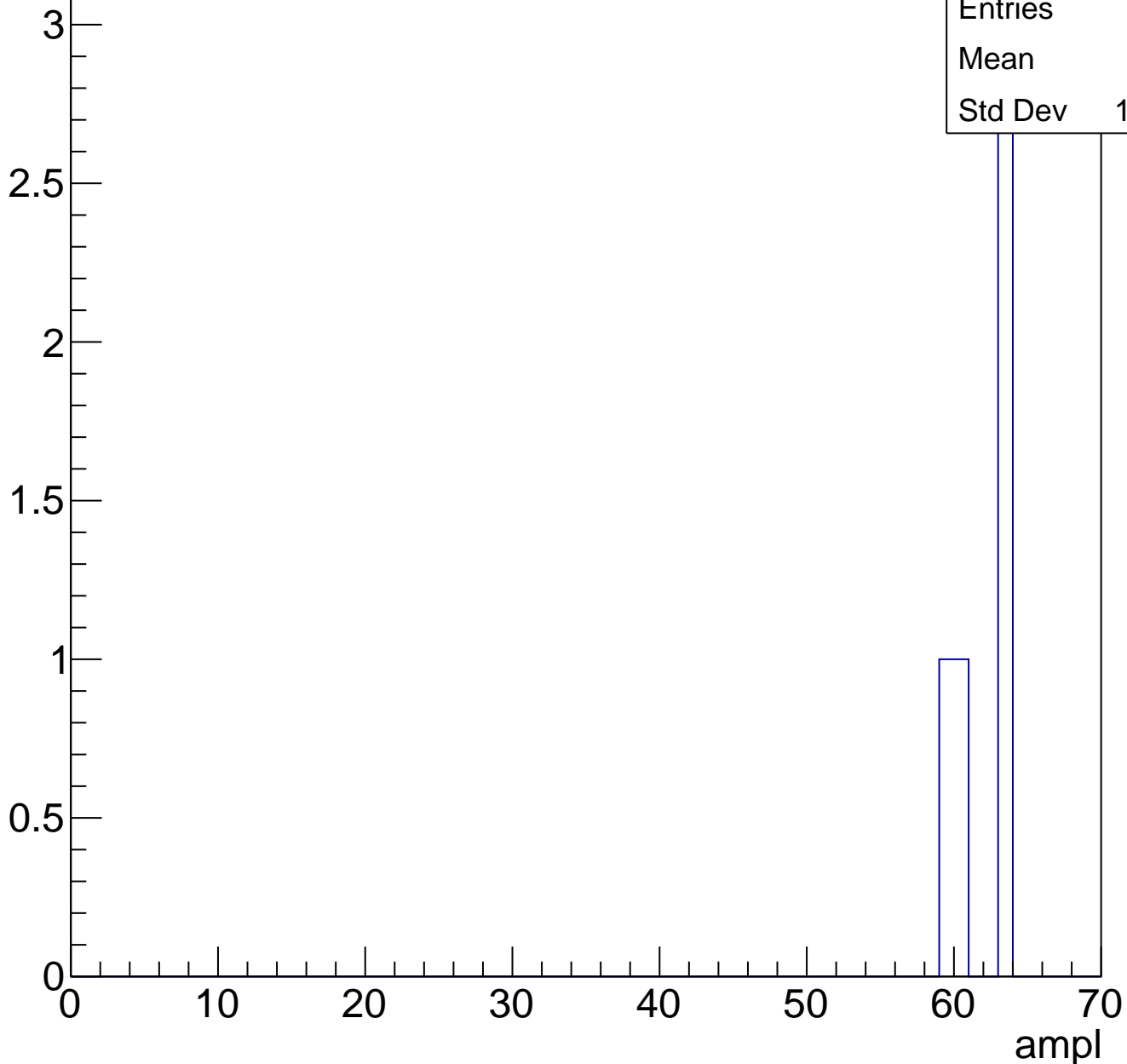
Entries	43
Mean	58.77
Std Dev	9.383



B1L100S, U12-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch111, adc0

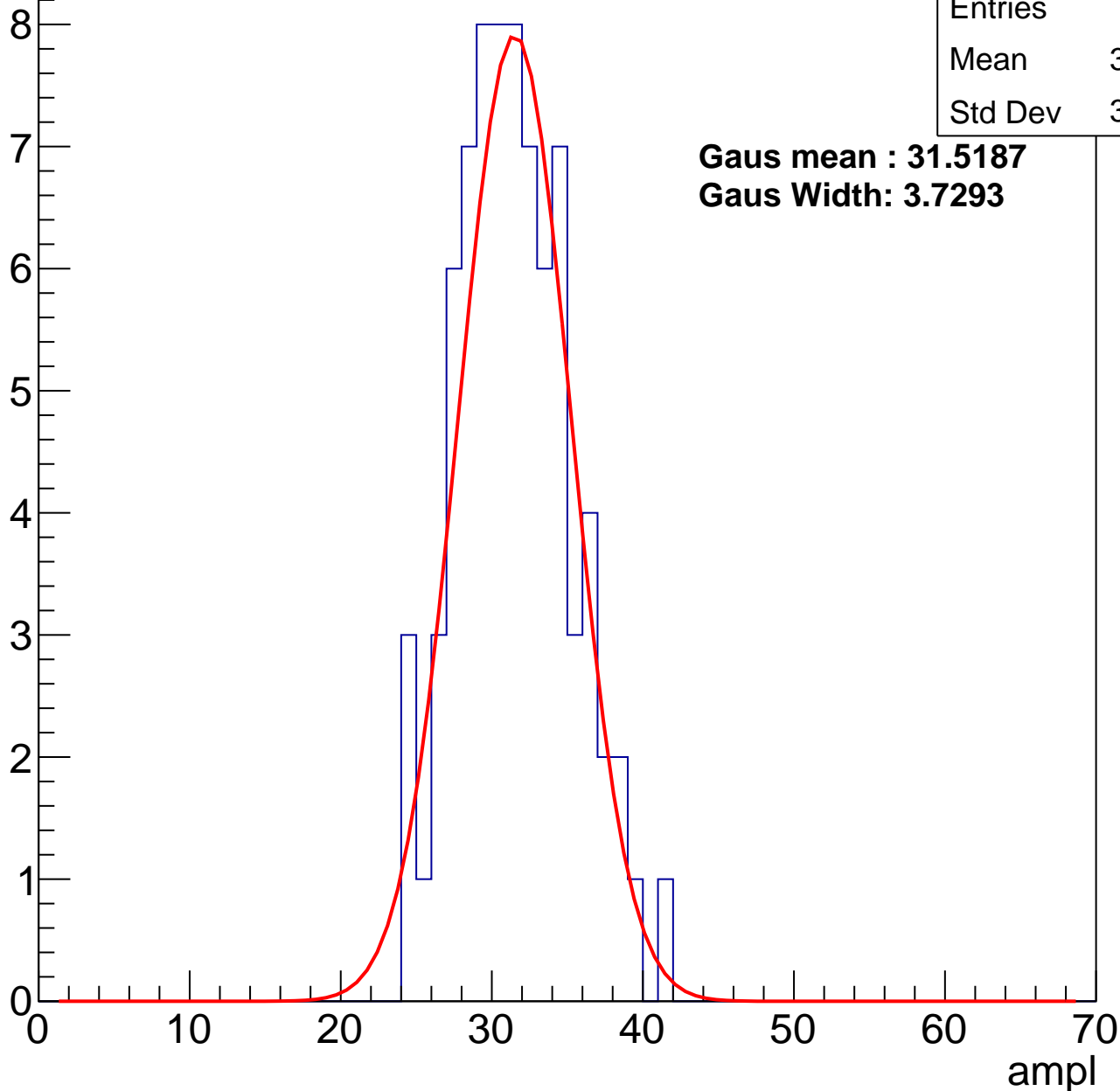
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	31.06
Std Dev	3.662

Gaus mean : 31.5187

Gaus Width: 3.7293



B1L100S, U12-ch111, adc1

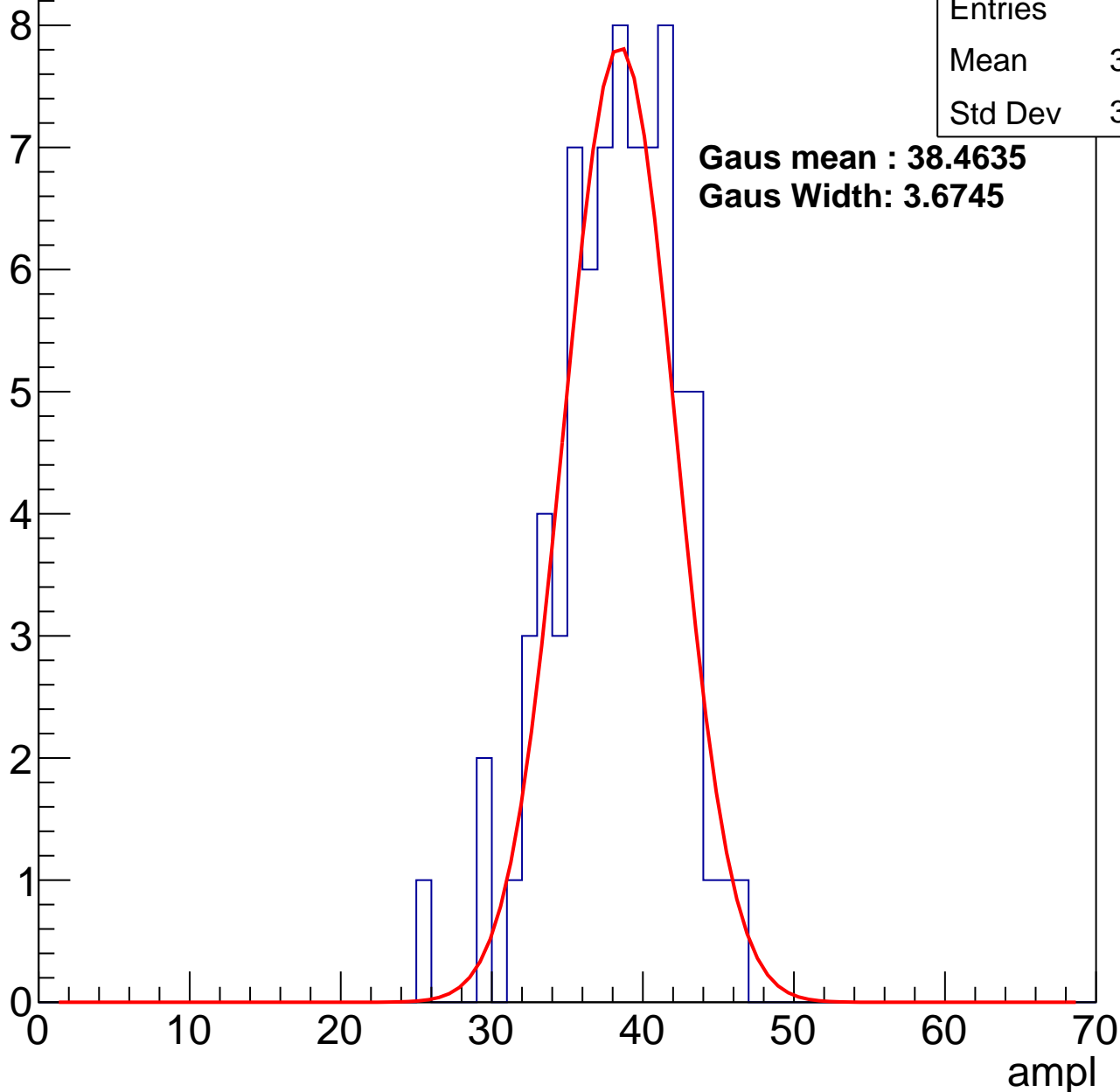
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	37.78
Std Dev	3.927

Gaus mean : 38.4635

Gaus Width: 3.6745



B1L100S, U12-ch111, adc2

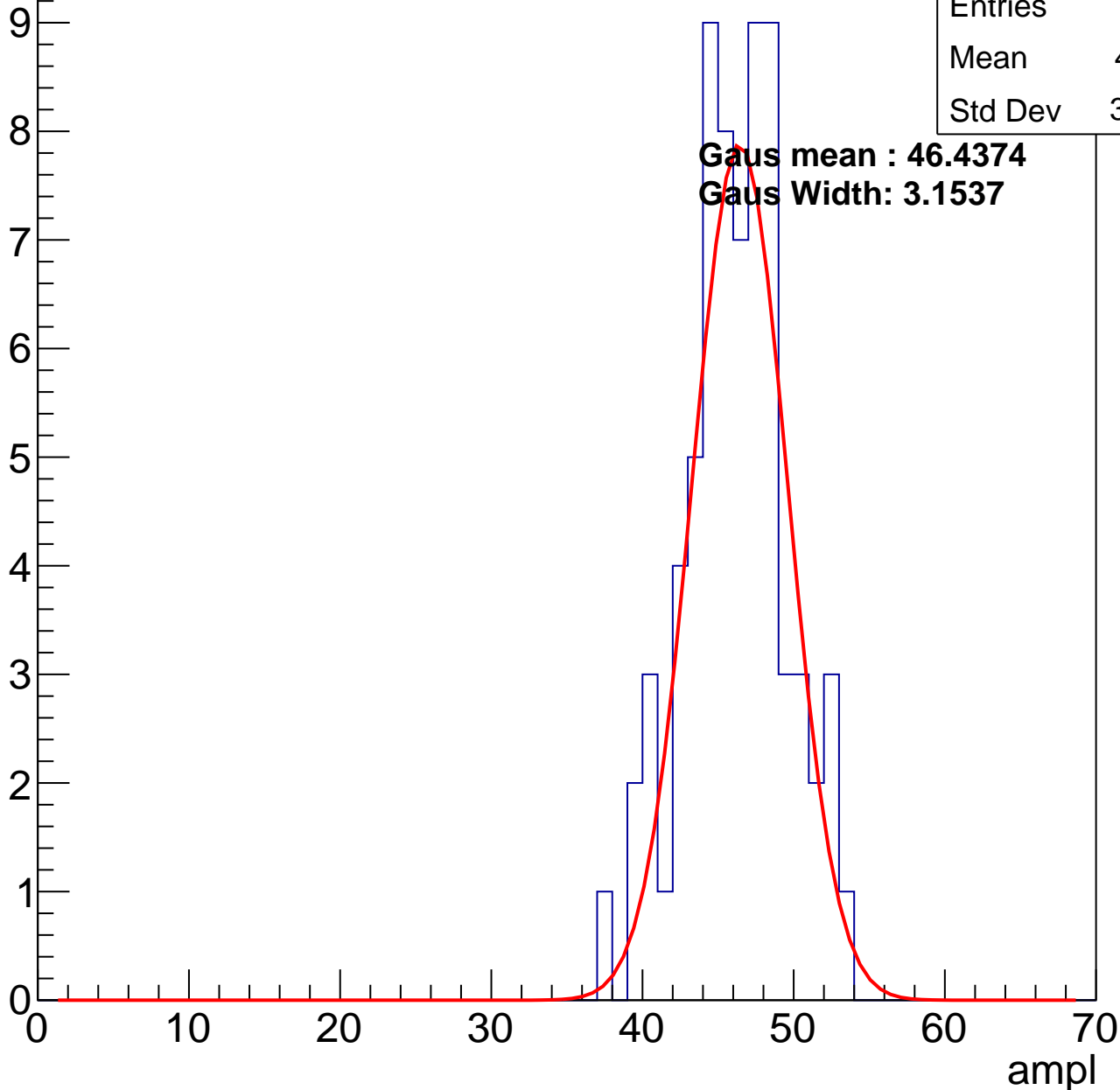
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	45.71
Std Dev	3.352

Gaus mean : 46.4374

Gaus Width: 3.1537

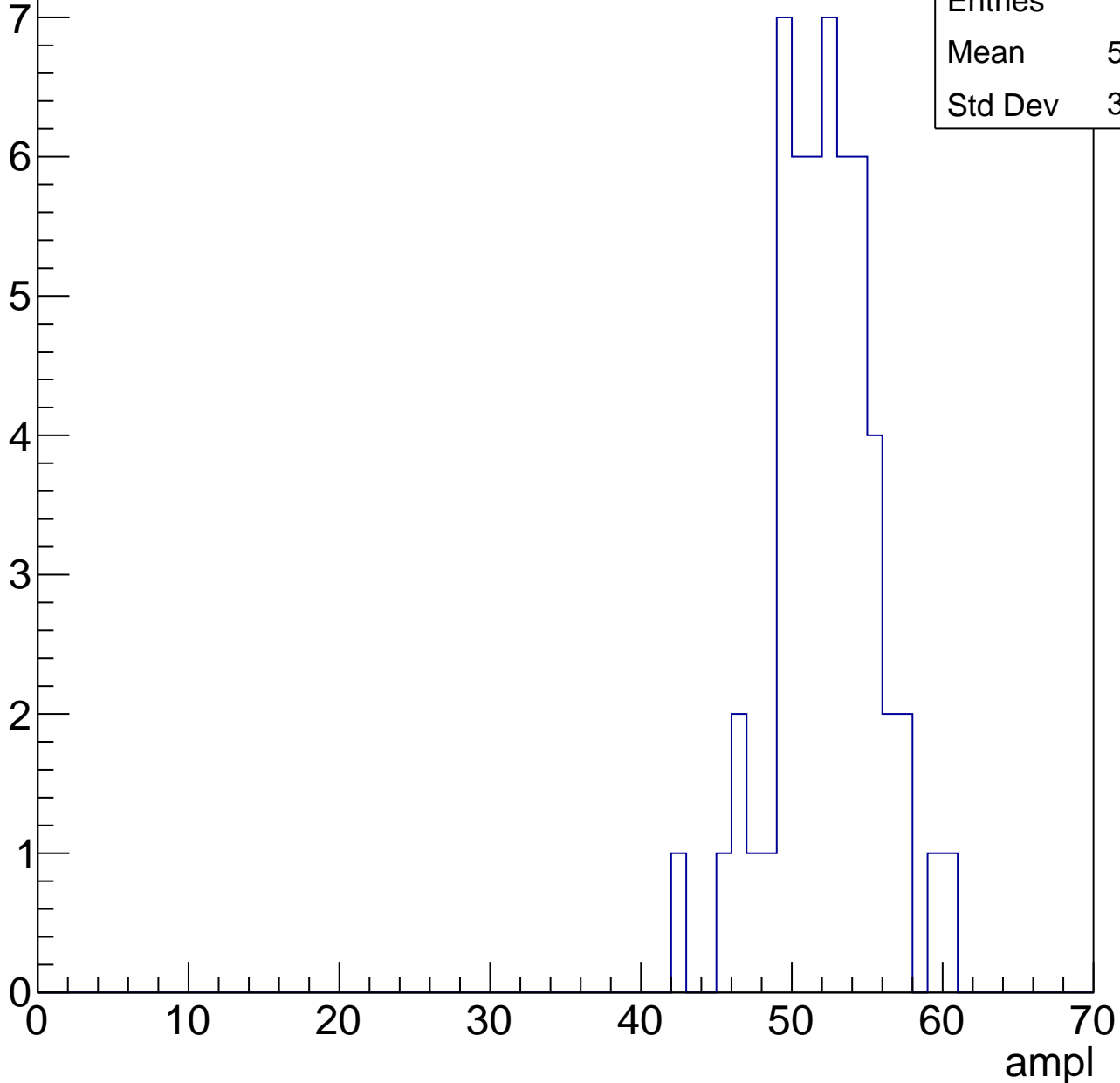


B1L100S, U12-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

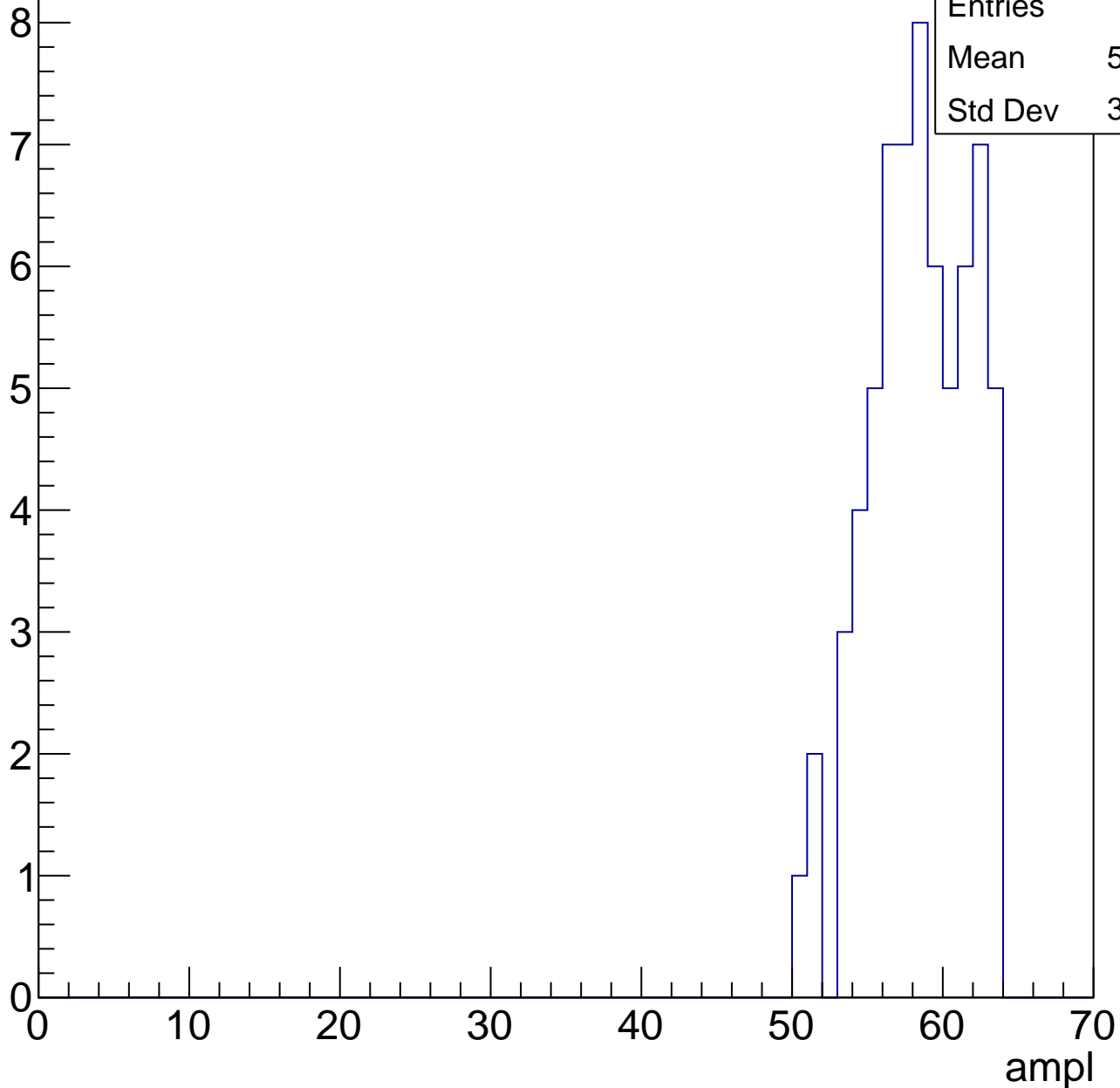
Entries	54
Mean	51.74
Std Dev	3.368



B1L100S, U12-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

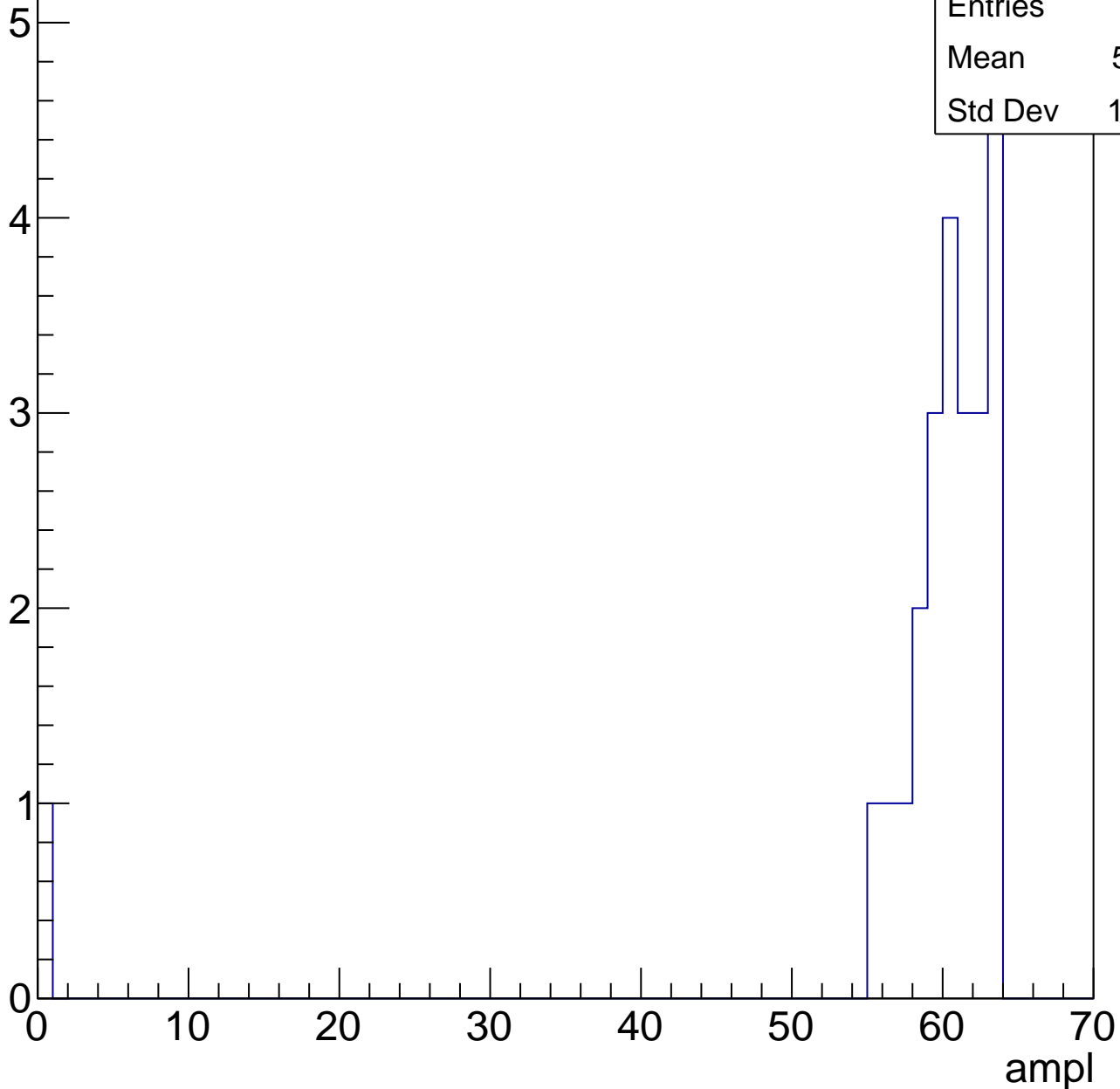


B1L100S, U12-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	57.71
Std Dev	12.24



B1L100S, U12-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	23
Std Dev	0

B1L100S, U12-ch112, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	31.13
Std Dev	3.476

Gaus mean : 31.5535

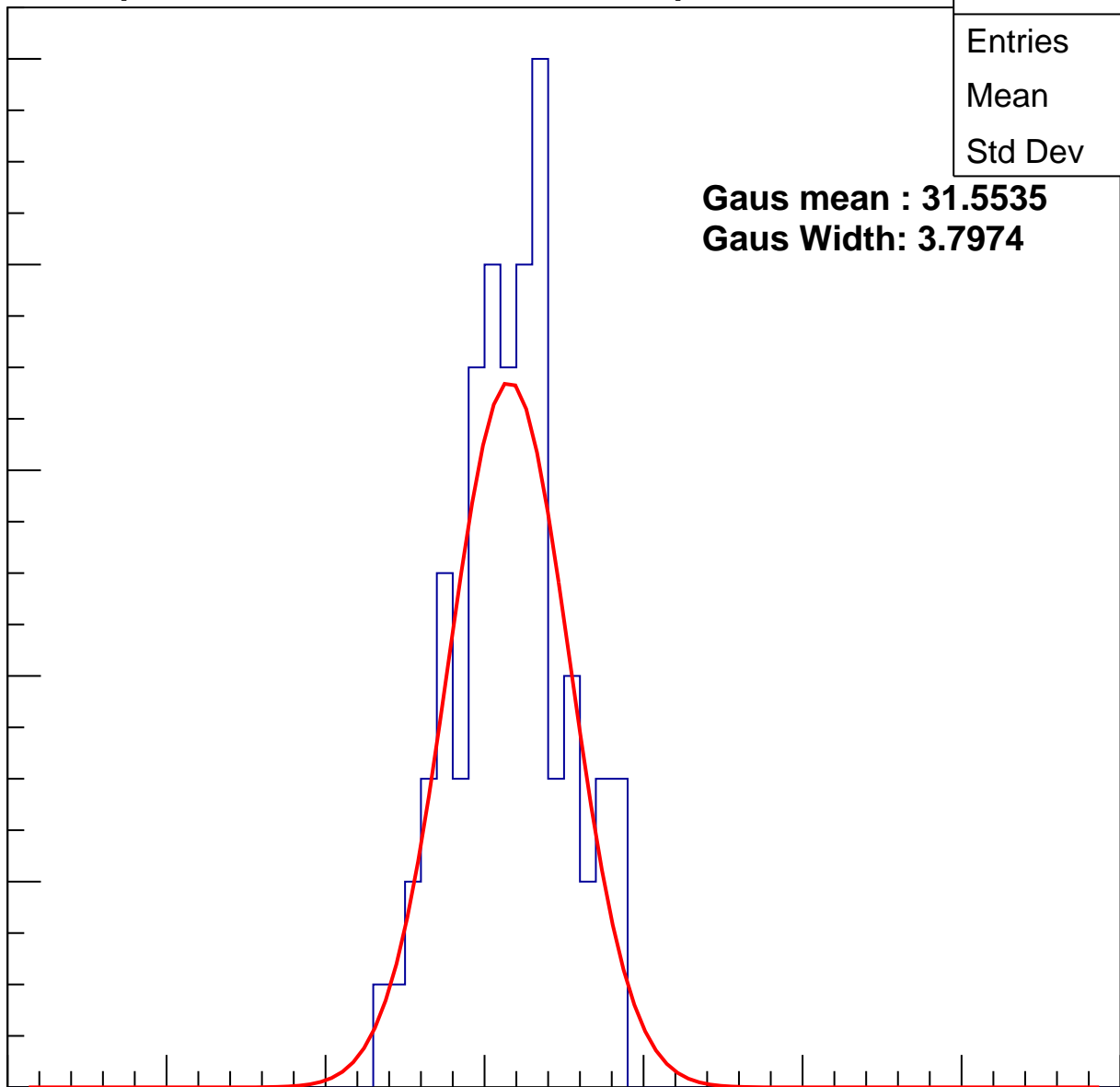
Gaus Width: 3.7974

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch112, adc1

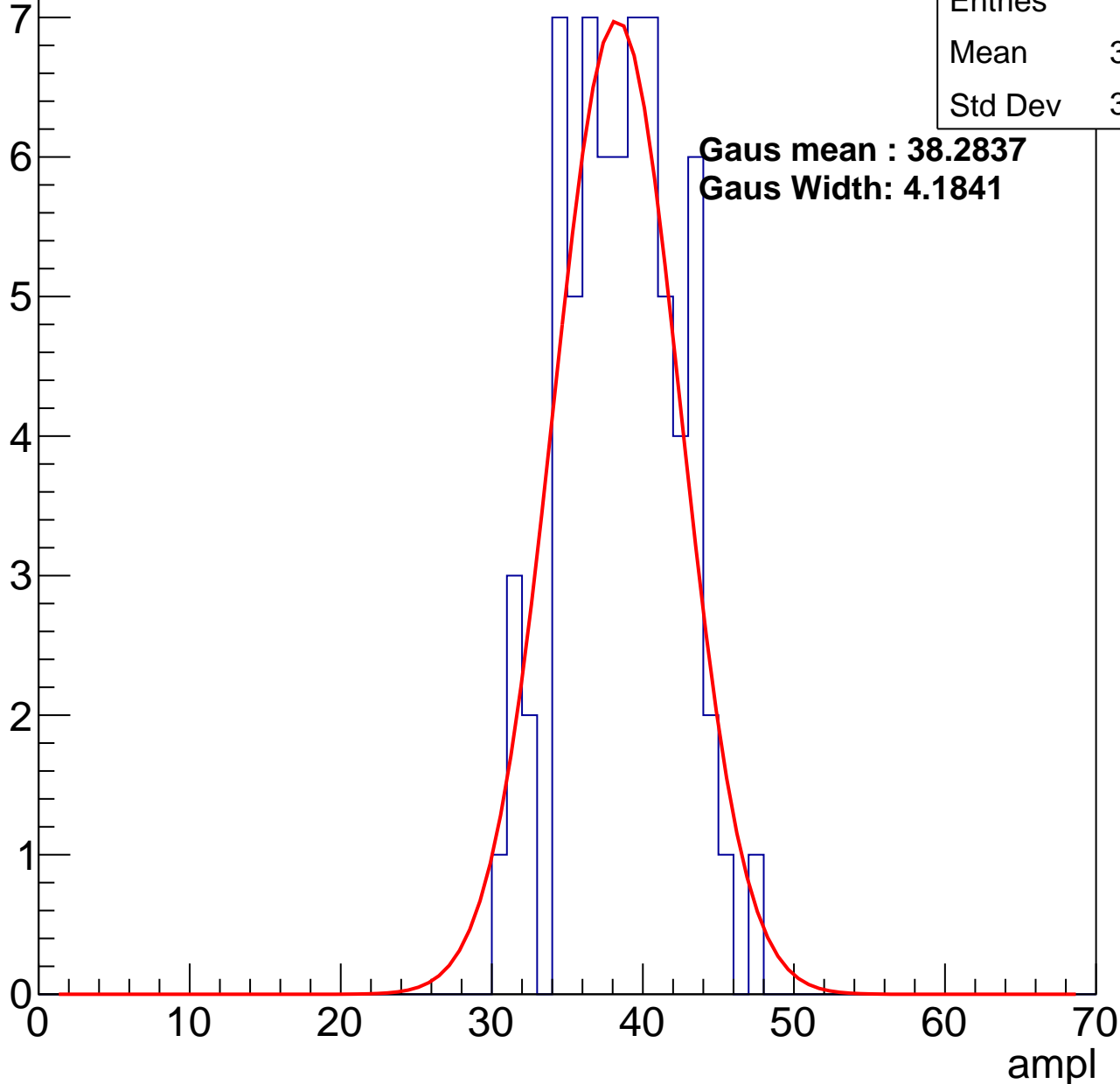
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	38.09
Std Dev	3.722

Gaus mean : 38.2837

Gaus Width: 4.1841



B1L100S, U12-ch112, adc2

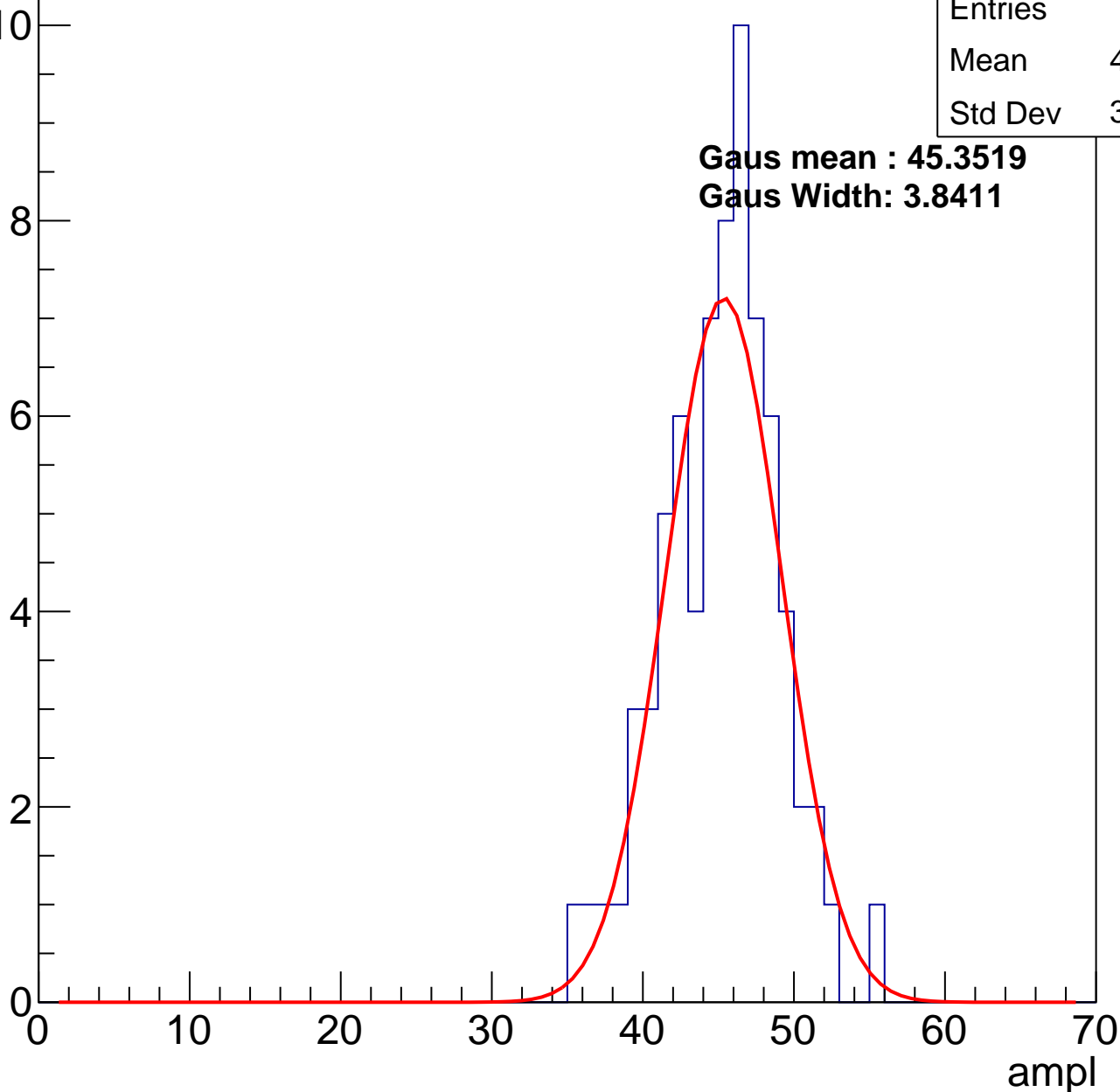
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	44.68
Std Dev	3.799

Gaus mean : 45.3519

Gaus Width: 3.8411

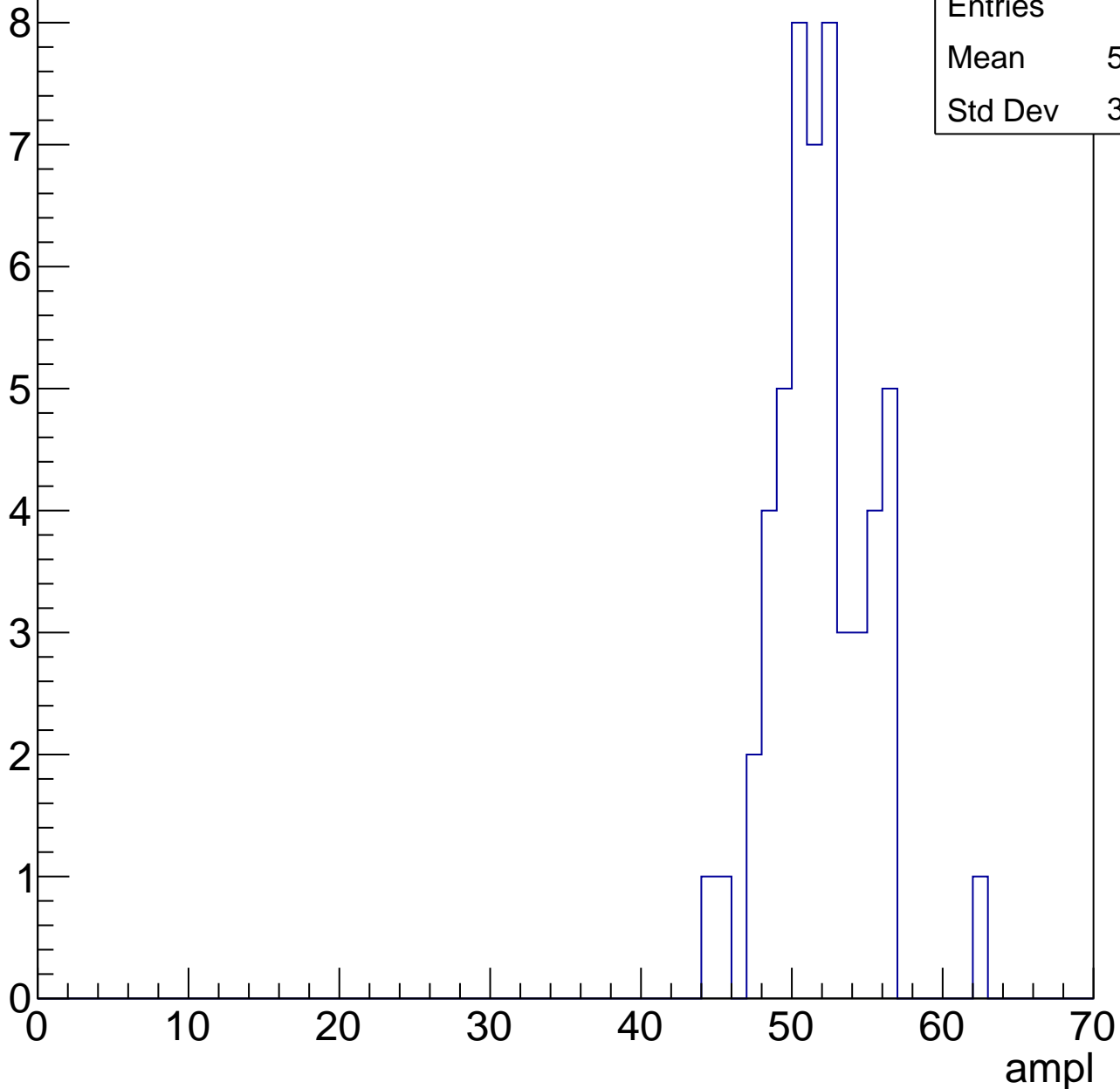


B1L100S, U12-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	51.46
Std Dev	3.183

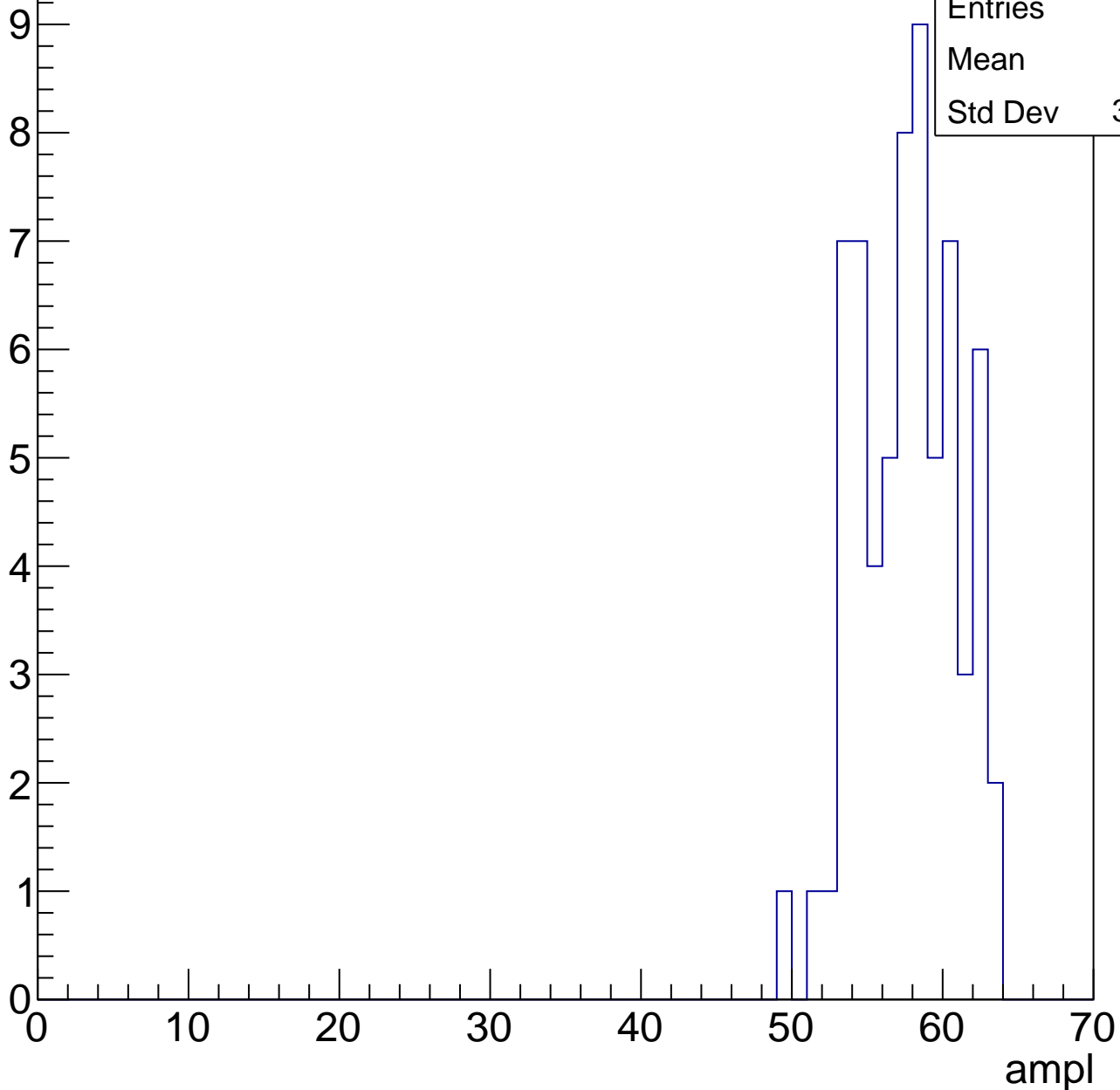


B1L100S, U12-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	57.2
Std Dev	3.211

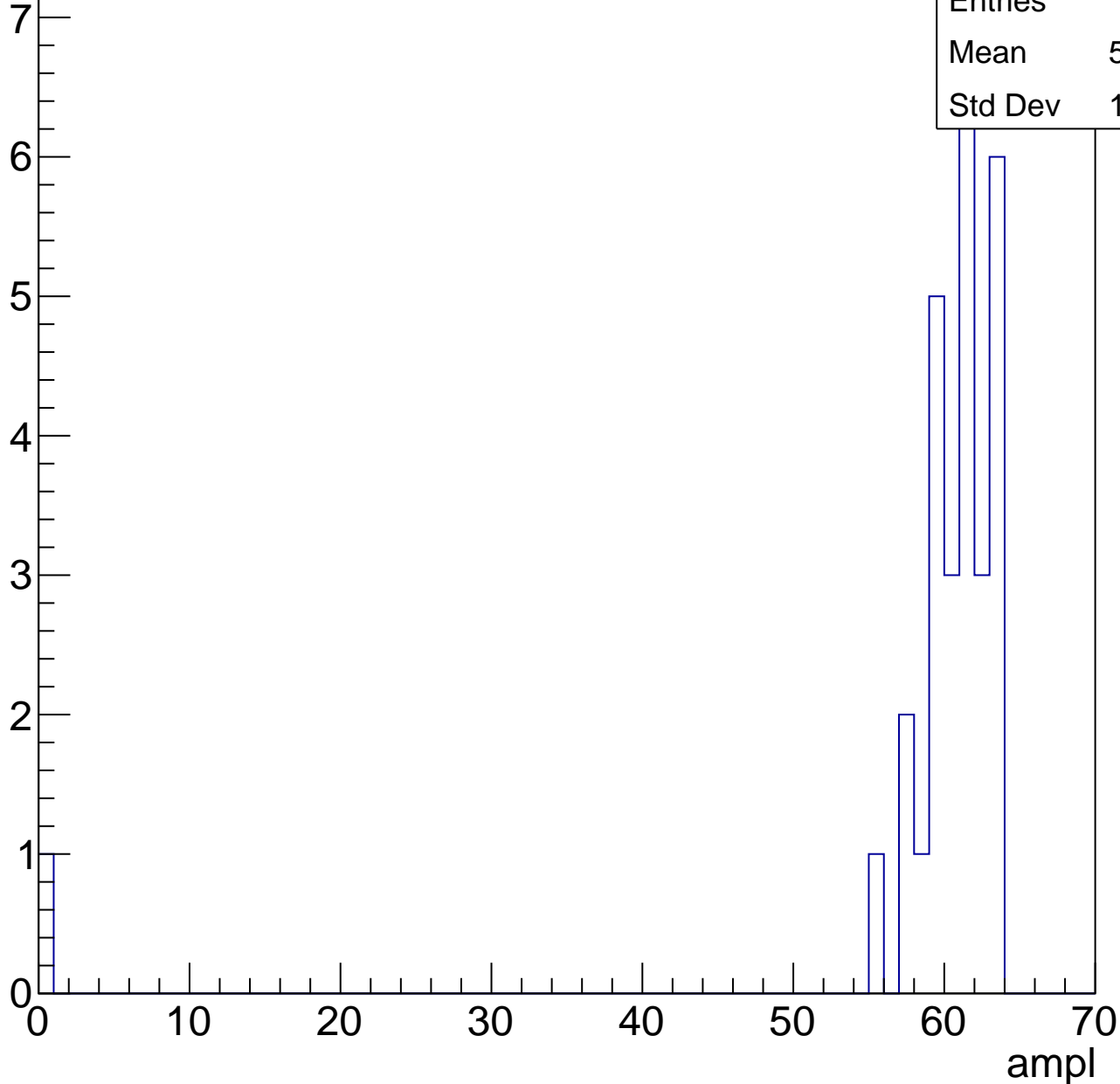


B1L100S, U12-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.38
Std Dev	11.22



B1L100S, U12-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch113, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	28.64
Std Dev	4.679

Gaus mean : 29.4827

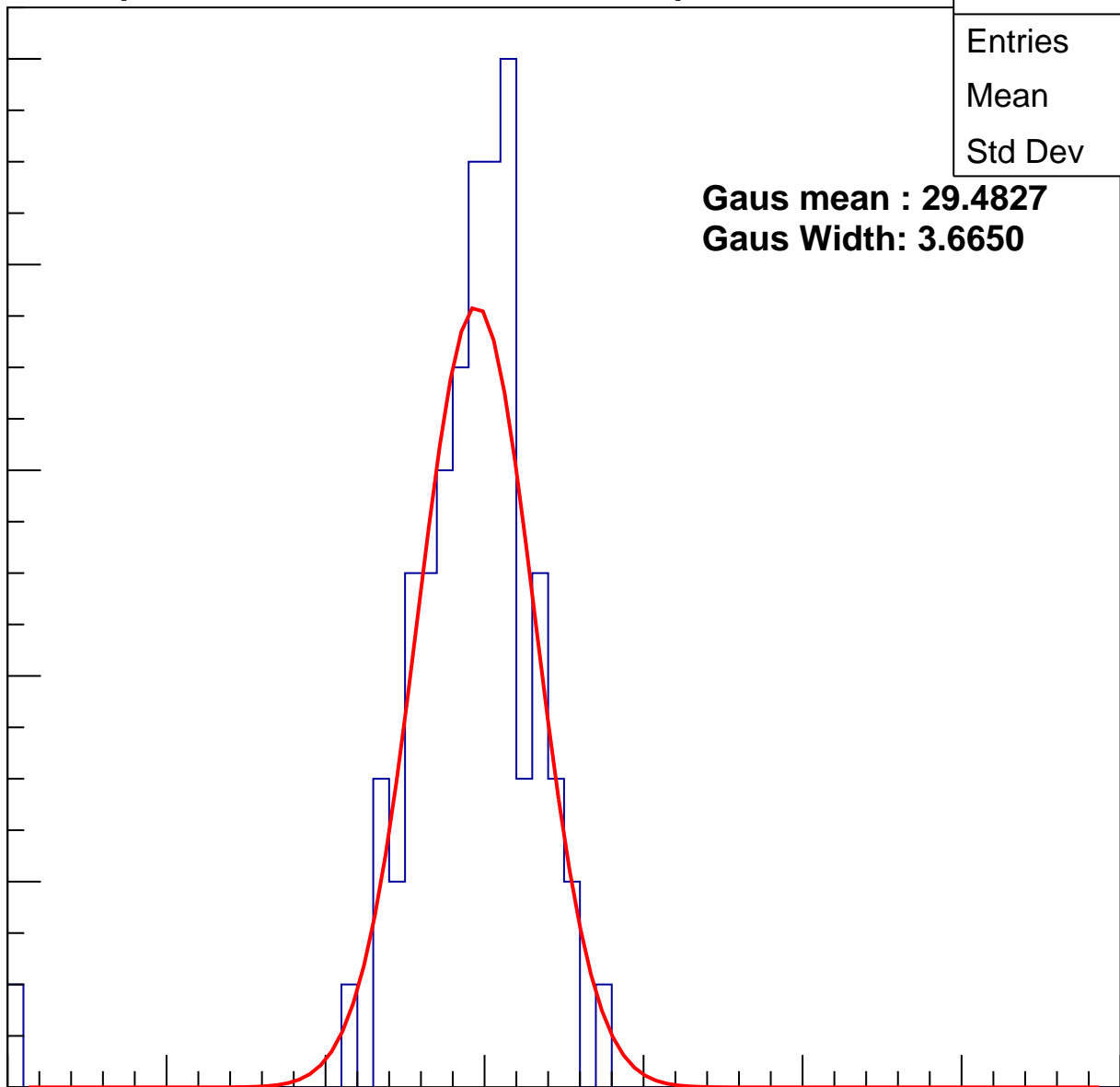
Gaus Width: 3.6650

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch113, adc1

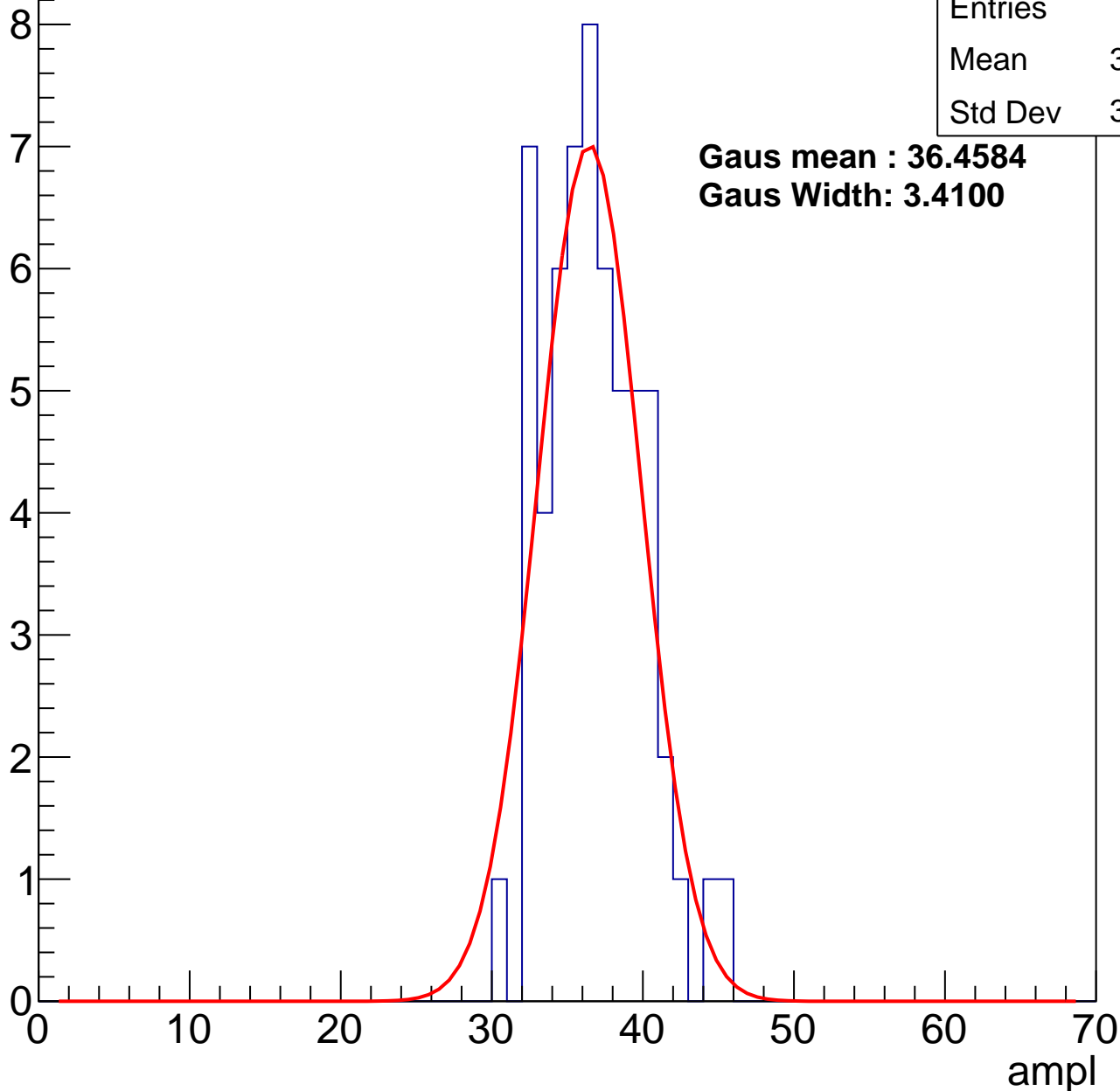
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.32
Std Dev	3.165

Gaus mean : 36.4584

Gaus Width: 3.4100



B1L100S, U12-ch113, adc2

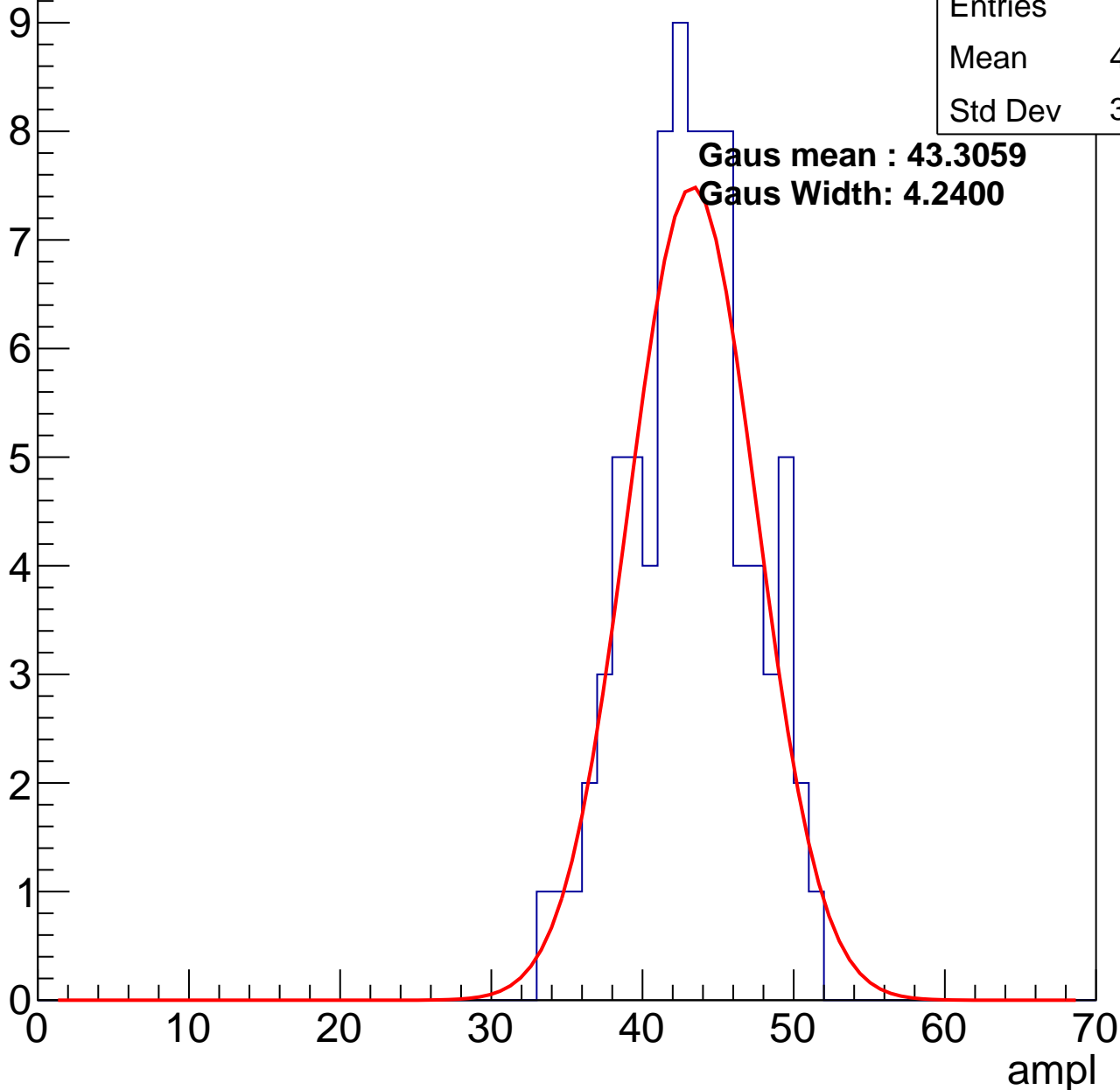
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	42.73
Std Dev	3.957

Gaus mean : 43.3059

Gaus Width: 4.2400

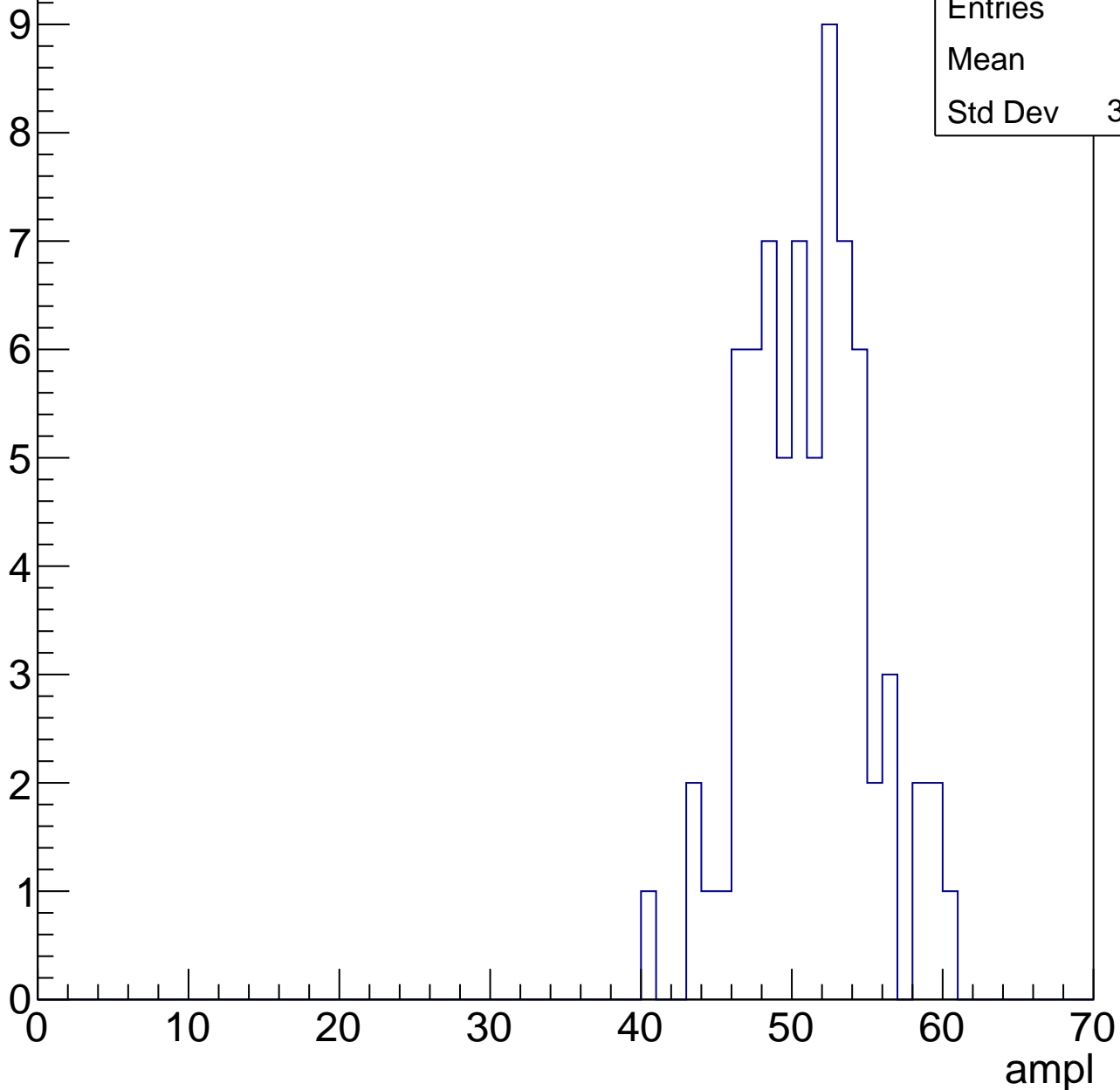


B1L100S, U12-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	50.6
Std Dev	3.992

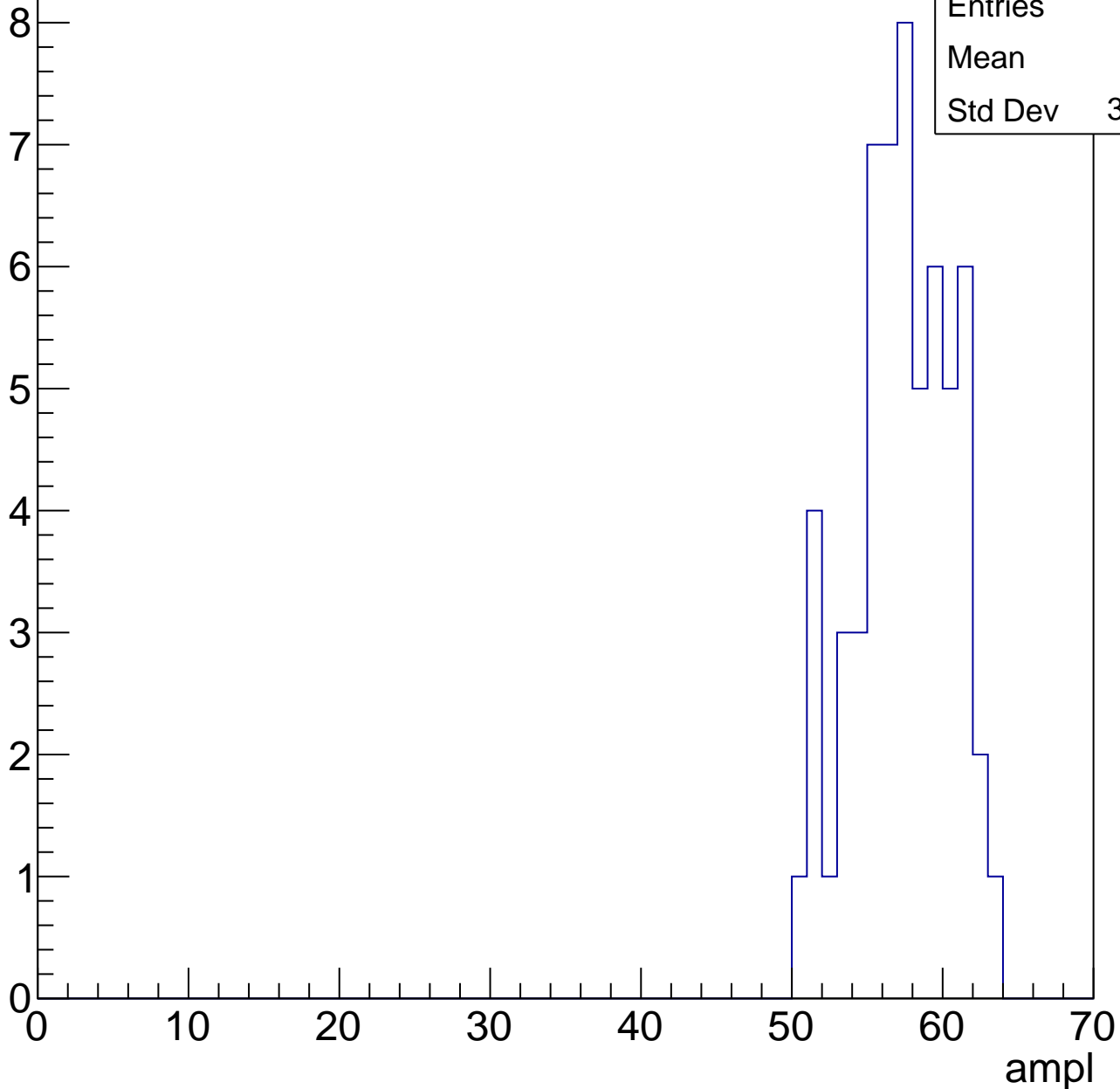


B1L100S, U12-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	56.9
Std Dev	3.145

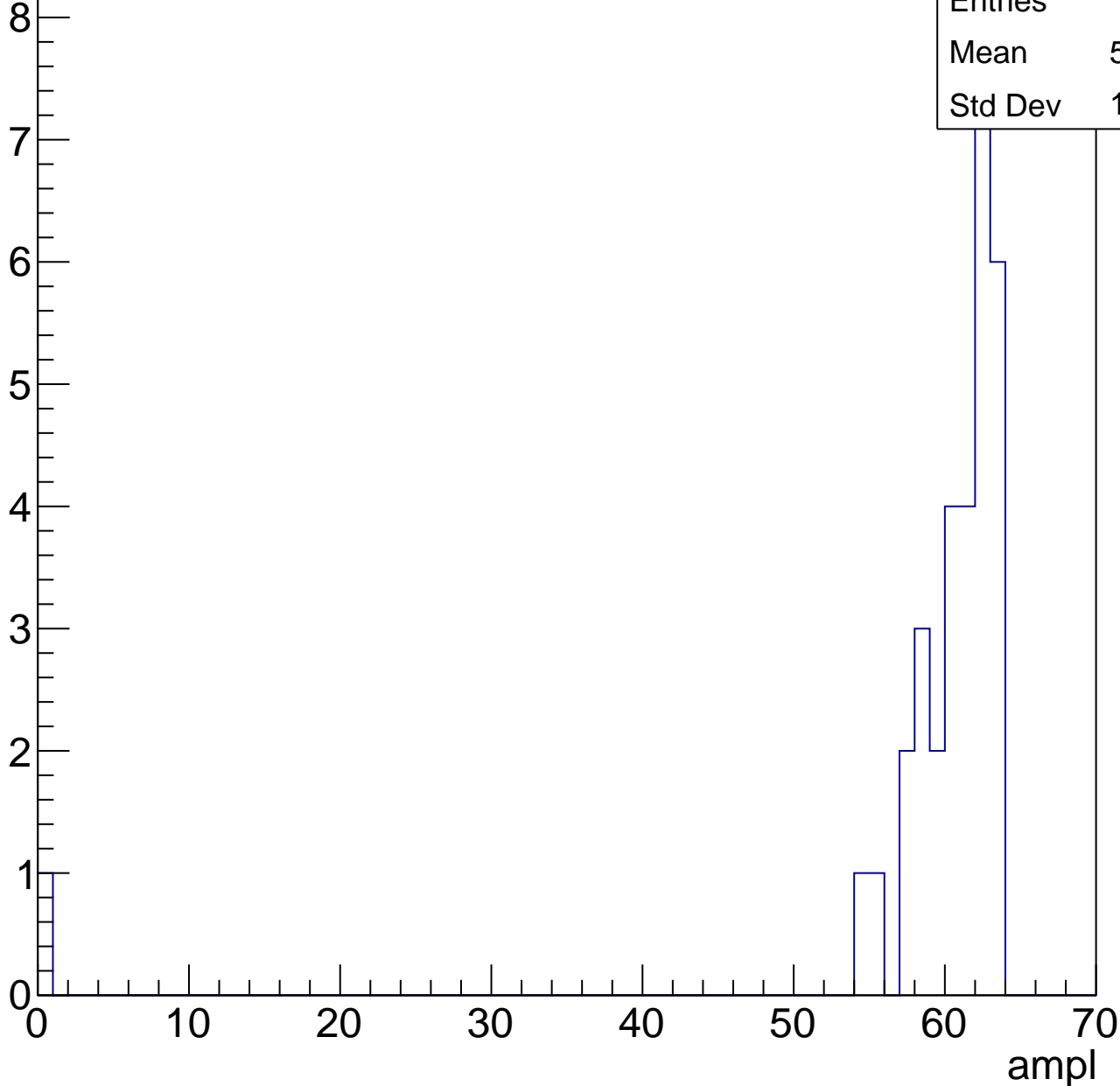


B1L100S, U12-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	32
Mean	58.53
Std Dev	10.77



B1L100S, U12-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	63
Std Dev	0

B1L100S, U12-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch114, adc0

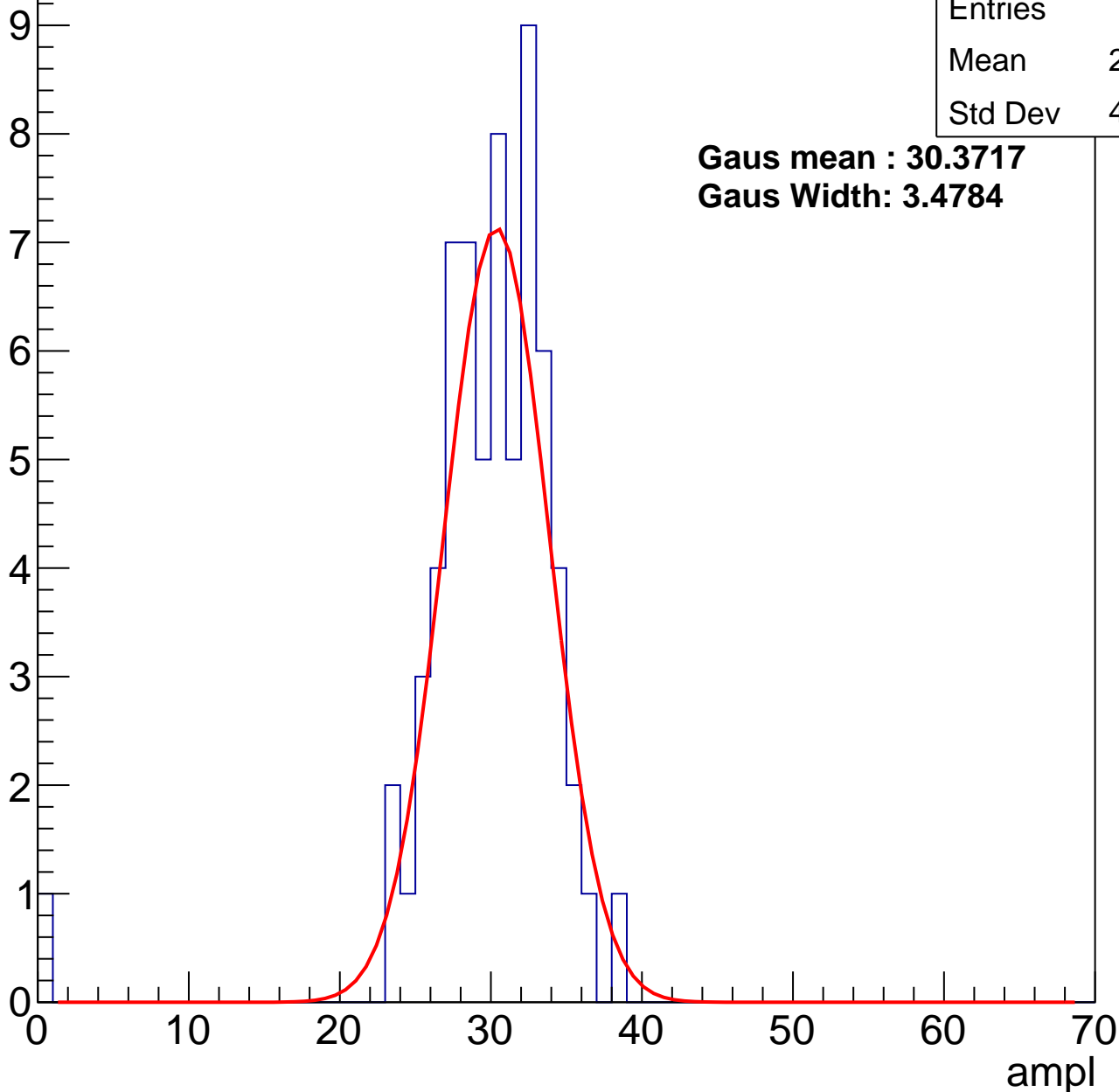
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	29.39
Std Dev	4.852

Gaus mean : 30.3717

Gaus Width: 3.4784



B1L100S, U12-ch114, adc1

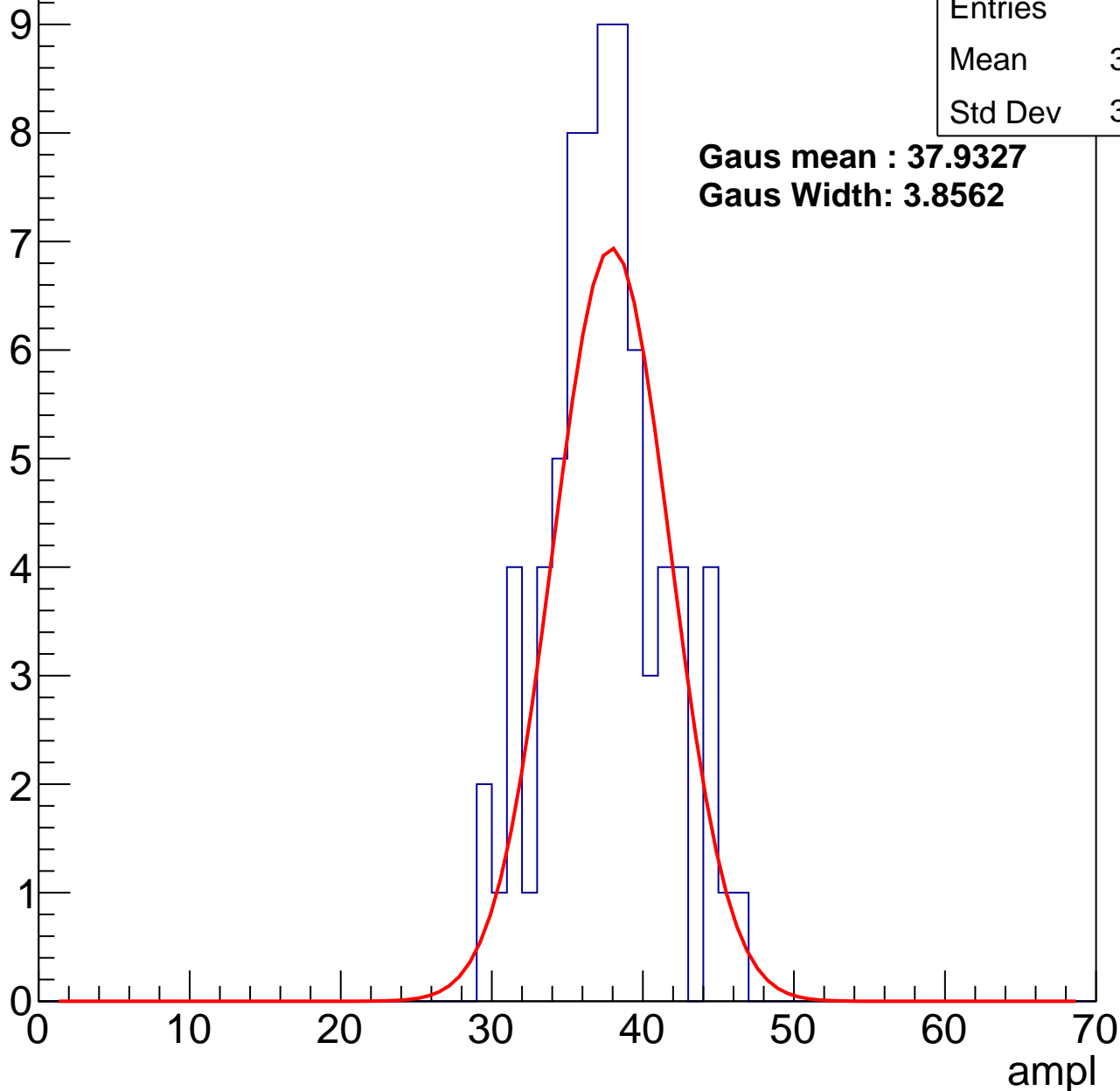
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.05
Std Dev	3.784

Gaus mean : 37.9327

Gaus Width: 3.8562



B1L100S, U12-ch114, adc2

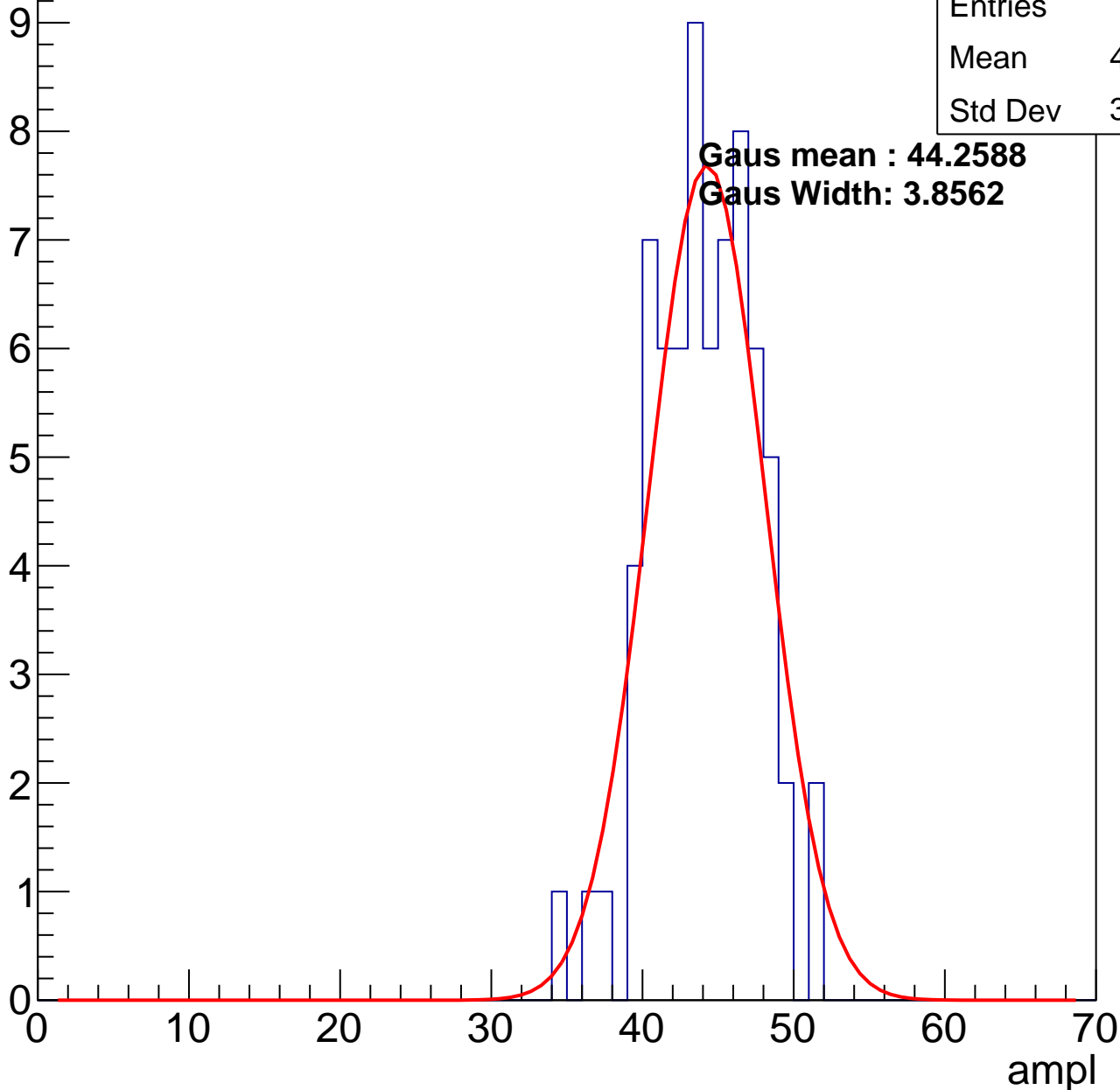
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.62
Std Dev	3.404

Gaus mean : 44.2588

Gaus Width: 3.8562

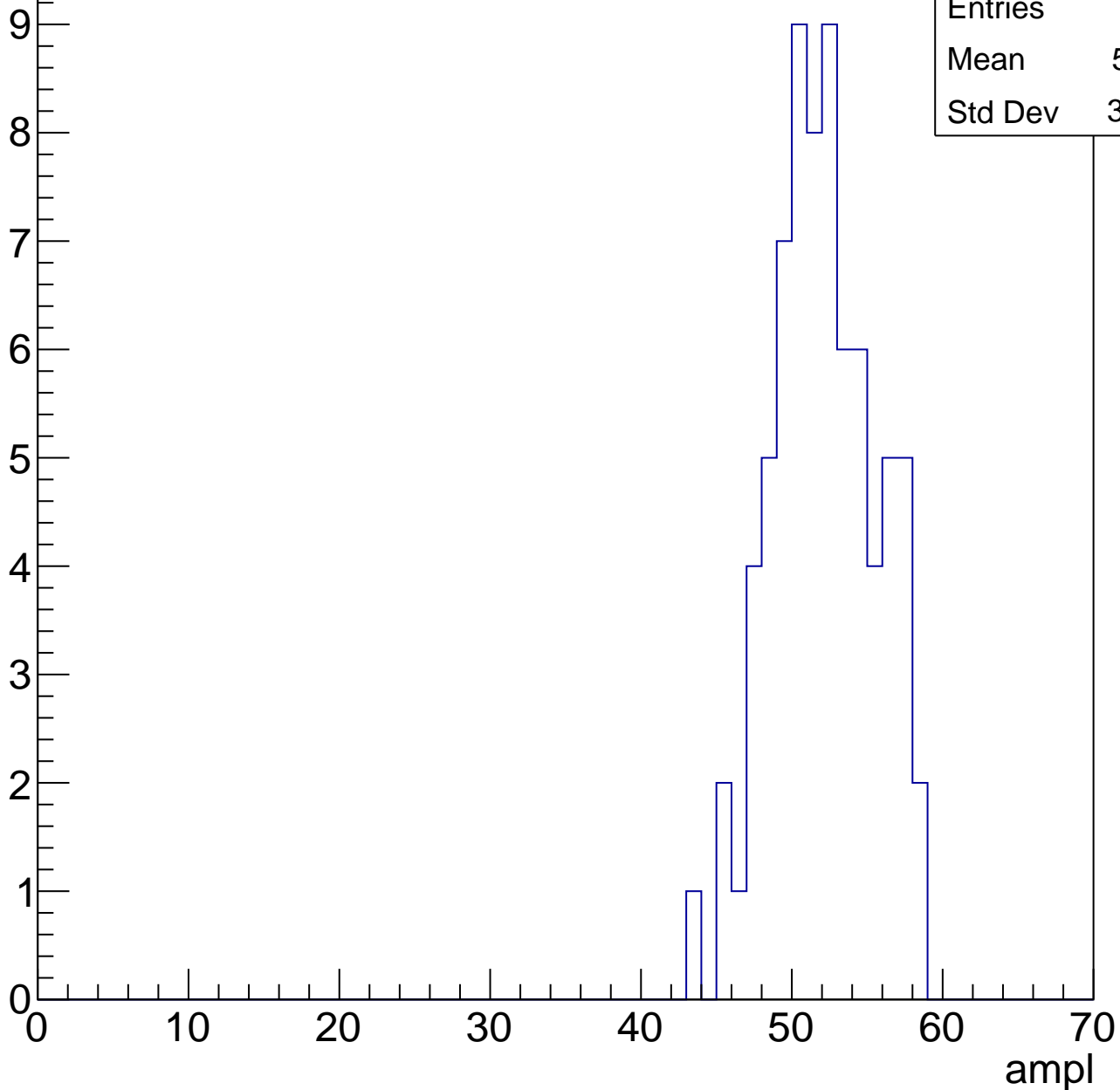


B1L100S, U12-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	51.61
Std Dev	3.356

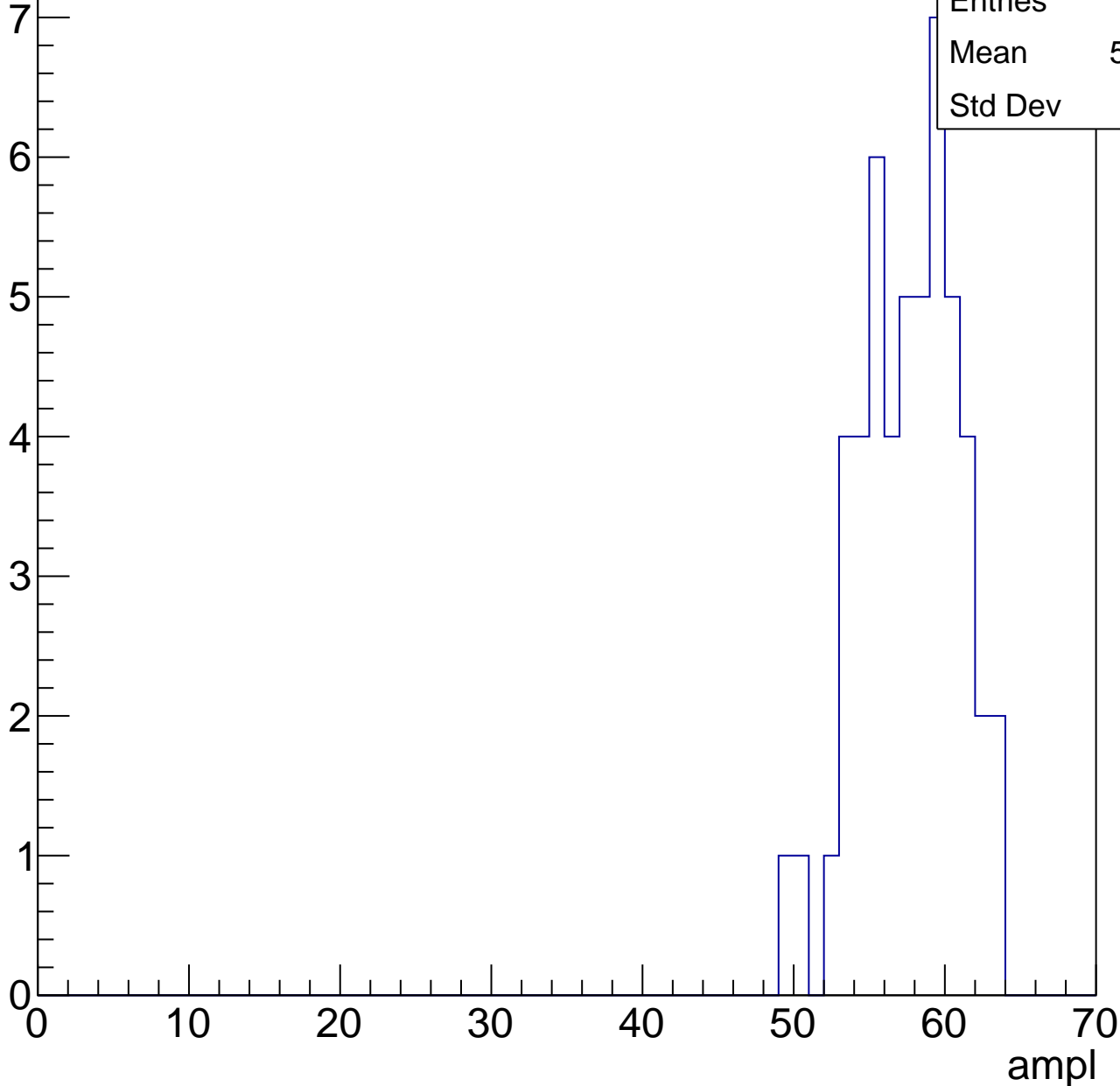


B1L100S, U12-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	57.16
Std Dev	3.22

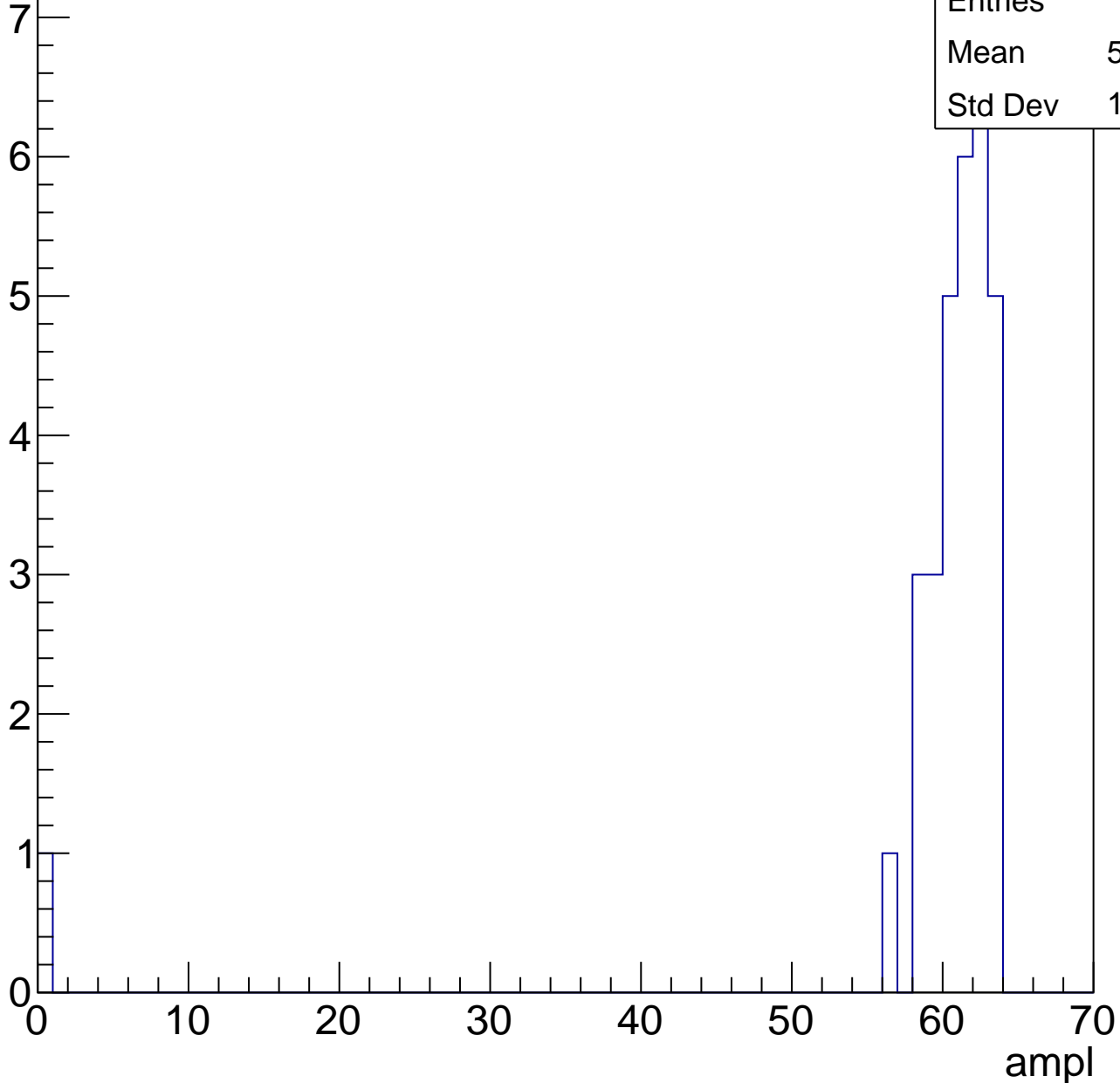


B1L100S, U12-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.77
Std Dev	10.87



B1L100S, U12-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U12-ch115, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	31.45
Std Dev	3.174

Gaus mean : 31.8265

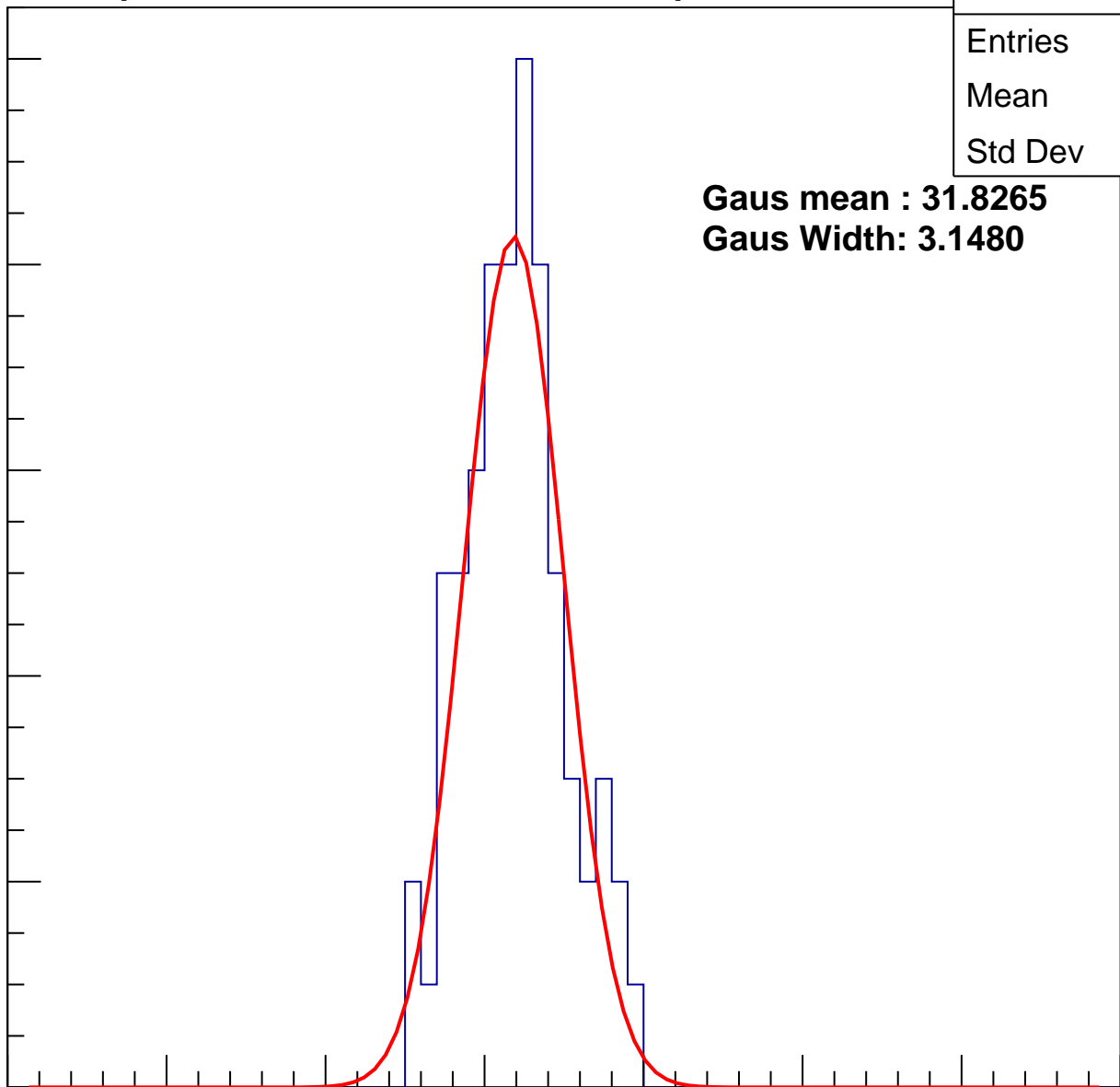
Gaus Width: 3.1480

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch115, adc1

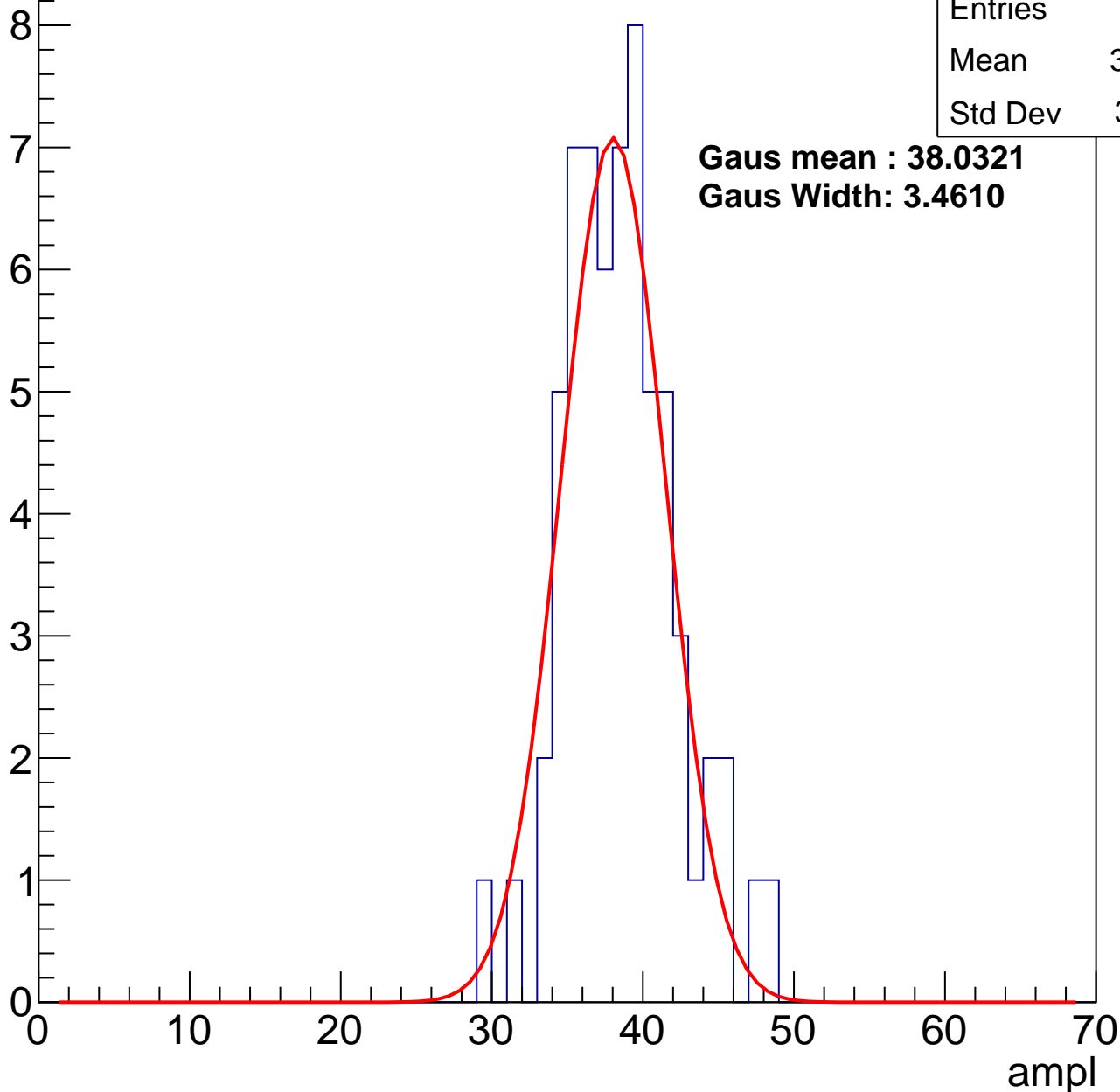
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	38.12
Std Dev	3.651

Gaus mean : 38.0321

Gaus Width: 3.4610



B1L100S, U12-ch115, adc2

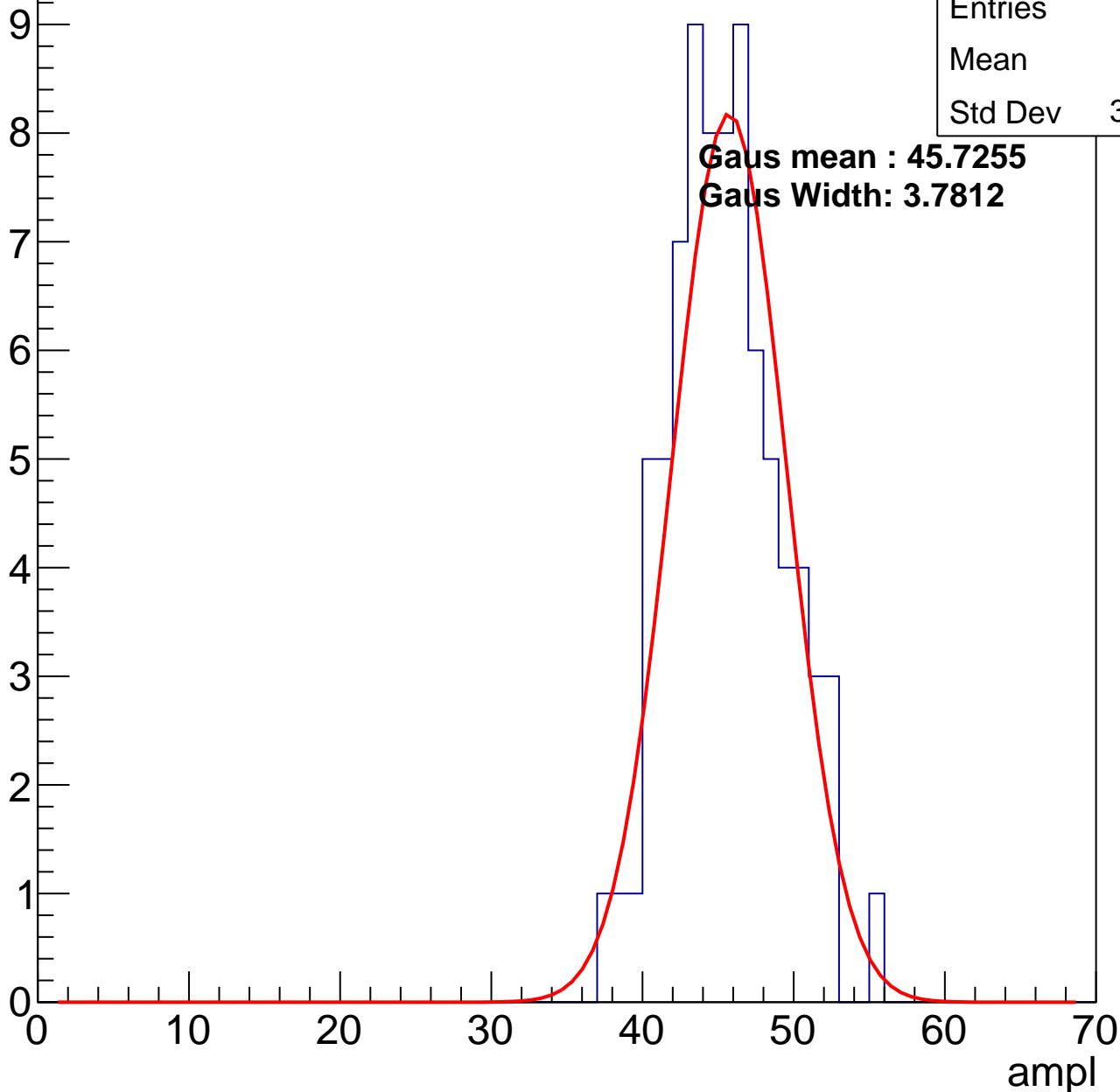
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	45.1
Std Dev	3.635

Gaus mean : 45.7255

Gaus Width: 3.7812

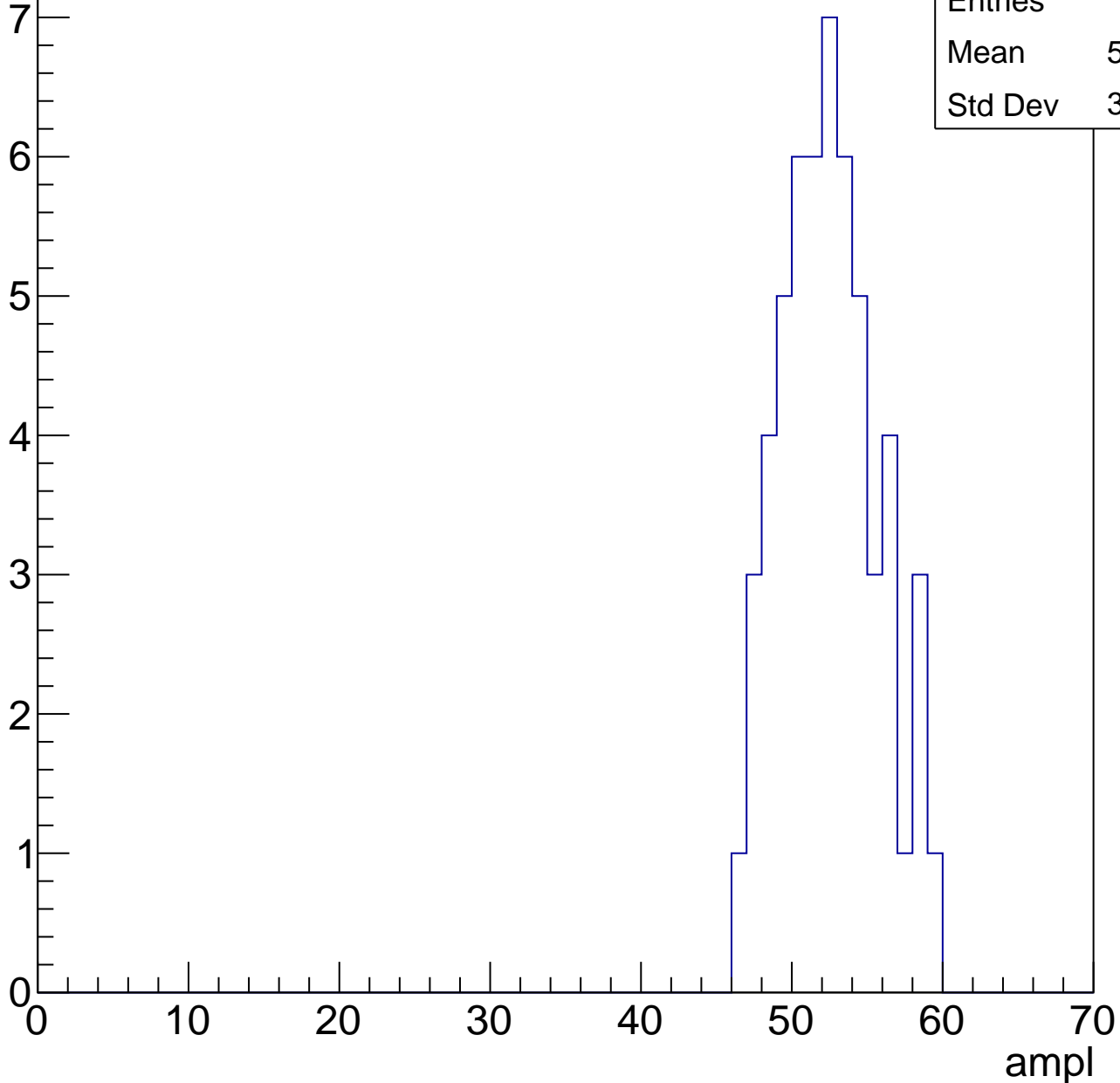


B1L100S, U12-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	52.02
Std Dev	3.159

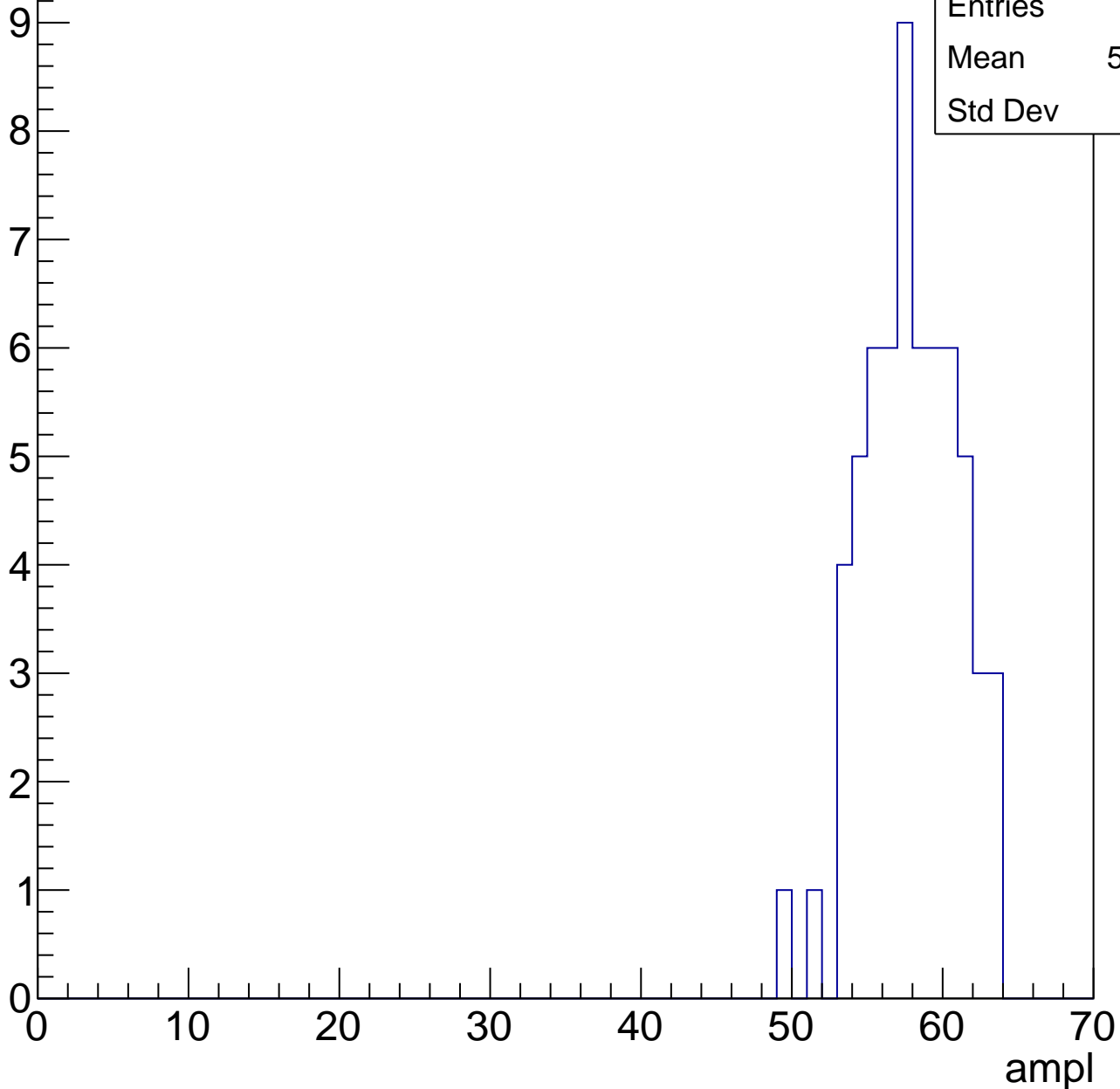


B1L100S, U12-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.43
Std Dev	3.07

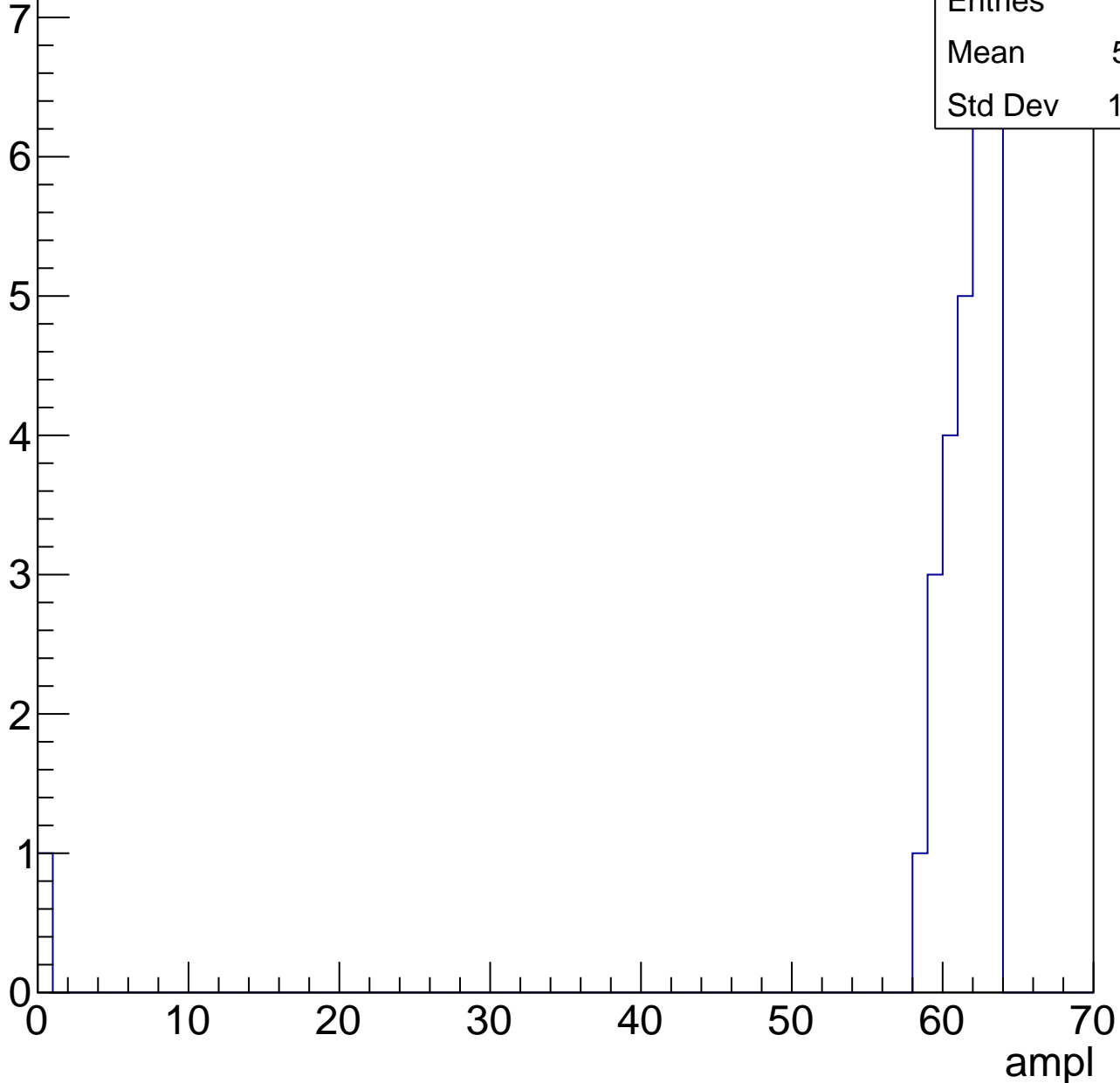


B1L100S, U12-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	28
Mean	59.11
Std Dev	11.47



B1L100S, U12-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

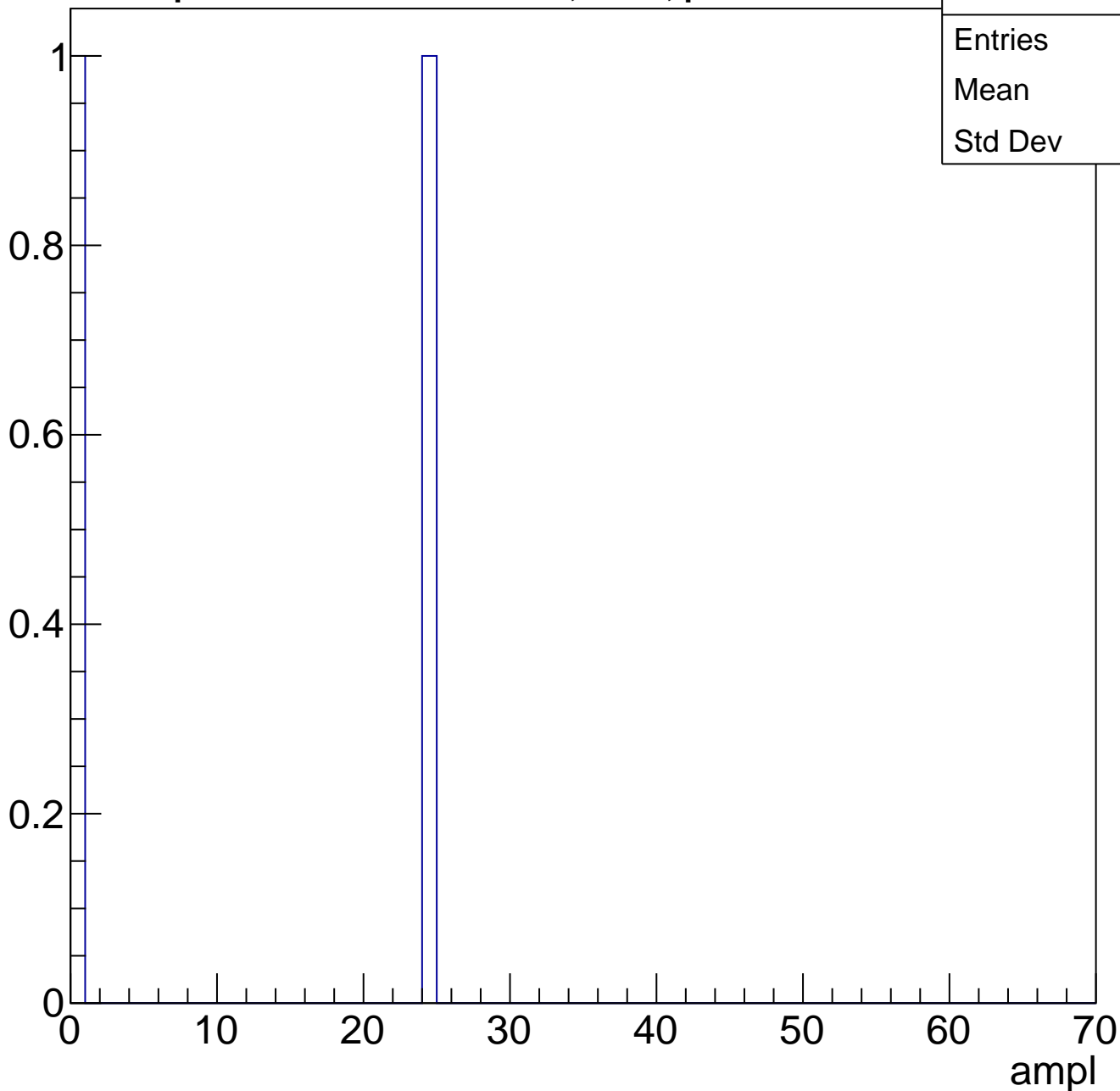
Entry



B1L100S, U12-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch116, adc0

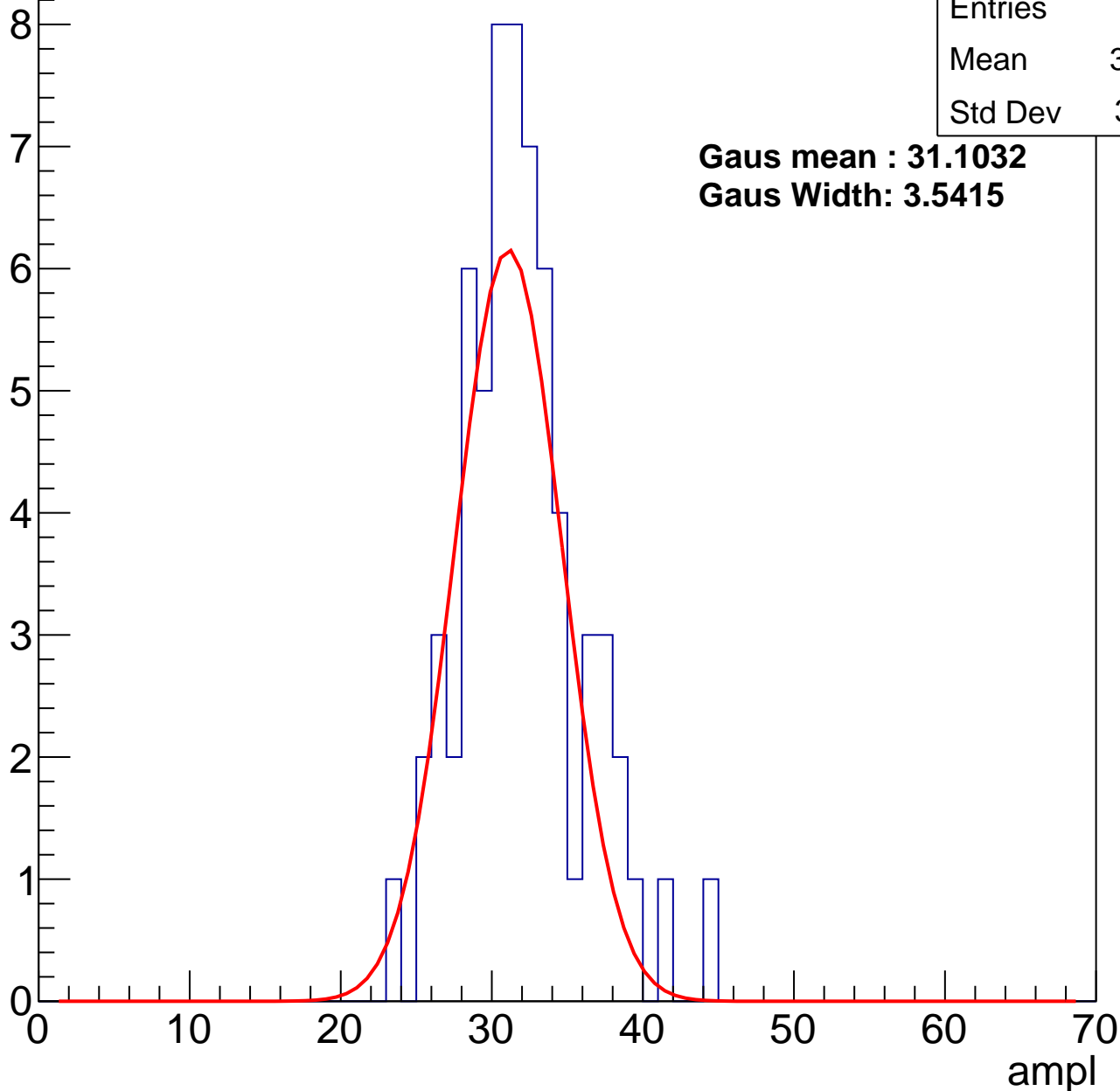
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	31.53
Std Dev	3.961

Gaus mean : 31.1032

Gaus Width: 3.5415



B1L100S, U12-ch116, adc1

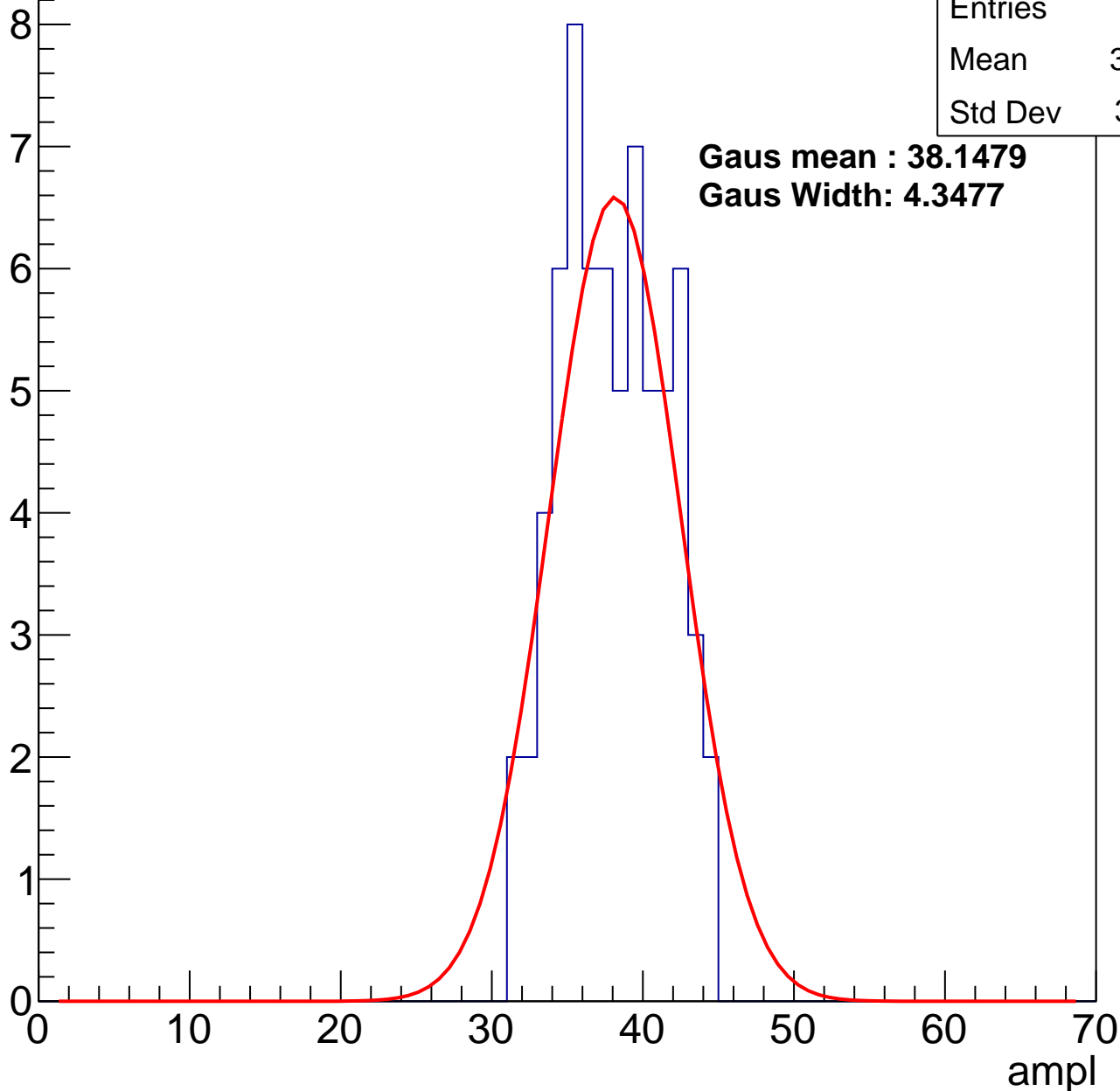
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	37.57
Std Dev	3.391

Gaus mean : 38.1479

Gaus Width: 4.3477



B1L100S, U12-ch116, adc2

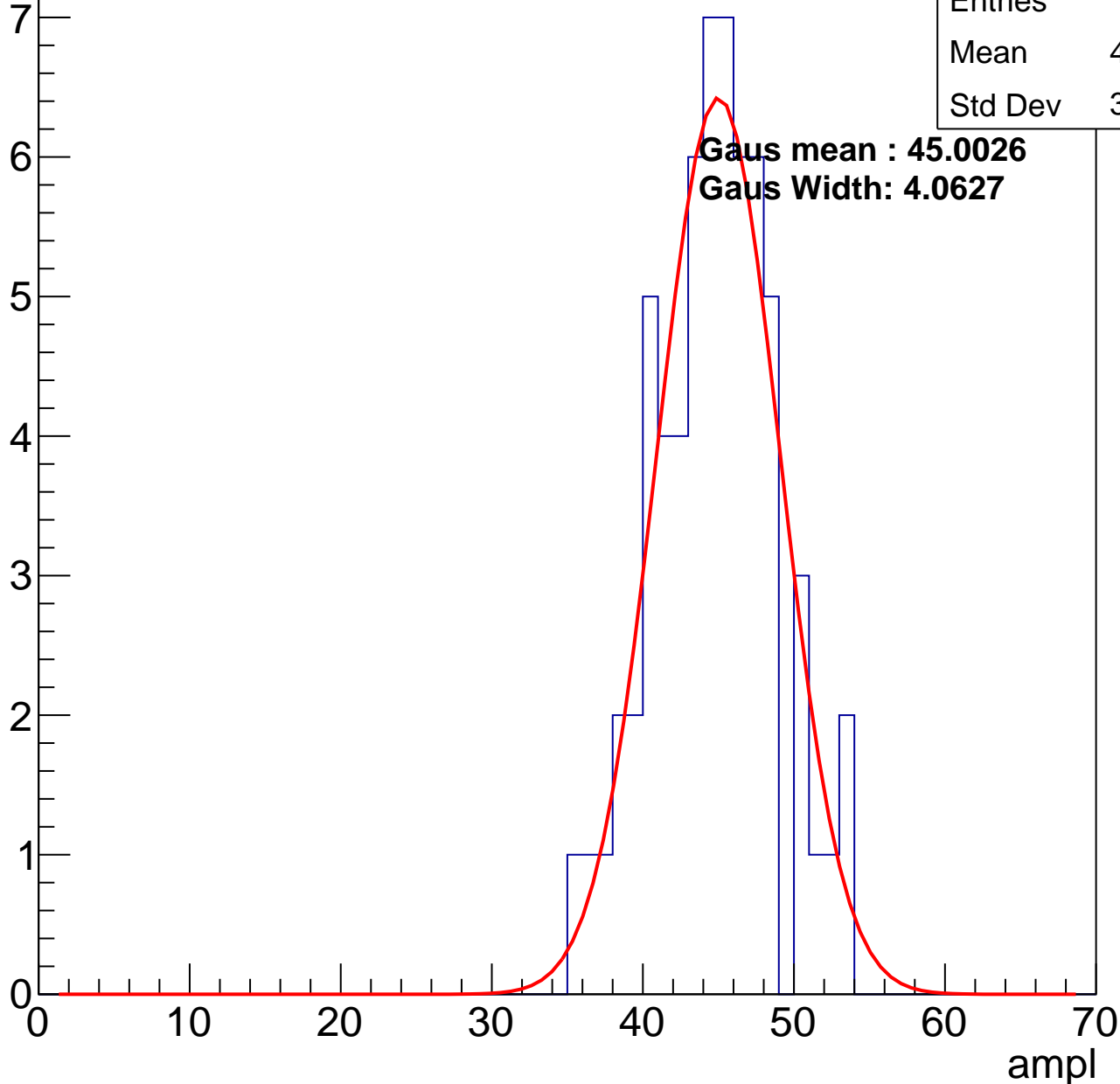
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	44.25
Std Dev	3.949

Gaus mean : 45.0026

Gaus Width: 4.0627



B1L100S, U12-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

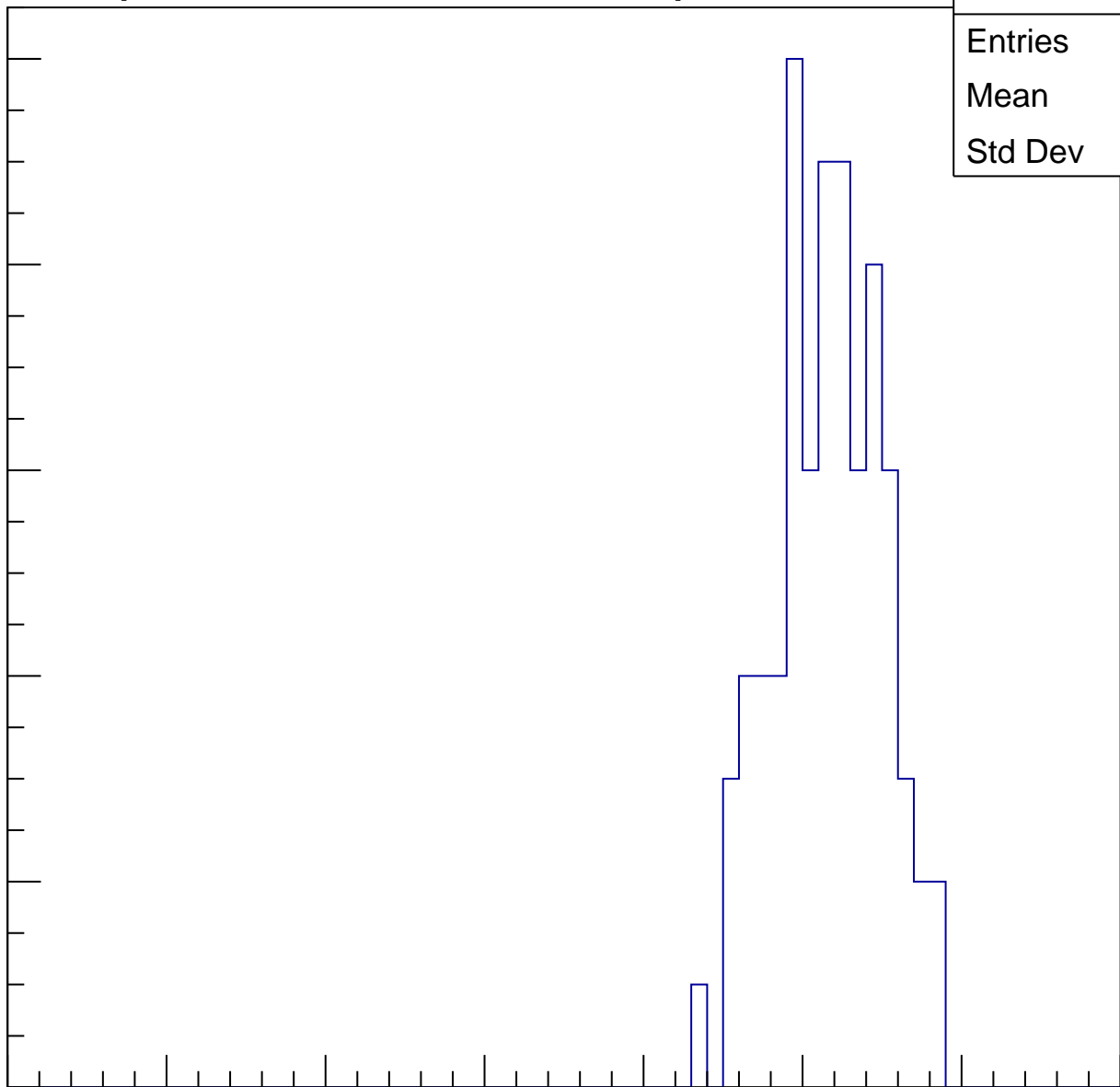
Entries	77
Mean	51.13
Std Dev	3.347

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

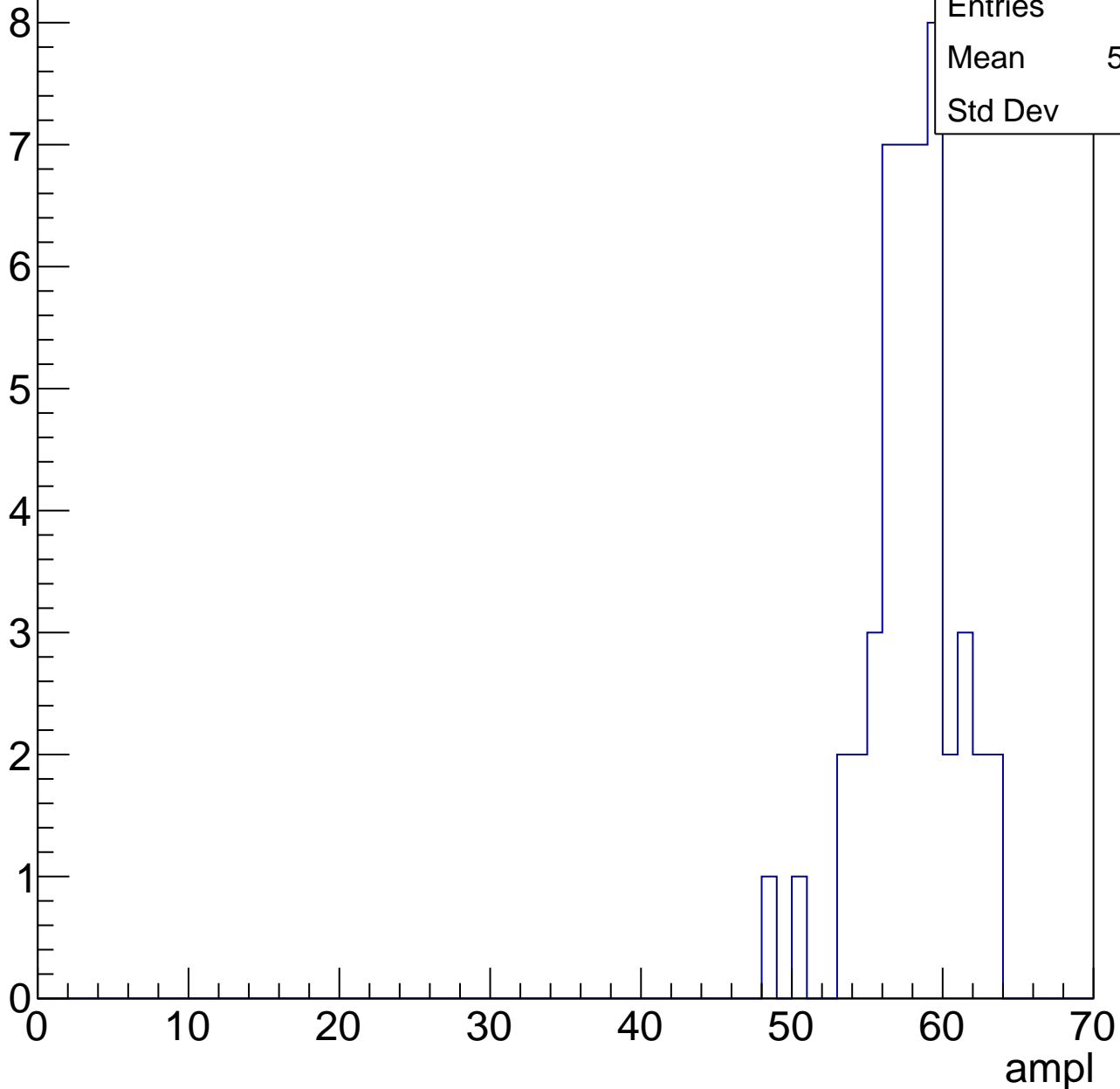


B1L100S, U12-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	57.43
Std Dev	2.98

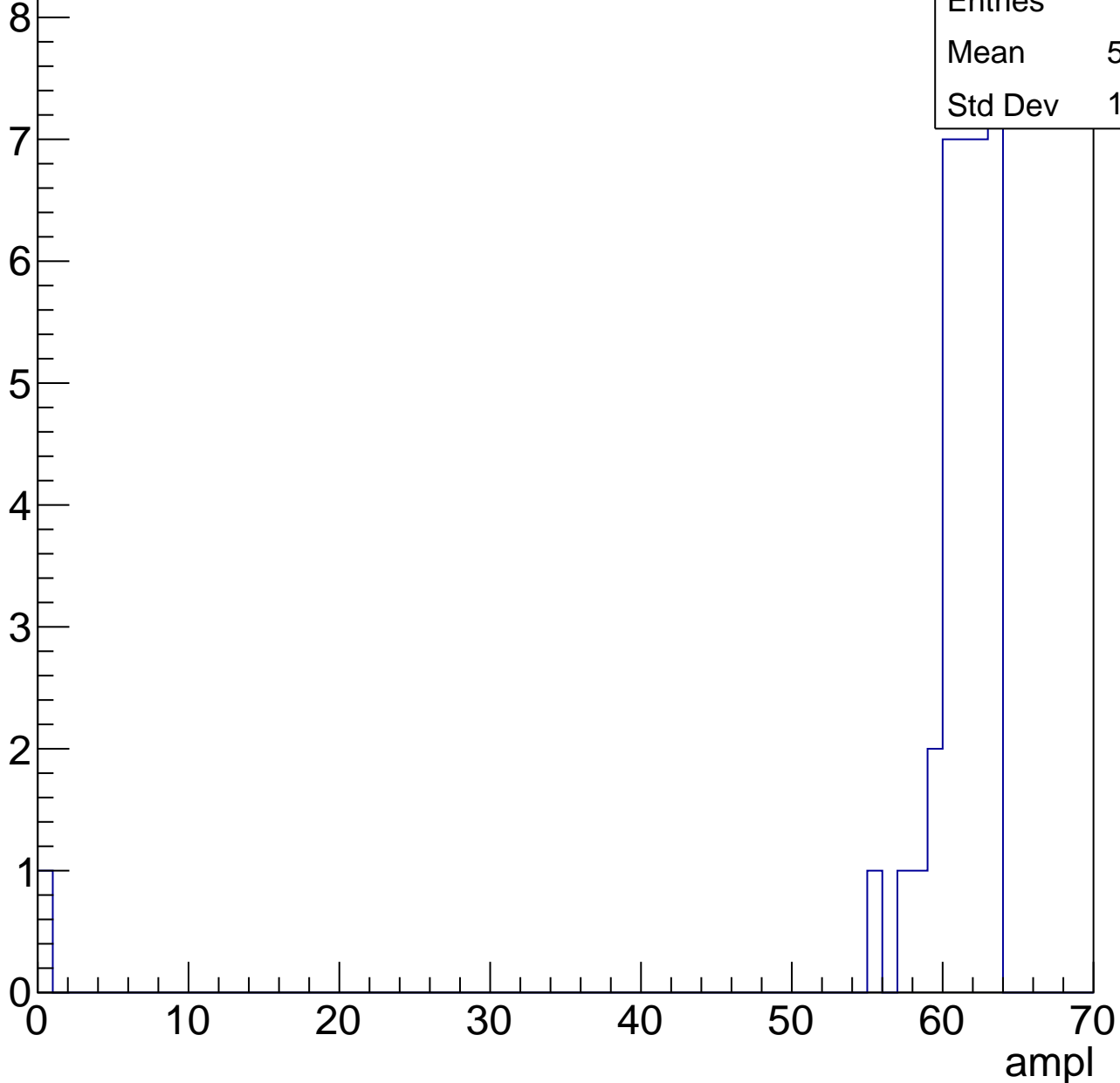


B1L100S, U12-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	59.23
Std Dev	10.32



B1L100S, U12-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L100S, U12-ch117, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	31.34
Std Dev	3.397

Gaus mean : 31.8583

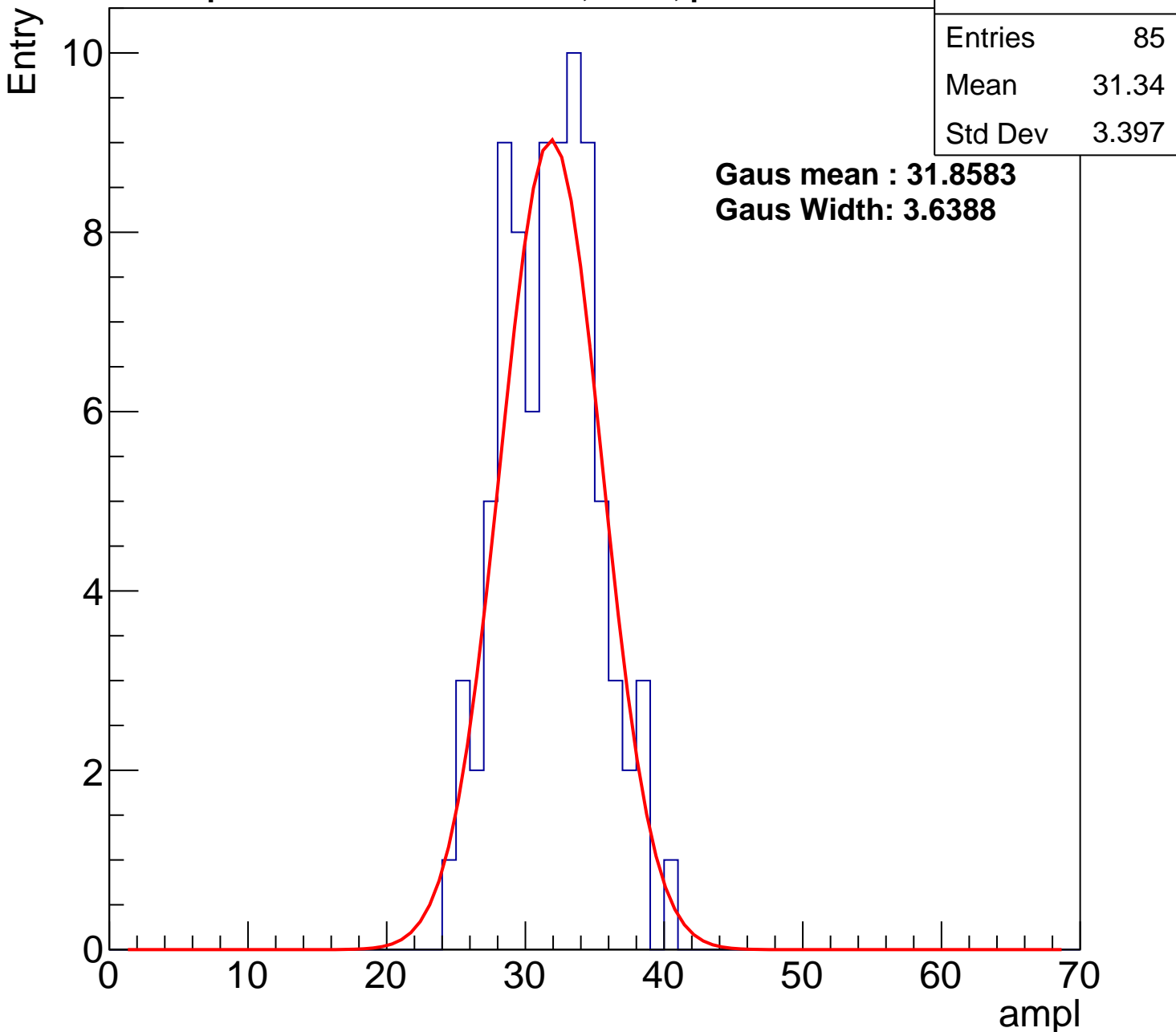
Gaus Width: 3.6388

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch117, adc1

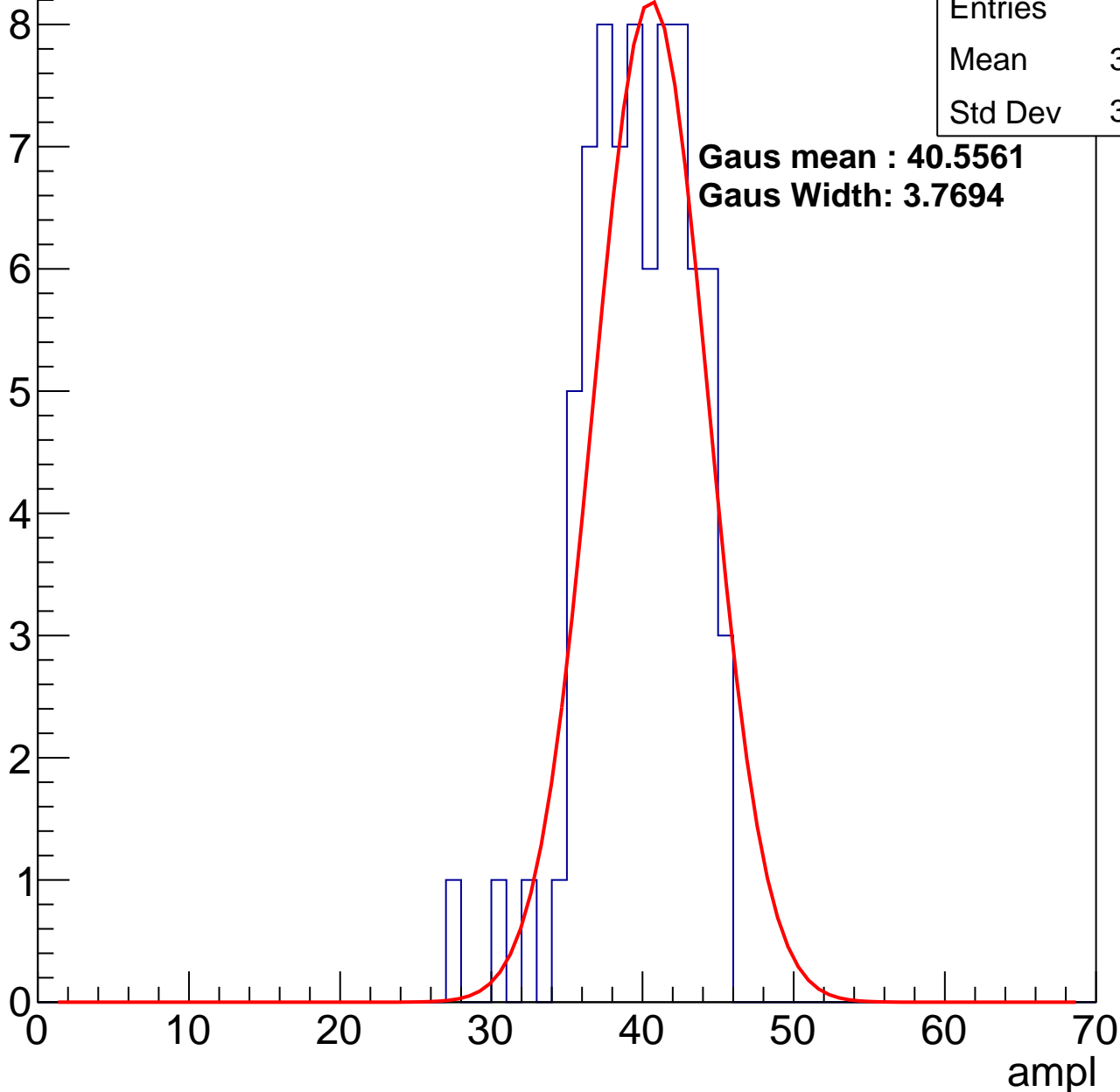
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	39.28
Std Dev	3.515

Gaus mean : 40.5561

Gaus Width: 3.7694



B1L100S, U12-ch117, adc2

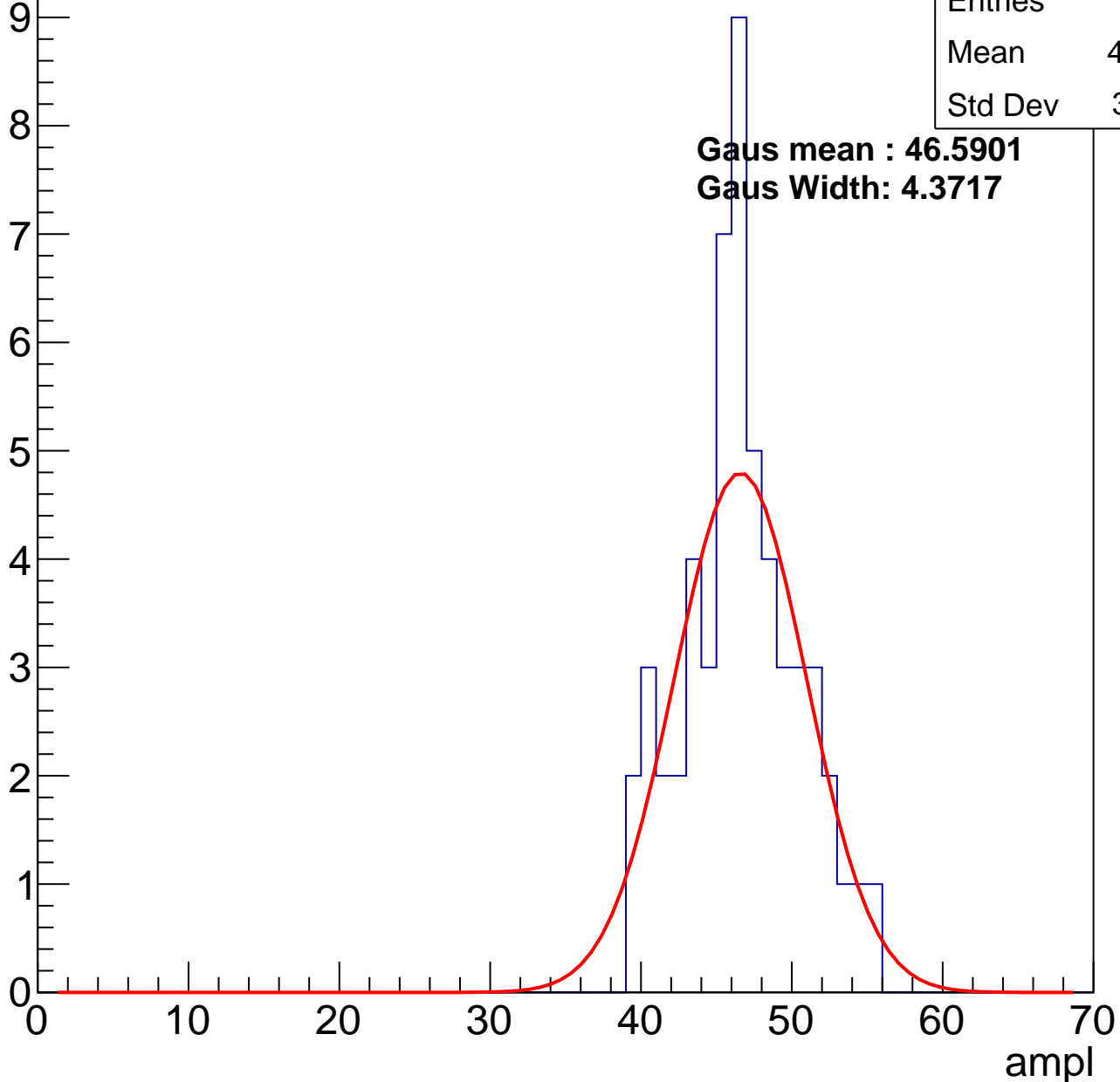
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	46.18
Std Dev	3.761

Gaus mean : 46.5901

Gaus Width: 4.3717

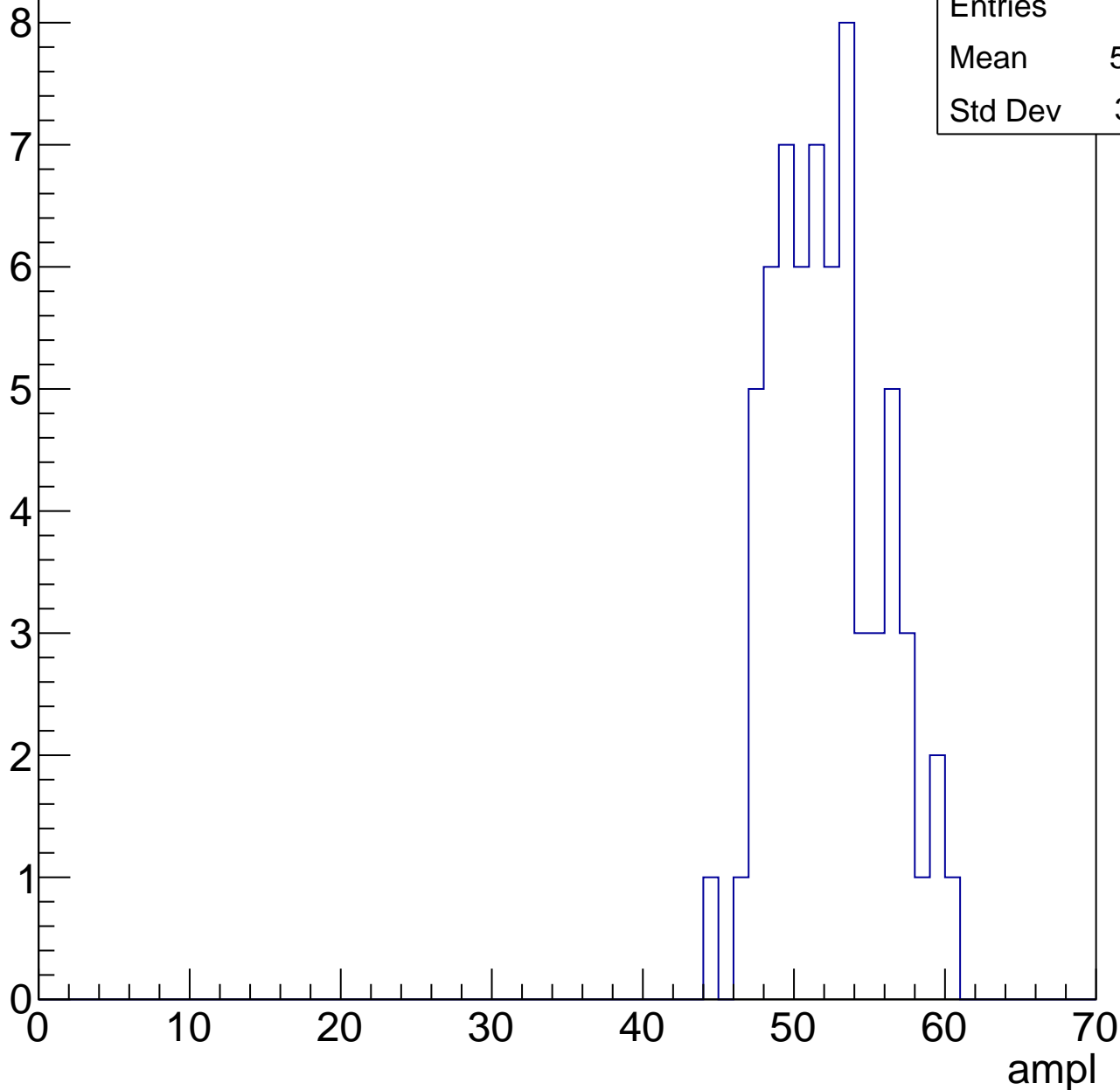


B1L100S, U12-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	51.74
Std Dev	3.531

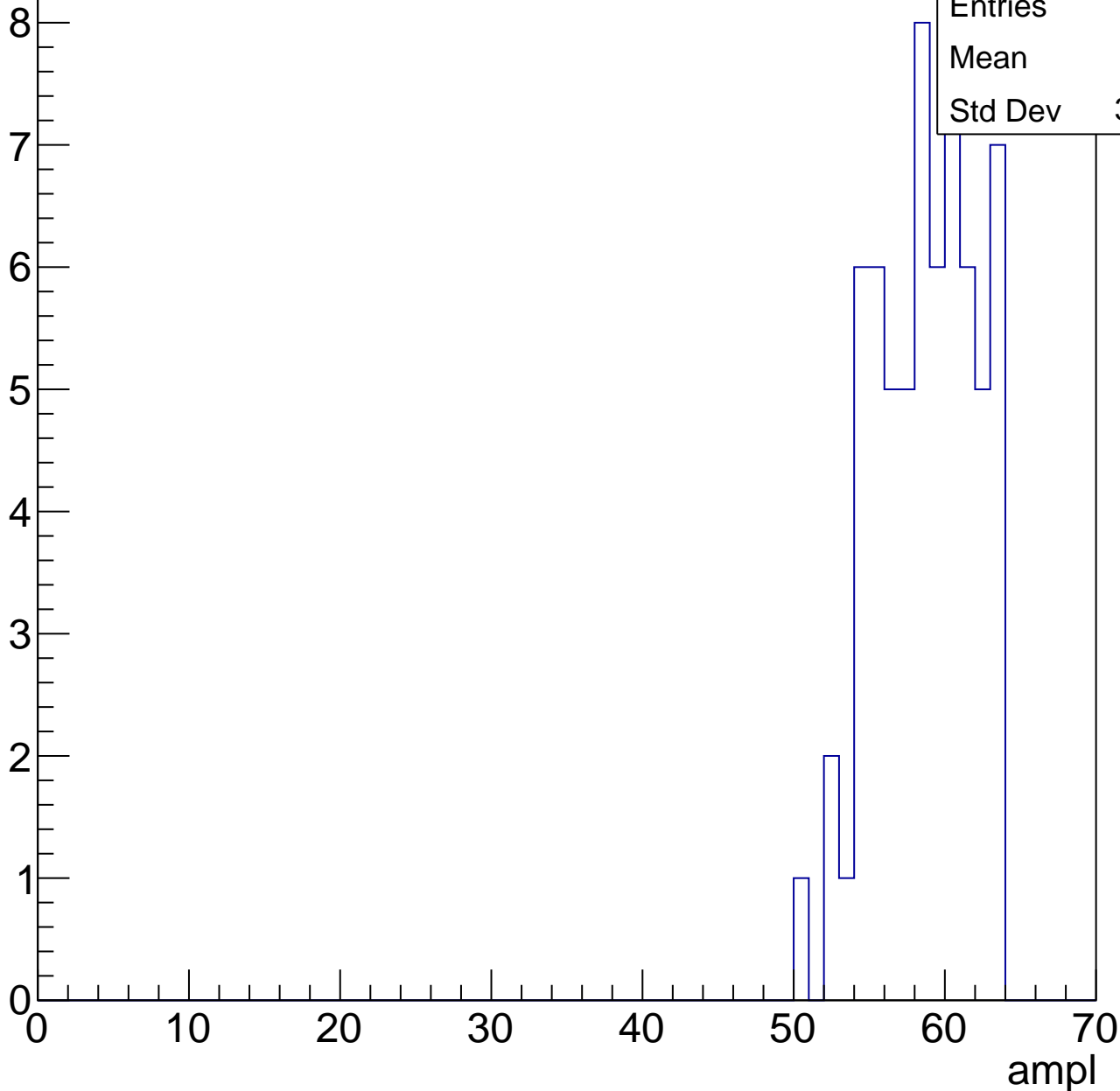


B1L100S, U12-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	58.2
Std Dev	3.211

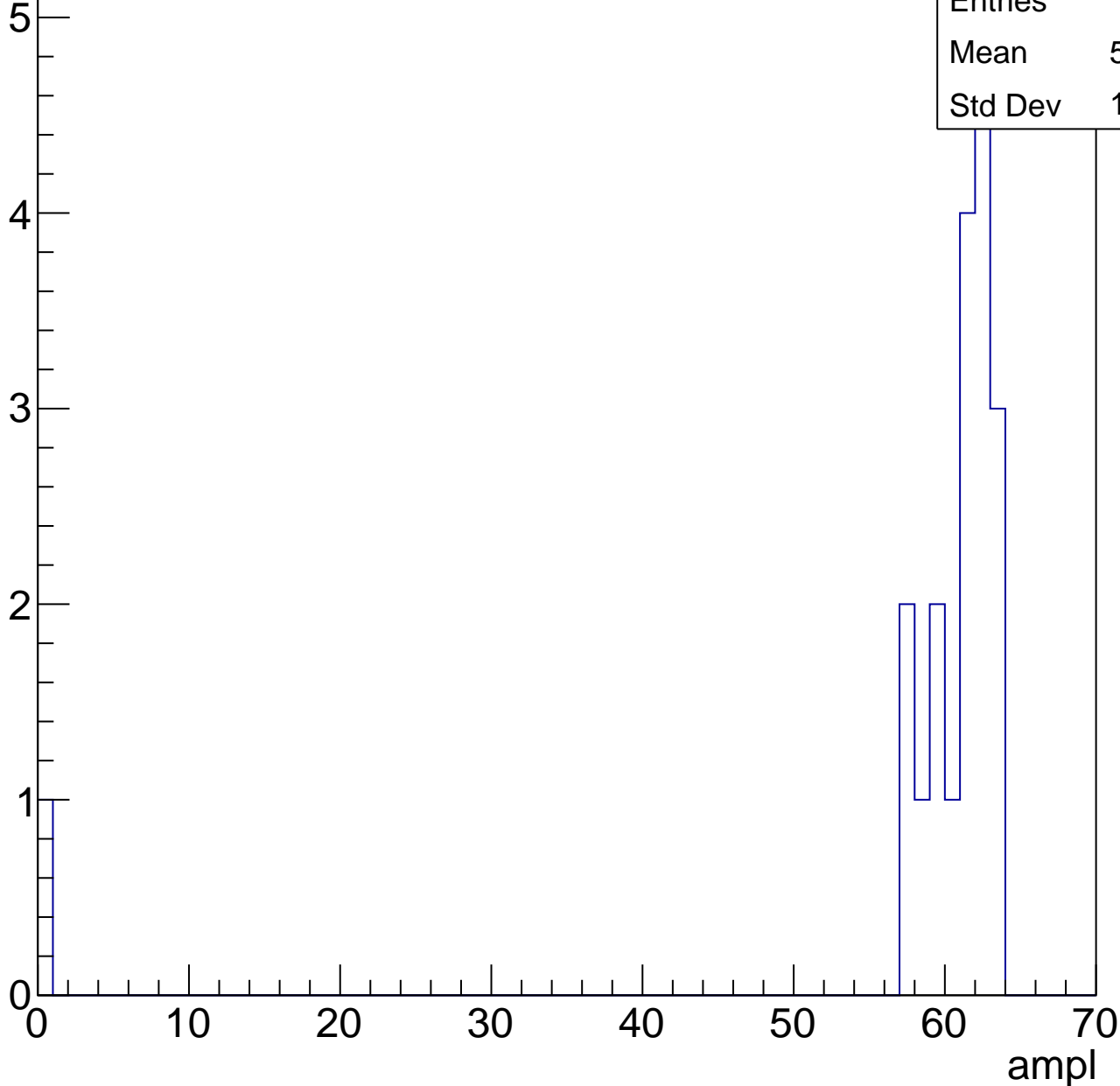


B1L100S, U12-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	57.53
Std Dev	13.69



B1L100S, U12-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch118, adc0

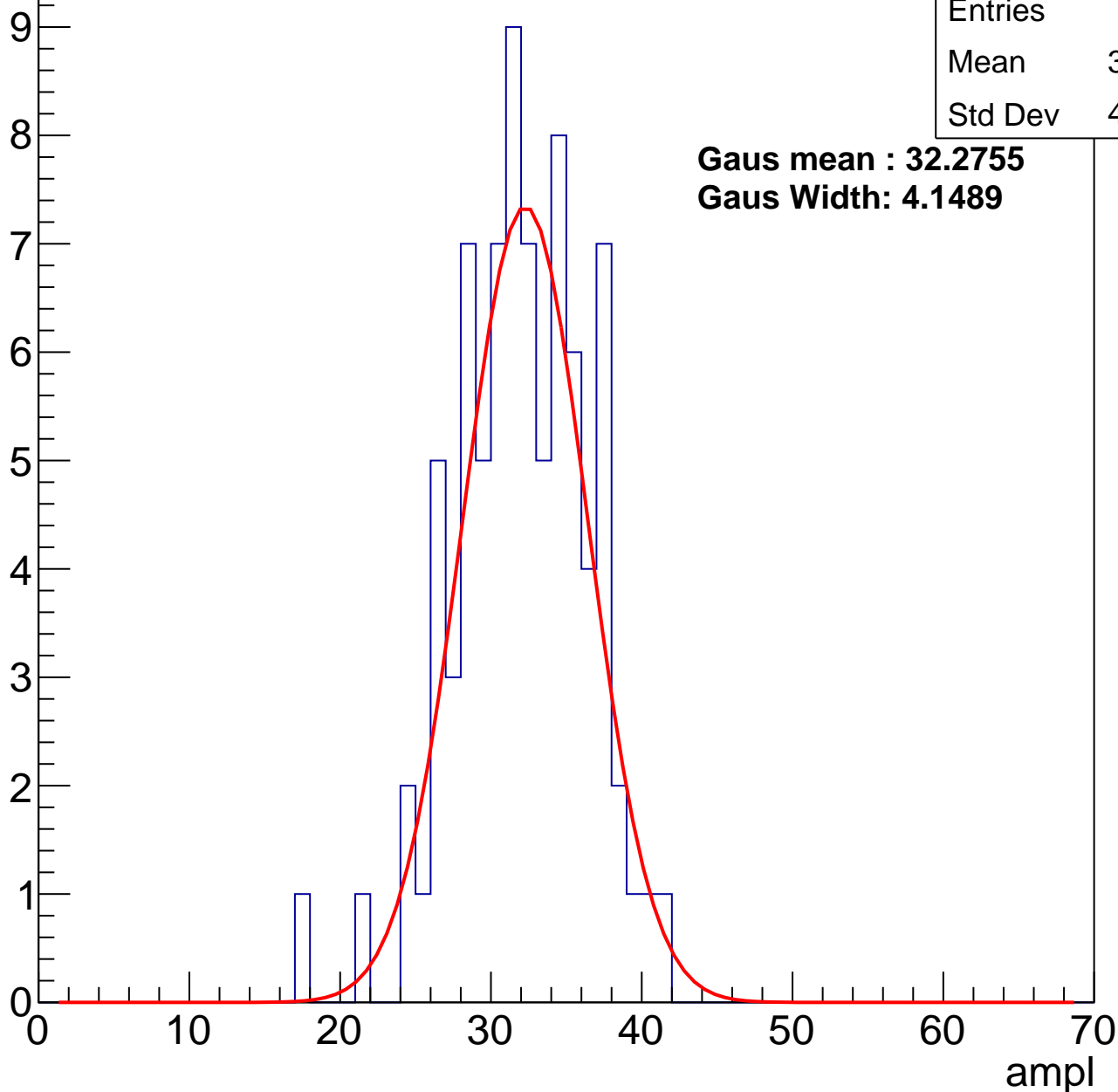
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	31.59
Std Dev	4.327

Gaus mean : 32.2755

Gaus Width: 4.1489



B1L100S, U12-ch118, adc1

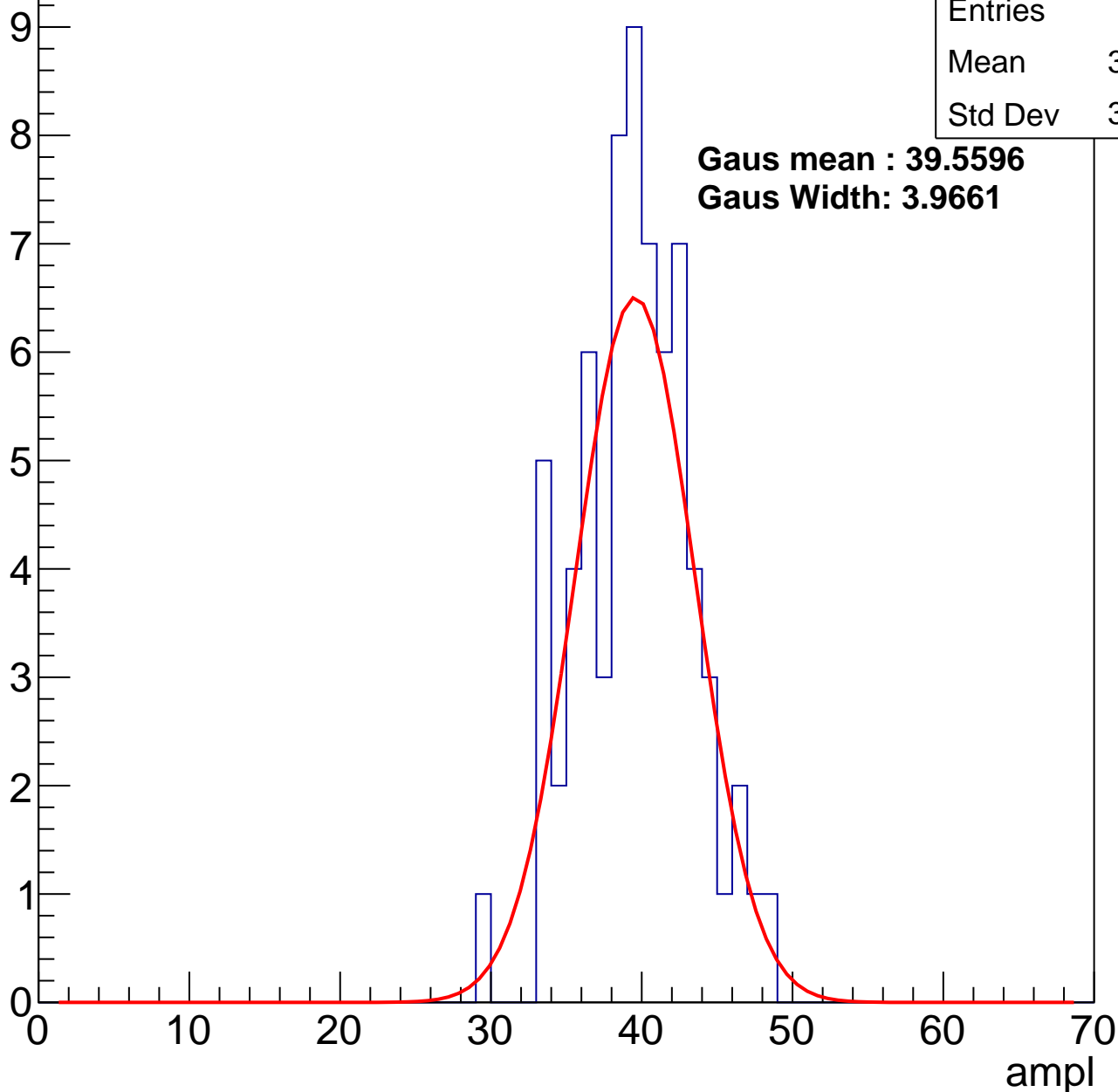
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	39.14
Std Dev	3.739

Gaus mean : 39.5596

Gaus Width: 3.9661



B1L100S, U12-ch118, adc2

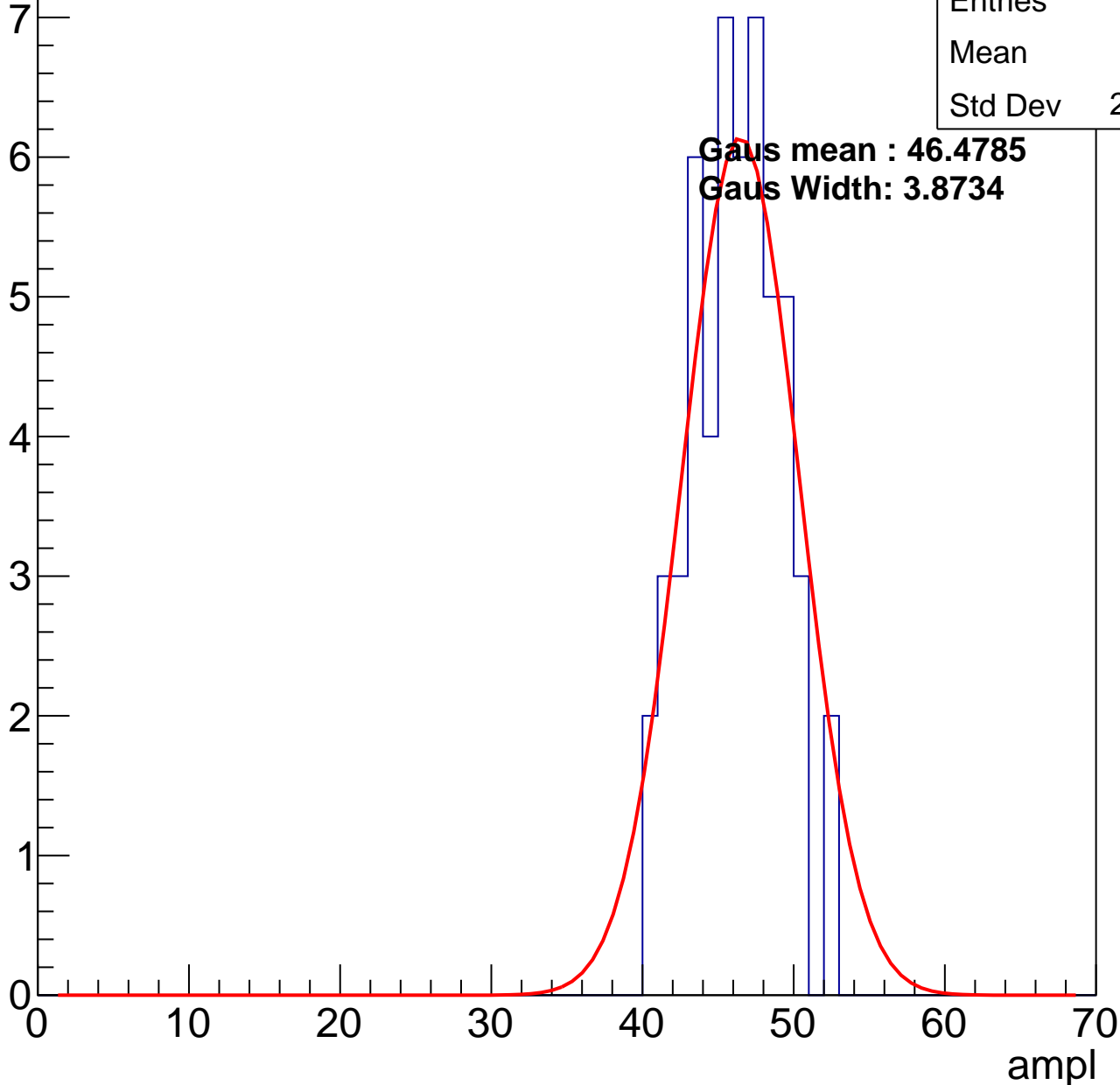
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	45.7
Std Dev	2.943

Gaus mean : 46.4785

Gaus Width: 3.8734

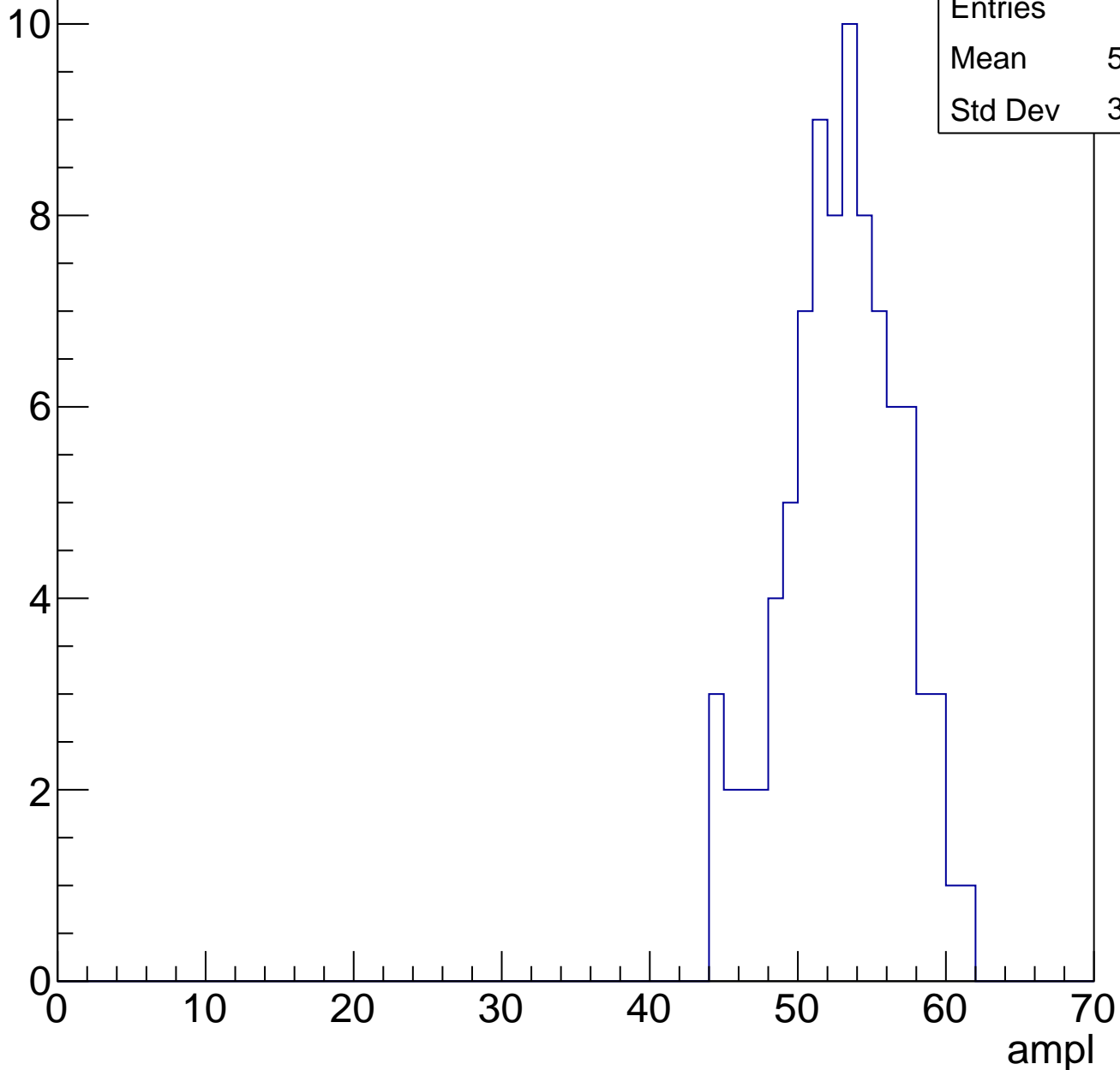


B1L100S, U12-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	52.49
Std Dev	3.847

Entry

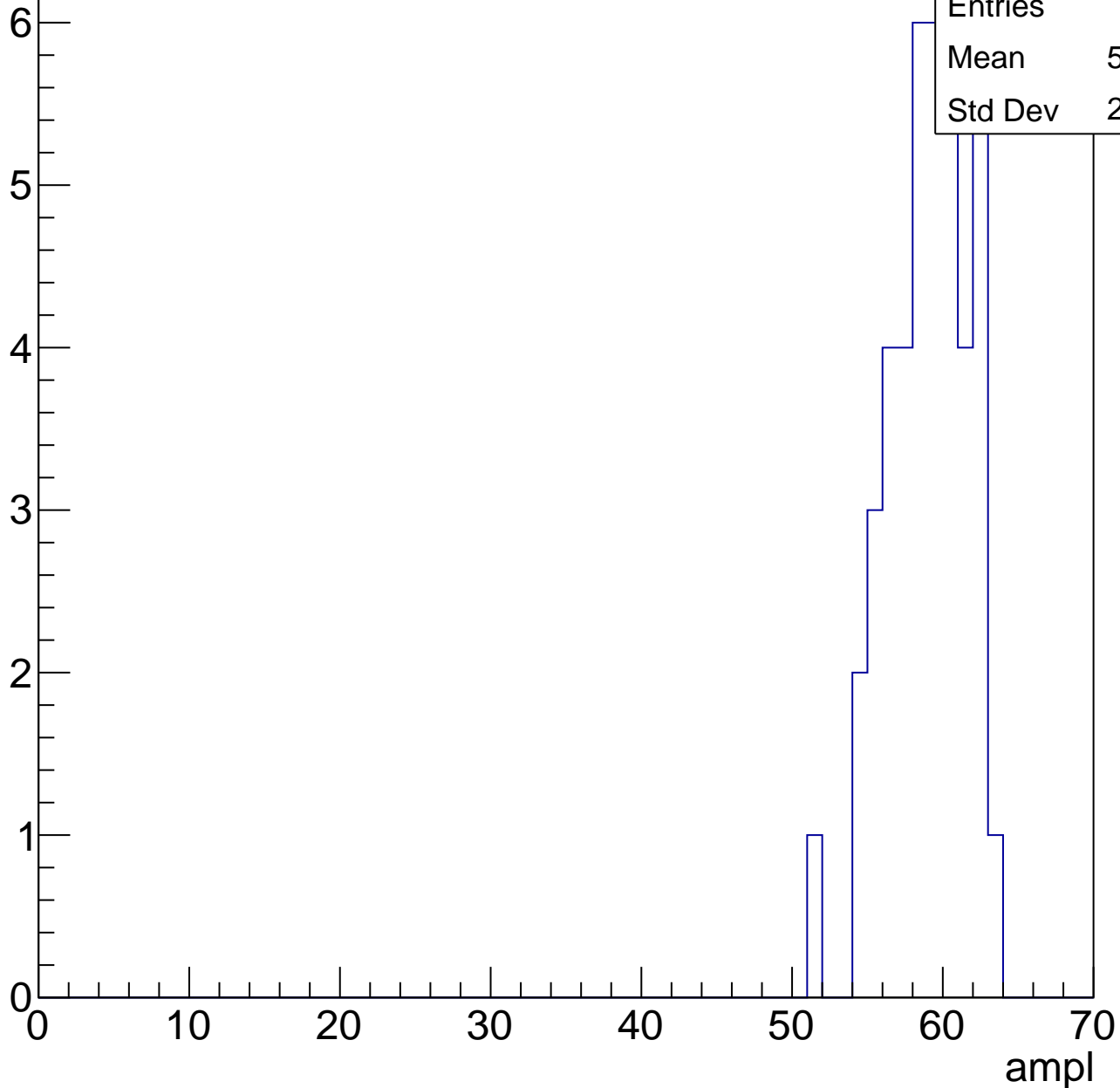


B1L100S, U12-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	58.53
Std Dev	2.653

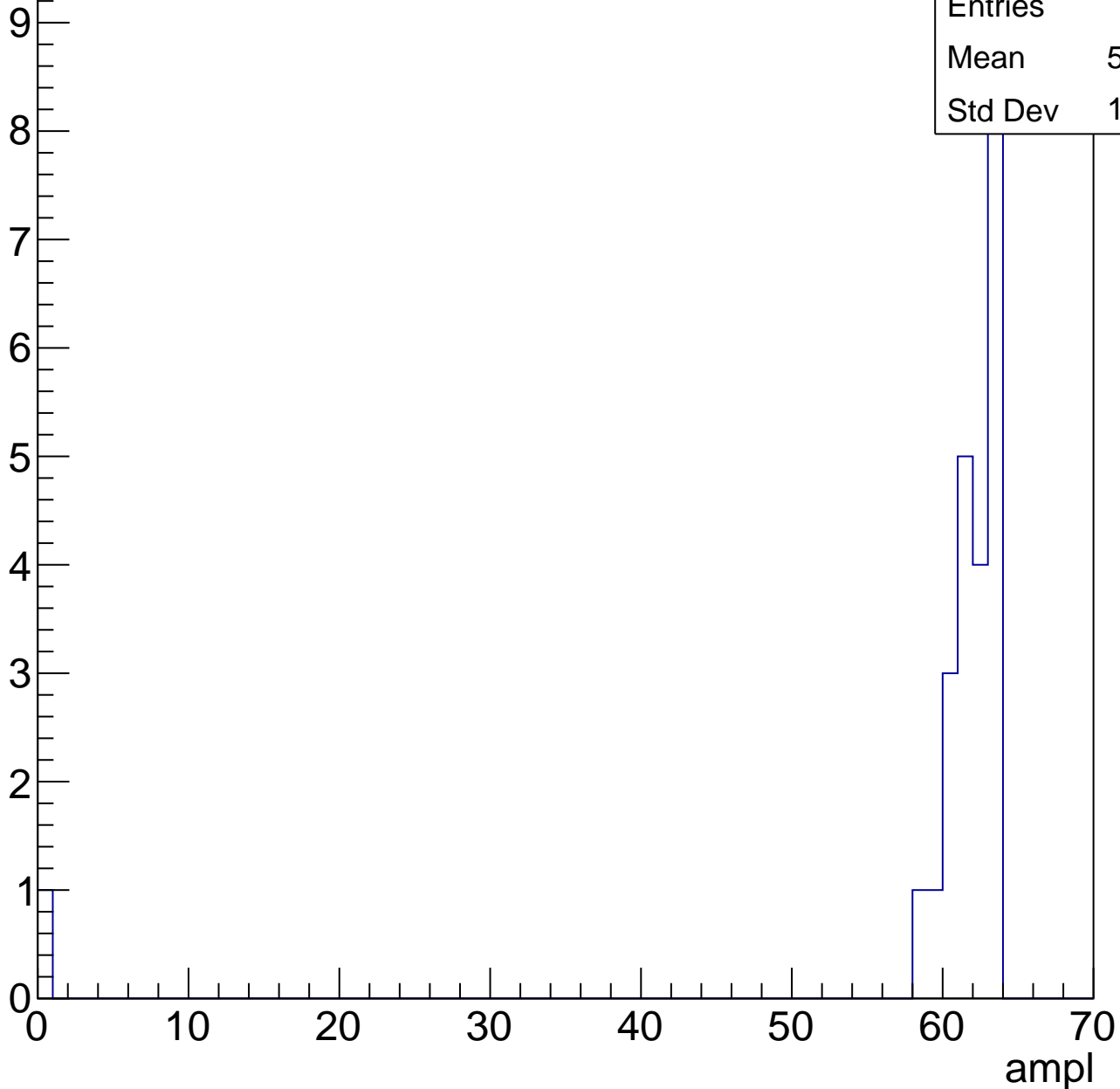


B1L100S, U12-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	59.04
Std Dev	12.39



B1L100S, U12-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch119, adc0

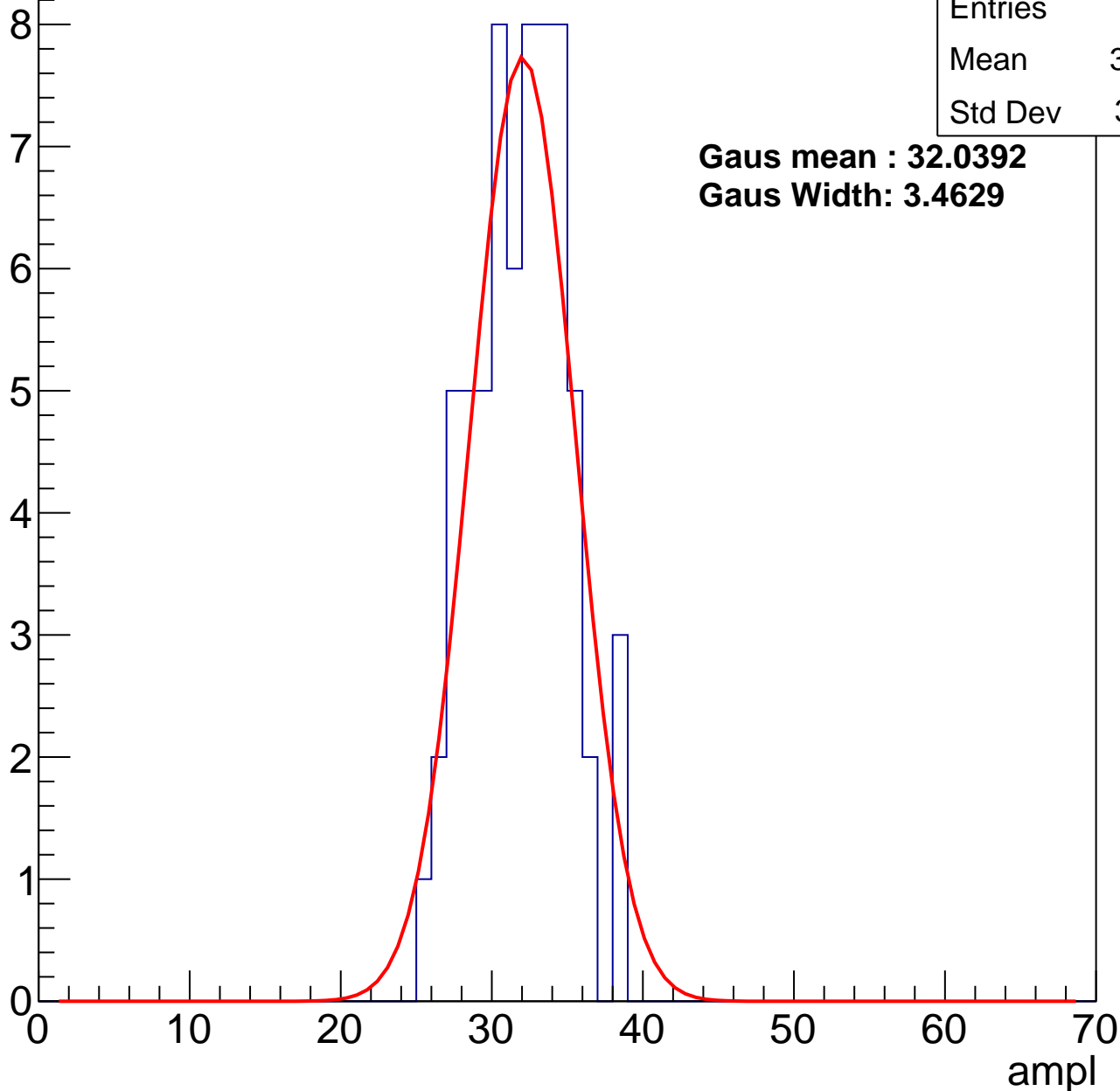
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	31.45
Std Dev	3.051

Gaus mean : 32.0392

Gaus Width: 3.4629



B1L100S, U12-ch119, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	38.12
Std Dev	4.006

Gaus mean : 39.2605

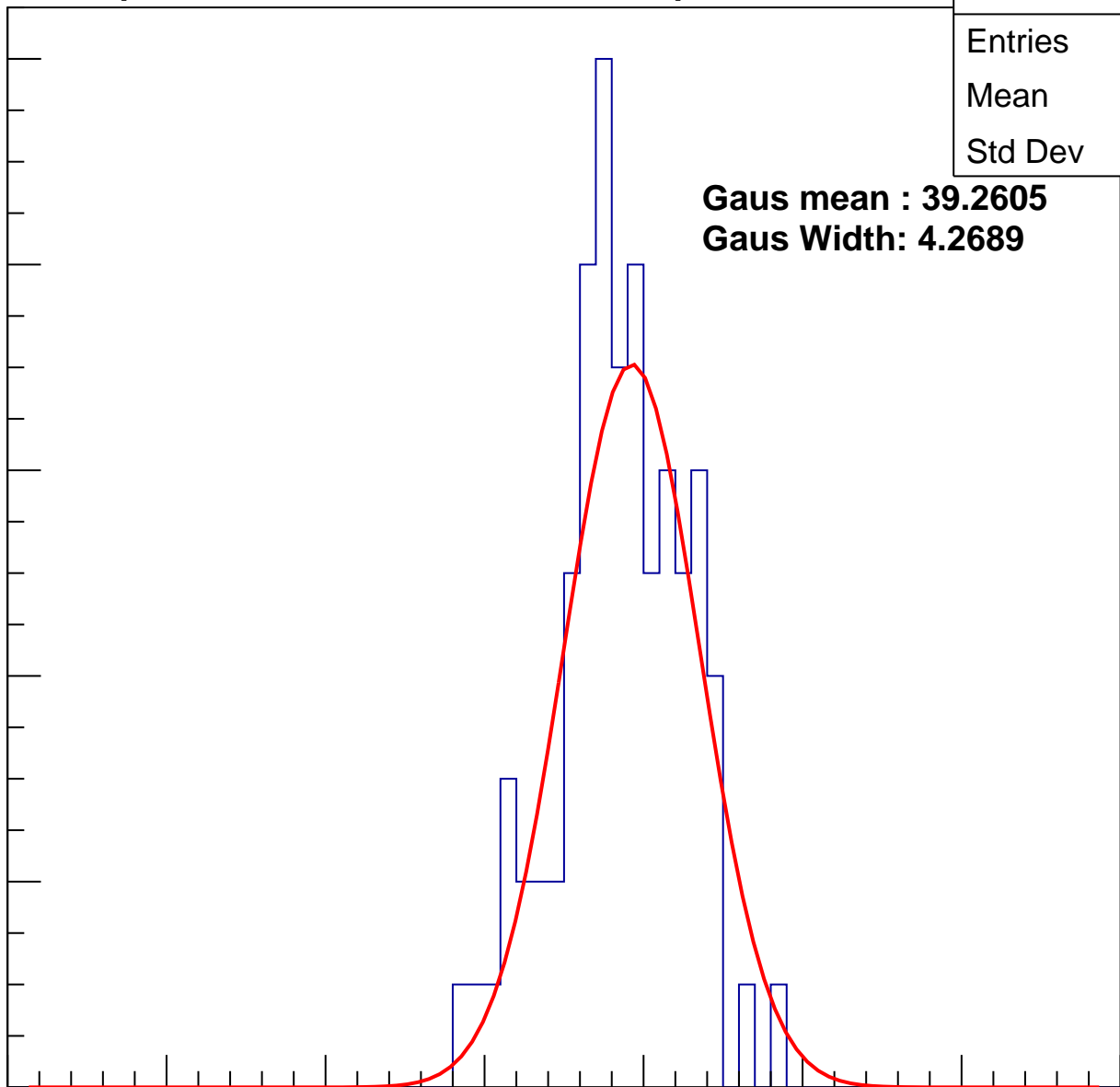
Gaus Width: 4.2689

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch119, adc2

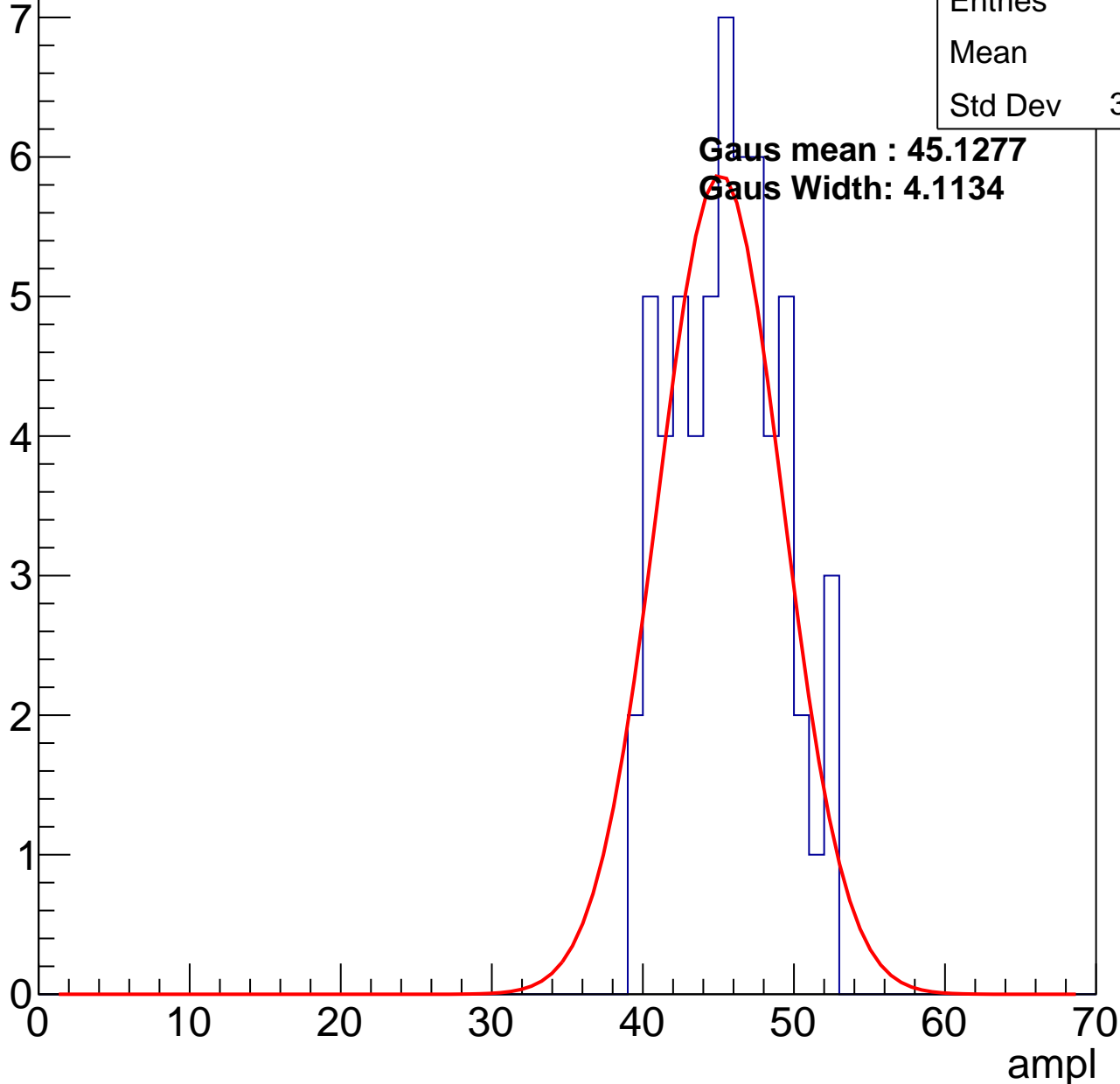
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	45.1
Std Dev	3.458

Gaus mean : 45.1277

Gaus Width: 4.1134

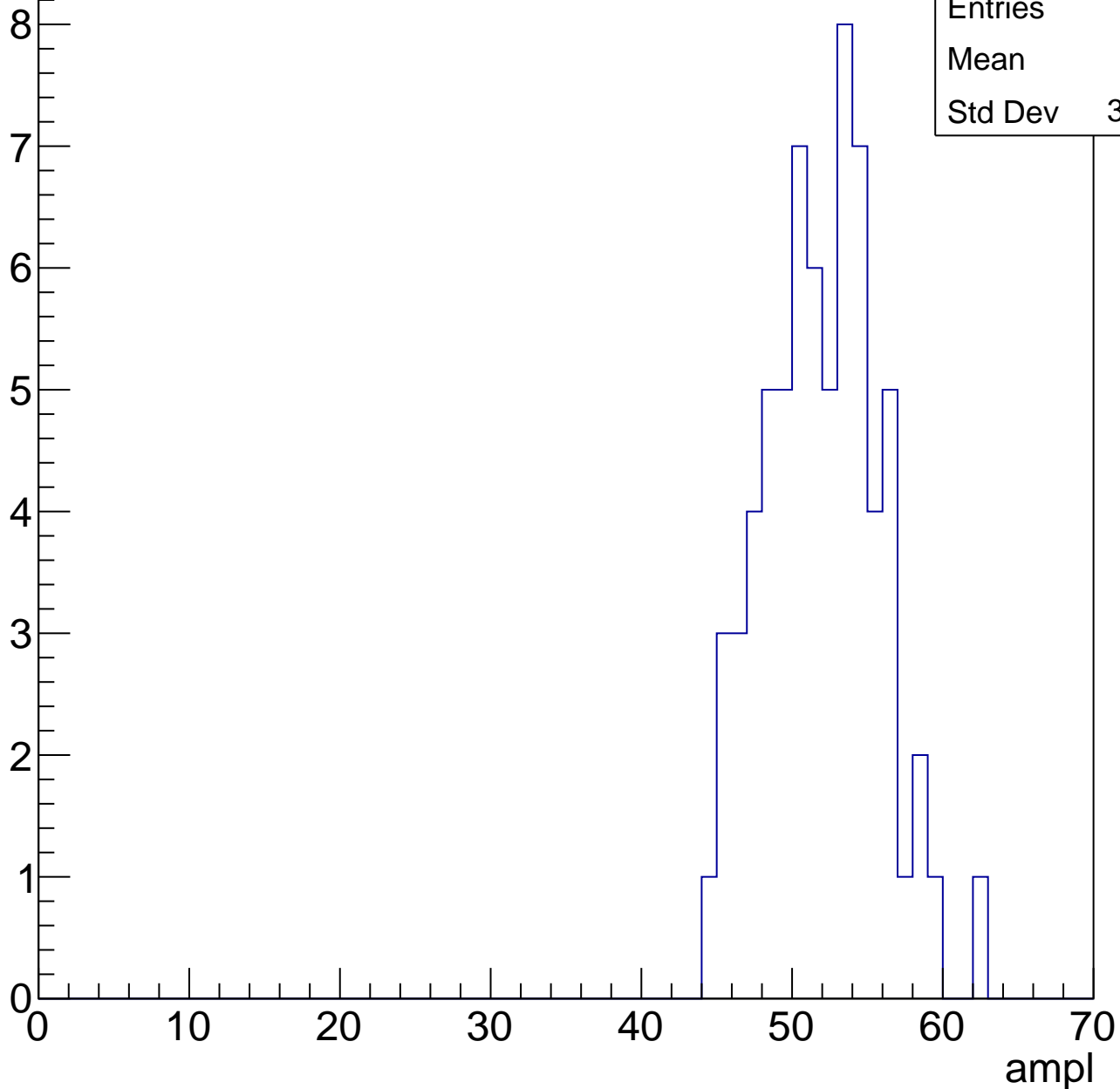


B1L100S, U12-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	51.5
Std Dev	3.755

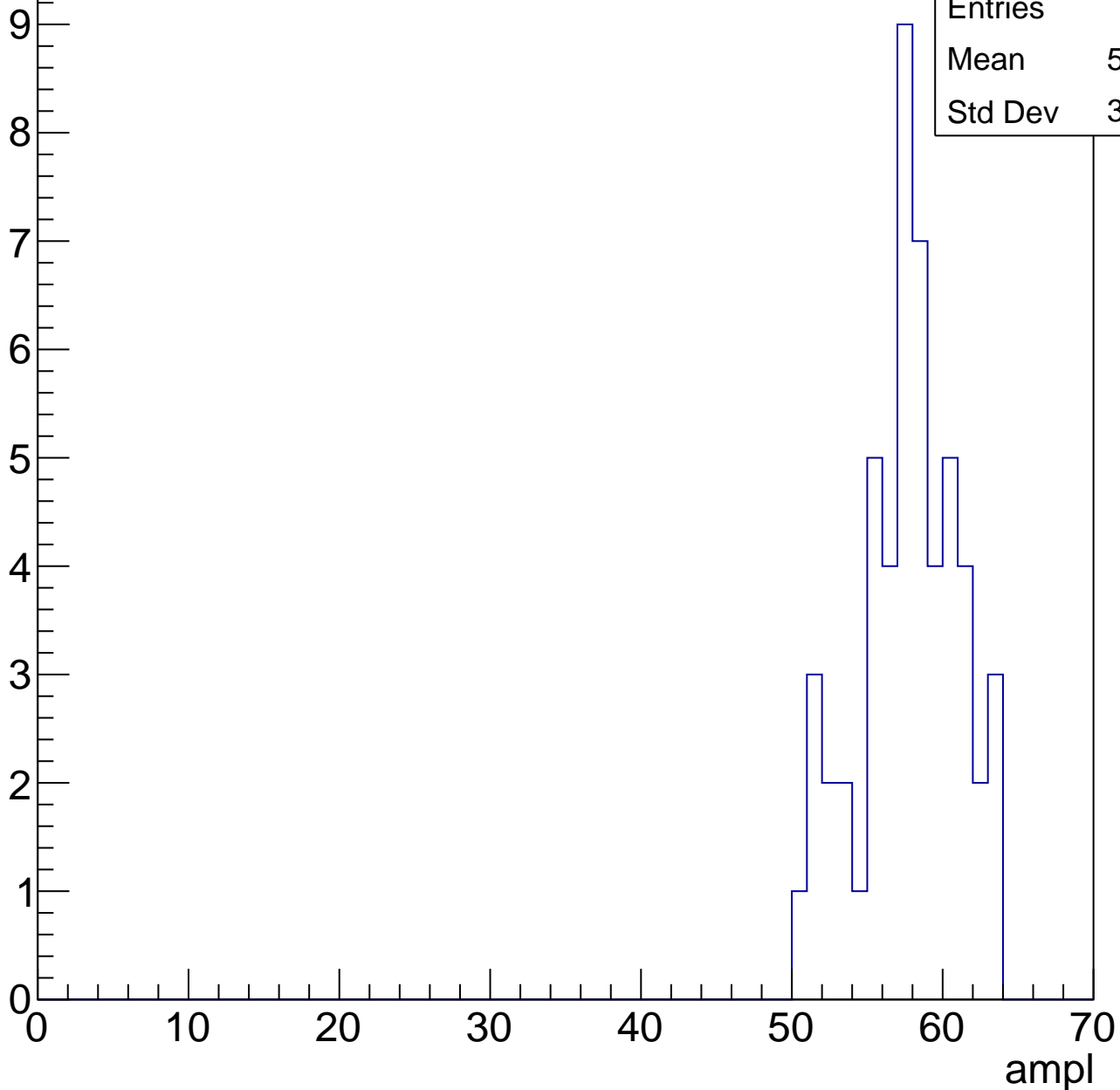


B1L100S, U12-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	57.27
Std Dev	3.276

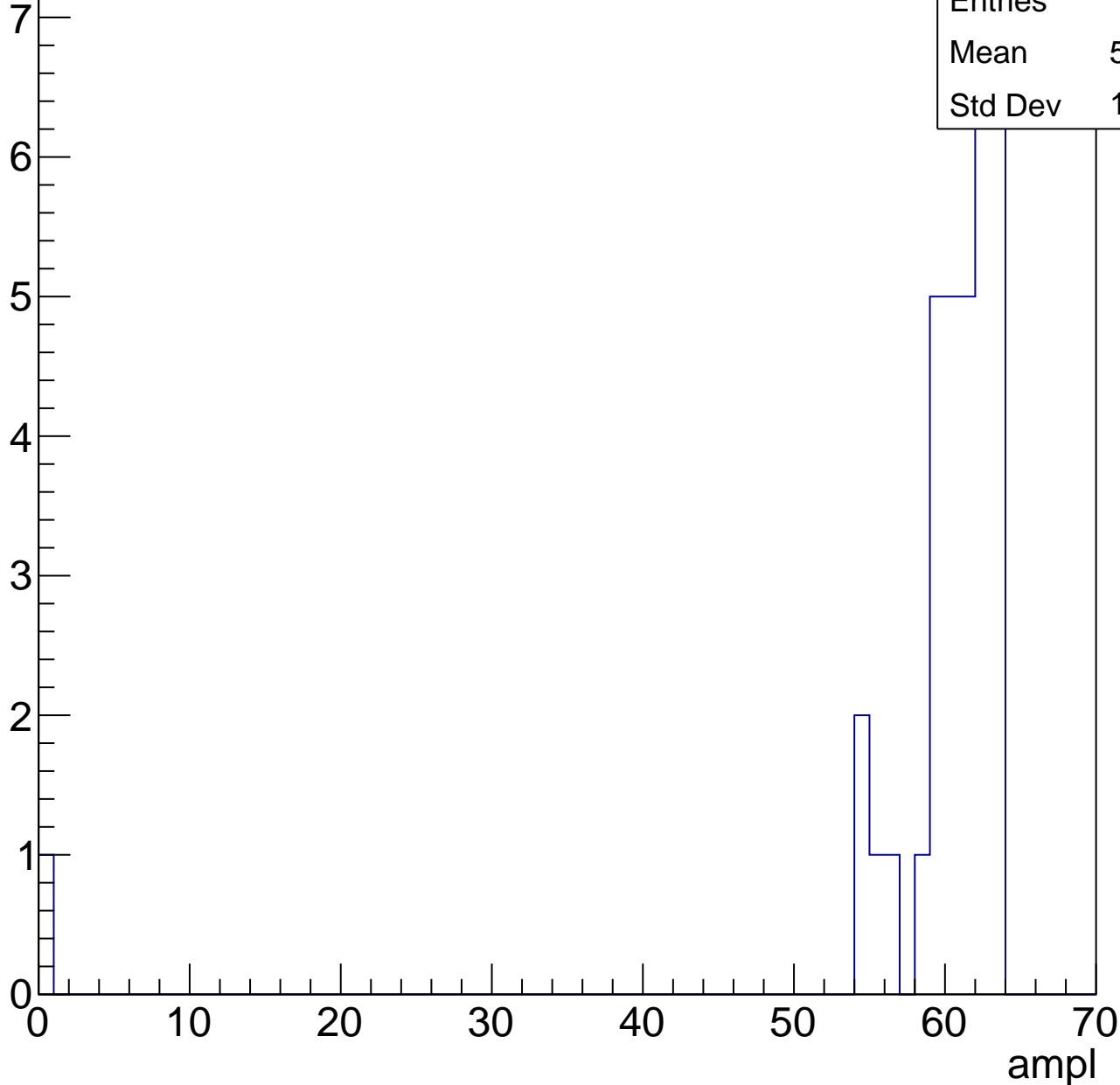


B1L100S, U12-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	35
Mean	58.63
Std Dev	10.35



B1L100S, U12-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch120, adc0

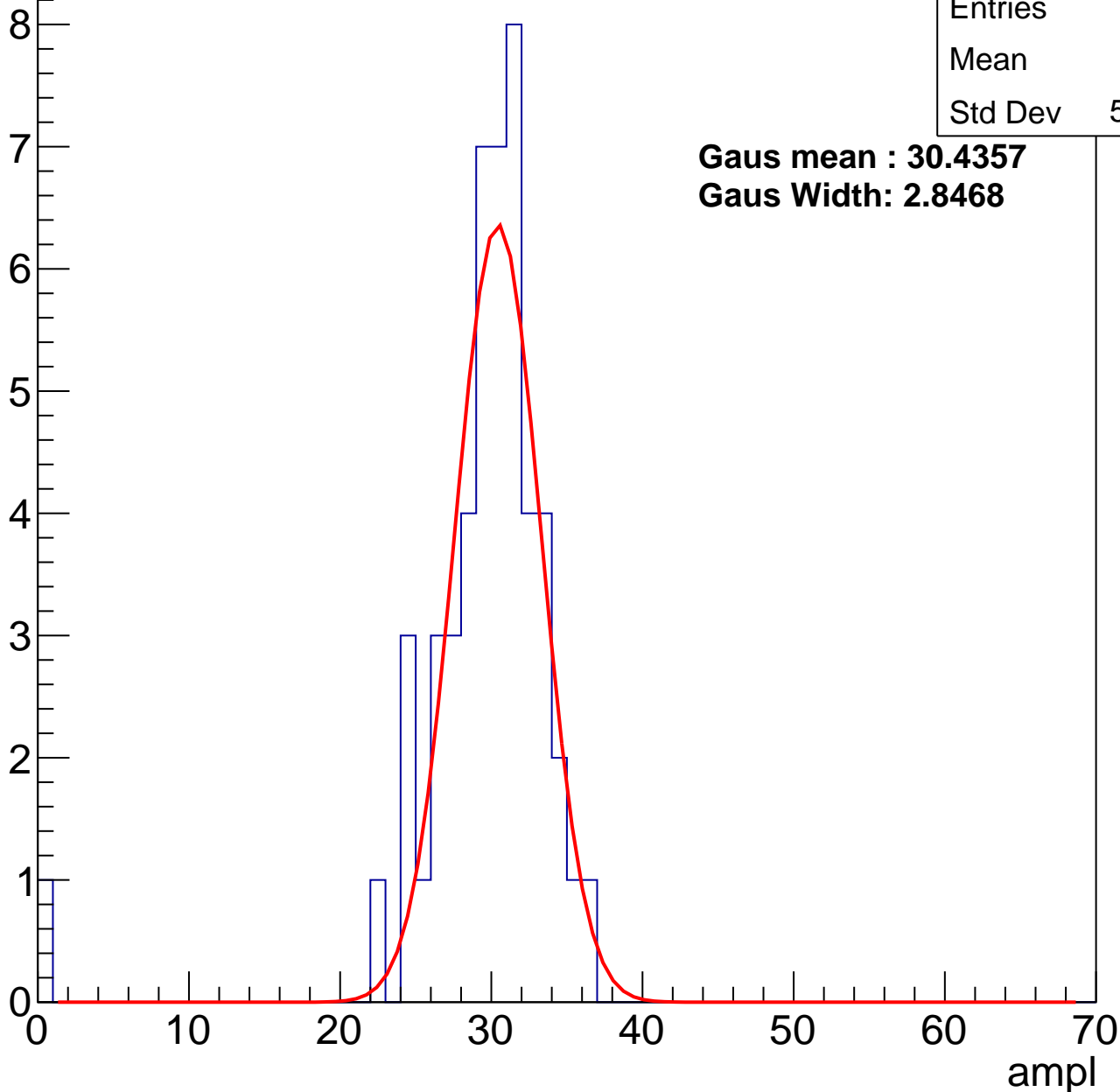
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	29
Std Dev	5.095

Gaus mean : 30.4357

Gaus Width: 2.8468



B1L100S, U12-ch120, adc1

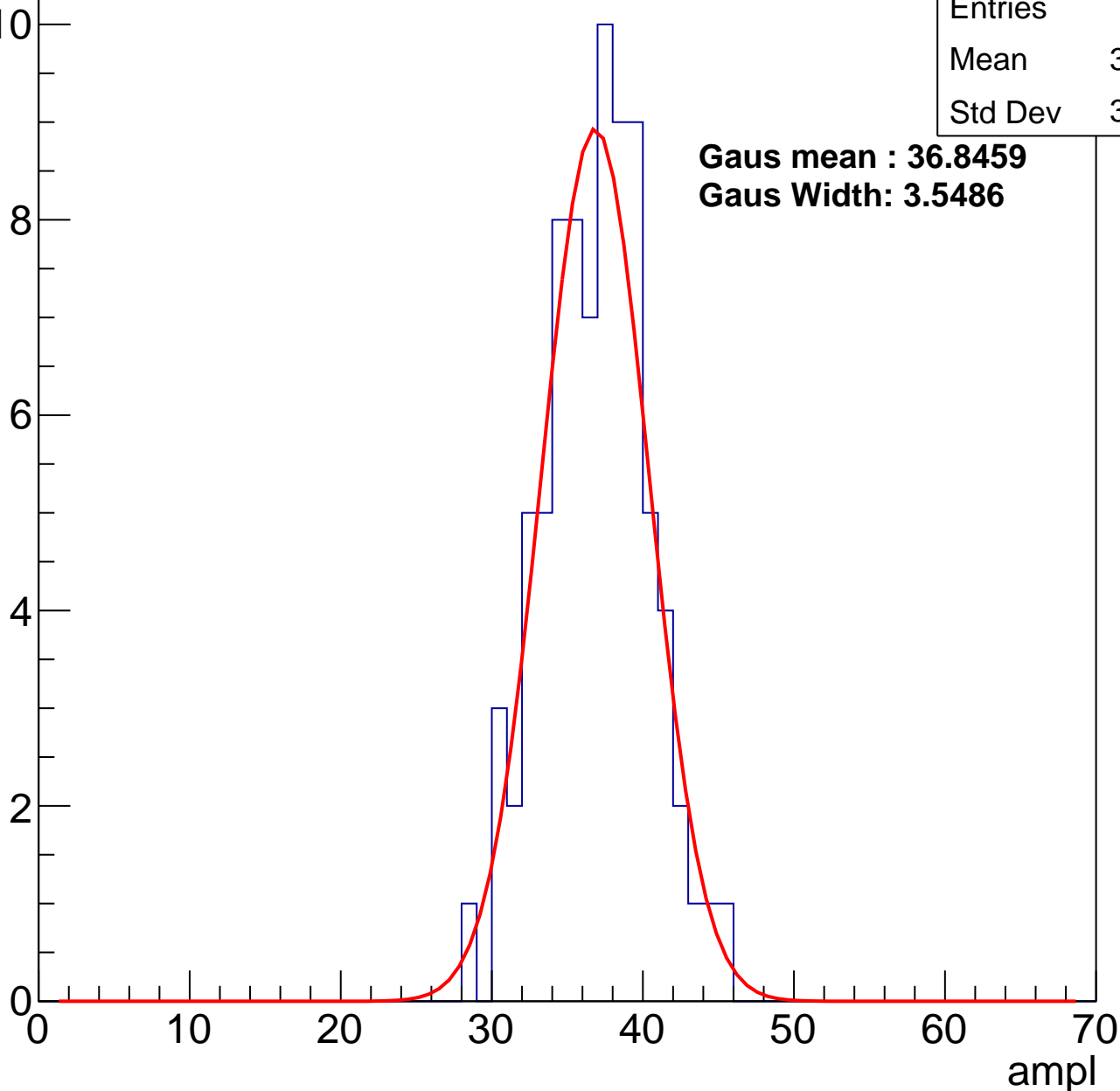
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.44
Std Dev	3.414

Gaus mean : 36.8459

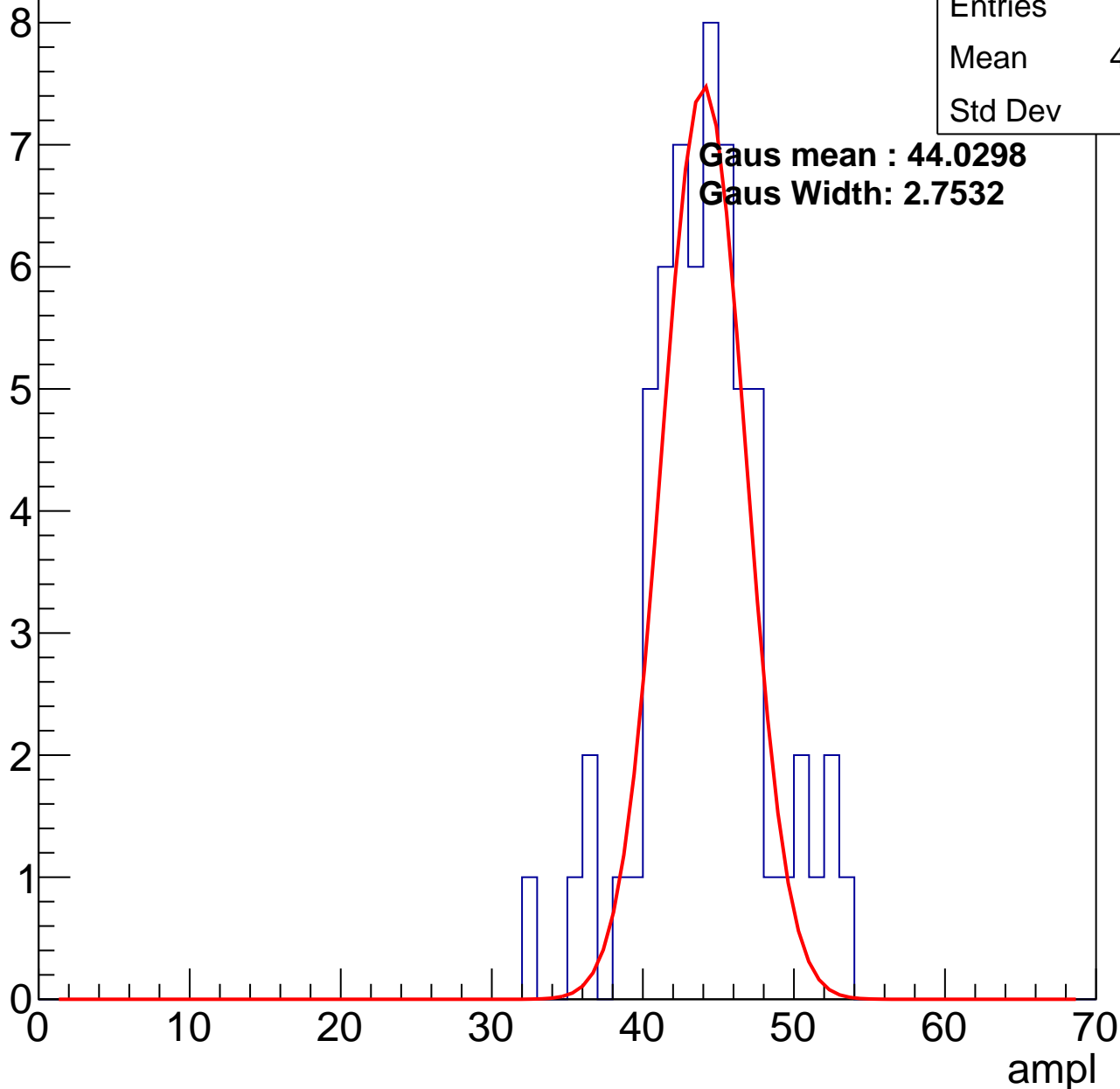
Gaus Width: 3.5486



B1L100S, U12-ch120, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	63
Mean	43.67
Std Dev	4.02

Gaus mean : 44.0298

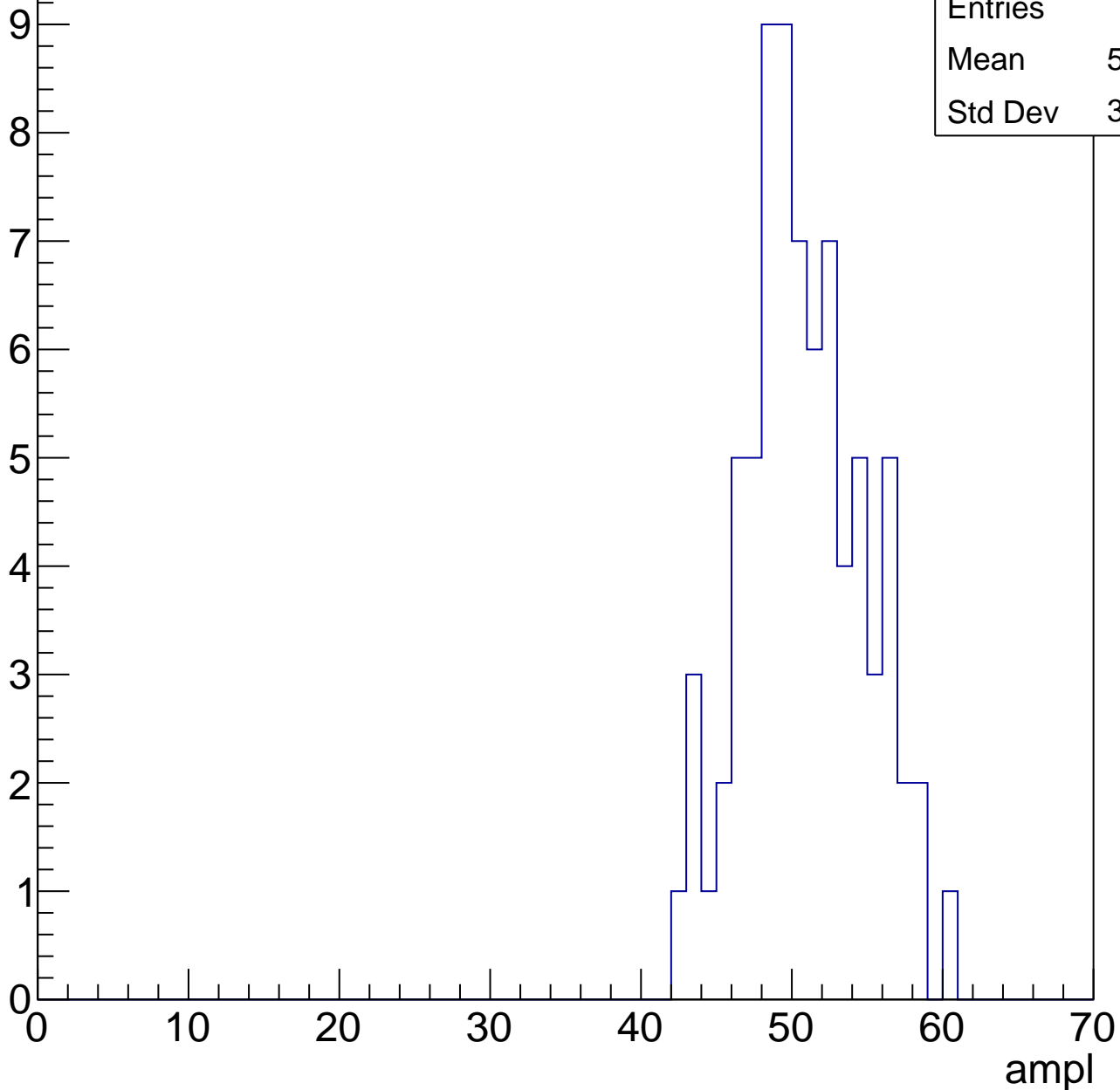
Gaus Width: 2.7532

B1L100S, U12-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	50.39
Std Dev	3.925



B1L100S, U12-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

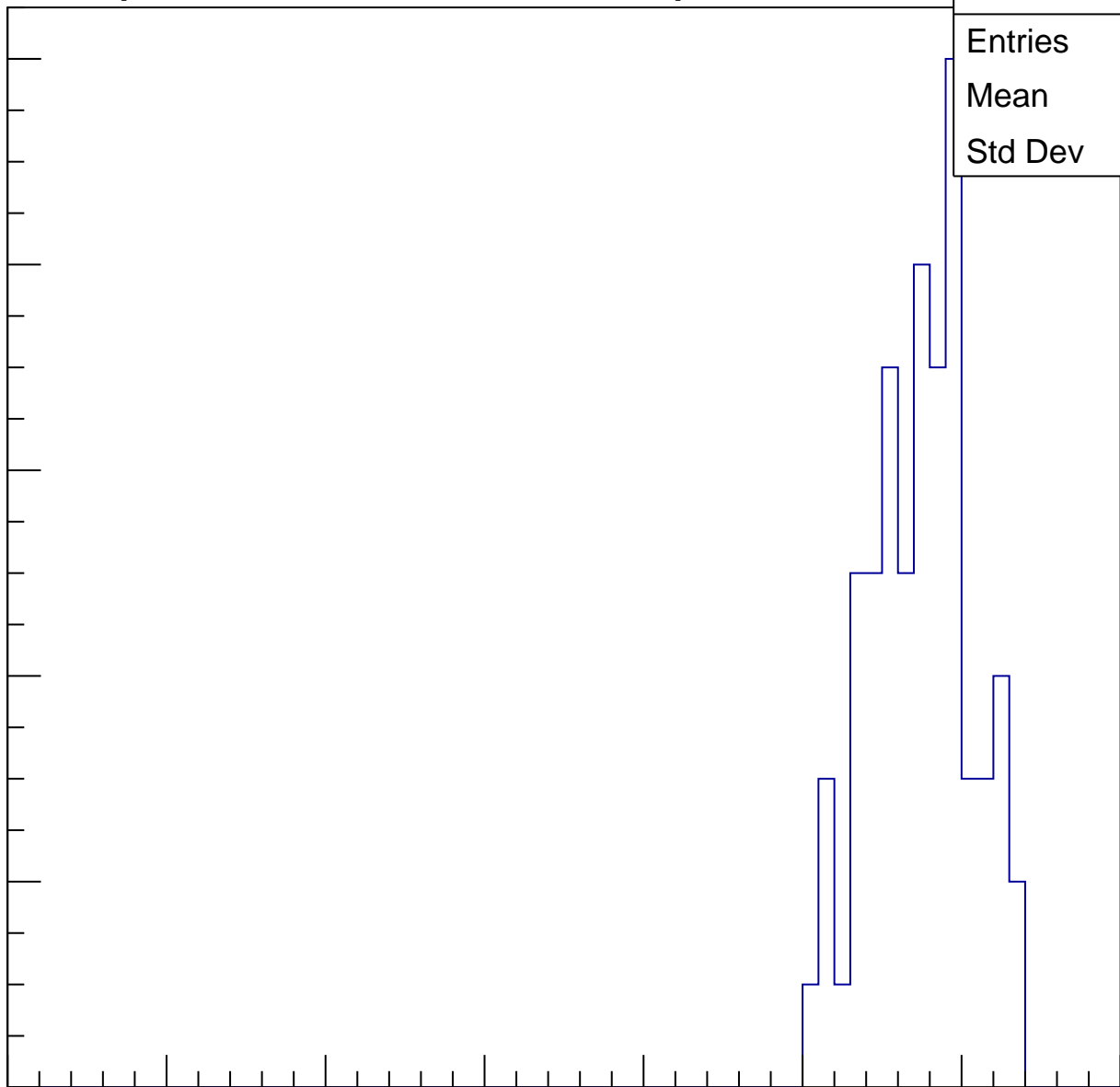
Entries	64
Mean	56.94
Std Dev	3.147

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

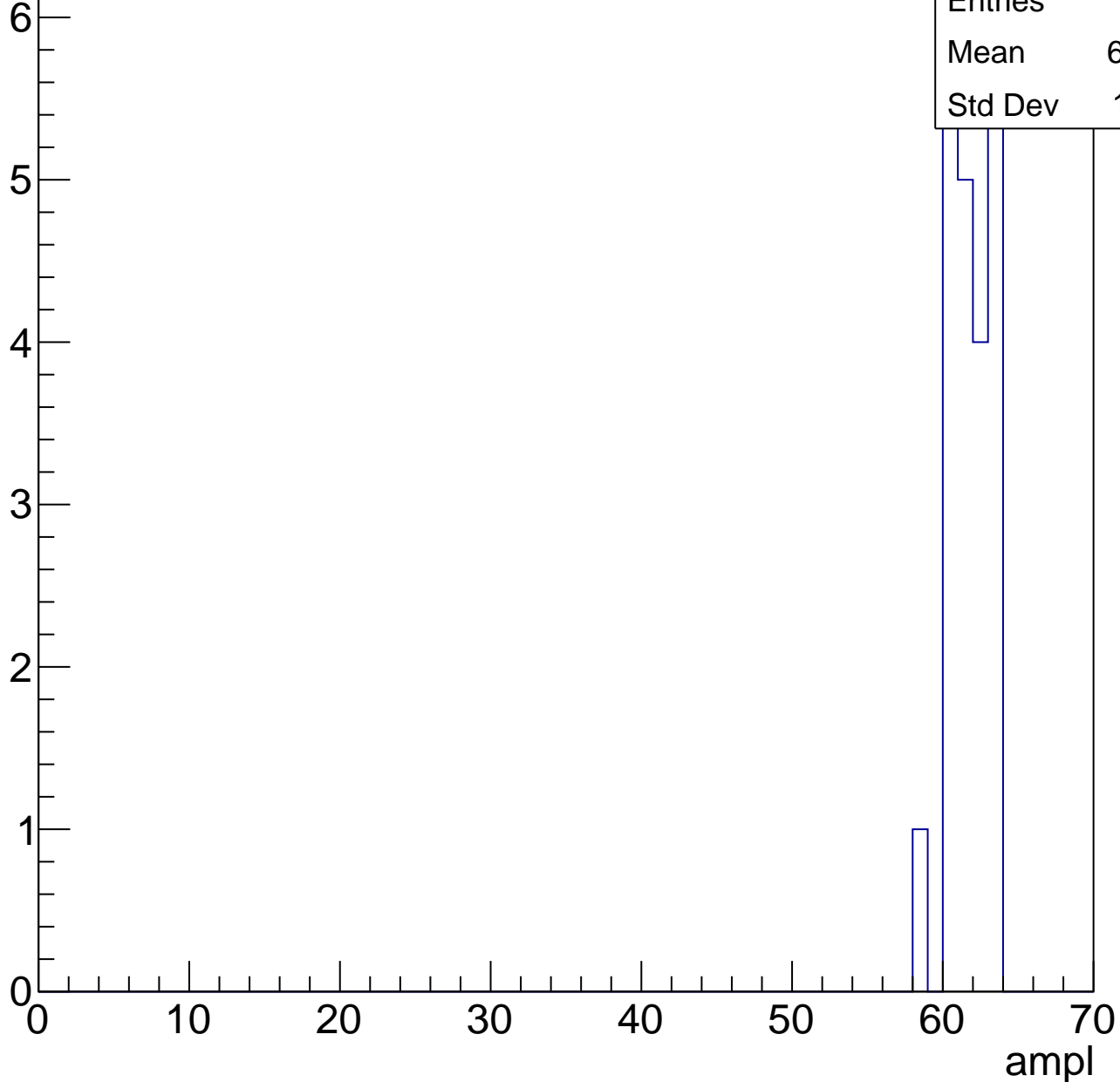
ampl



B1L100S, U12-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

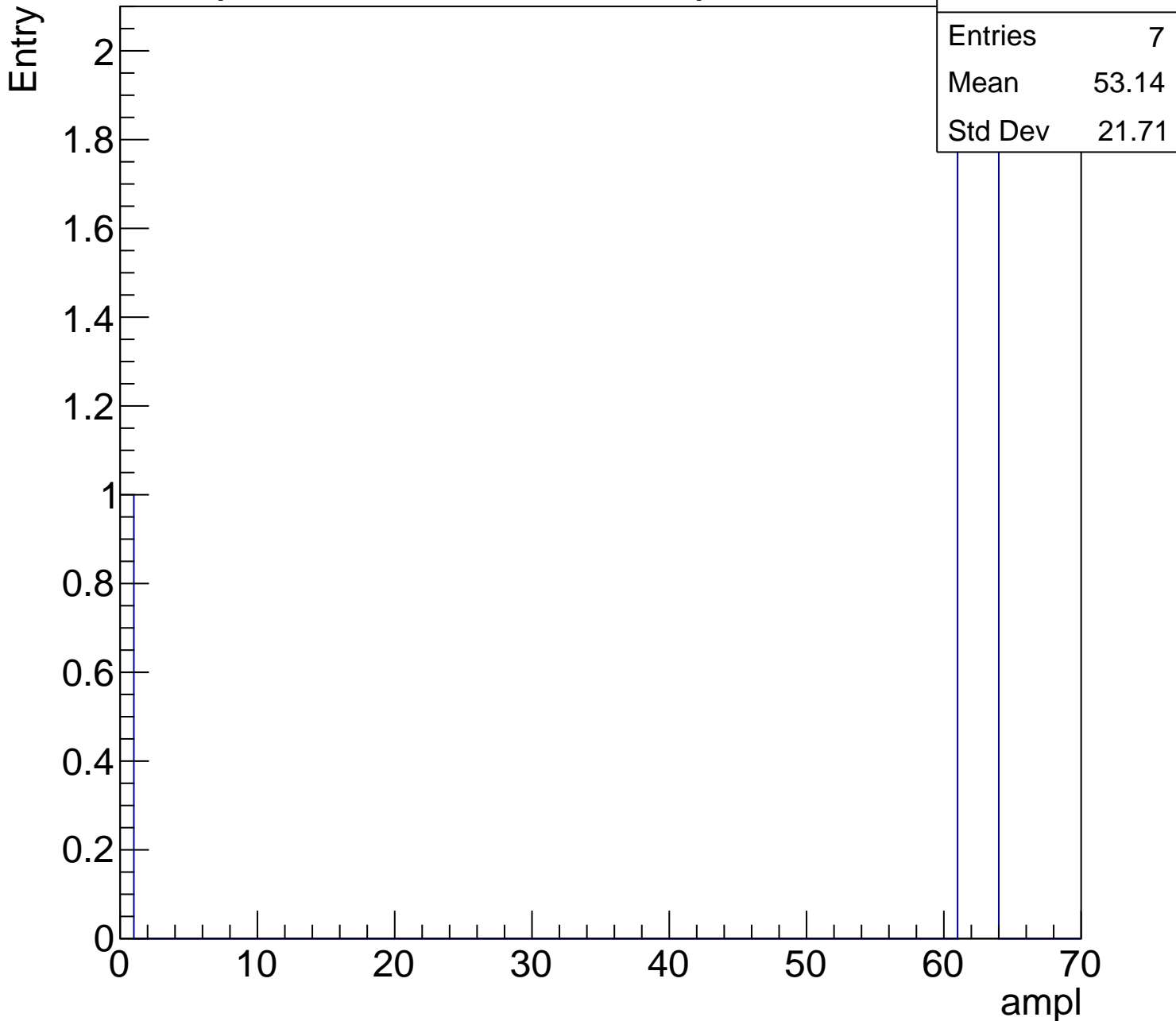
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	53.14
Std Dev	21.71

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch121, adc0

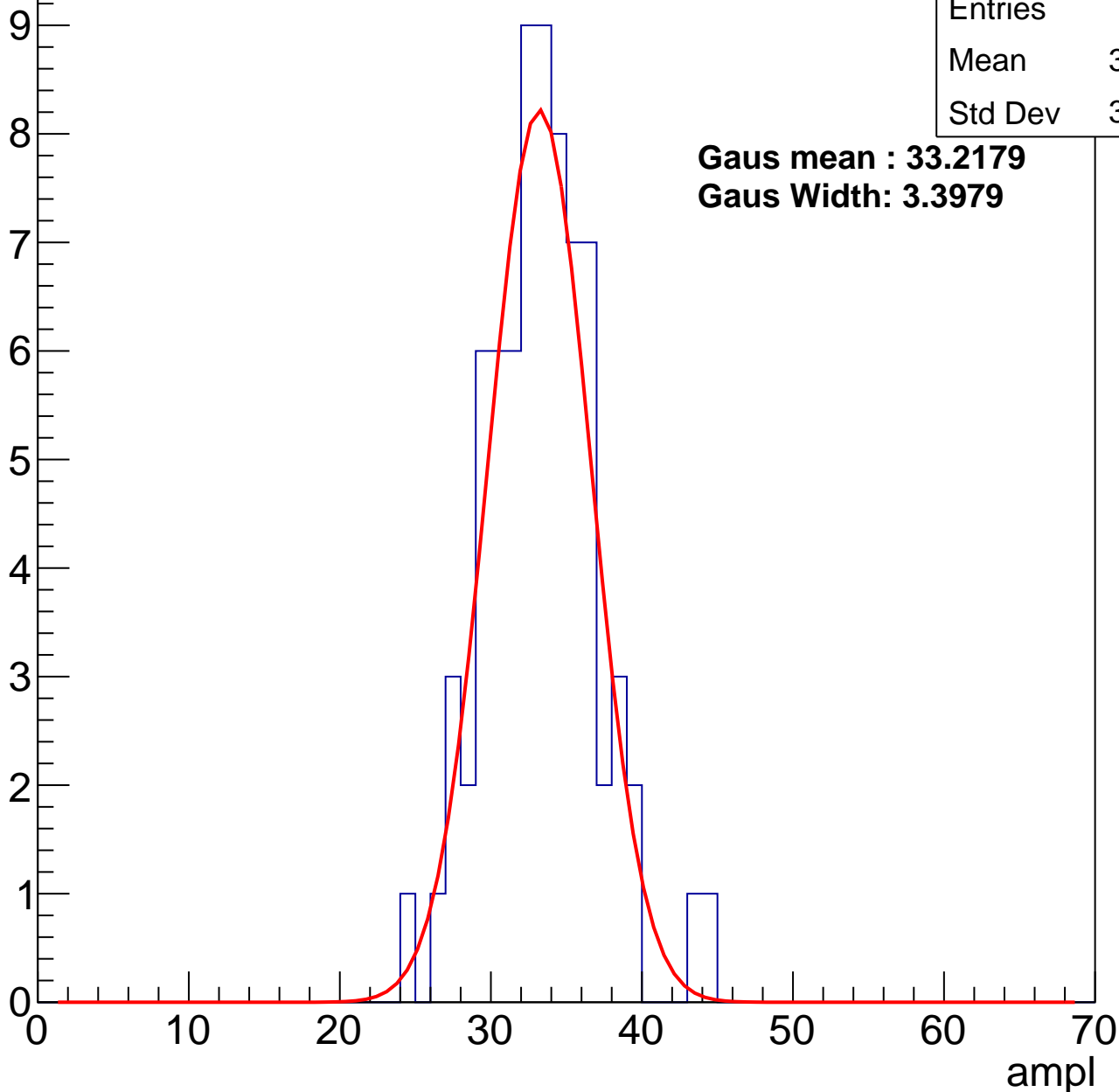
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	32.89
Std Dev	3.619

Gaus mean : 33.2179

Gaus Width: 3.3979



B1L100S, U12-ch121, adc1

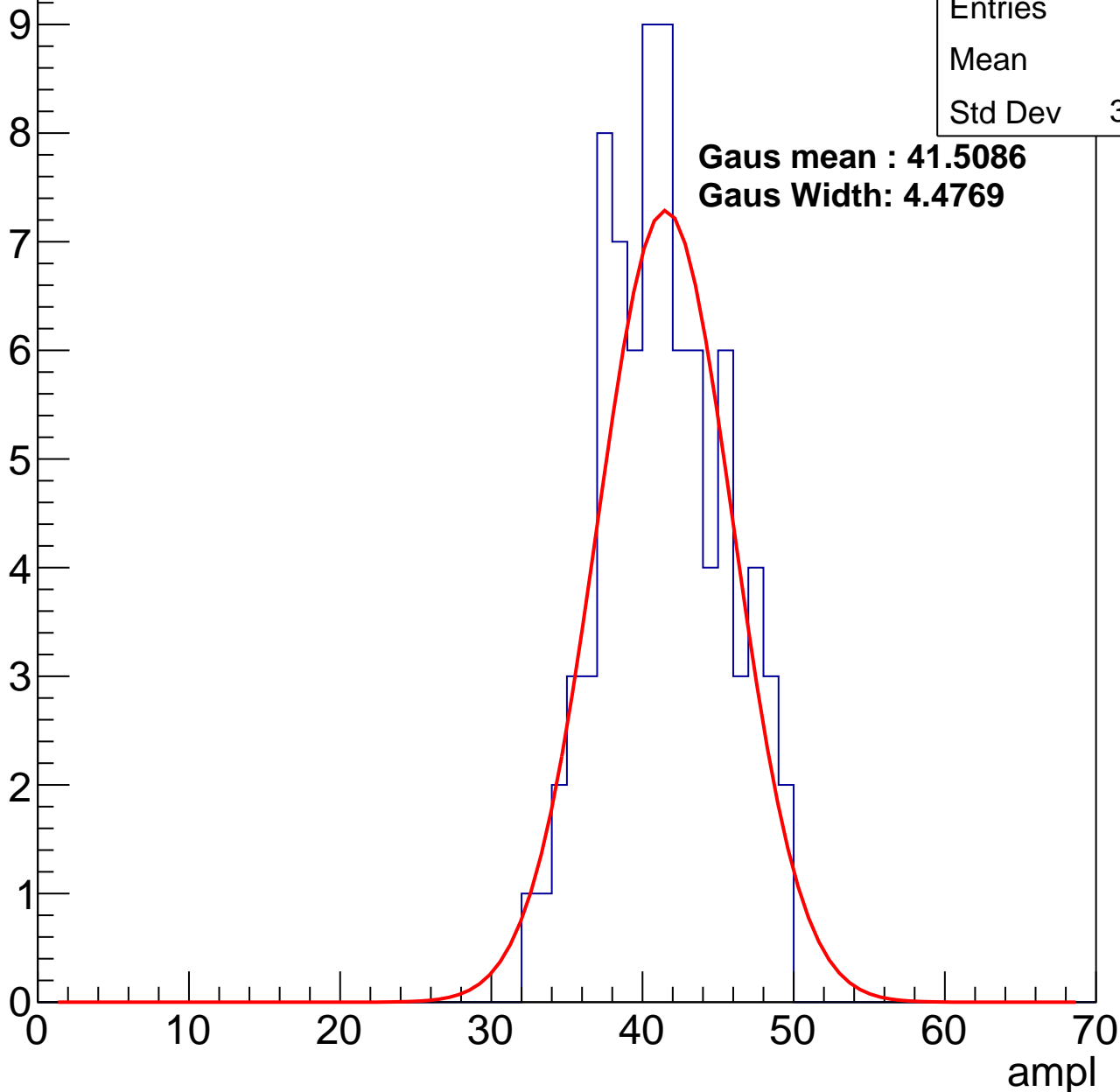
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	40.9
Std Dev	3.966

Gaus mean : 41.5086

Gaus Width: 4.4769



B1L100S, U12-ch121, adc2

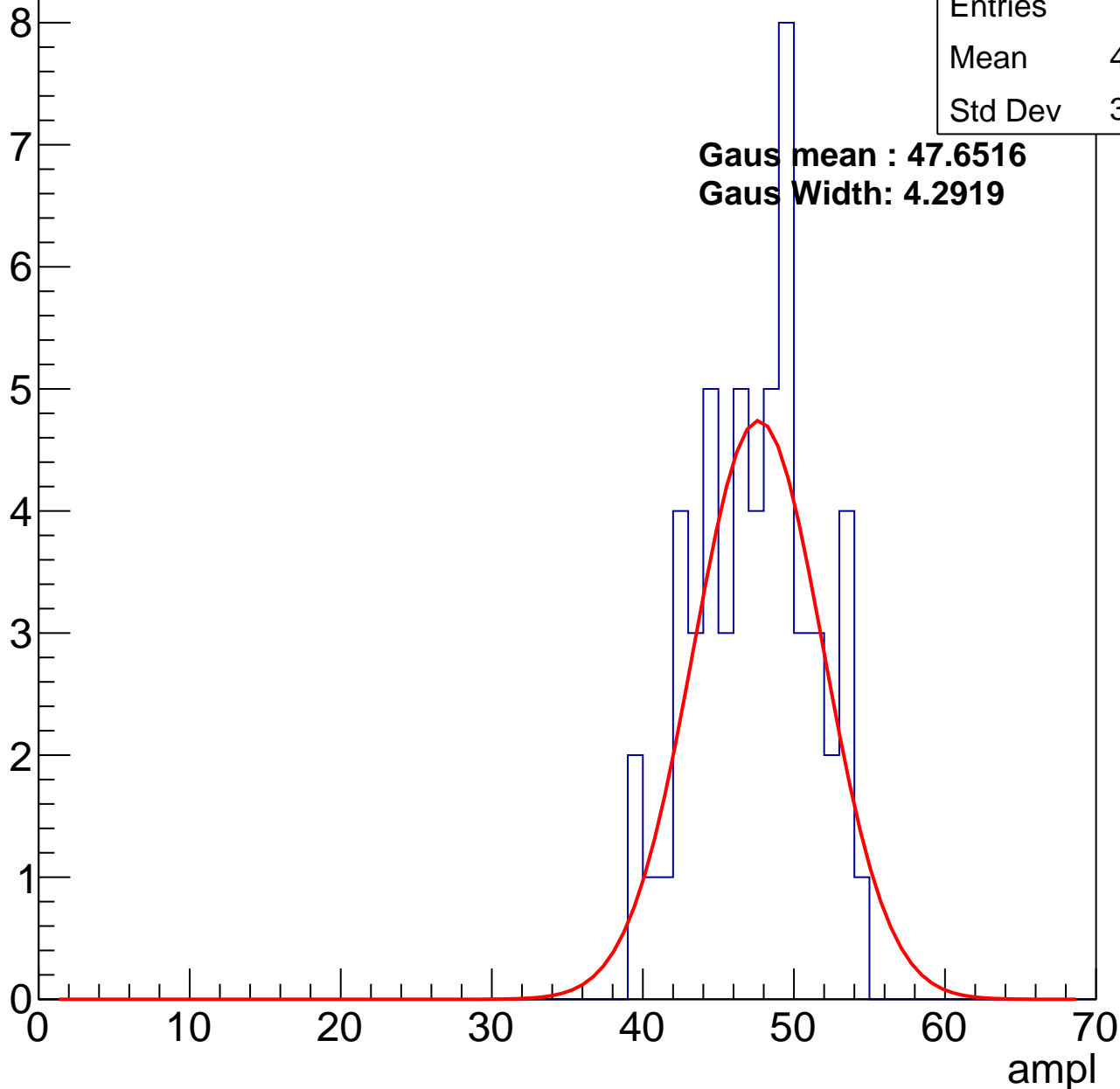
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	46.93
Std Dev	3.795

Gaus mean : 47.6516

Gaus Width: 4.2919

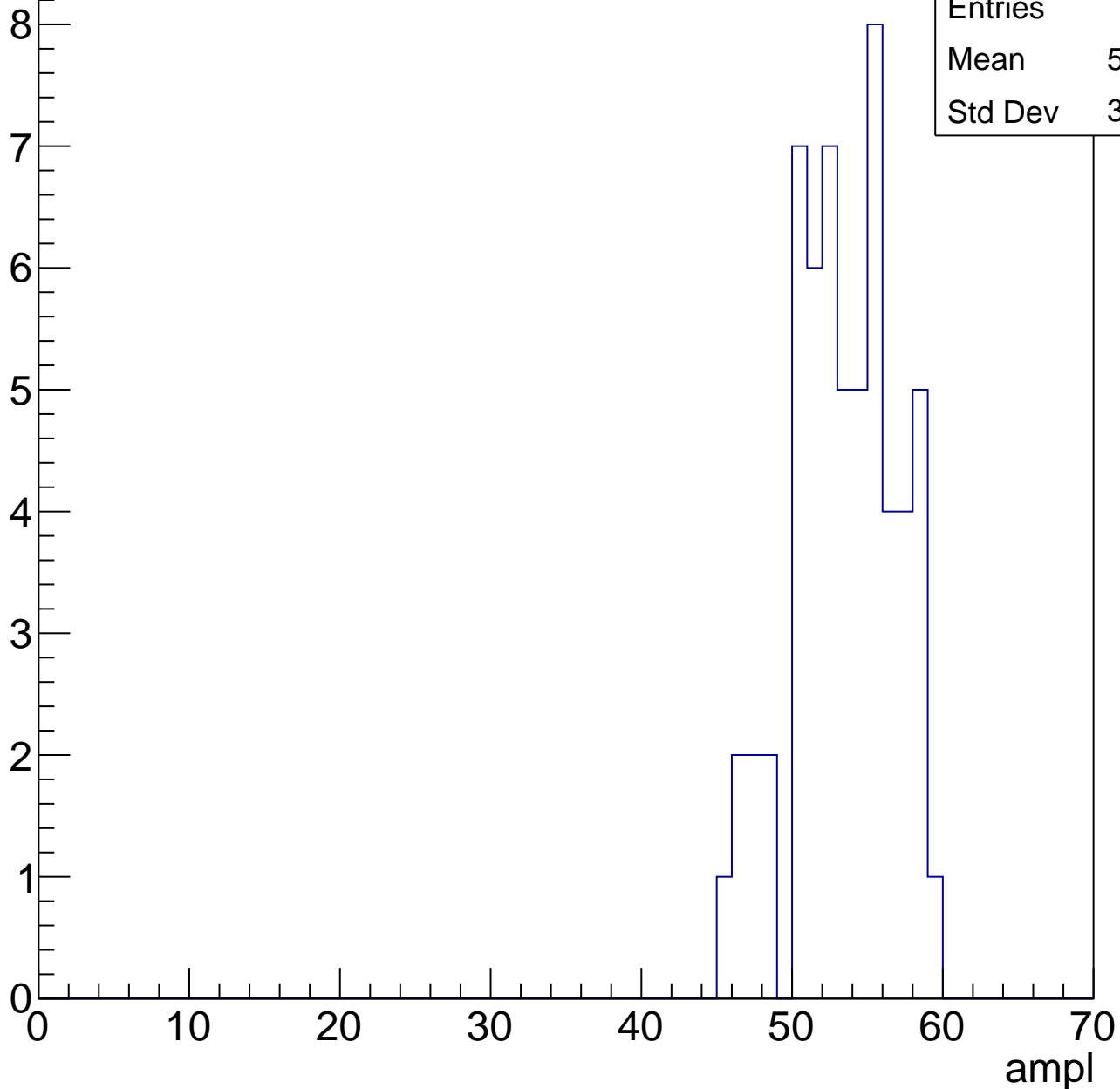


B1L100S, U12-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	52.93
Std Dev	3.384

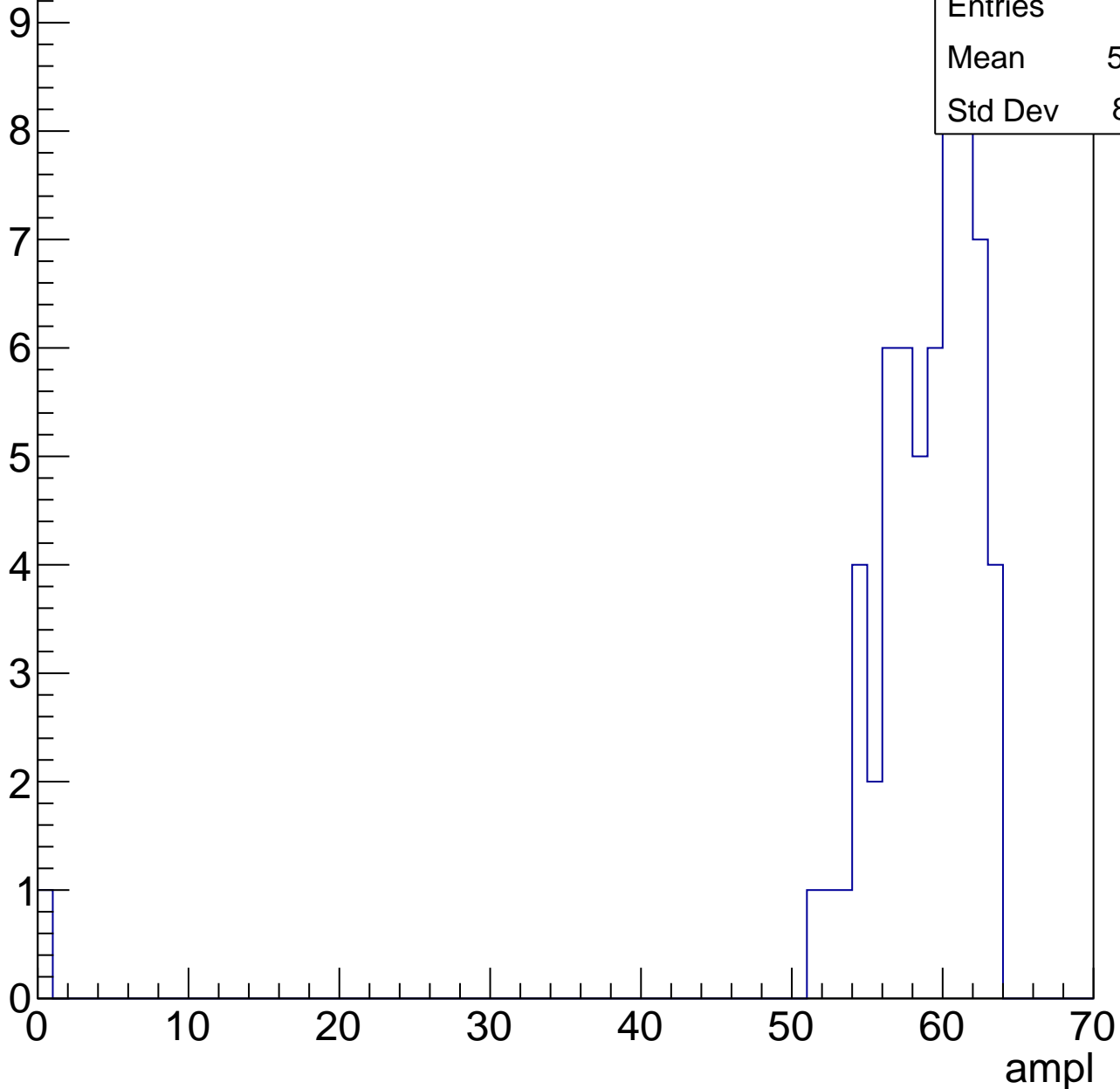


B1L100S, U12-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	57.67
Std Dev	8.001

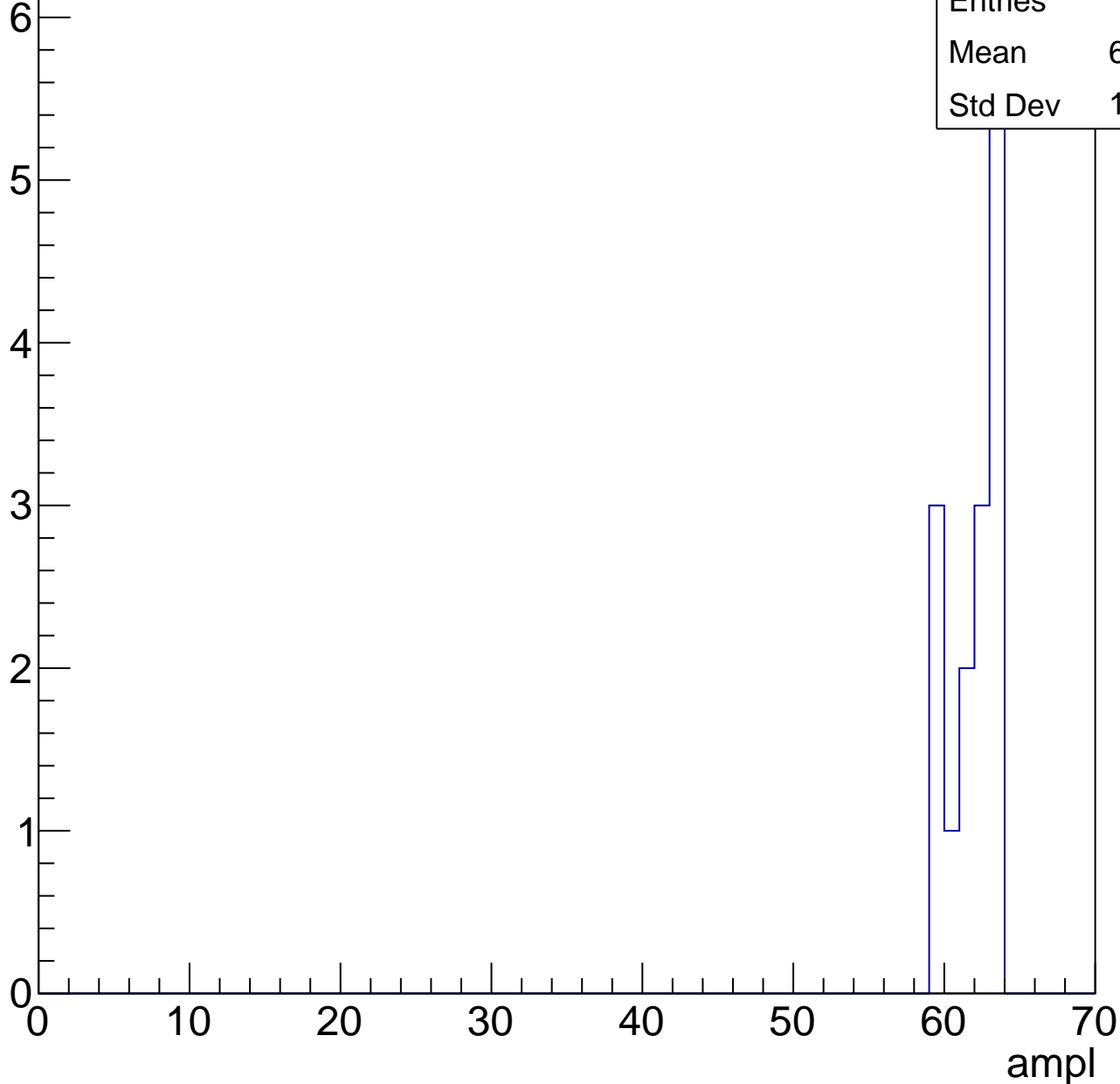


B1L100S, U12-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.53
Std Dev	1.543



B1L100S, U12-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

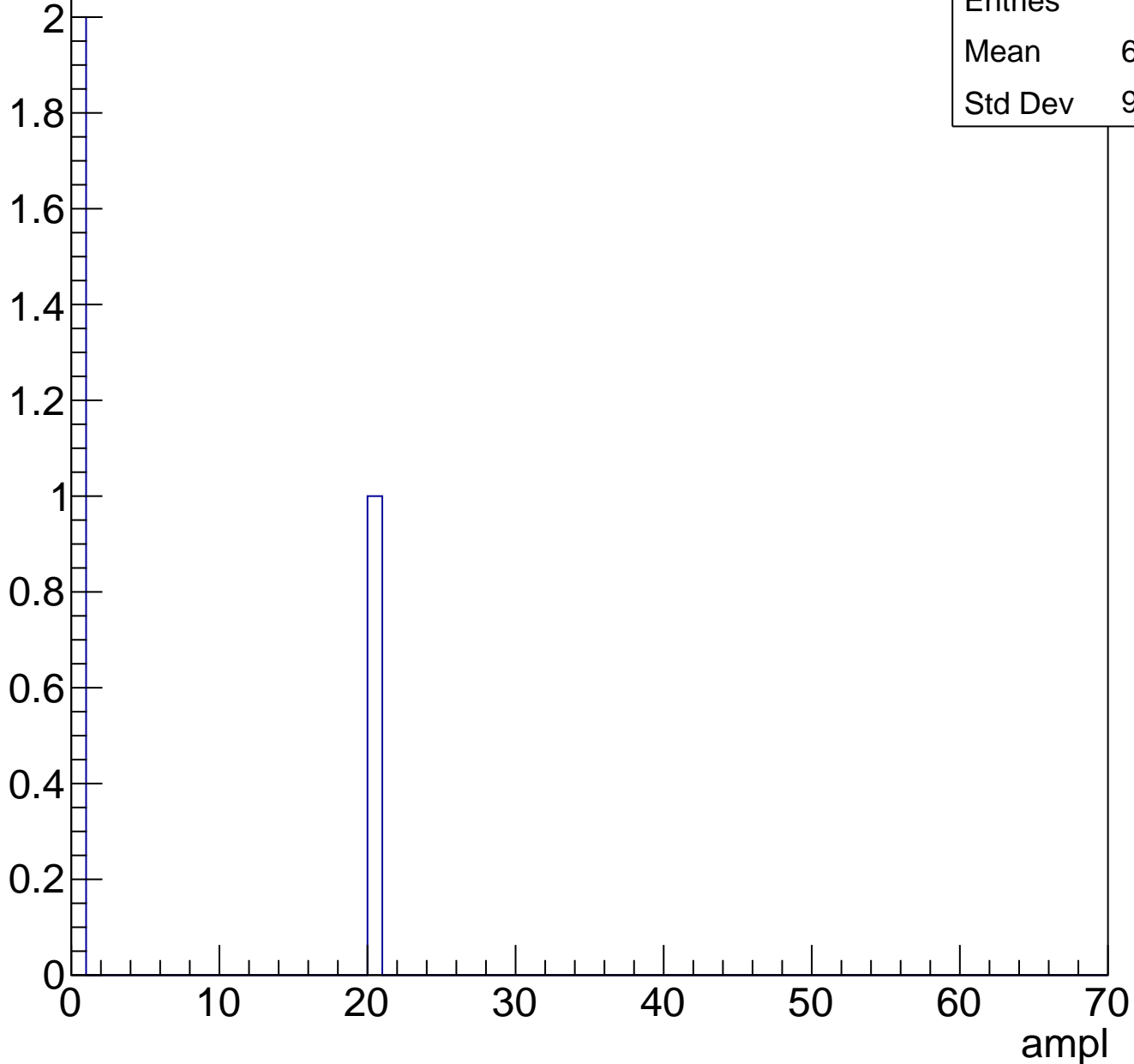


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L100S, U12-ch122, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	29.88
Std Dev	5.119

Gaus mean : 31.1036

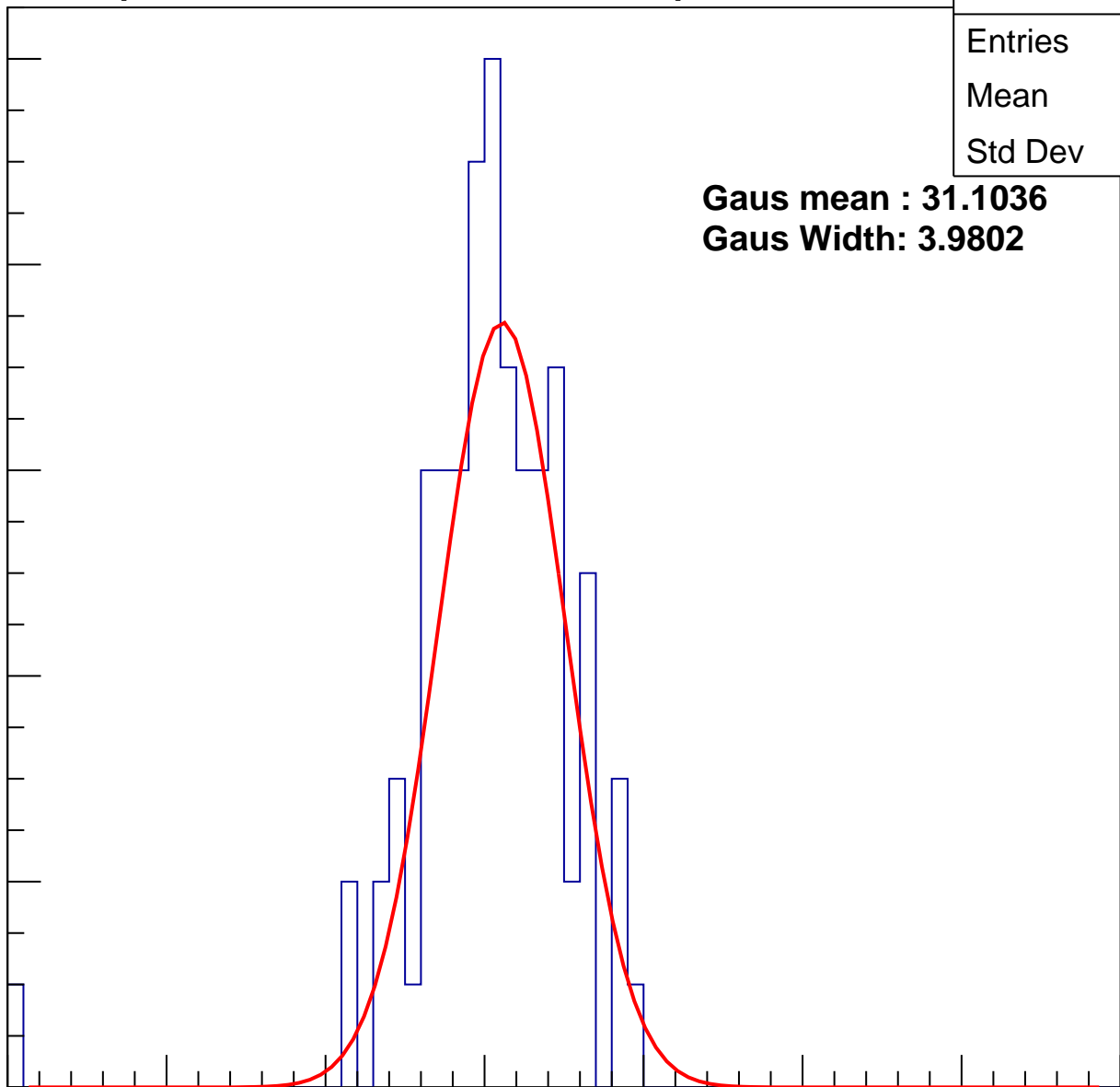
Gaus Width: 3.9802

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U12-ch122, adc1

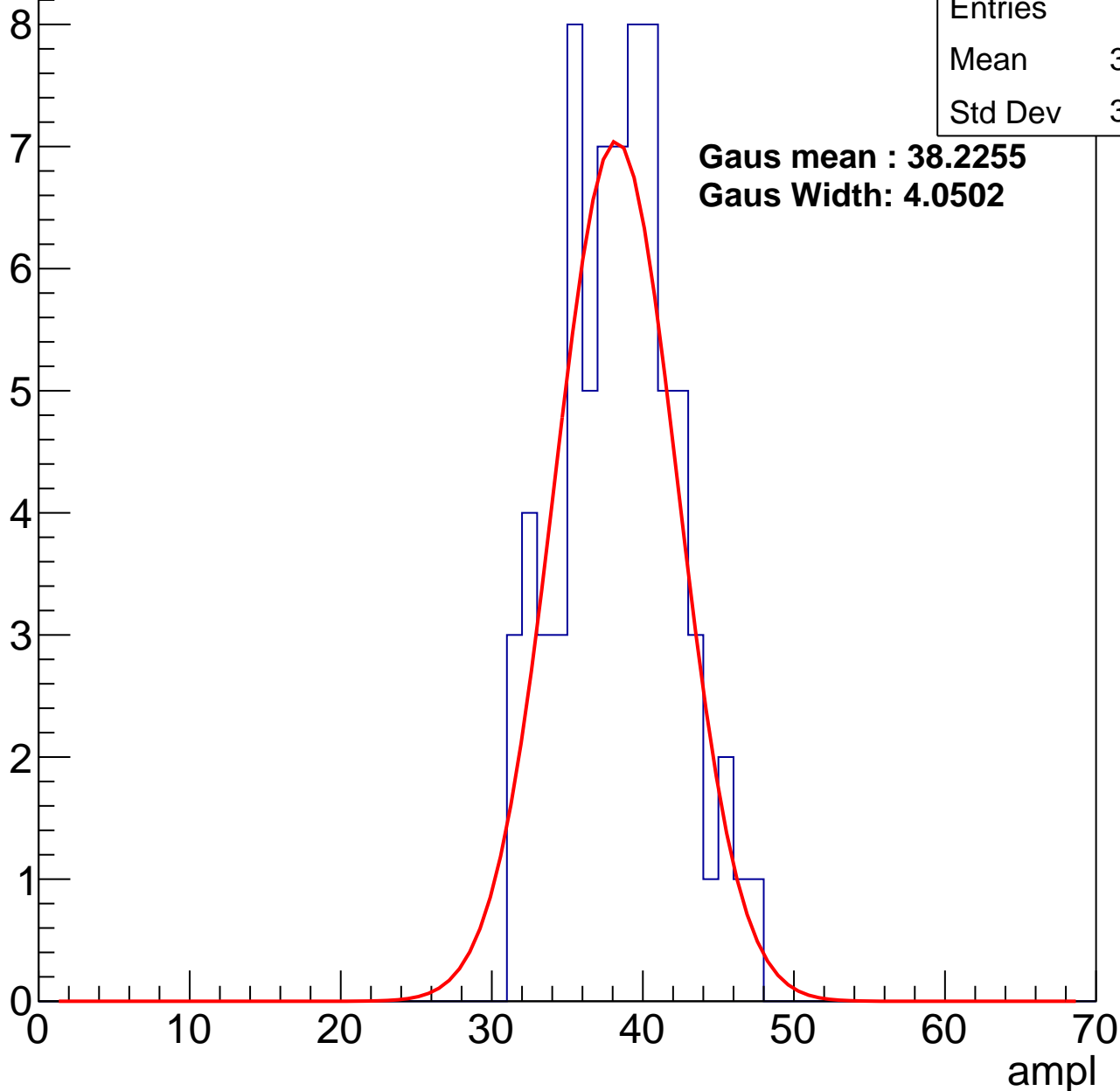
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.97
Std Dev	3.734

Gaus mean : 38.2255

Gaus Width: 4.0502



B1L100S, U12-ch122, adc2

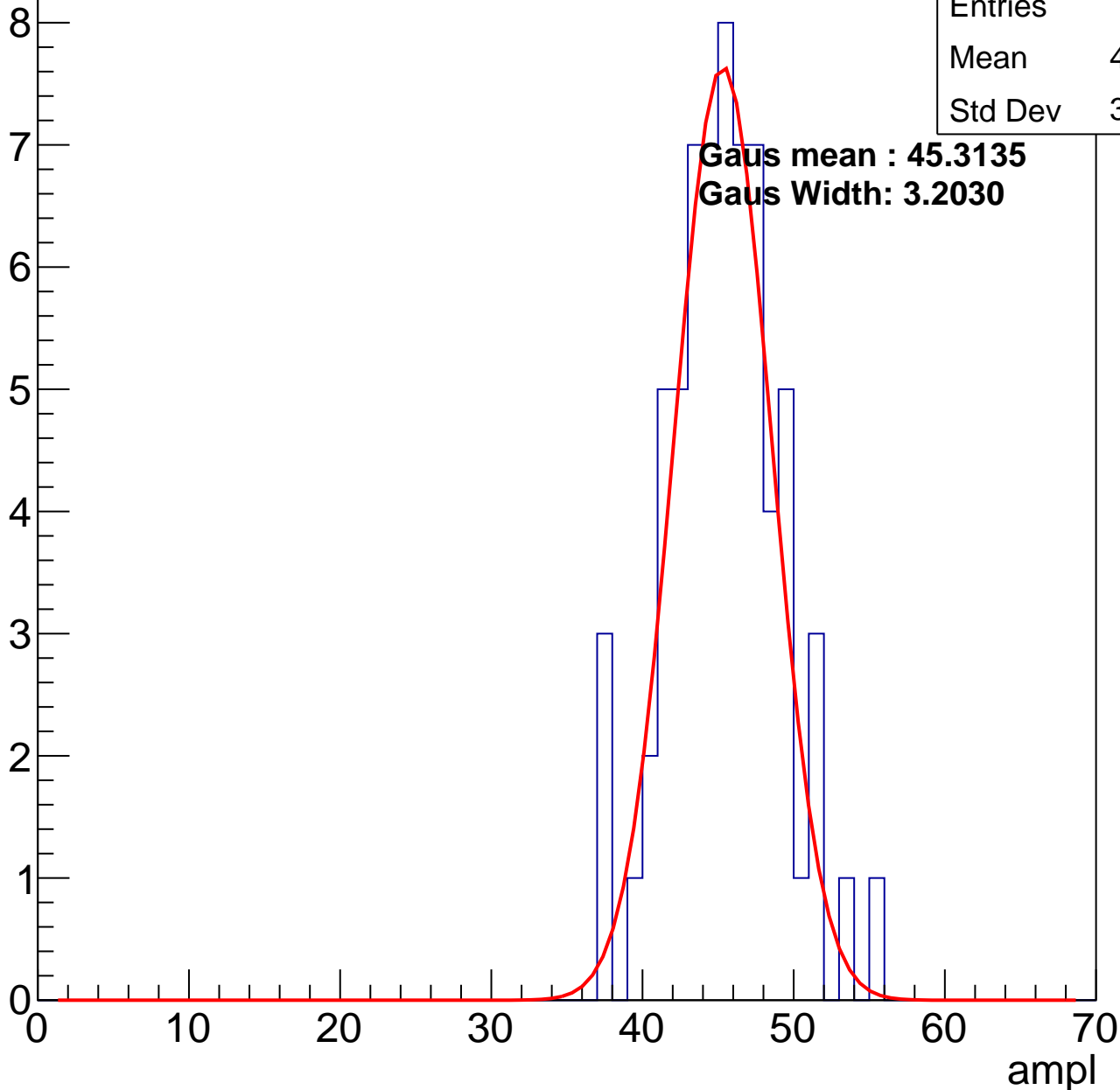
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	44.97
Std Dev	3.636

Gaus mean : 45.3135

Gaus Width: 3.2030

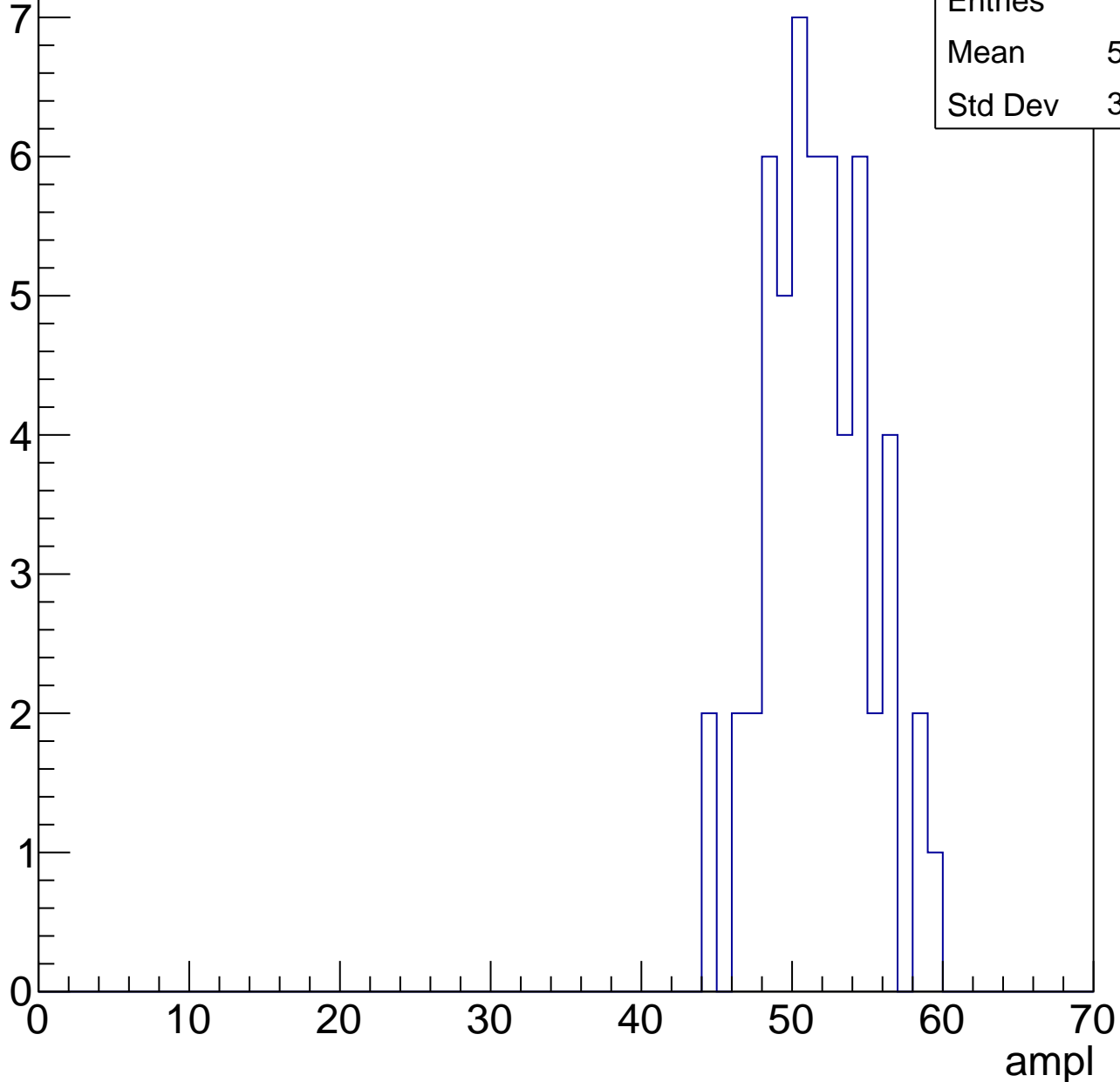


B1L100S, U12-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	51.27
Std Dev	3.376

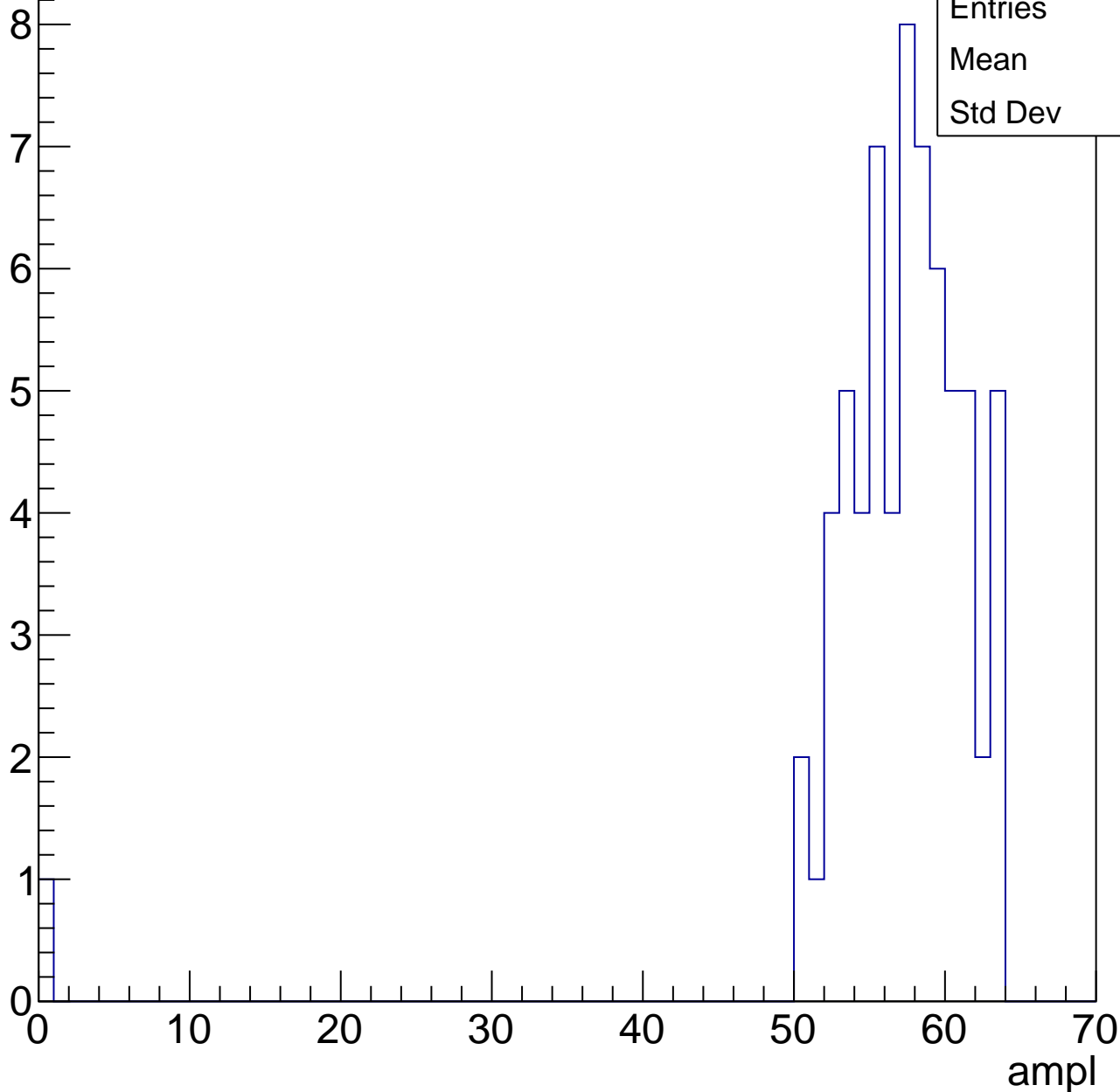


B1L100S, U12-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	56.2
Std Dev	7.76

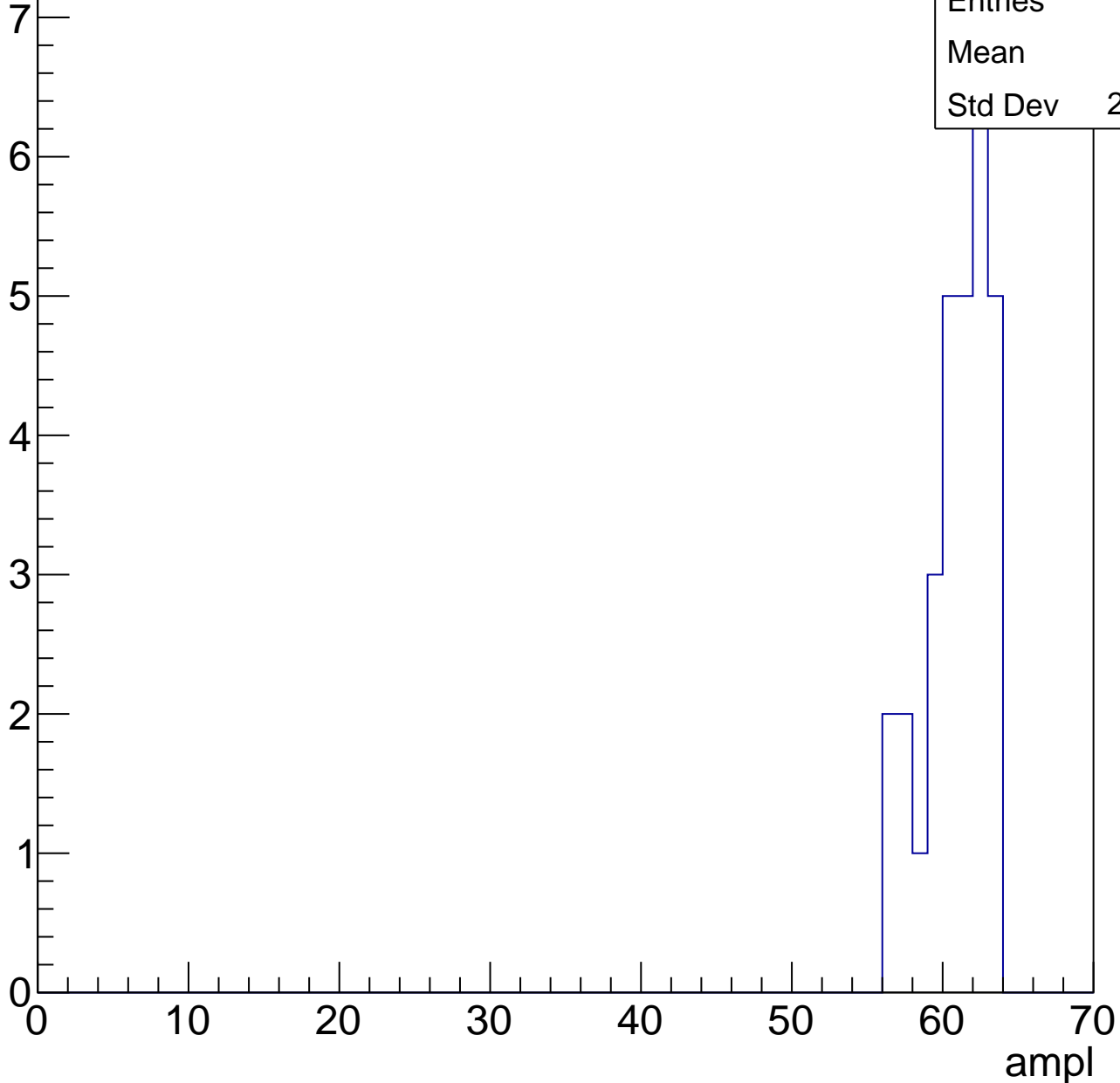


B1L100S, U12-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	60.5
Std Dev	2.062



B1L100S, U12-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch123, adc0

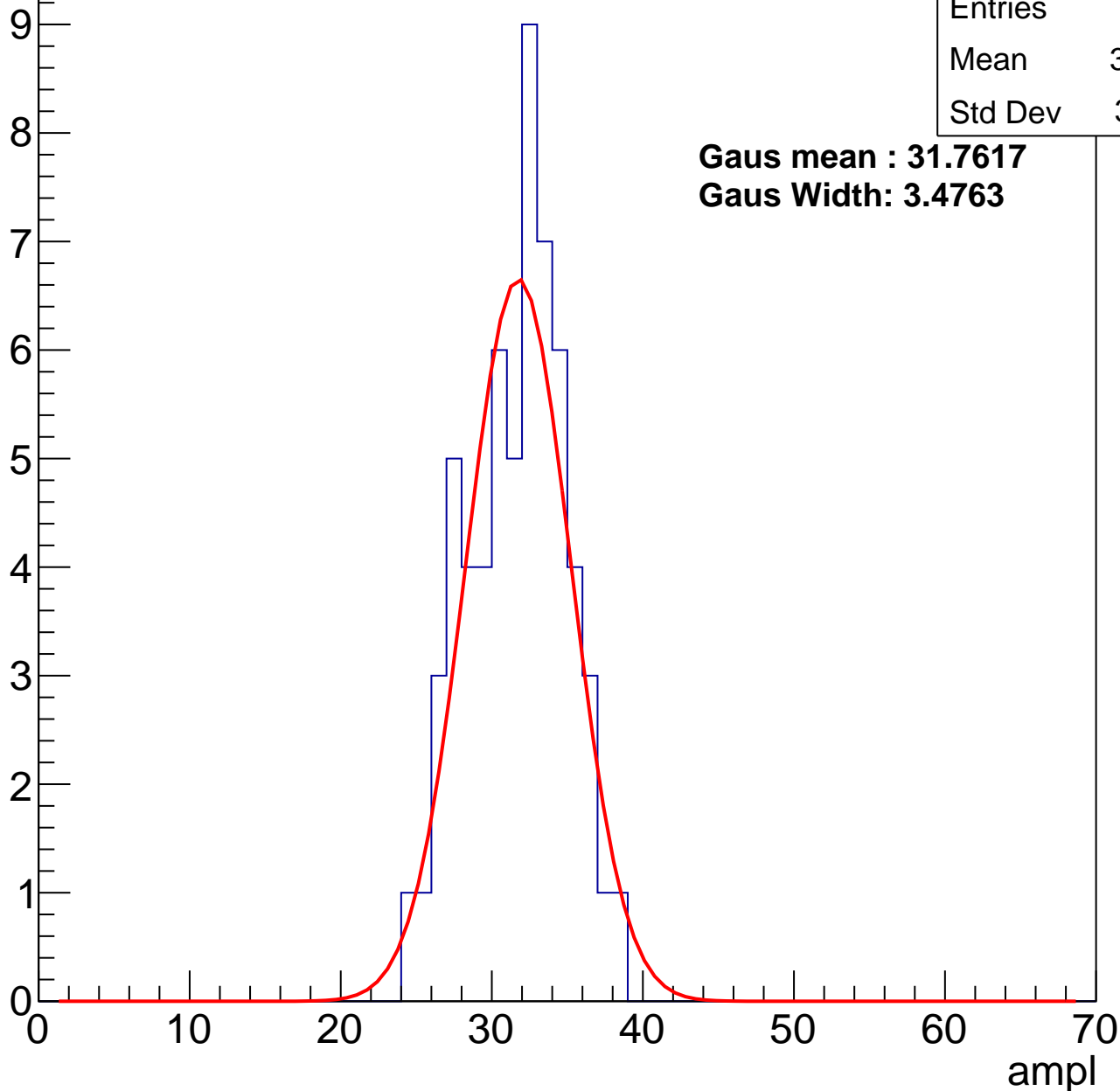
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	31.18
Std Dev	3.191

Gaus mean : 31.7617

Gaus Width: 3.4763



B1L100S, U12-ch123, adc1

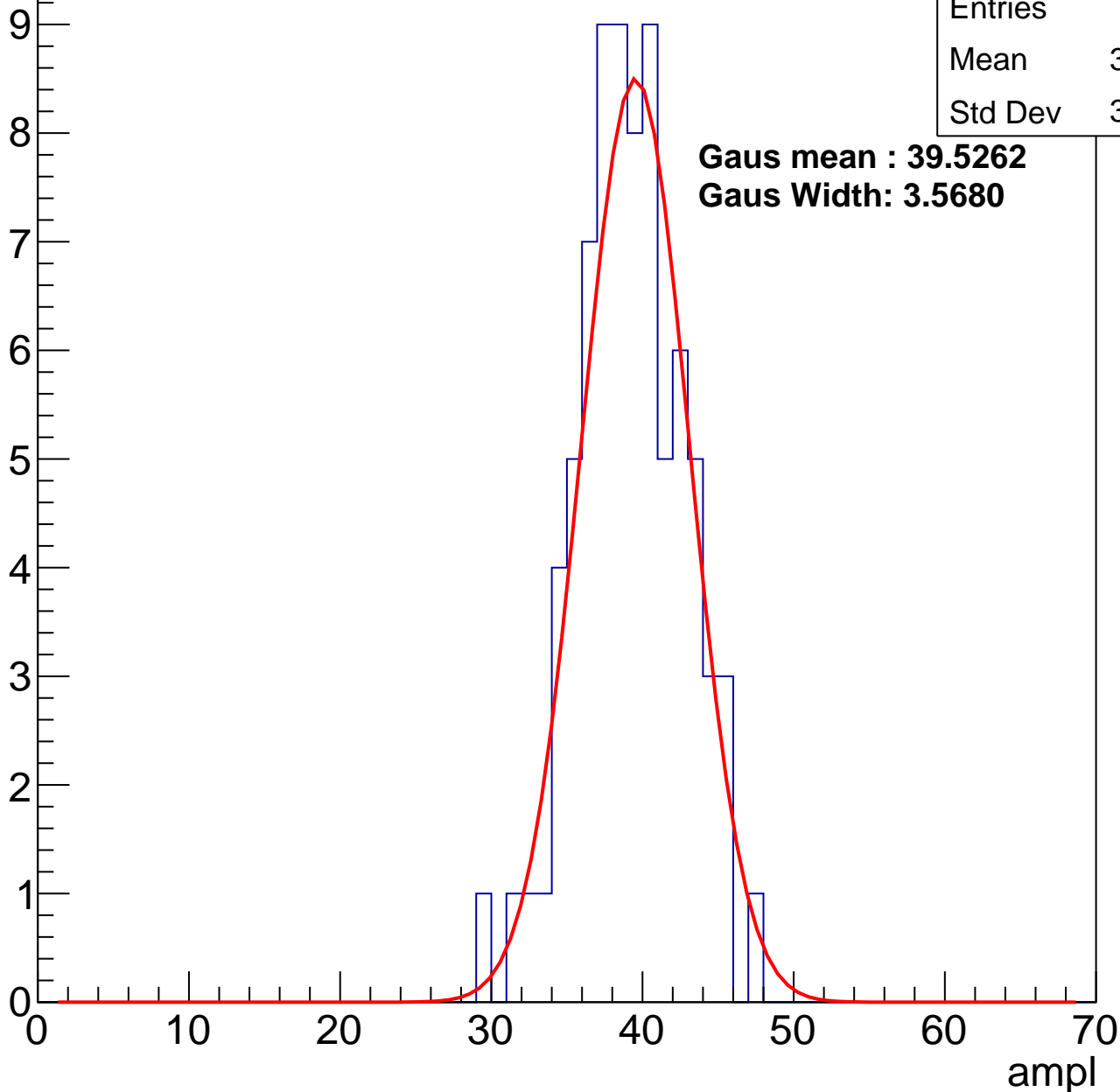
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	38.73
Std Dev	3.474

Gaus mean : 39.5262

Gaus Width: 3.5680



B1L100S, U12-ch123, adc2

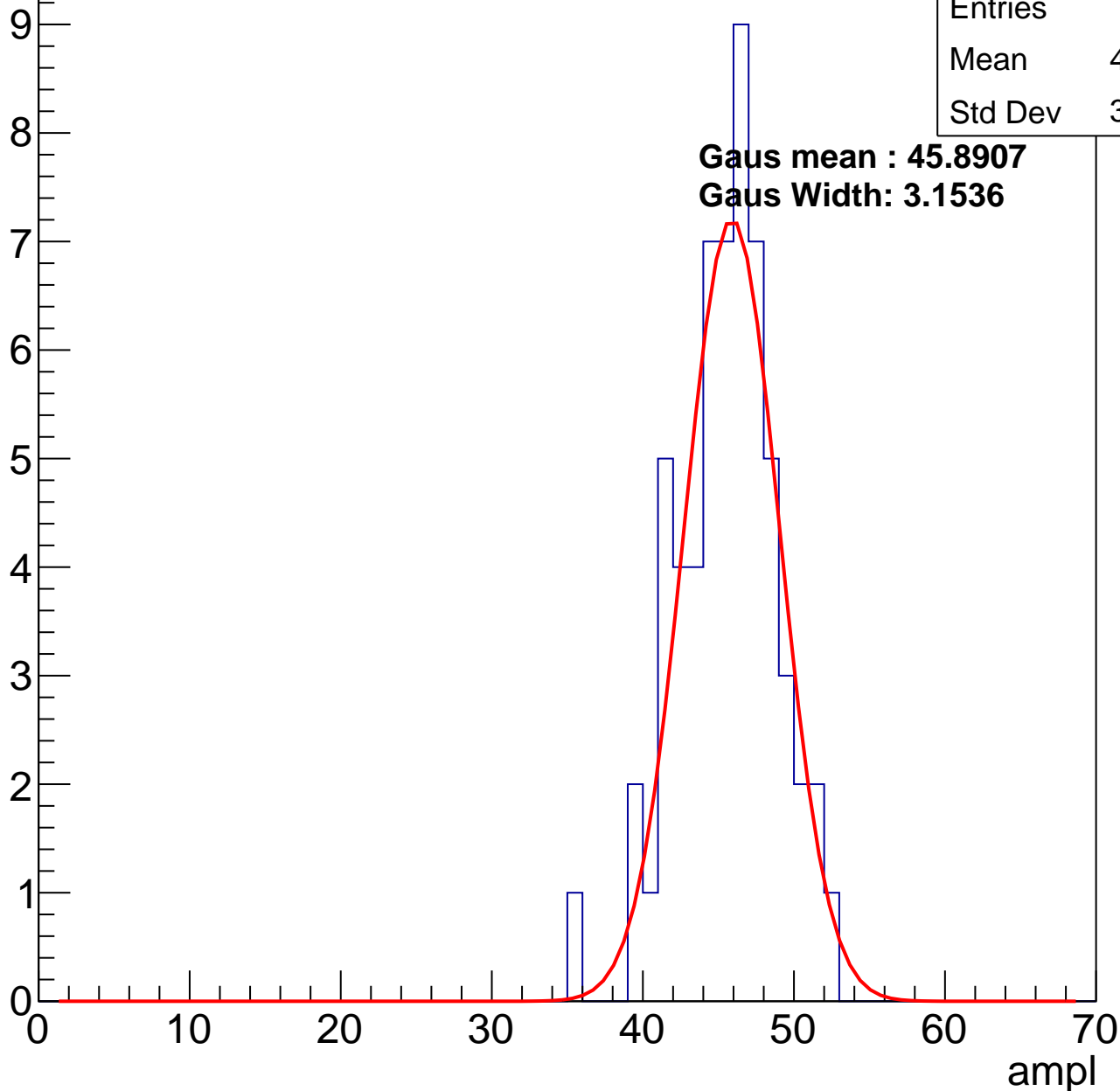
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	45.08
Std Dev	3.257

Gaus mean : 45.8907

Gaus Width: 3.1536

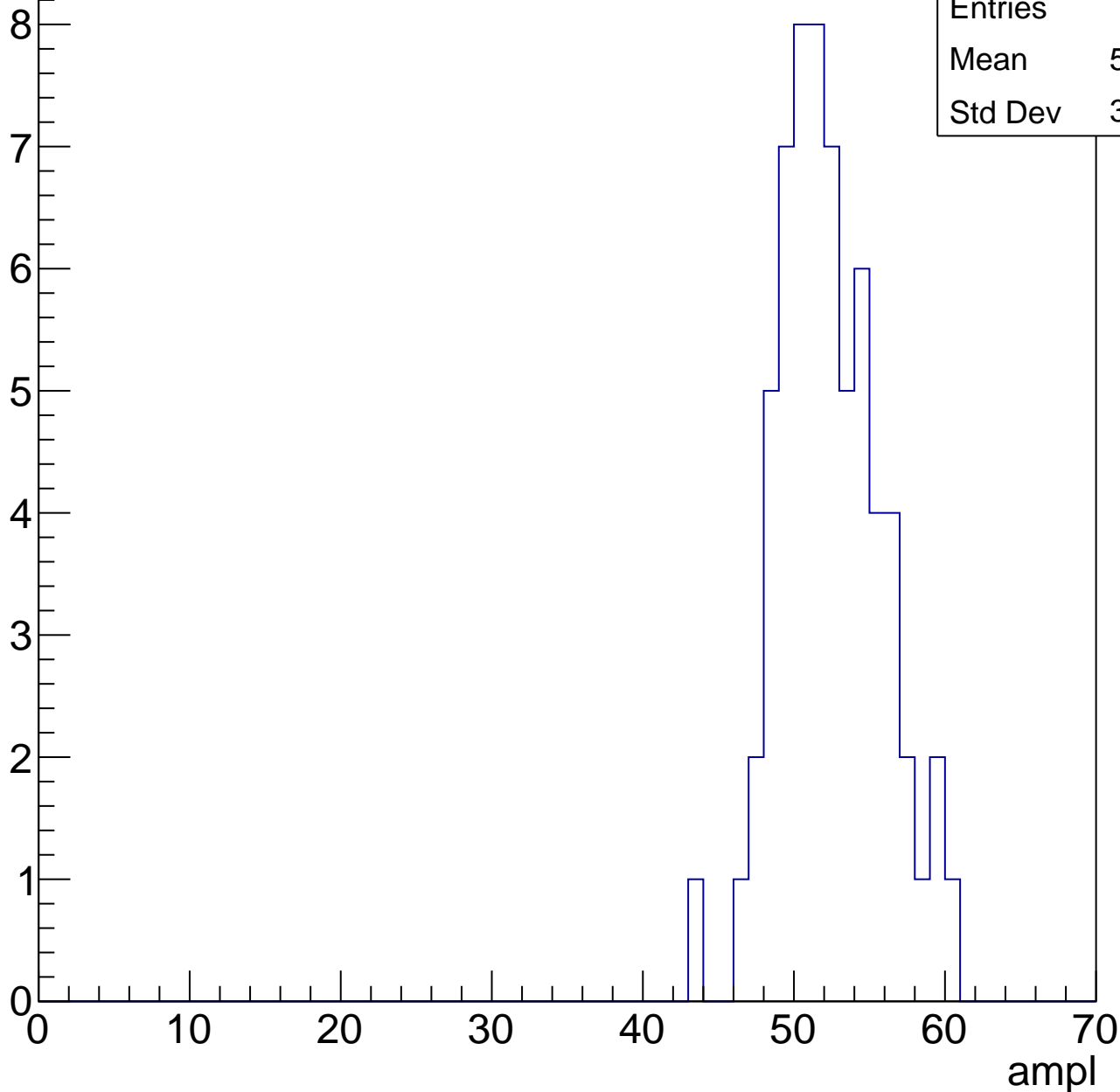


B1L100S, U12-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

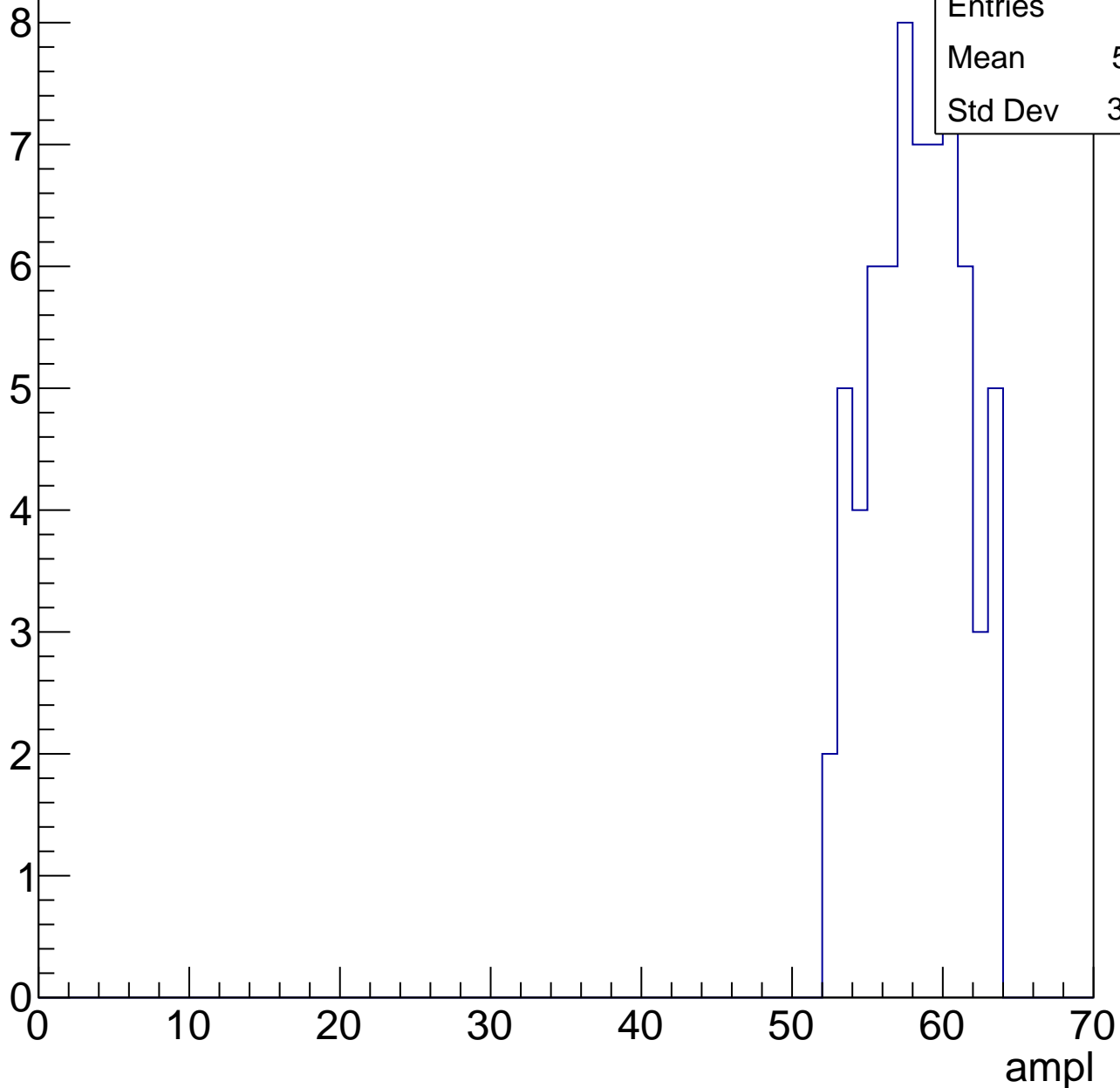
Entries	64
Mean	51.89
Std Dev	3.369



B1L100S, U12-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

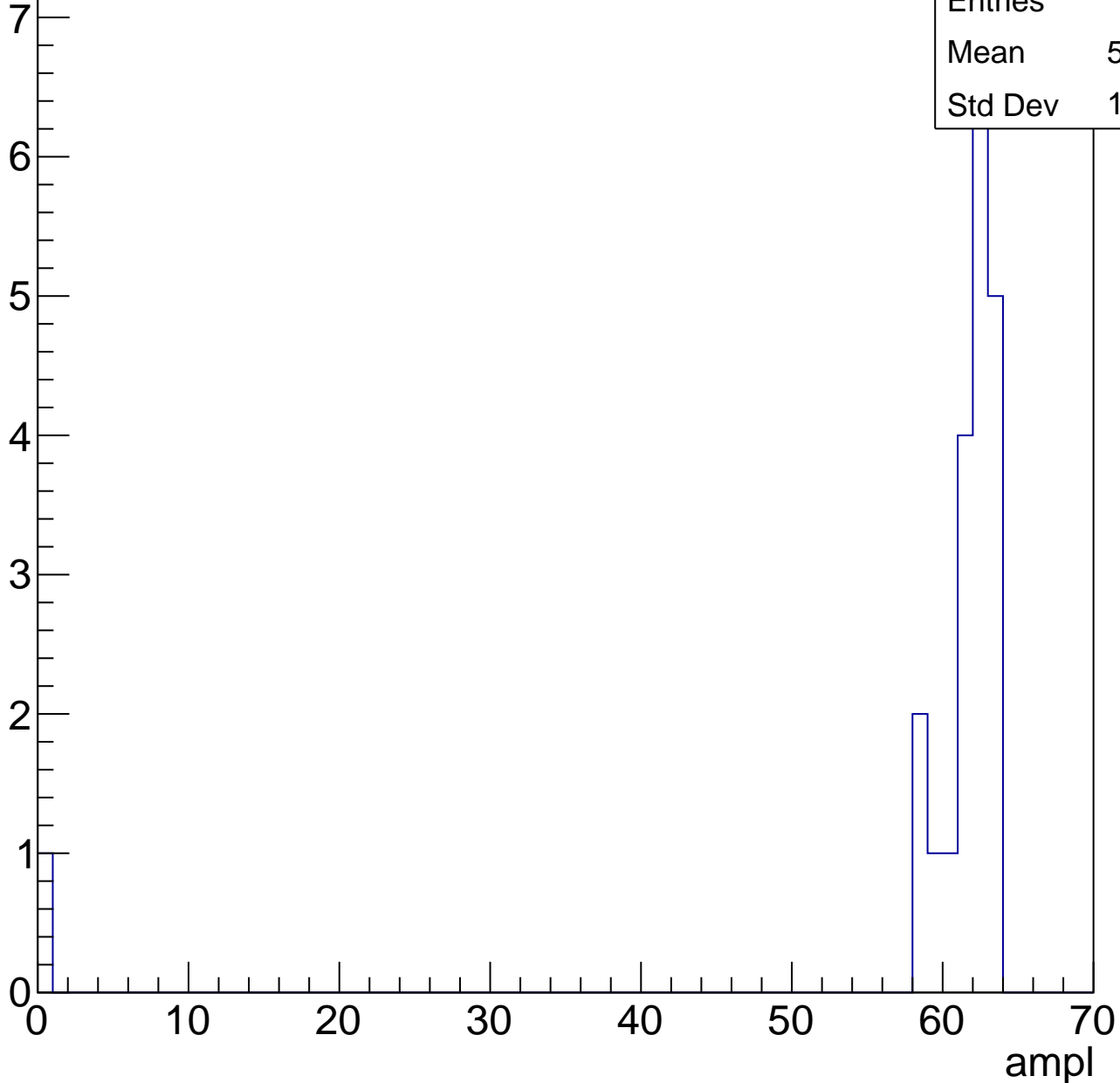


B1L100S, U12-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.48
Std Dev	13.16



B1L100S, U12-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

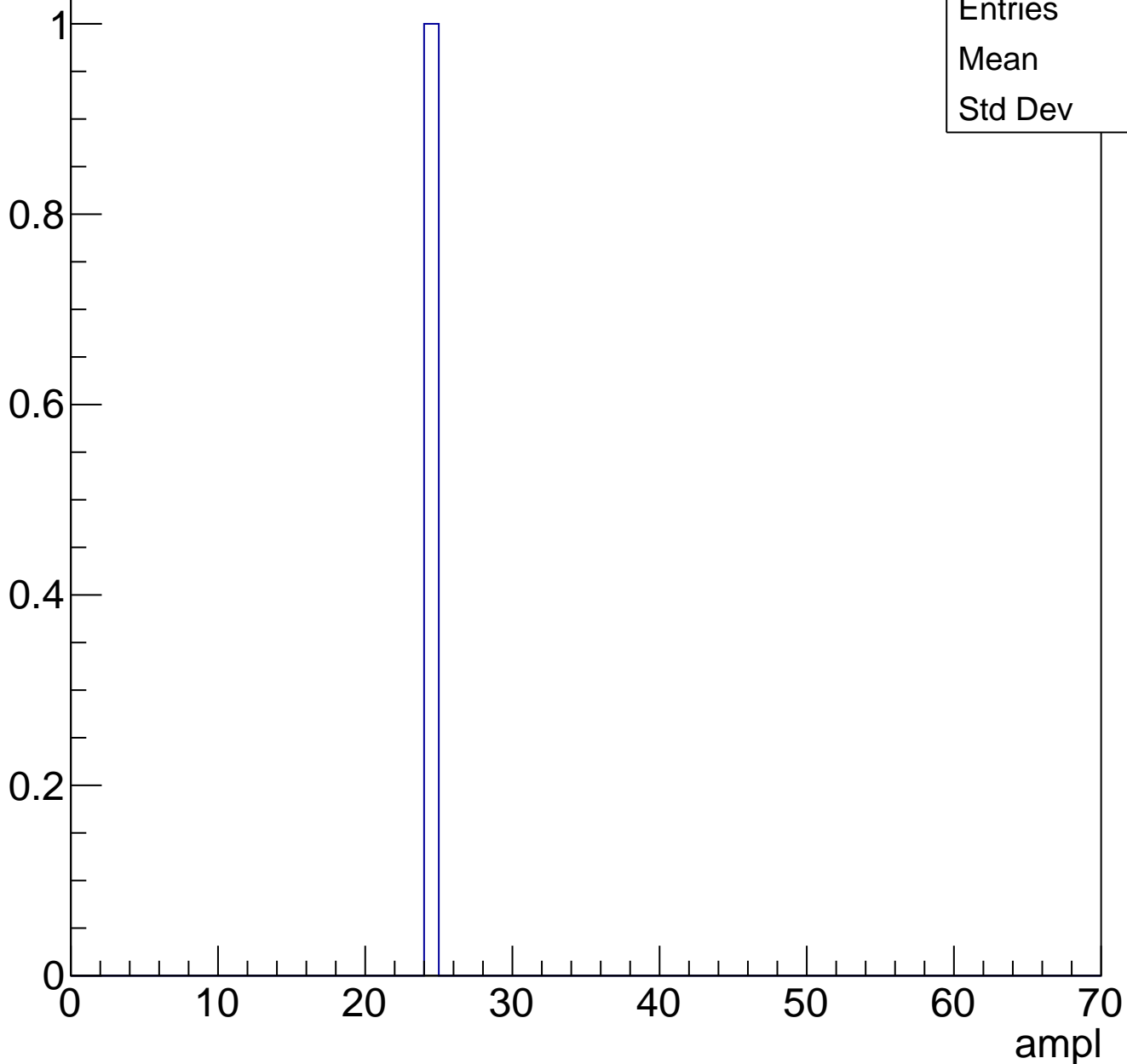


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



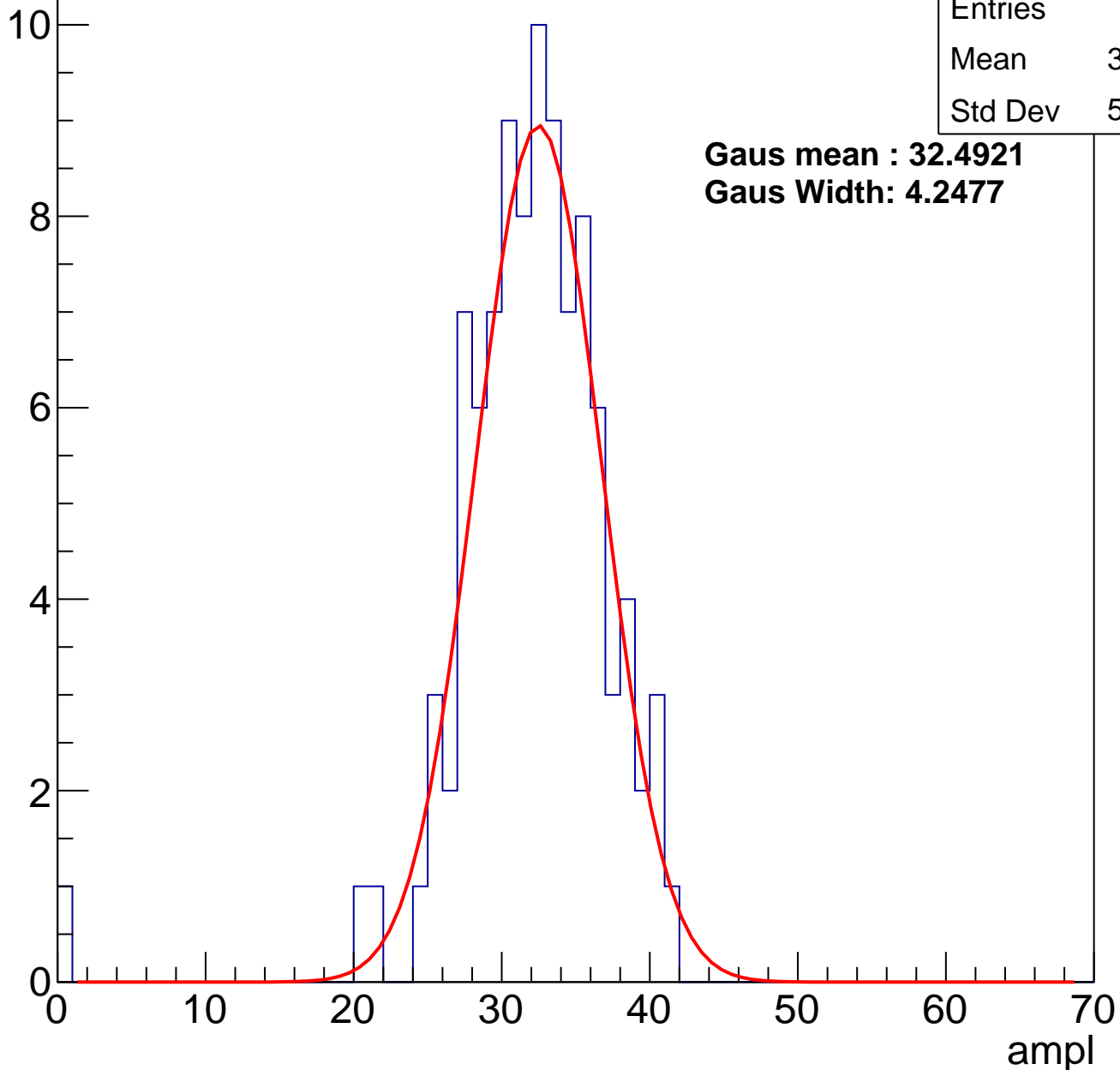
B1L100S, U12-ch124, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	99
Mean	31.55
Std Dev	5.246

Gaus mean : 32.4921
Gaus Width: 4.2477

Entry



B1L100S, U12-ch124, adc1

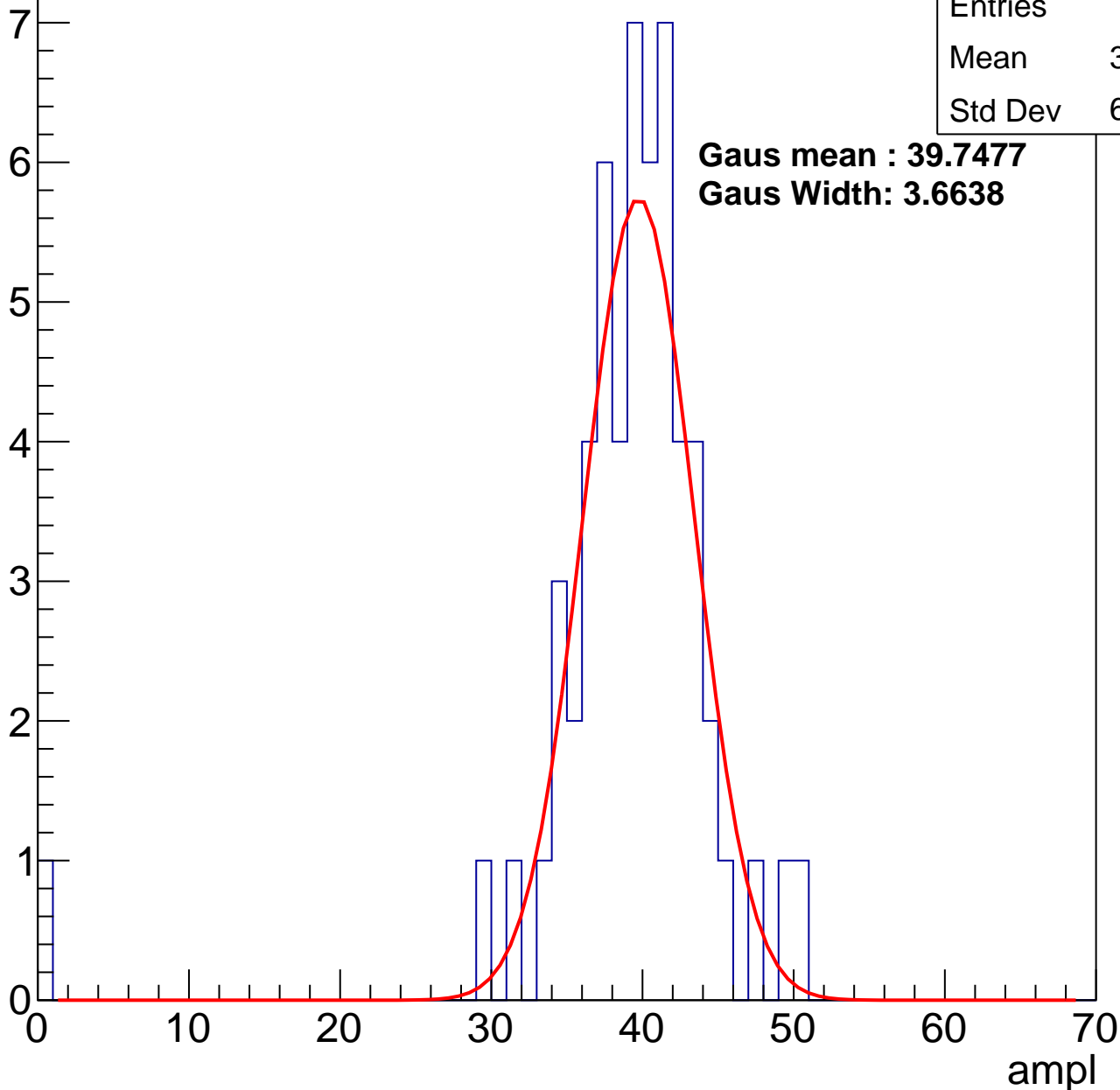
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	38.63
Std Dev	6.474

Gaus mean : 39.7477

Gaus Width: 3.6638



B1L100S, U12-ch124, adc2

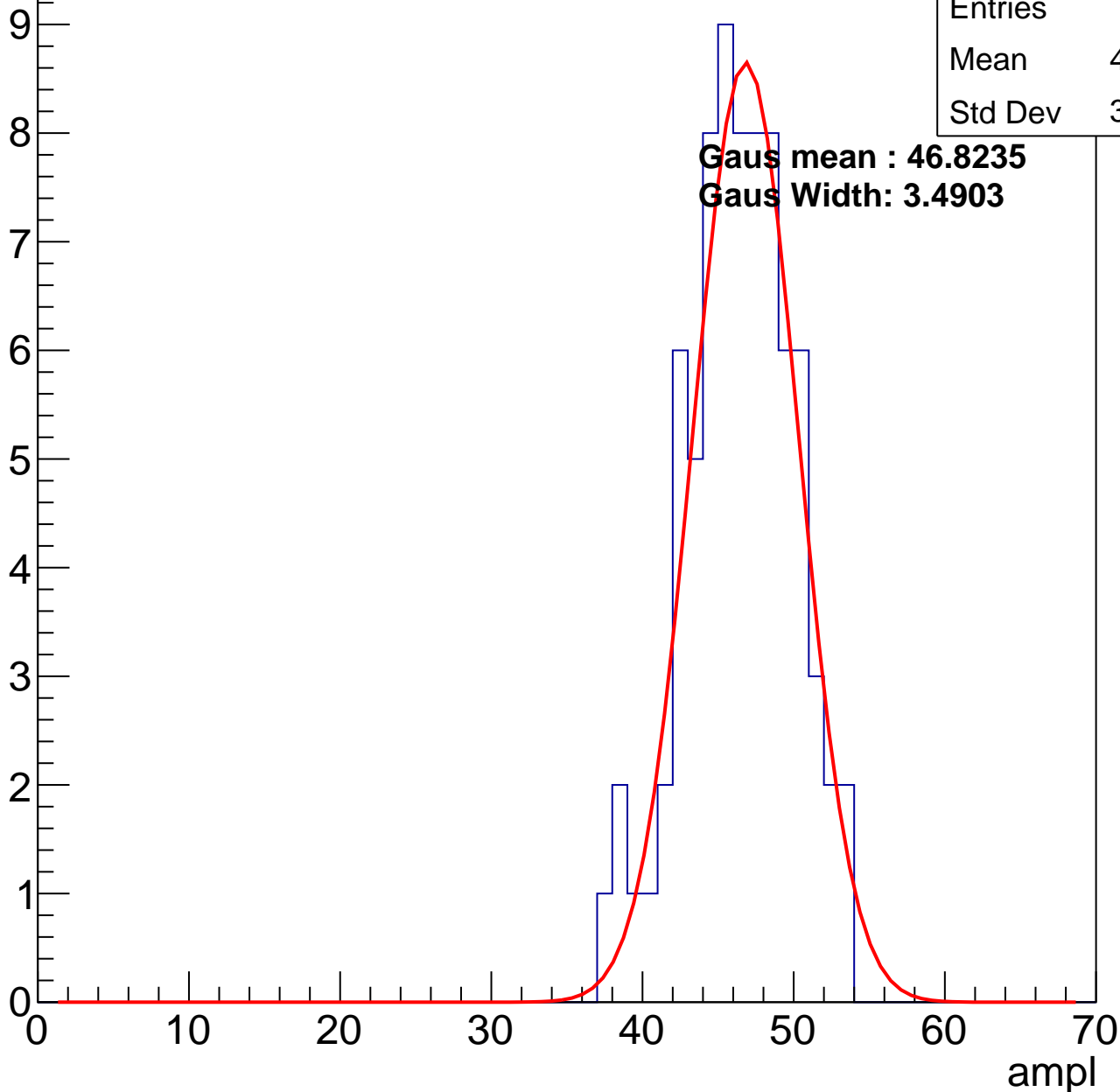
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	45.94
Std Dev	3.509

Gaus mean : 46.8235

Gaus Width: 3.4903



B1L100S, U12-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

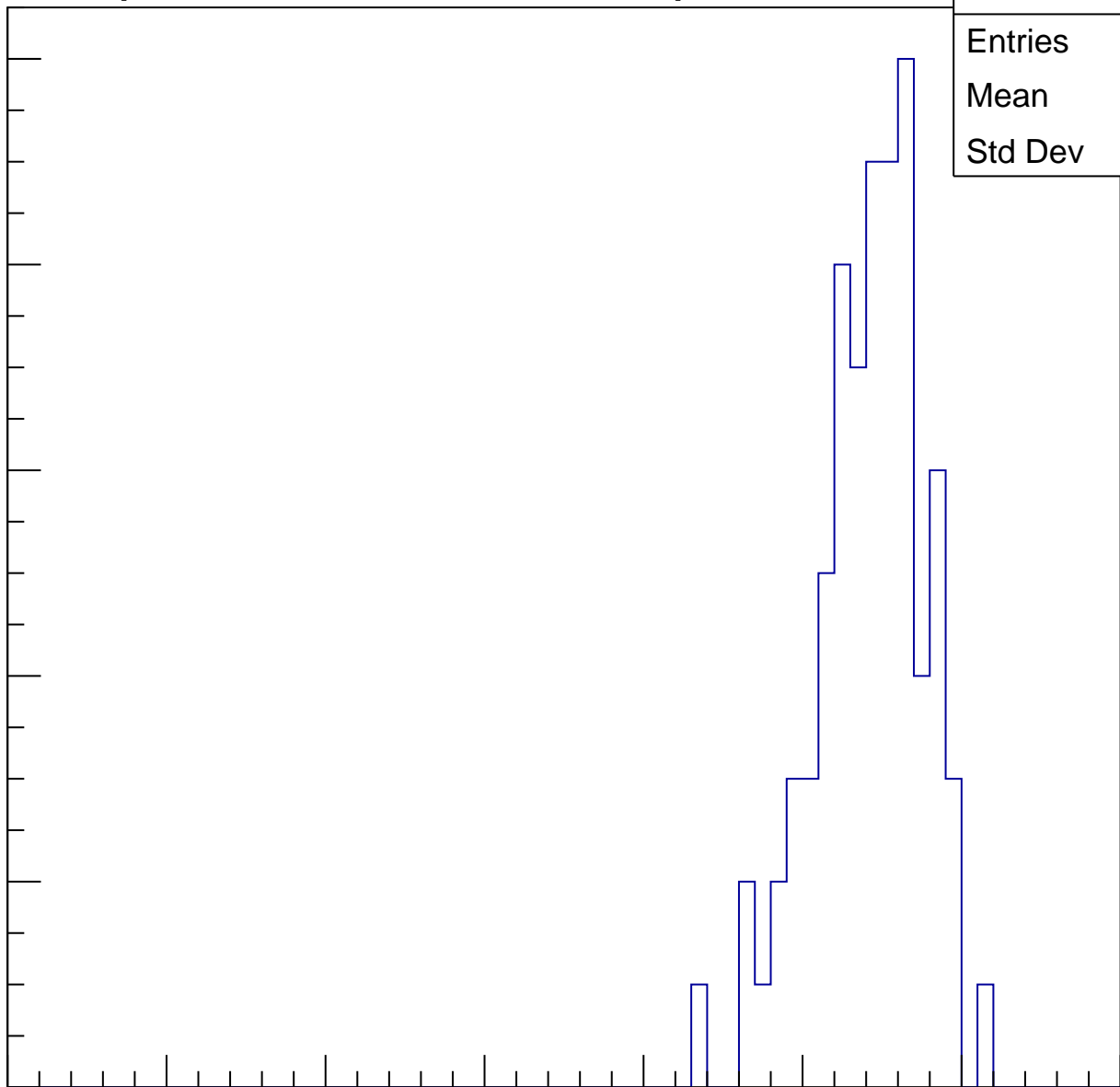
Entries	74
Mean	53.68
Std Dev	3.437

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U12-ch124, adc4

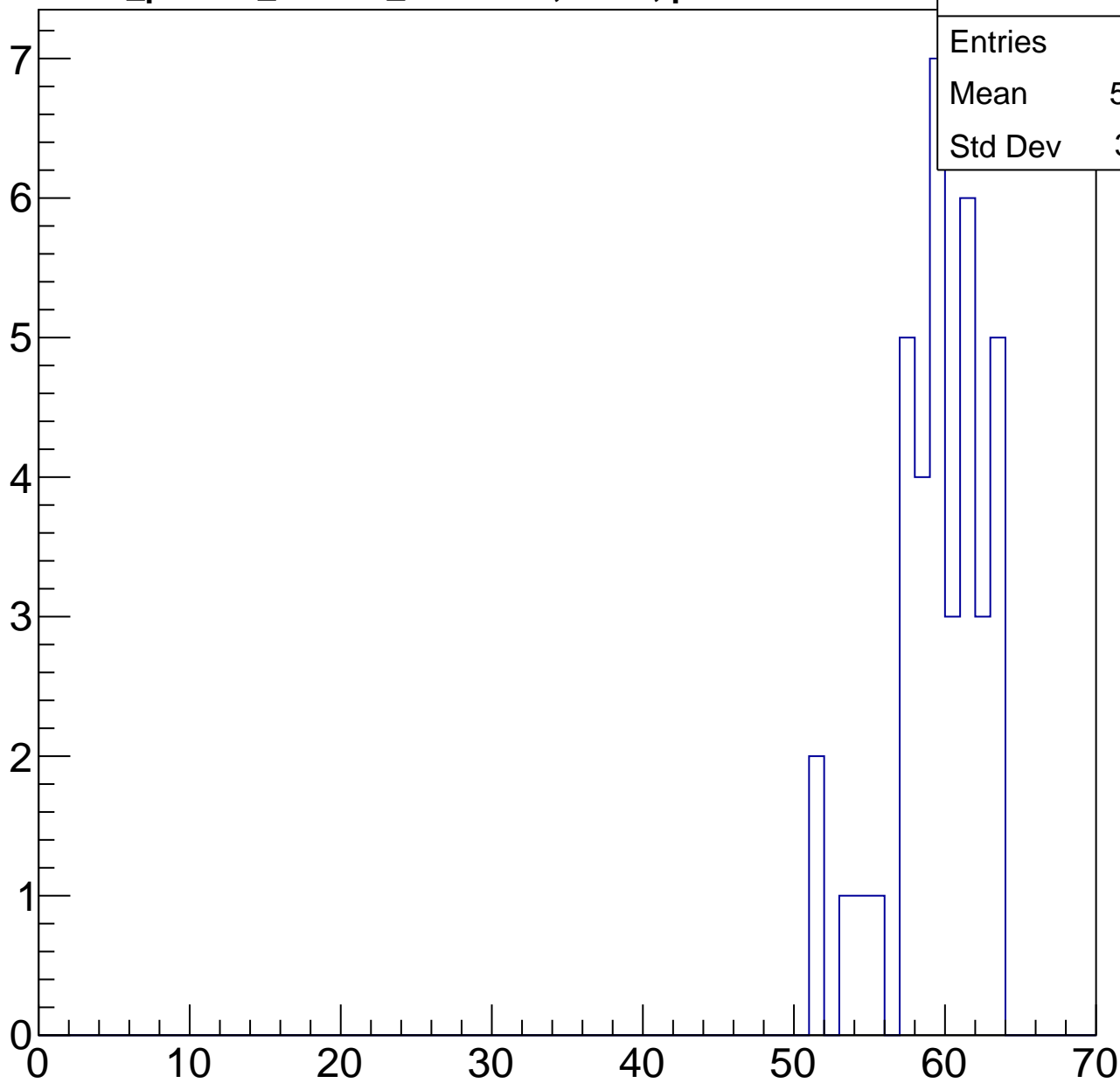
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	38
Mean	58.97
Std Dev	3.091

ampl

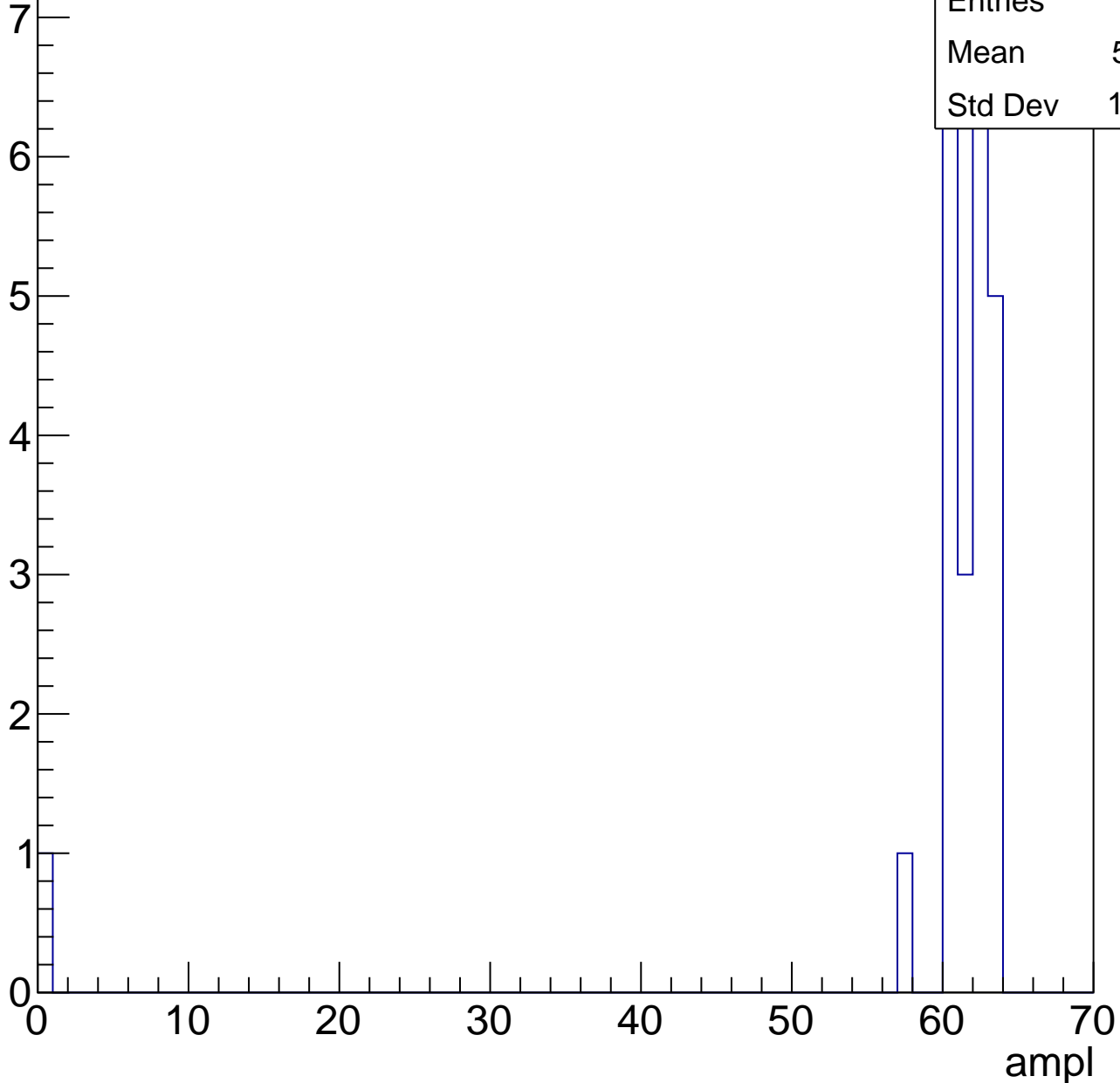


B1L100S, U12-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	24
Mean	58.71
Std Dev	12.32



B1L100S, U12-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

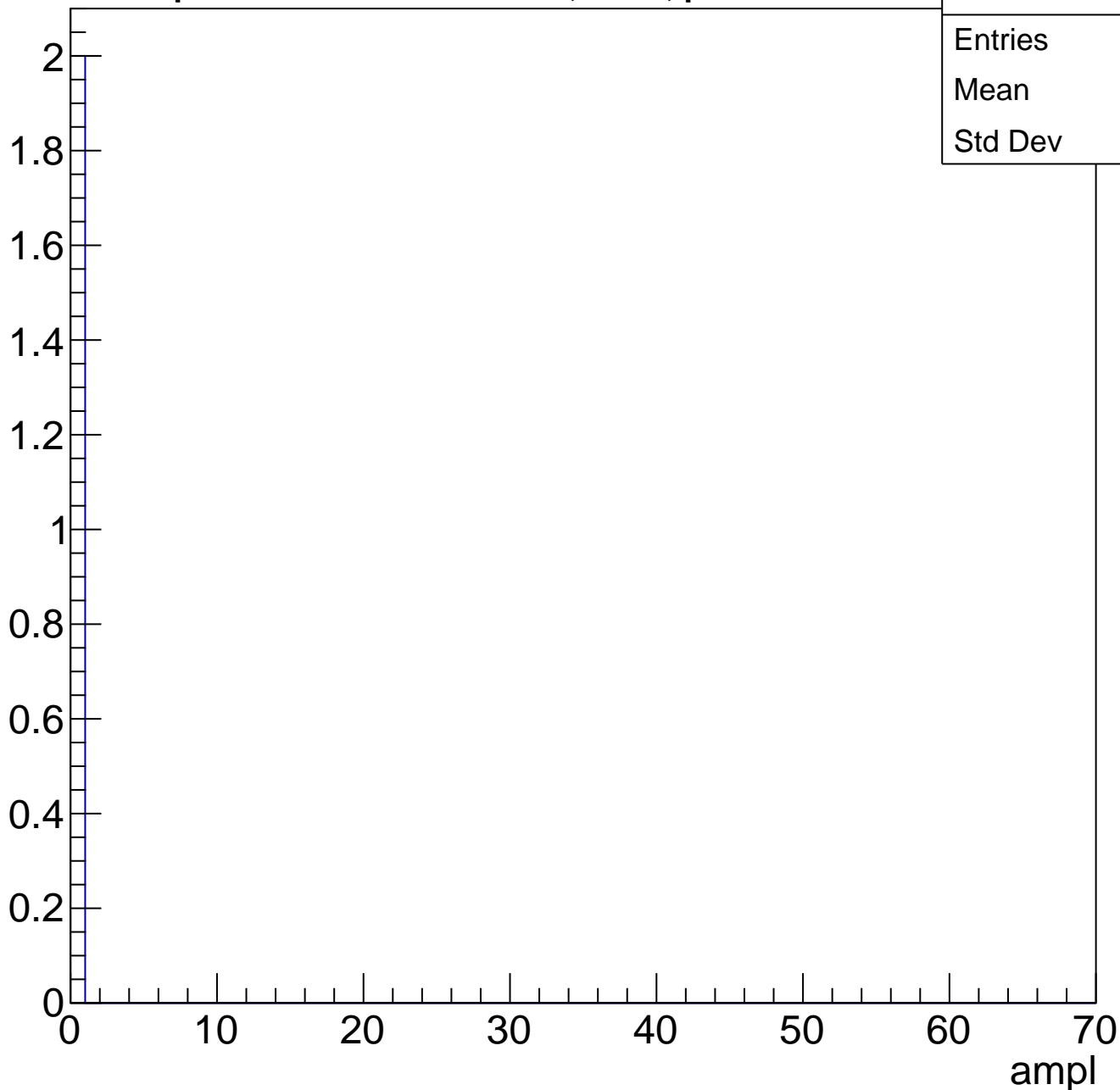


Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch125, adc0

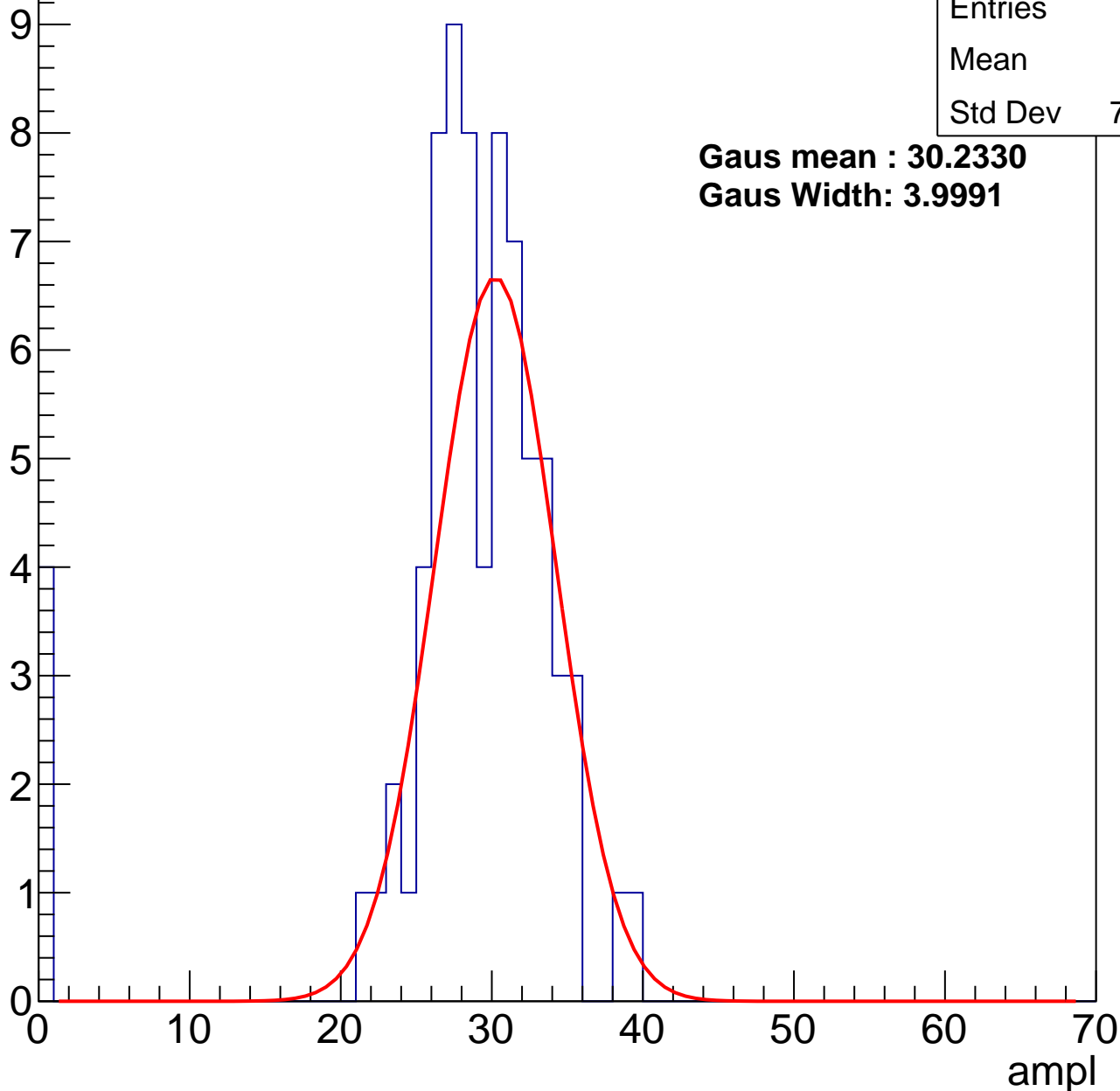
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	27.6
Std Dev	7.422

Gaus mean : 30.2330

Gaus Width: 3.9991



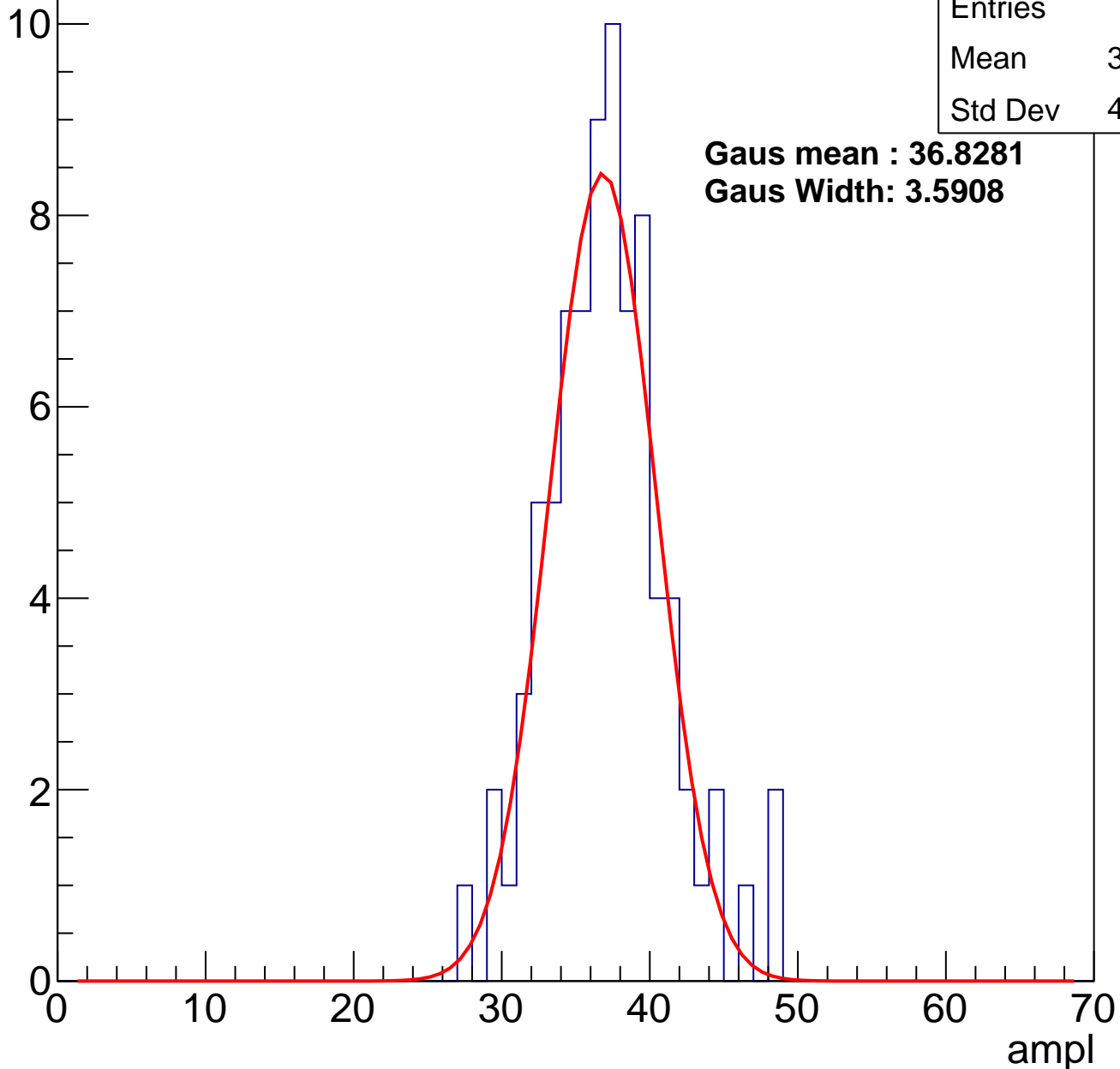
B1L100S, U12-ch125, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	36.65
Std Dev	4.047

Gaus mean : 36.8281
Gaus Width: 3.5908

Entry



B1L100S, U12-ch125, adc2

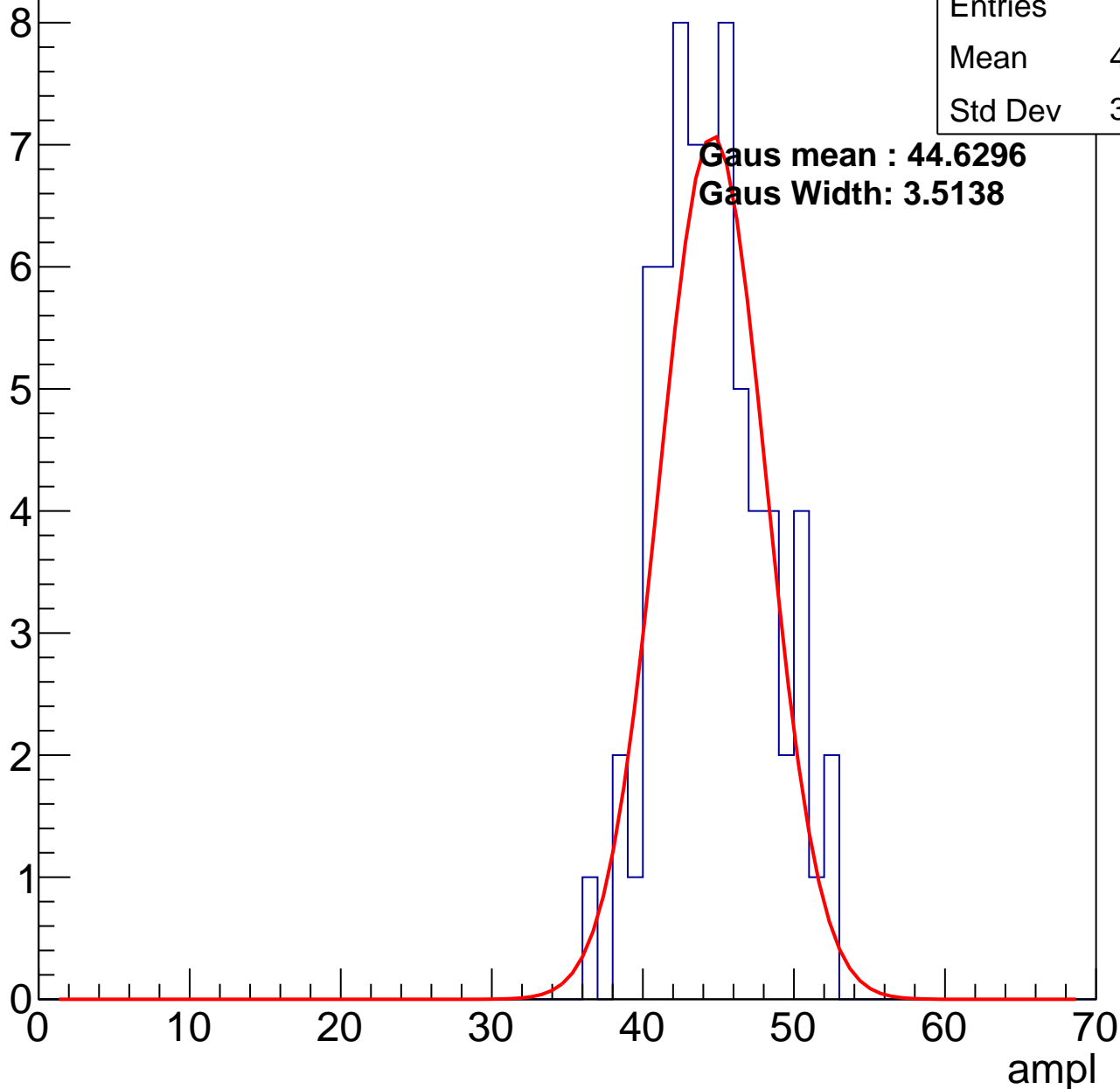
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.19
Std Dev	3.528

Gaus mean : 44.6296

Gaus Width: 3.5138

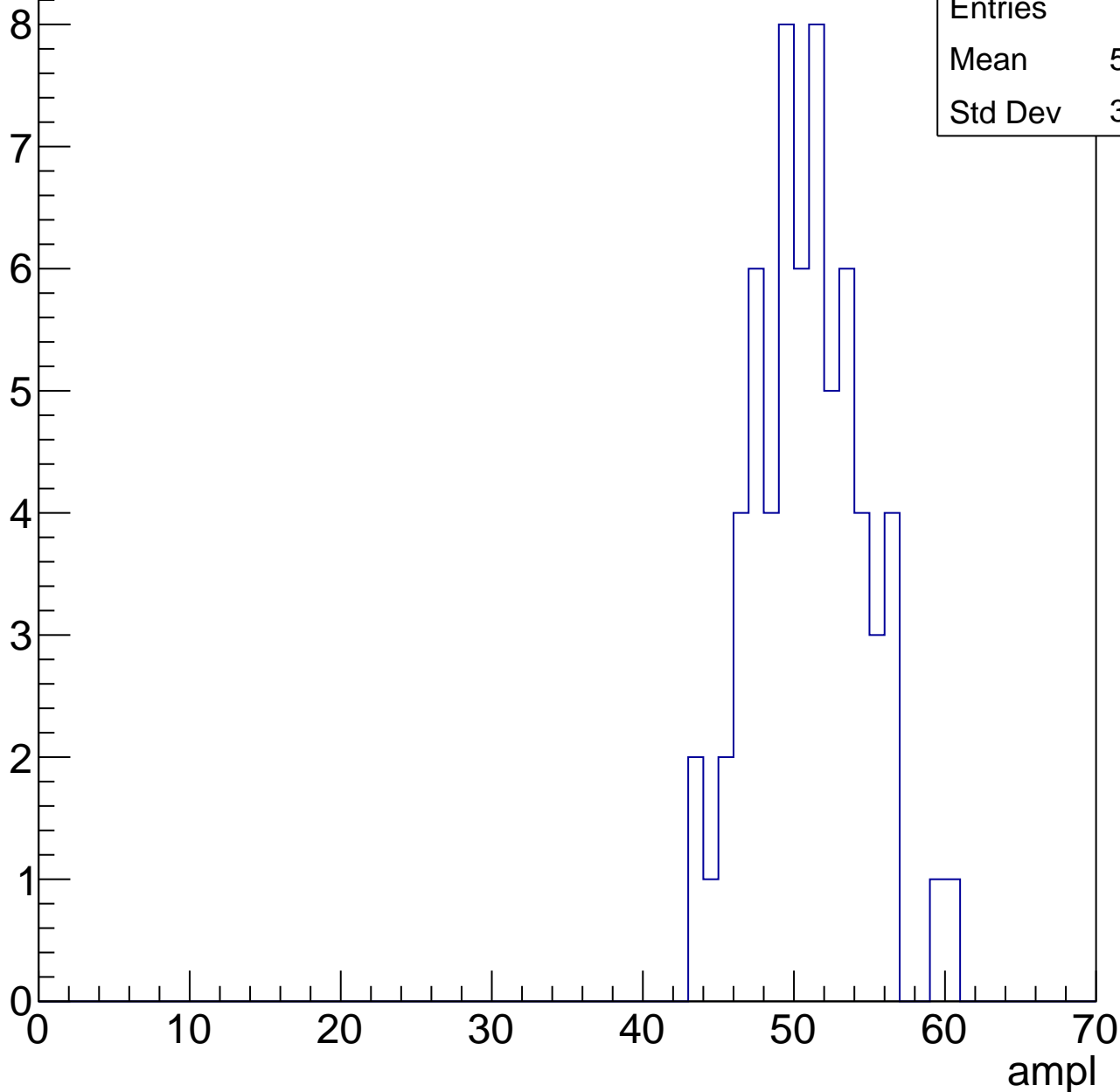


B1L100S, U12-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	50.46
Std Dev	3.634

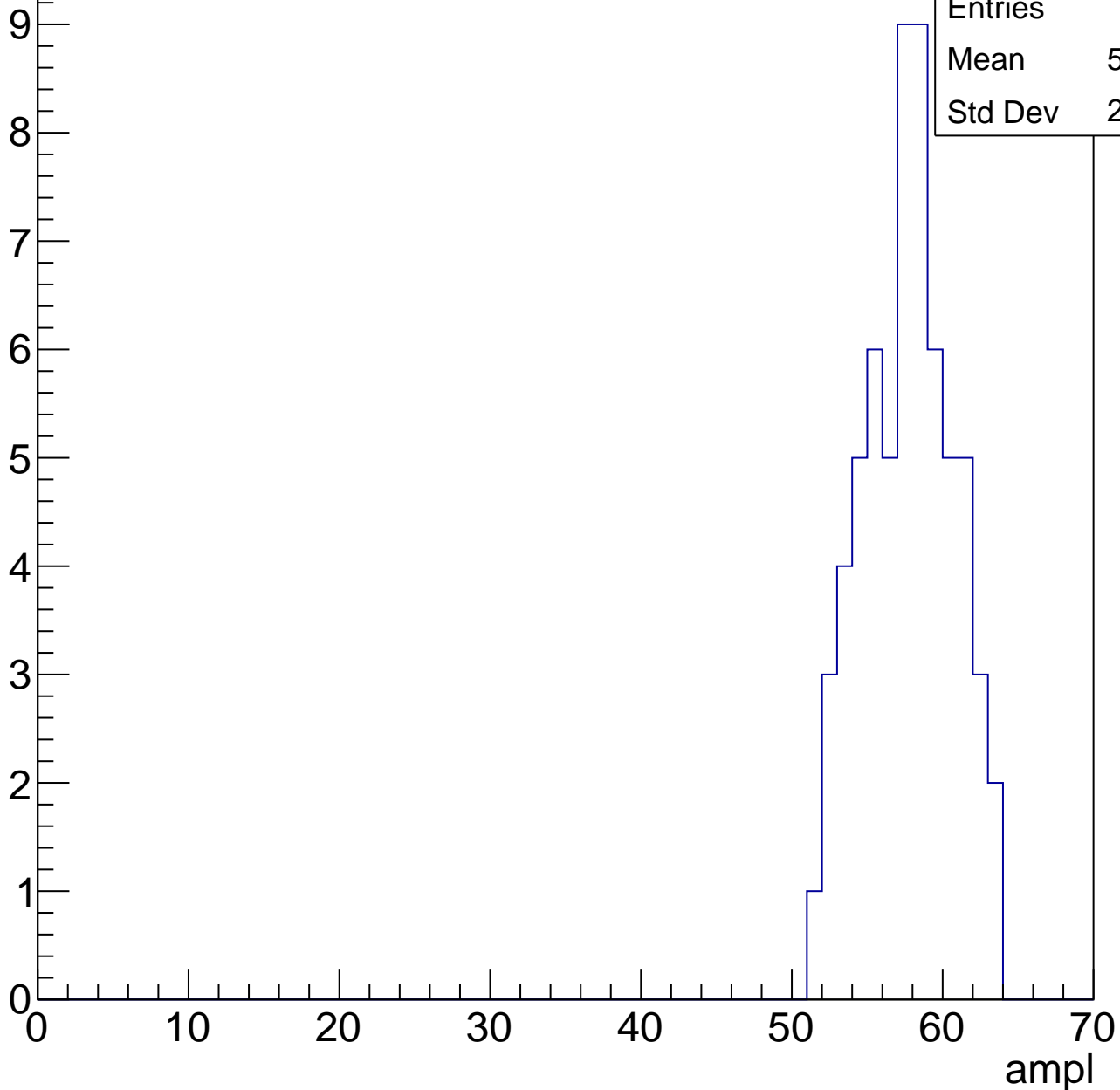


B1L100S, U12-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	57.22
Std Dev	2.957

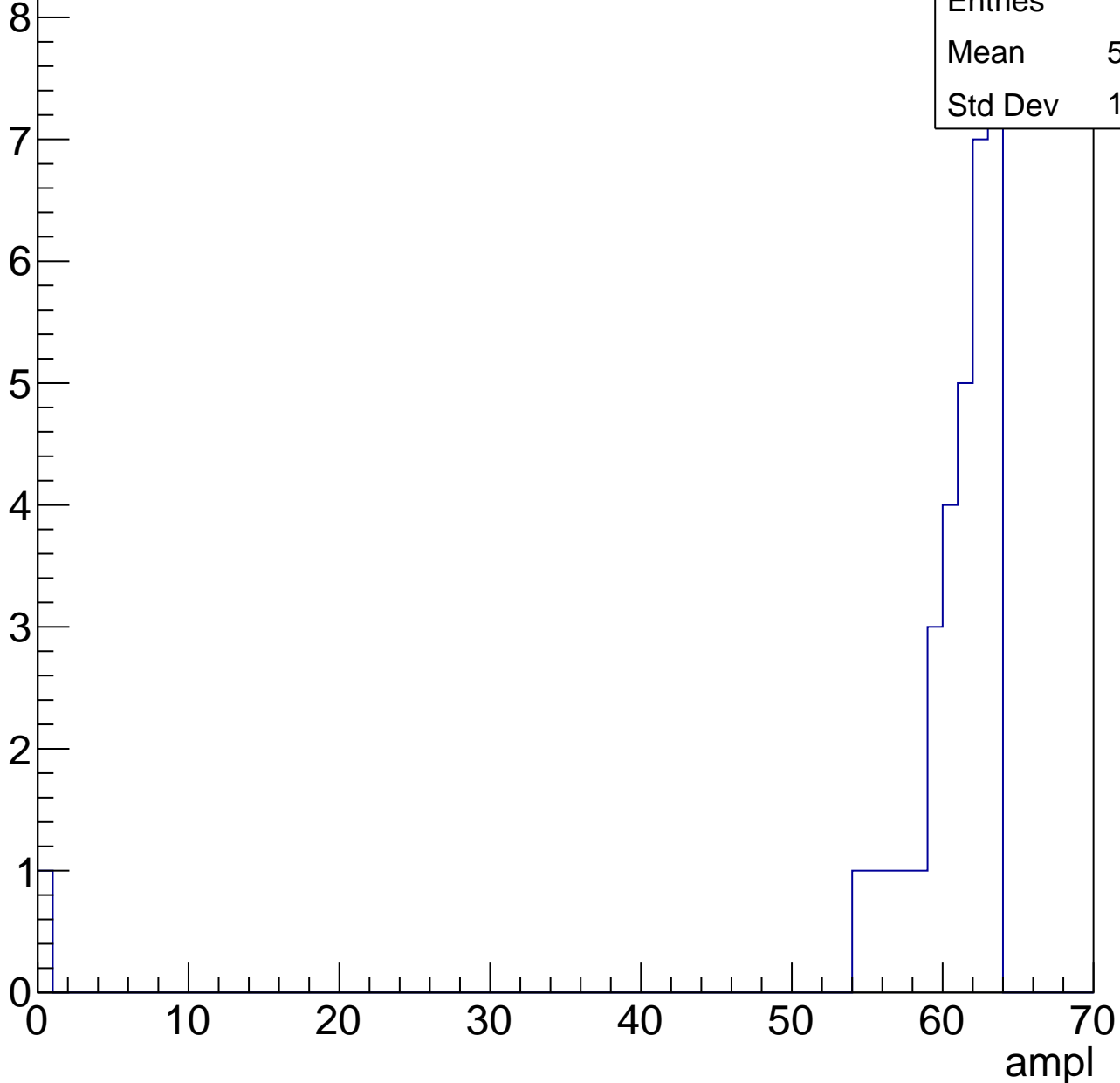


B1L100S, U12-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	33
Mean	58.79
Std Dev	10.66



B1L100S, U12-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch126, adc0

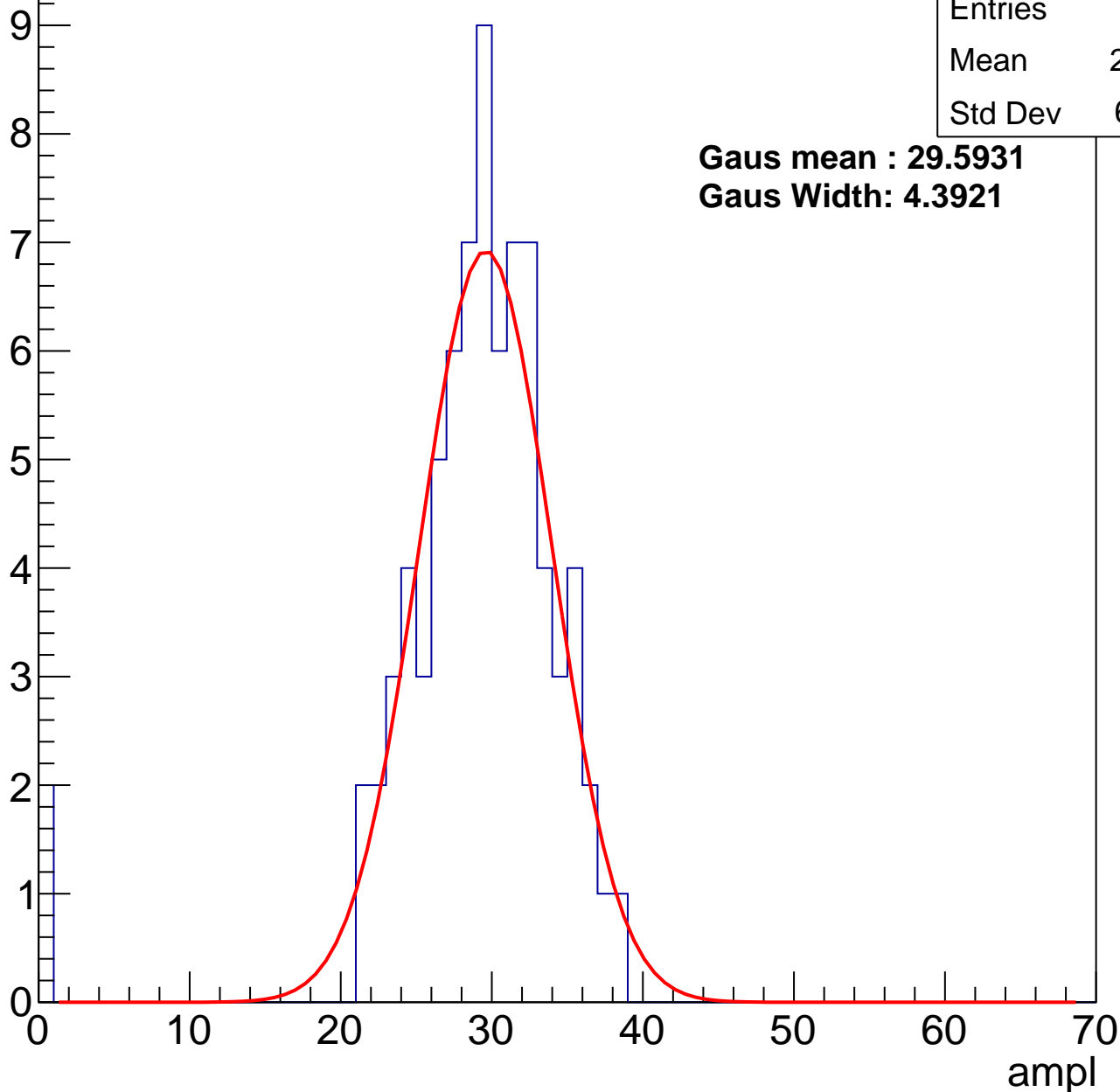
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.42
Std Dev	6.031

Gaus mean : 29.5931

Gaus Width: 4.3921



B1L100S, U12-ch126, adc1

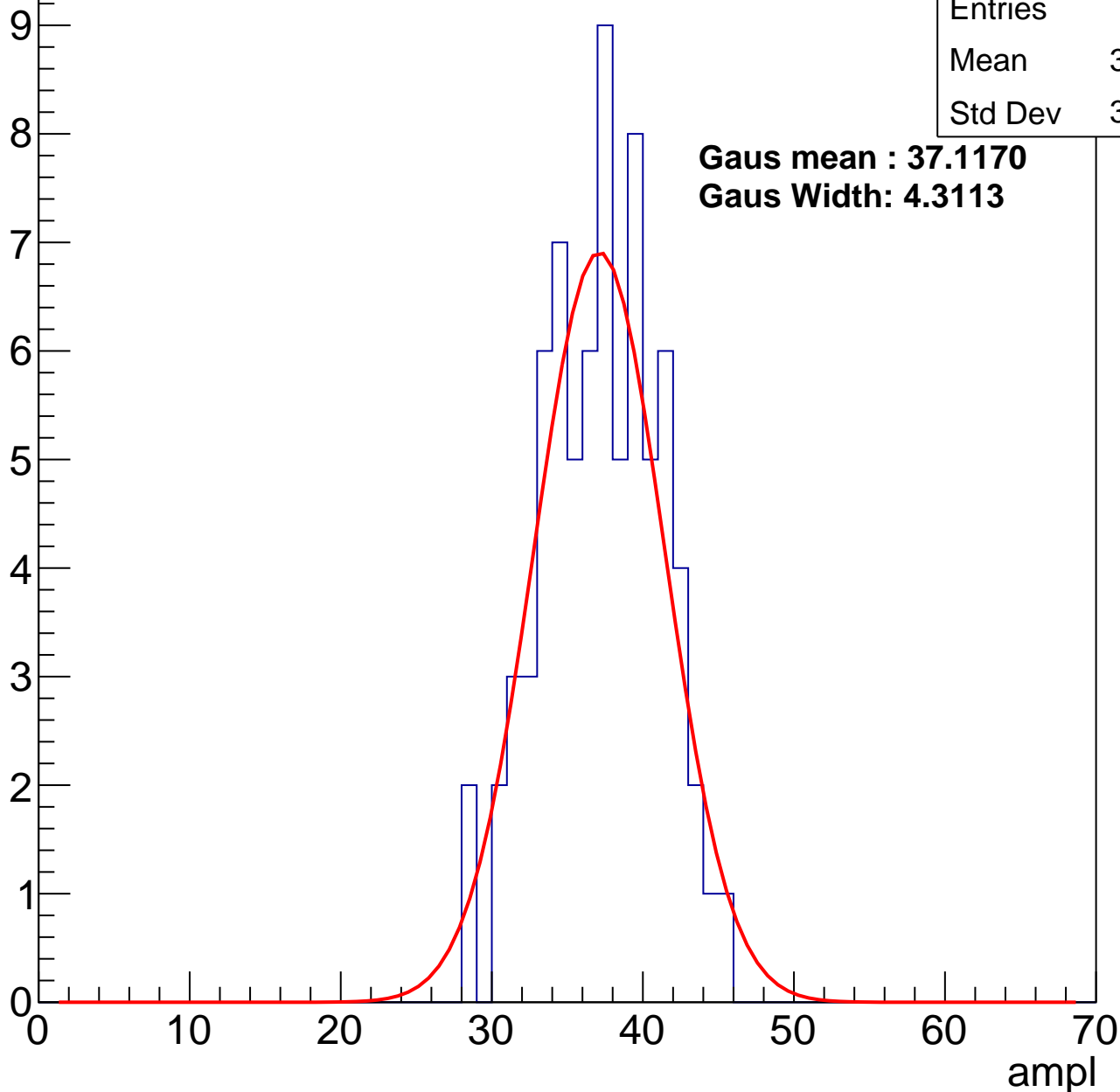
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.75
Std Dev	3.806

Gaus mean : 37.1170

Gaus Width: 4.3113



B1L100S, U12-ch126, adc2

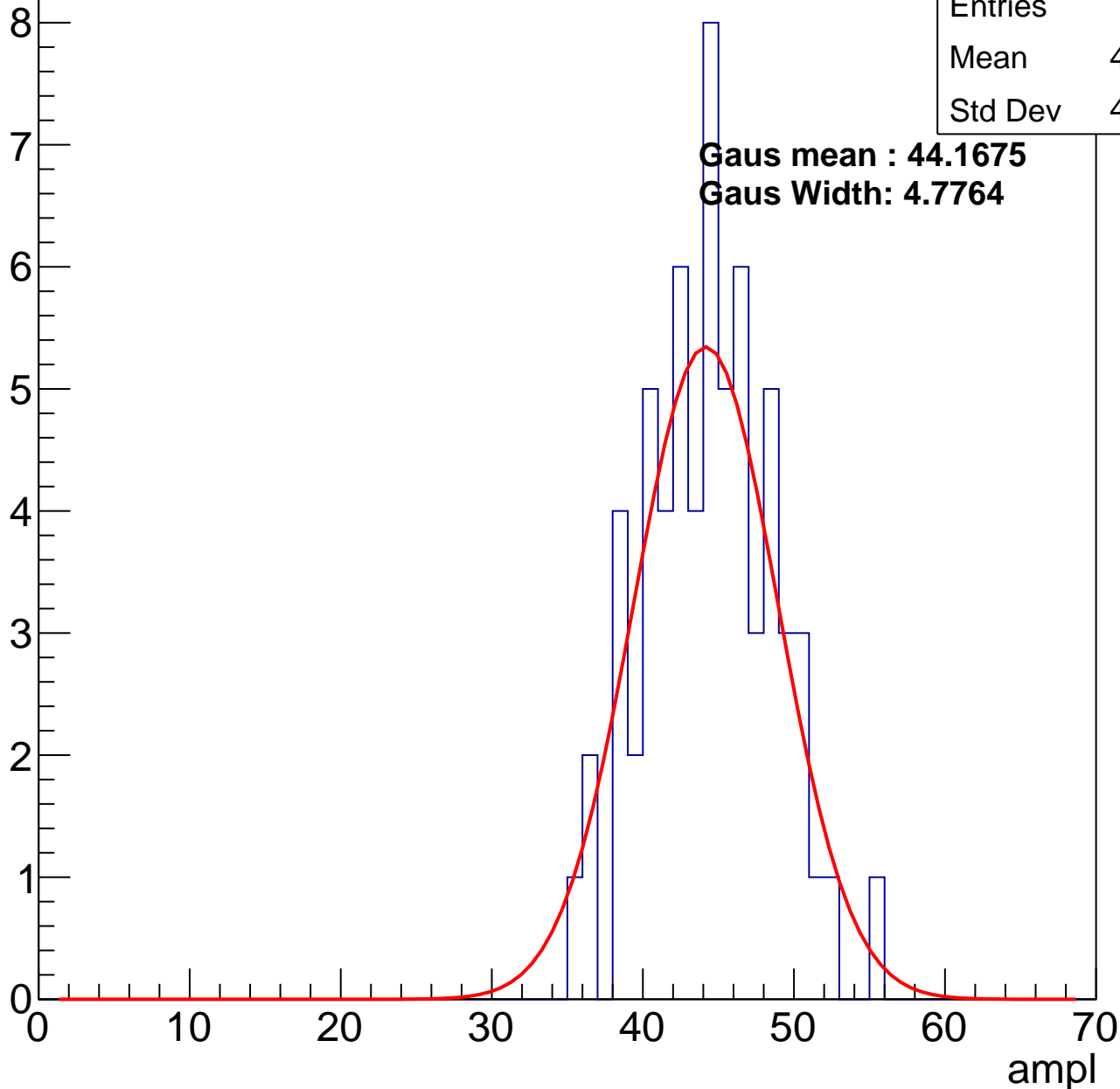
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.97
Std Dev	4.146

Gaus mean : 44.1675

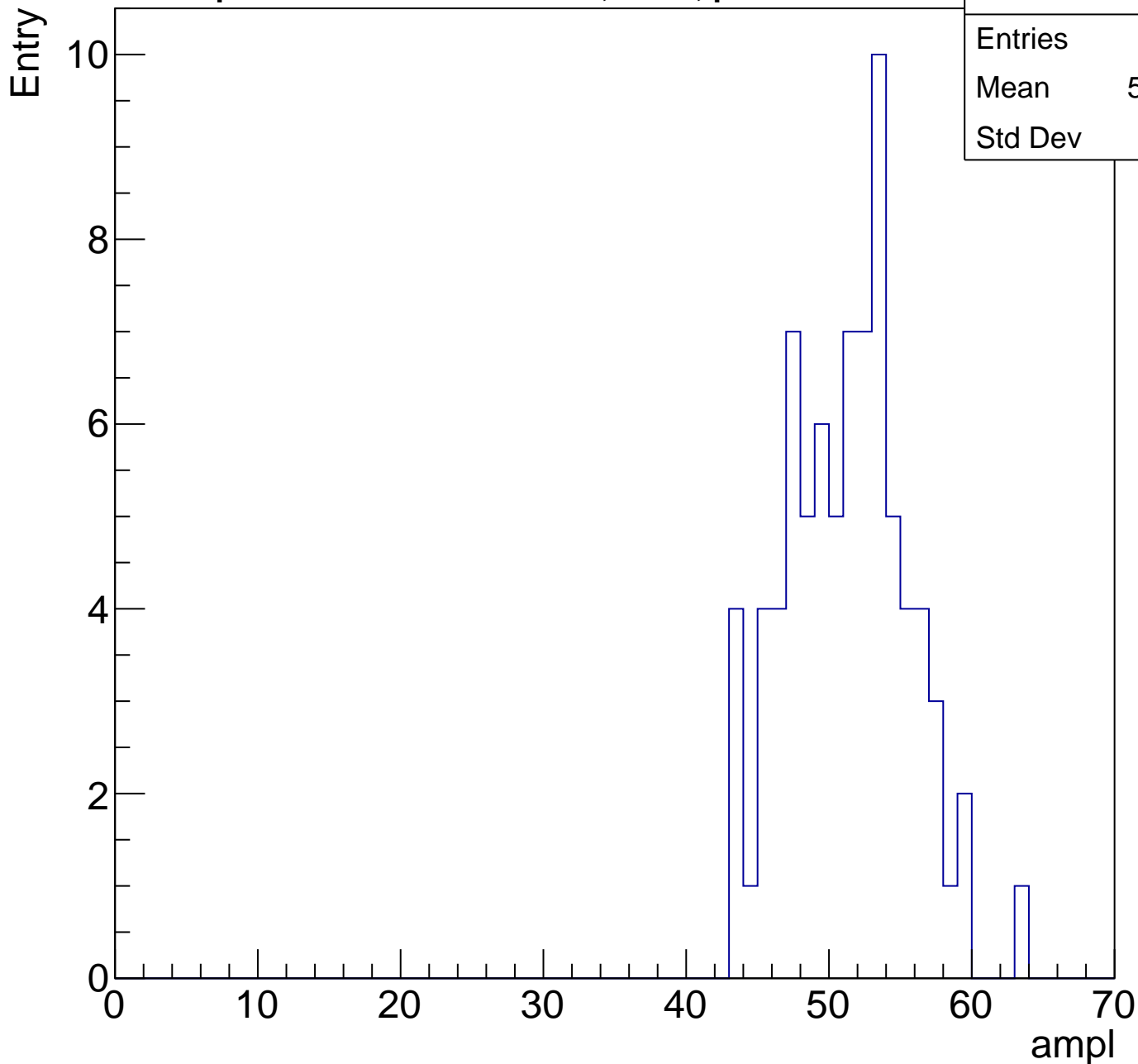
Gaus Width: 4.7764



B1L100S, U12-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	50.85
Std Dev	4.21

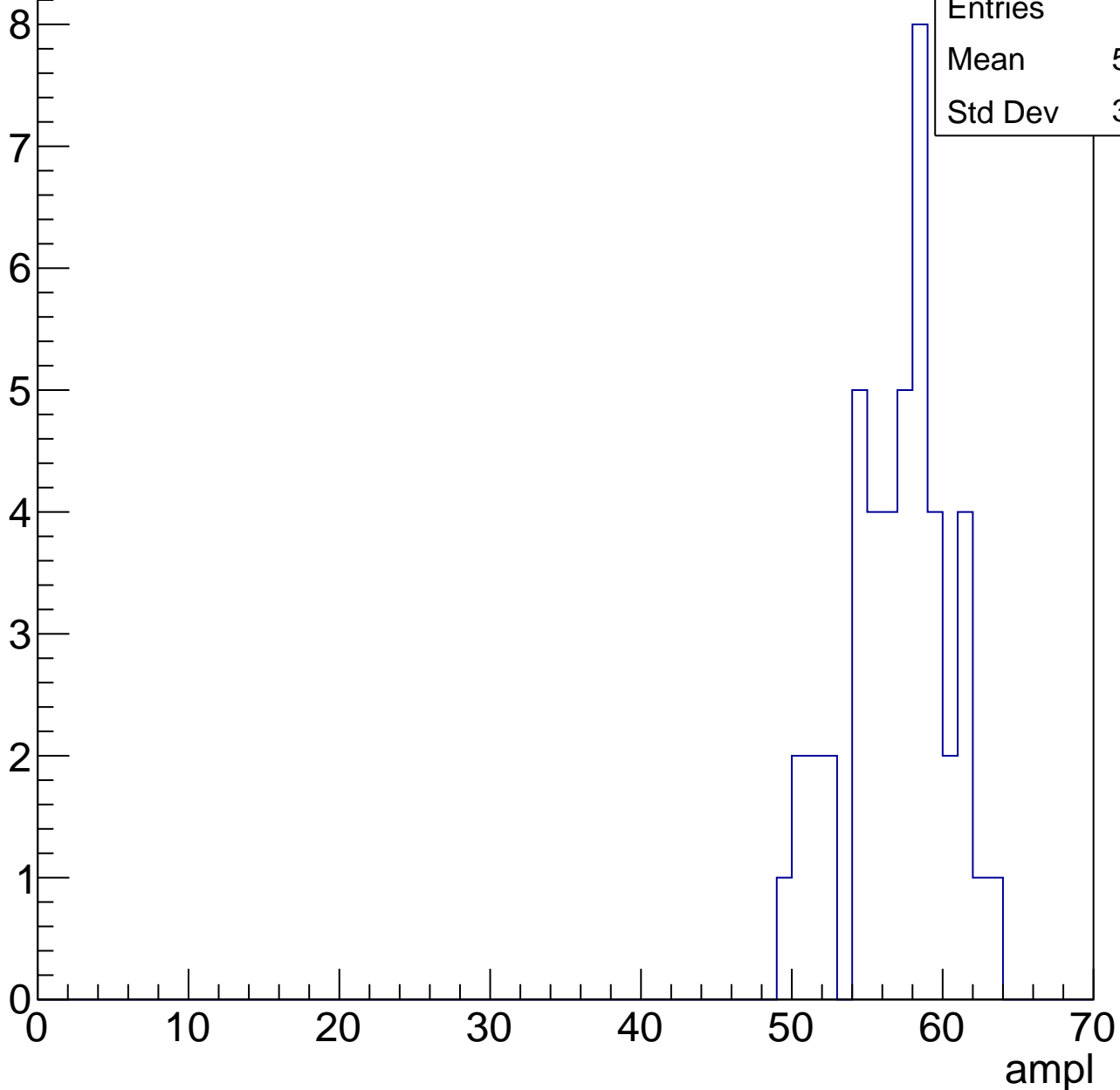


B1L100S, U12-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	56.51
Std Dev	3.351

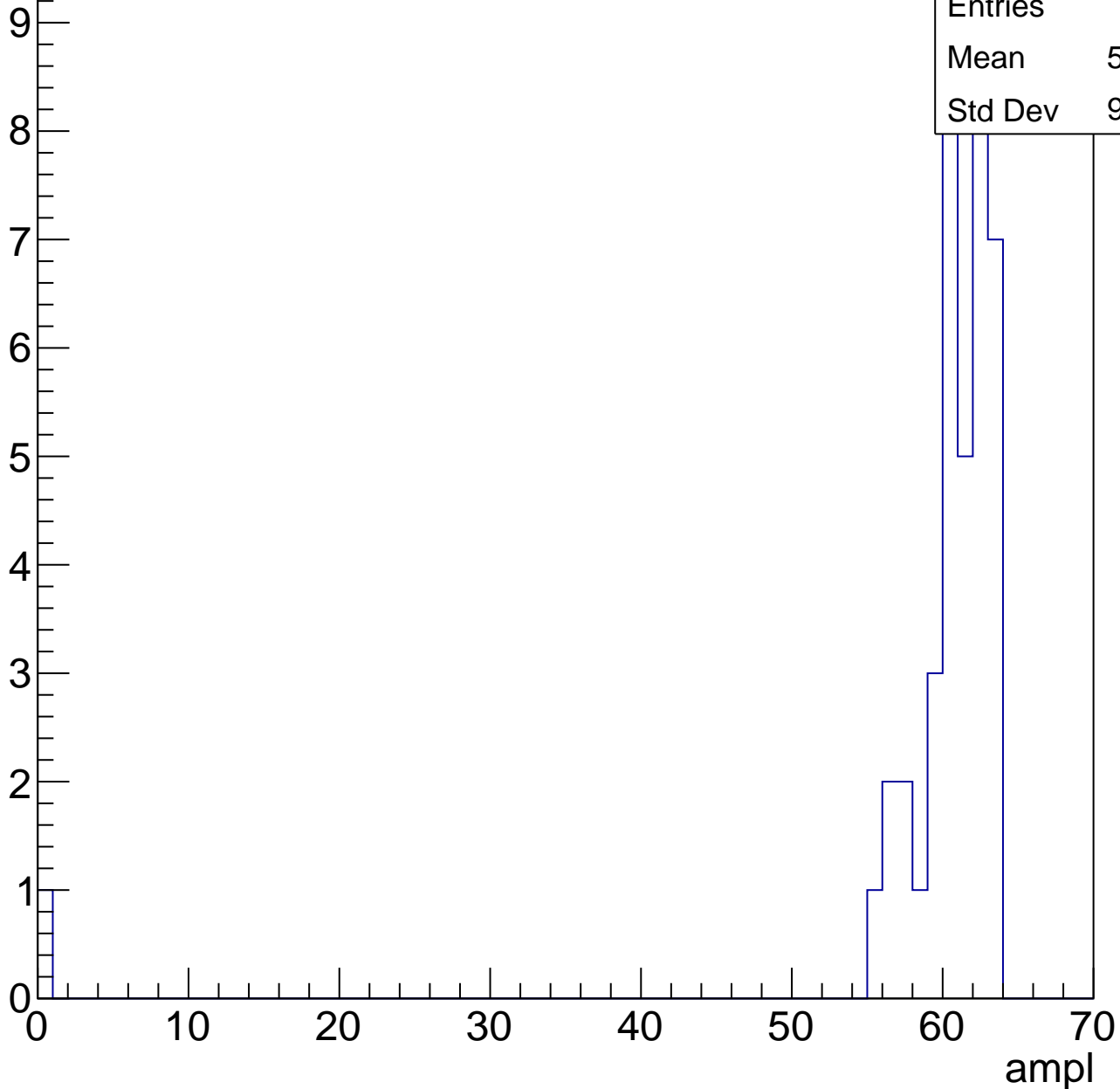


B1L100S, U12-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

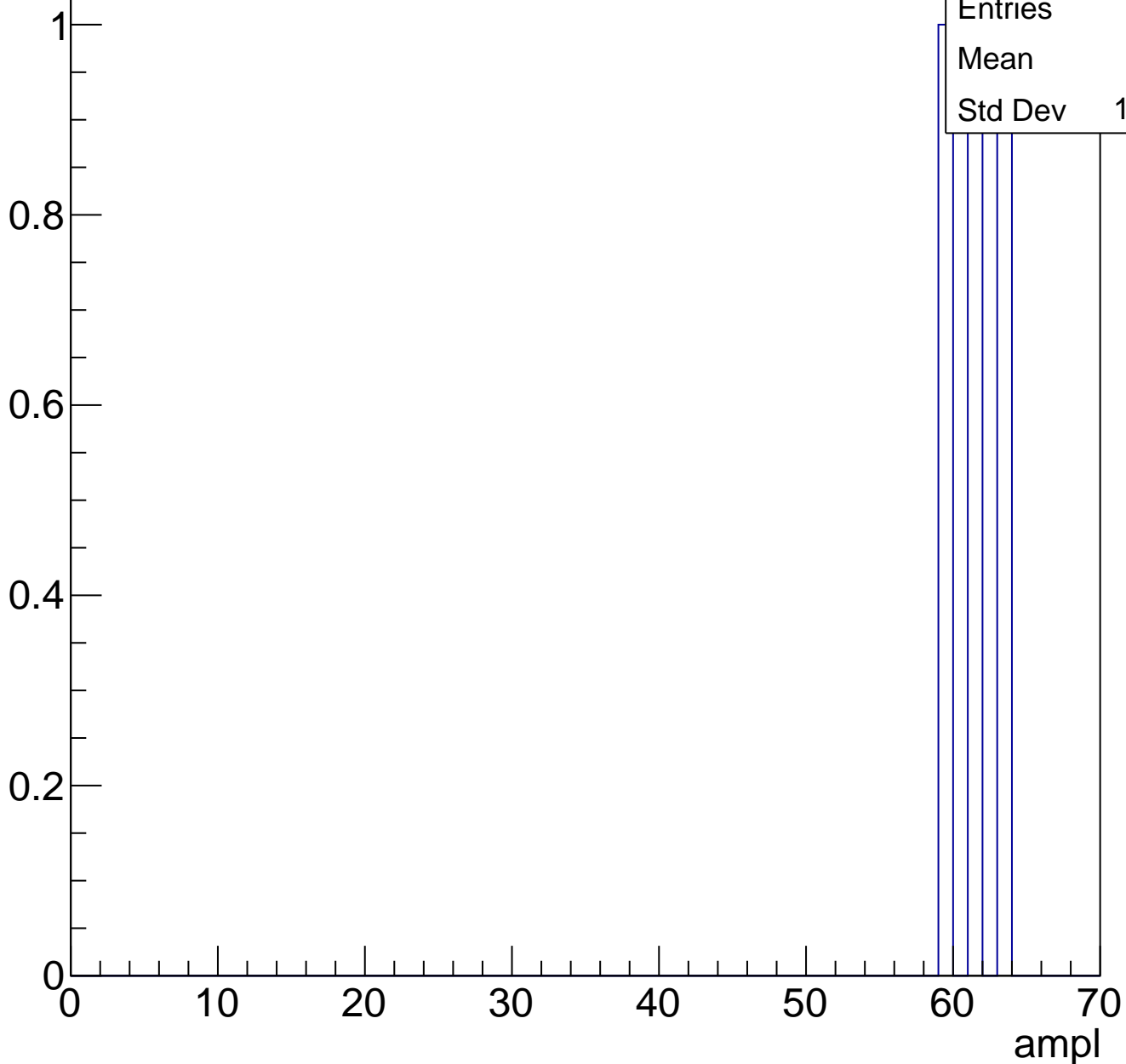
Entries	39
Mean	58.97
Std Dev	9.799



B1L100S, U12-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U12-ch127, adc0

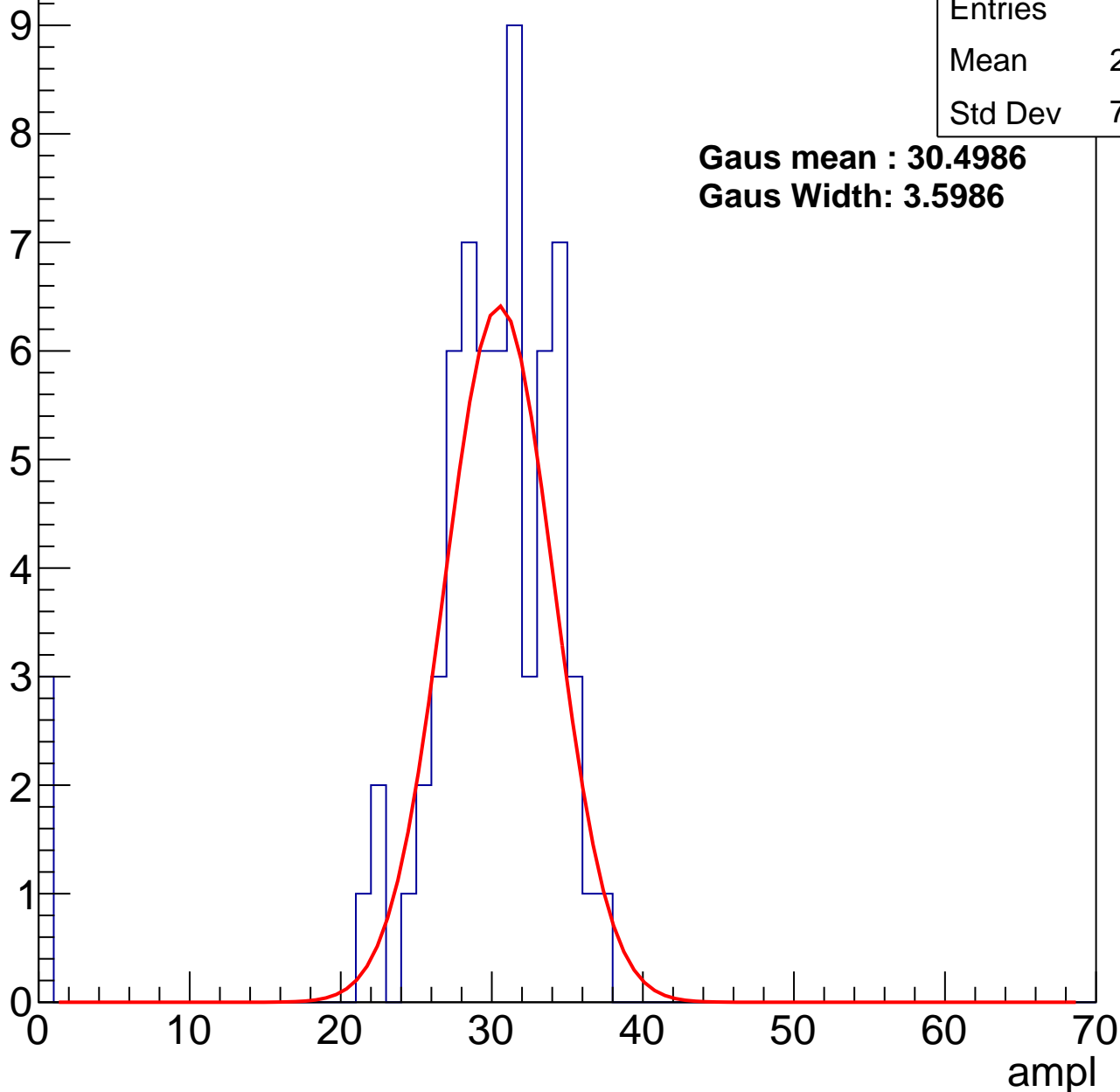
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.63
Std Dev	7.073

Gaus mean : 30.4986

Gaus Width: 3.5986



B1L100S, U12-ch127, adc1

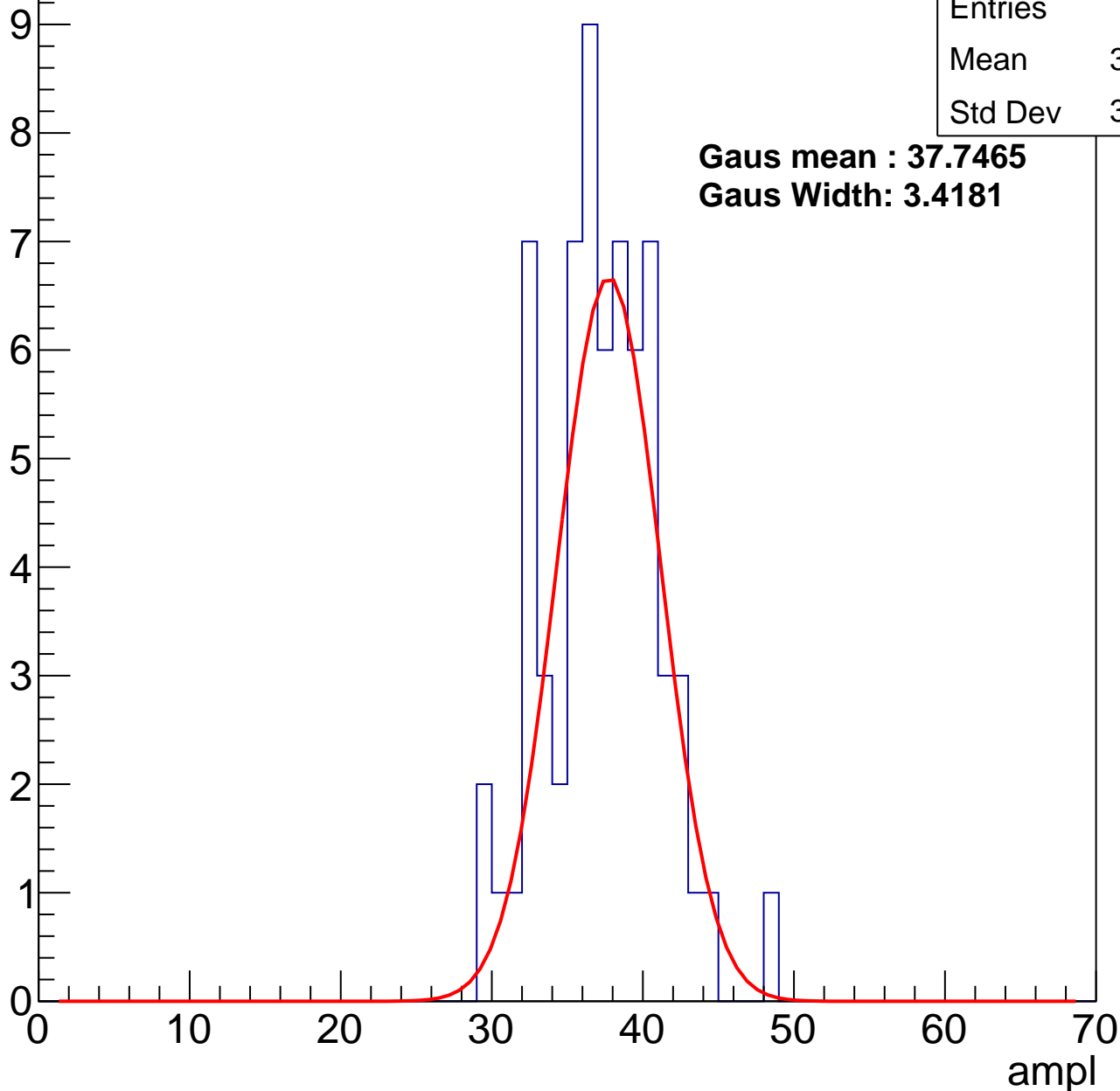
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	36.79
Std Dev	3.688

Gaus mean : 37.7465

Gaus Width: 3.4181



B1L100S, U12-ch127, adc2

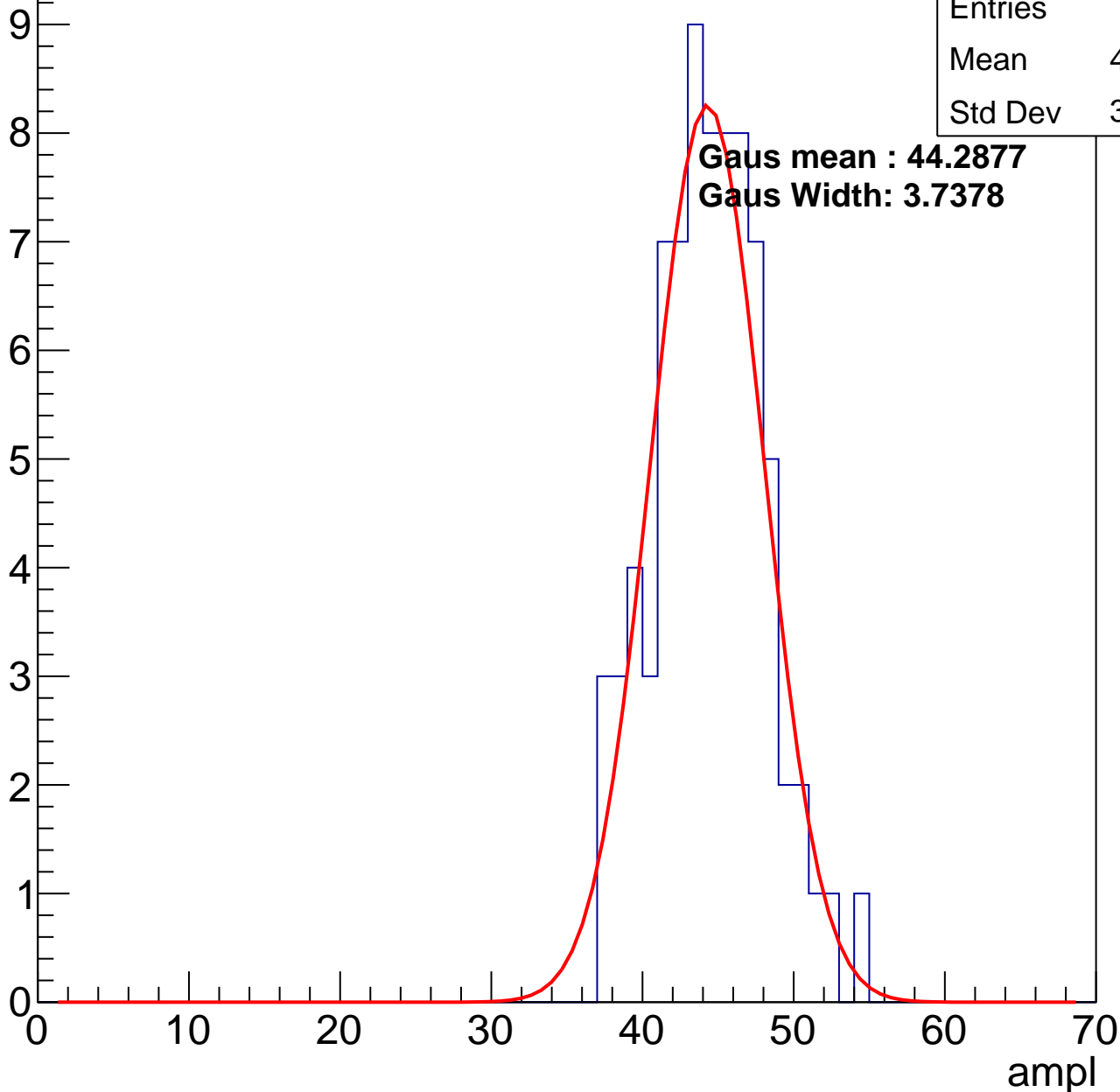
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	43.96
Std Dev	3.595

Gaus mean : 44.2877

Gaus Width: 3.7378

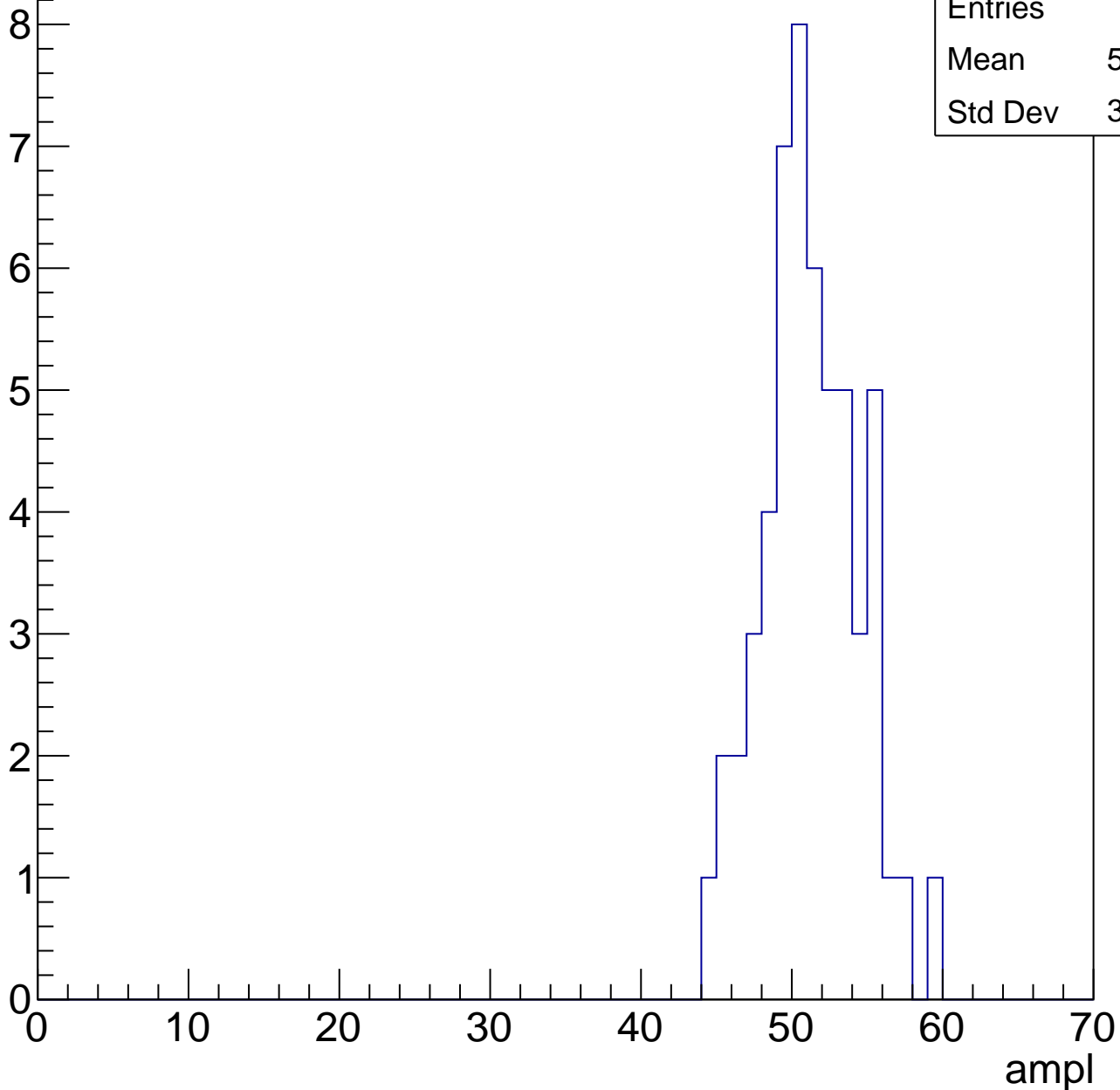


B1L100S, U12-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

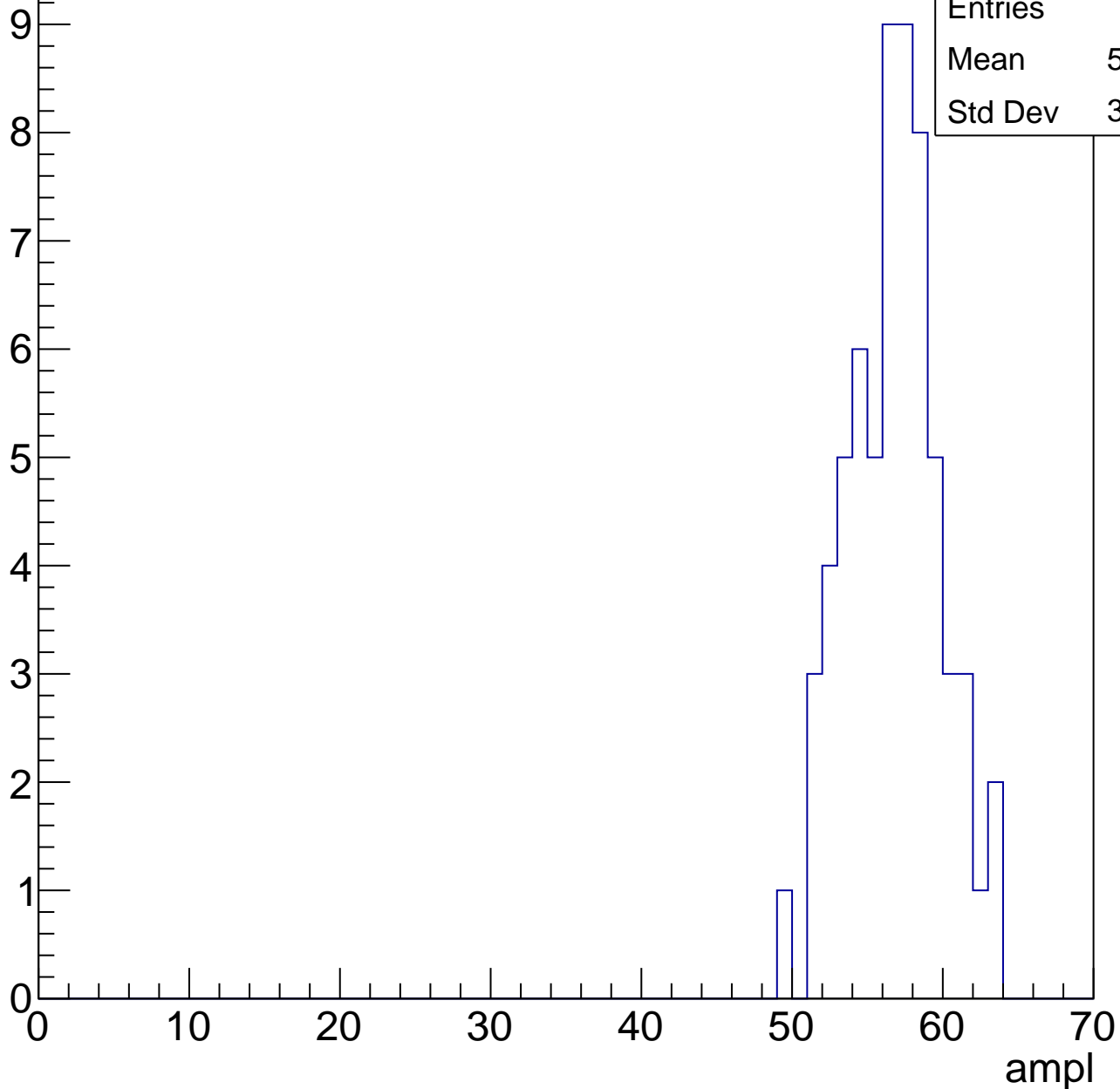
Entries	54
Mean	50.78
Std Dev	3.178



B1L100S, U12-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

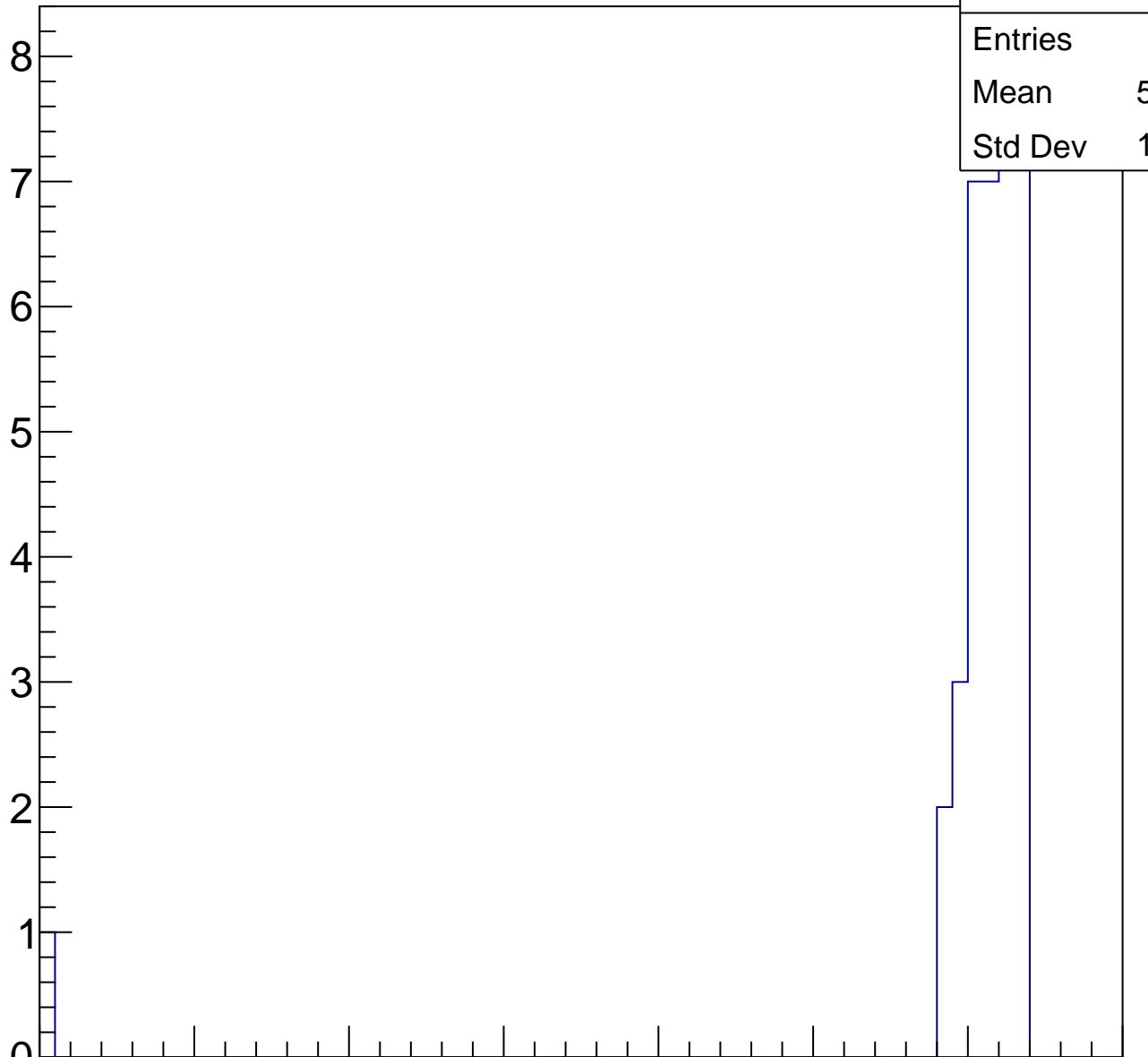
Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	59.44
Std Dev	10.15

ampl

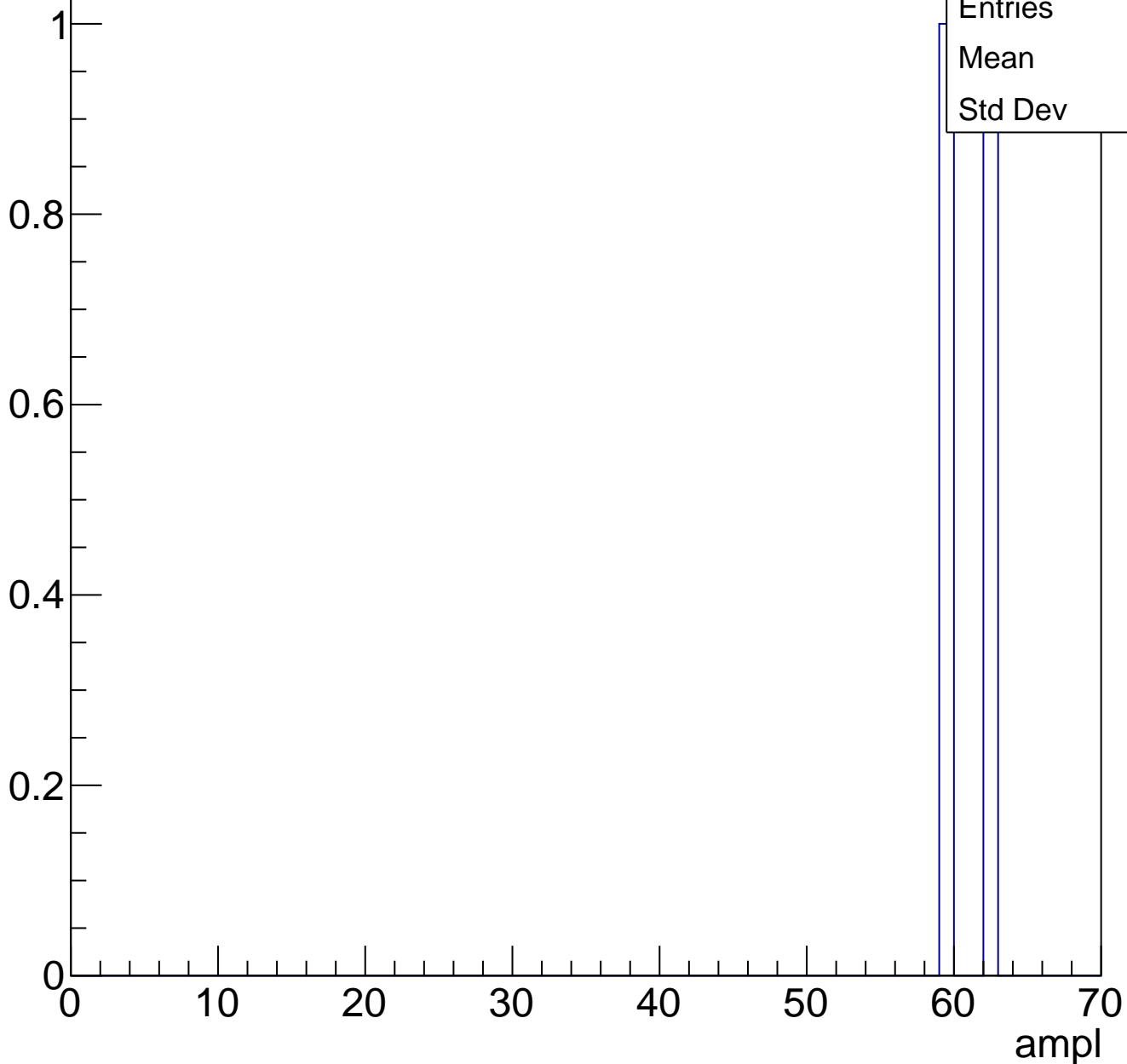
0 10 20 30 40 50 60 70



B1L100S, U12-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

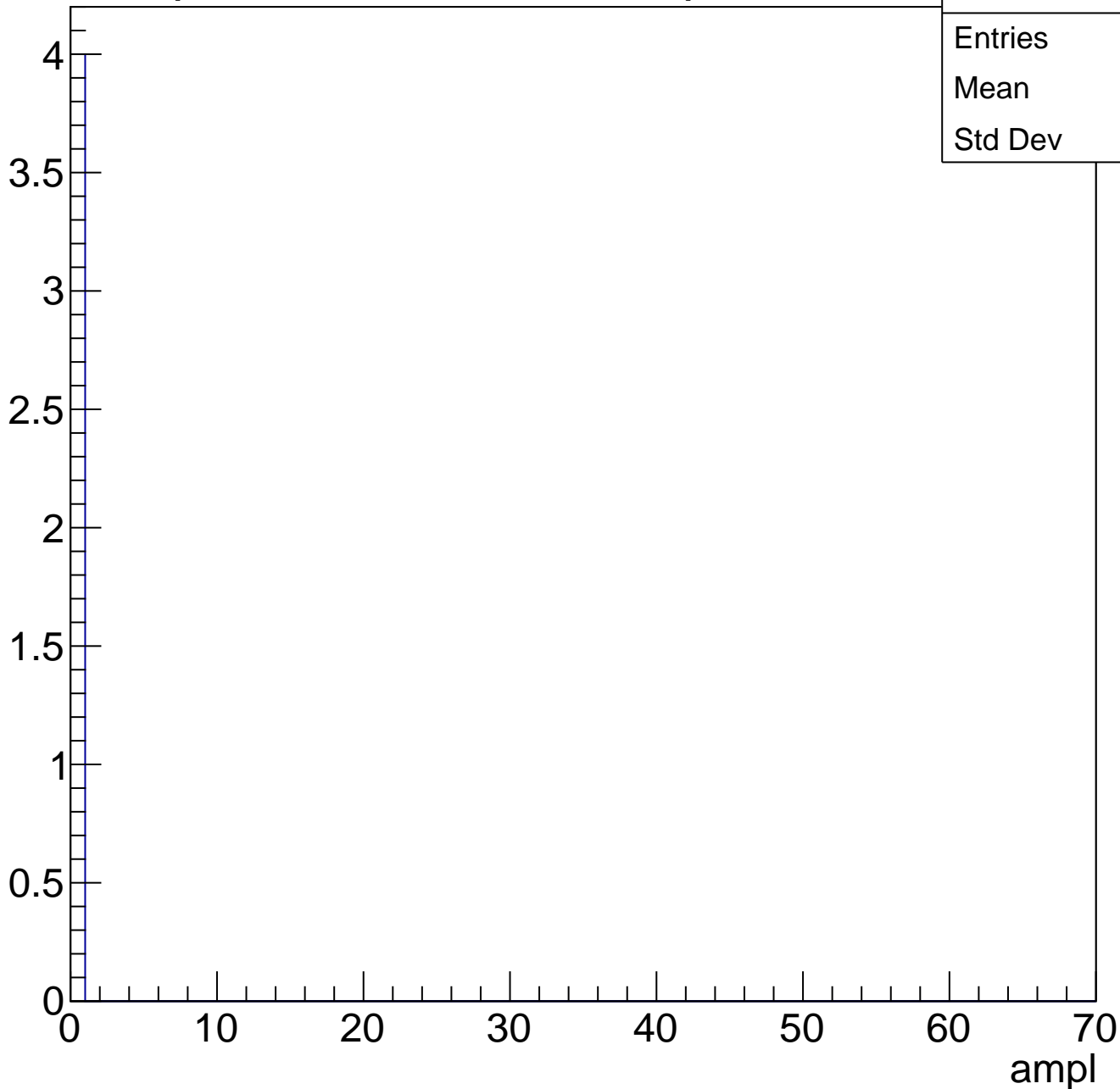


Entries	4
Mean	0
Std Dev	0

B1L100S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	0
Std Dev	0