

B0L103S, U10-ch0

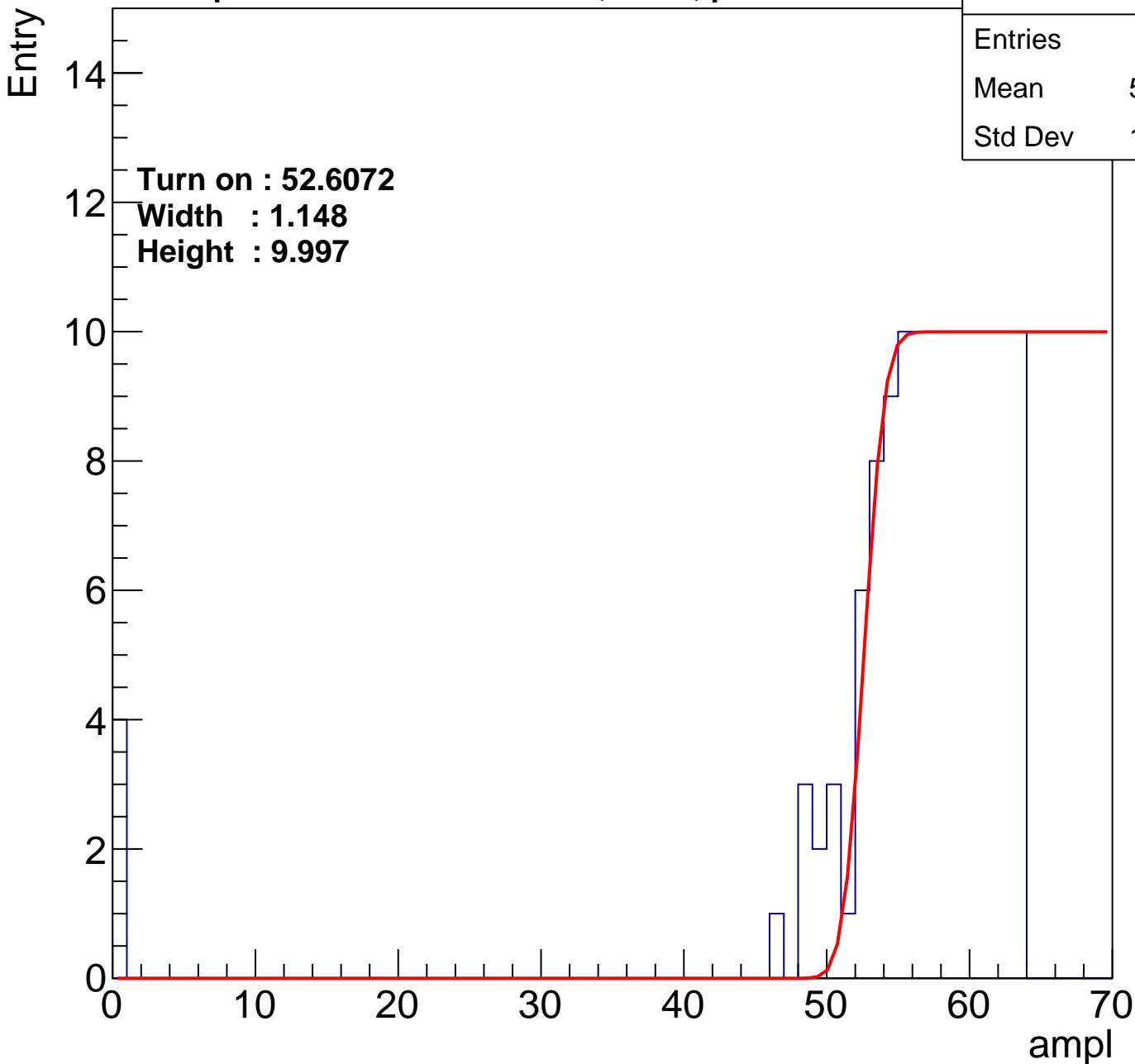
calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.28
Std Dev	10.73

Turn on : 52.6072

Width : 1.148

Height : 9.997



B0L103S, U10-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	54.78
Std Dev	10.28

Turn on : 50.6984

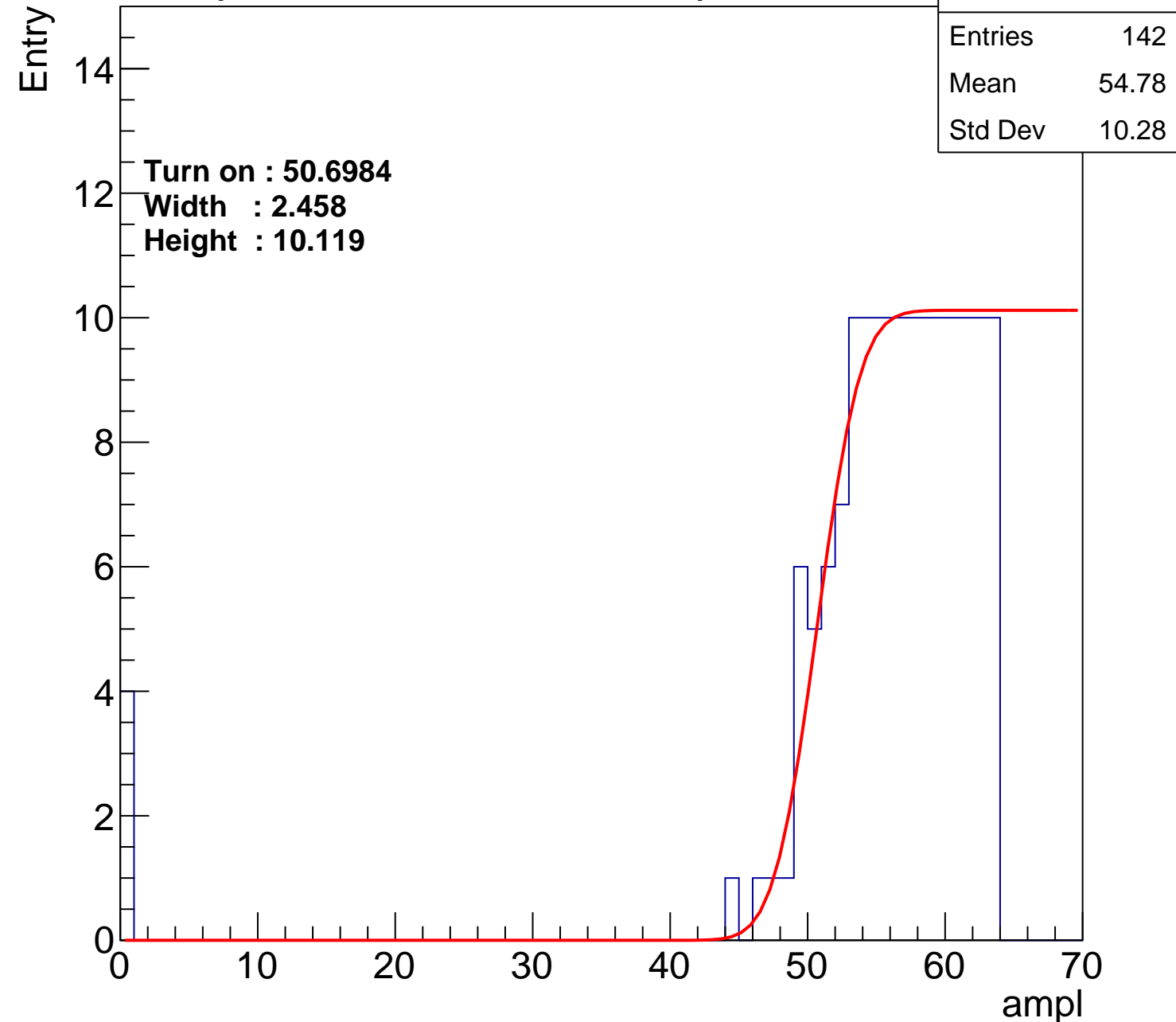
Width : 2.458

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch2

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.74
Std Dev	8.299

Turn on : 51.8661

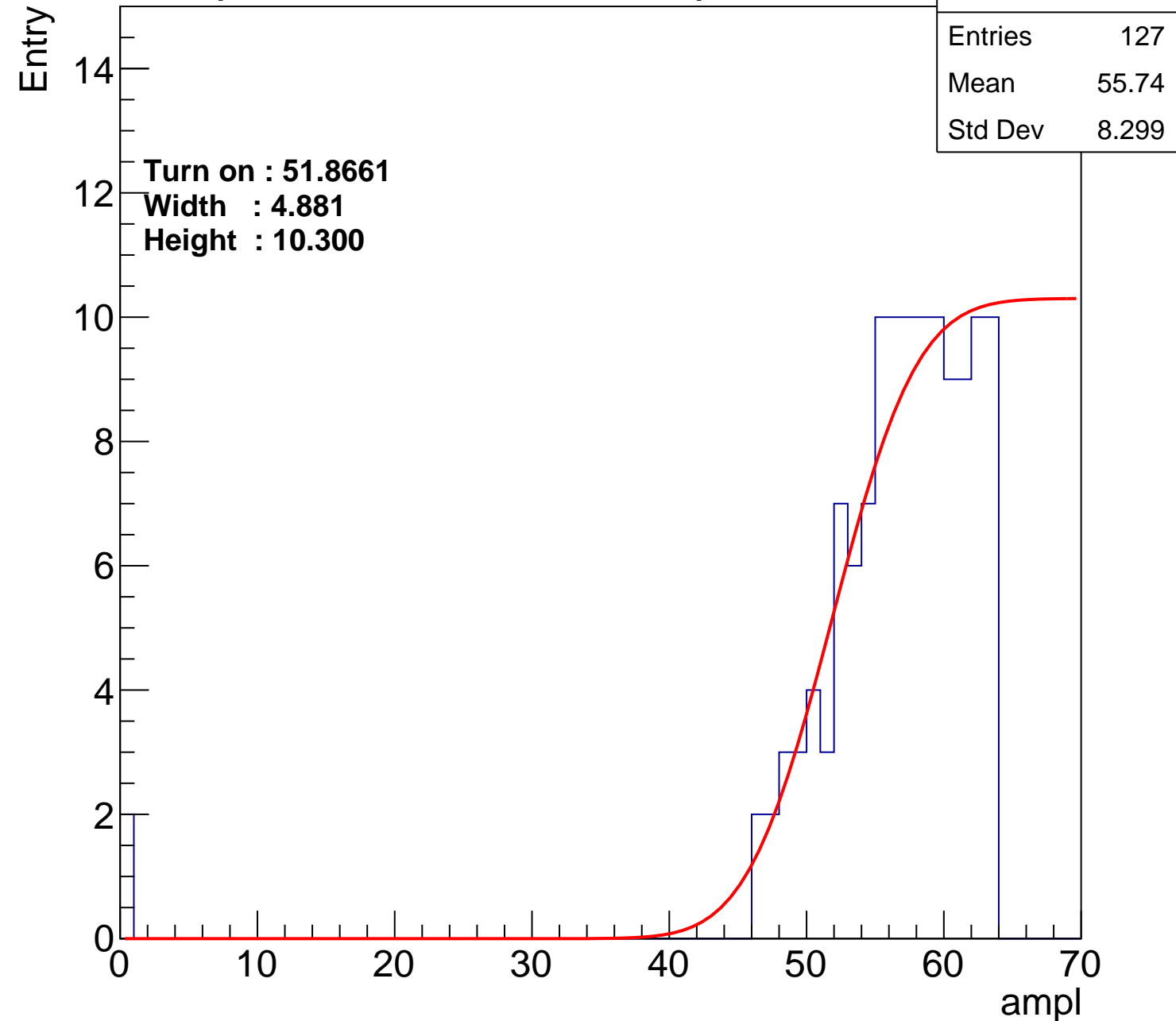
Width : 4.881

Height : 10.300

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch3

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.44
Std Dev	10.98

Turn on : 52.3303

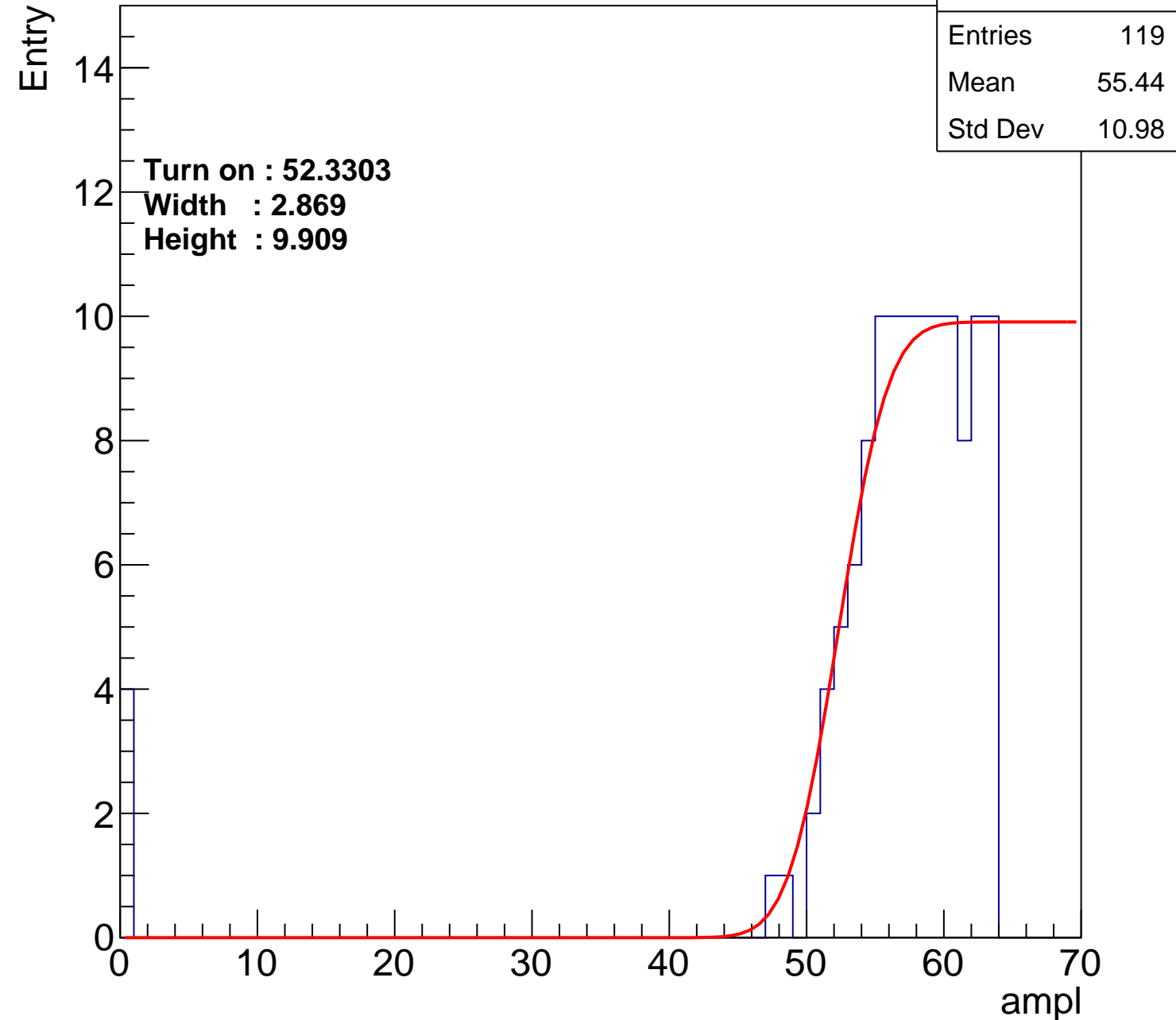
Width : 2.869

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch4

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	56.92
Std Dev	6.51

Turn on : 52.9192

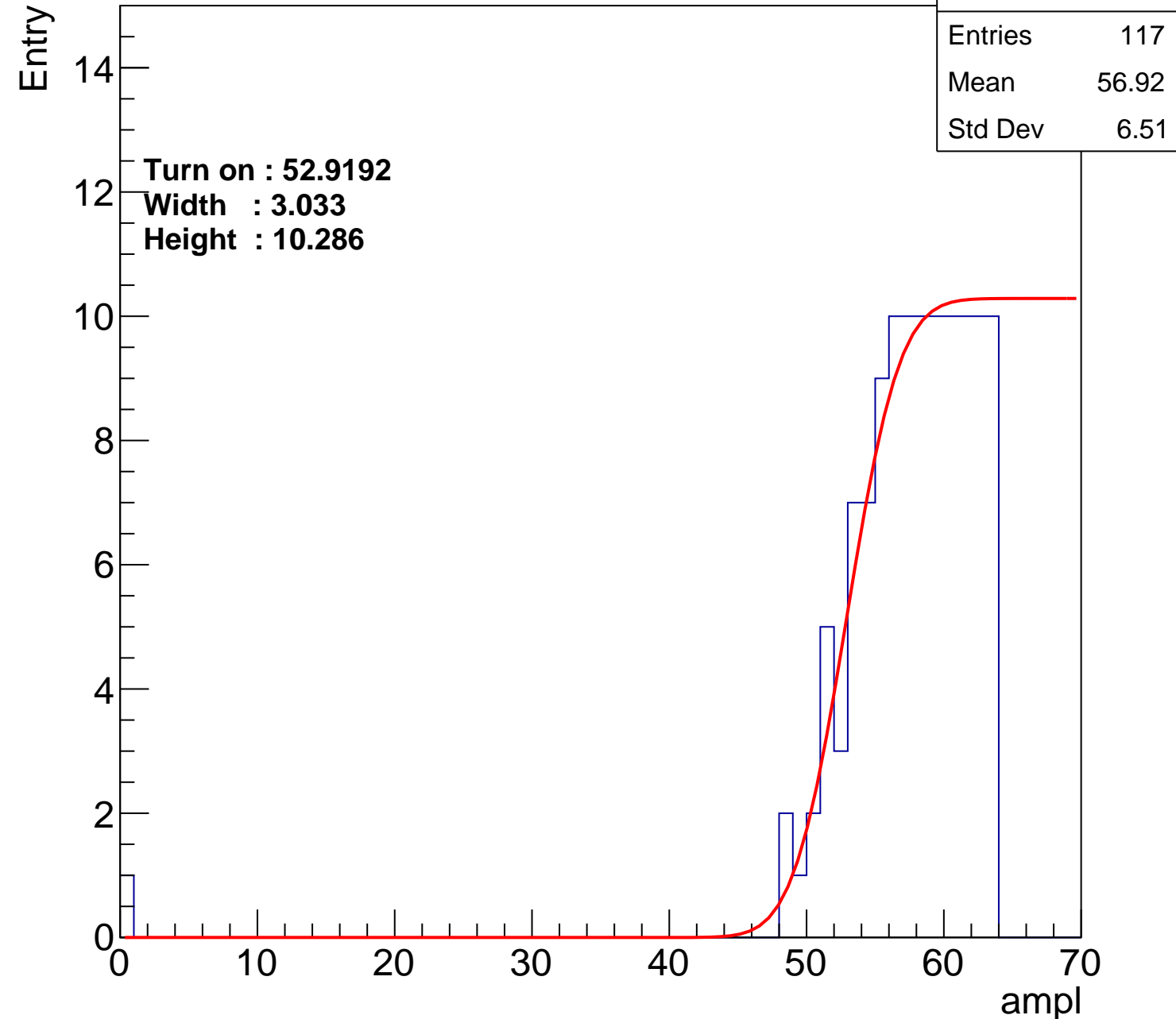
Width : 3.033

Height : 10.286

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch5

calib_packv5_040323_1717.root, FC#2, port C3

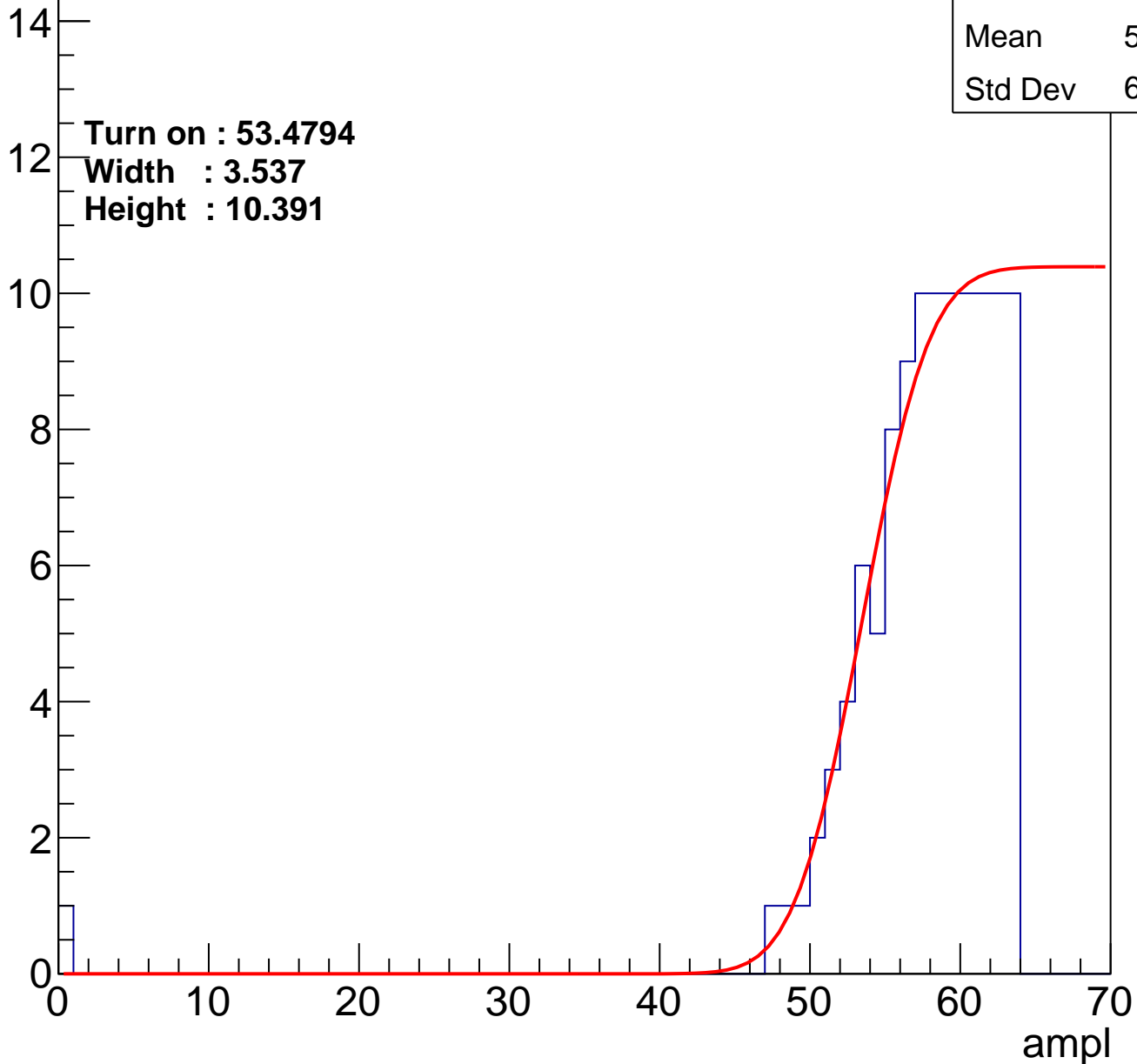
Entry

Entries	111
Mean	57.09
Std Dev	6.638

Turn on : 53.4794

Width : 3.537

Height : 10.391



B0L103S, U10-ch6

calib_packv5_040323_1717.root, FC#2, port C3

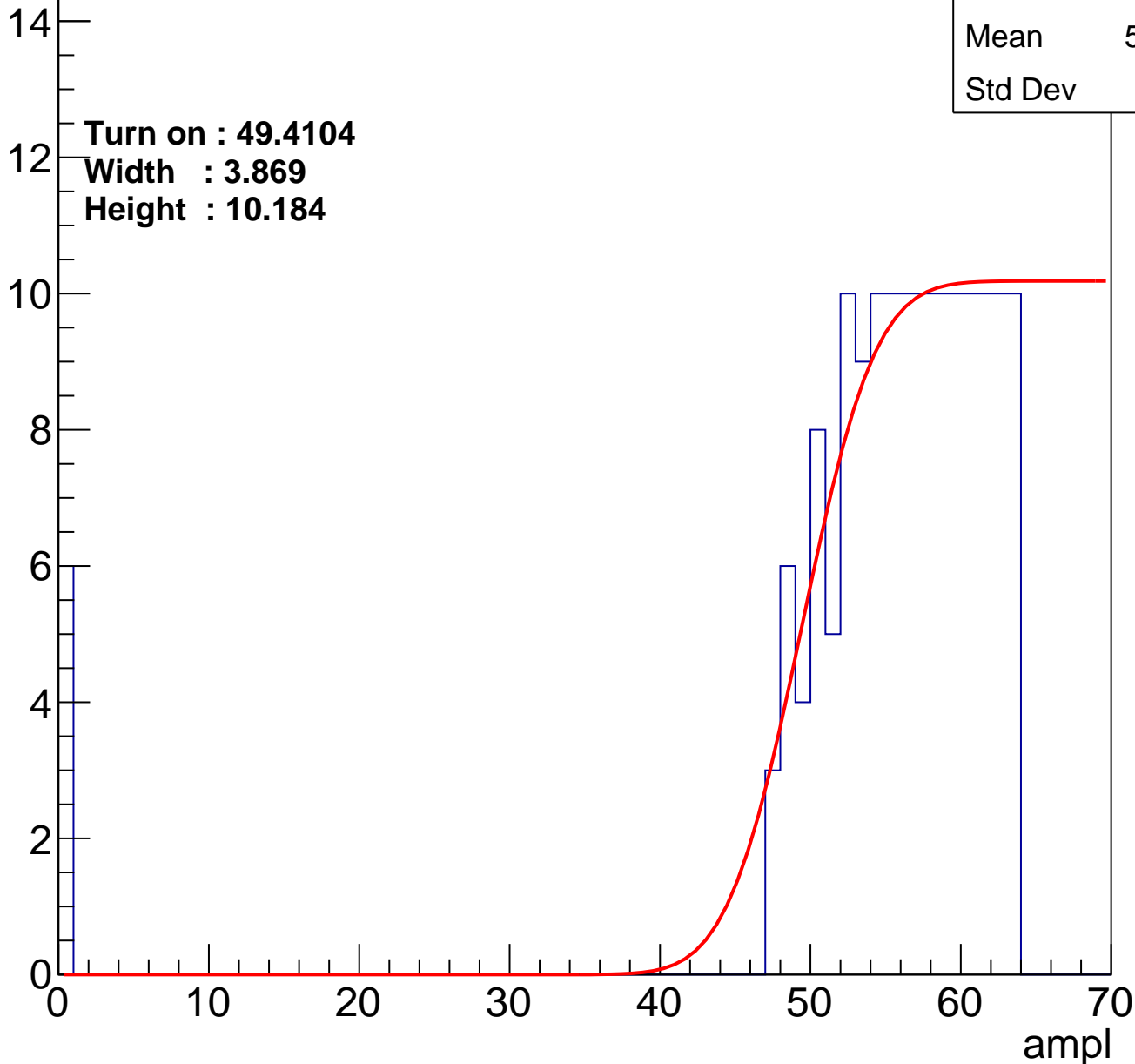
Entry

Entries	151
Mean	53.82
Std Dev	11.8

Turn on : 49.4104

Width : 3.869

Height : 10.184



B0L103S, U10-ch7

calib_packv5_040323_1717.root, FC#2, port C3

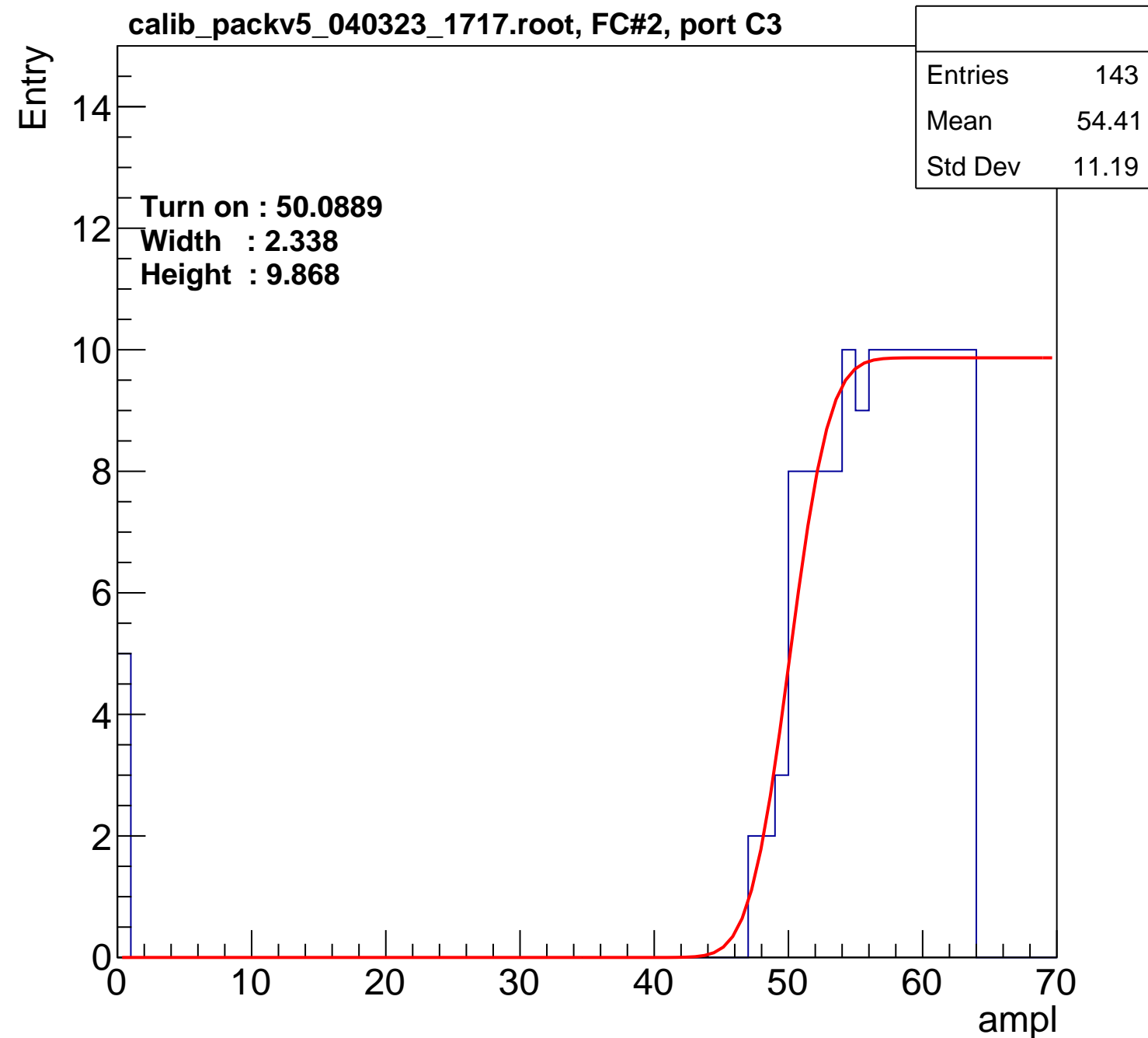
Entry

14
12
10
8
6
4
2
0

Turn on : 50.0889
Width : 2.338
Height : 9.868

Entries	143
Mean	54.41
Std Dev	11.19

ampl



B0L103S, U10-ch8

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	54.99
Std Dev	9.286

Turn on : 50.5314

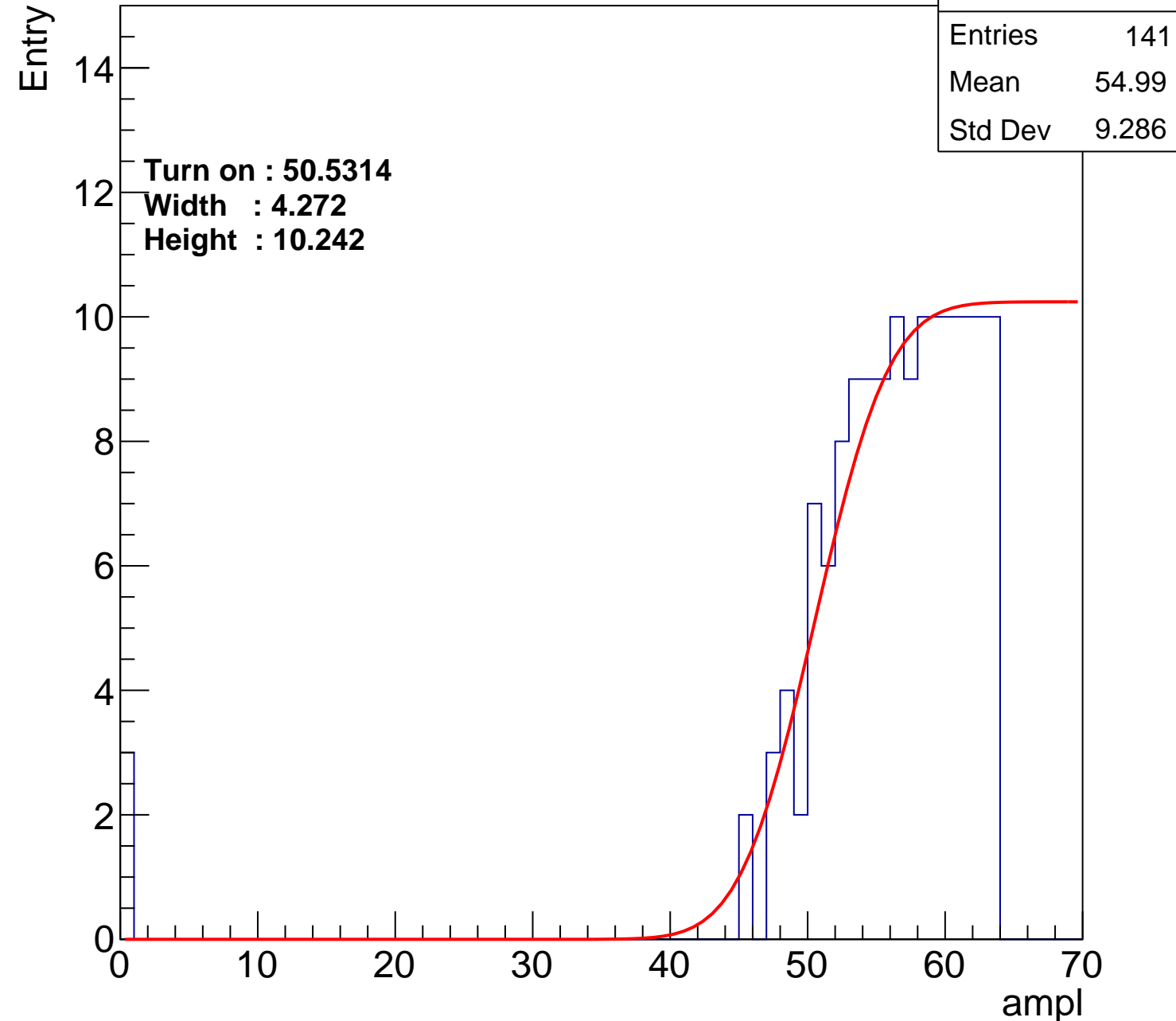
Width : 4.272

Height : 10.242

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch9

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.18
Std Dev	10.06

Turn on : 53.7124

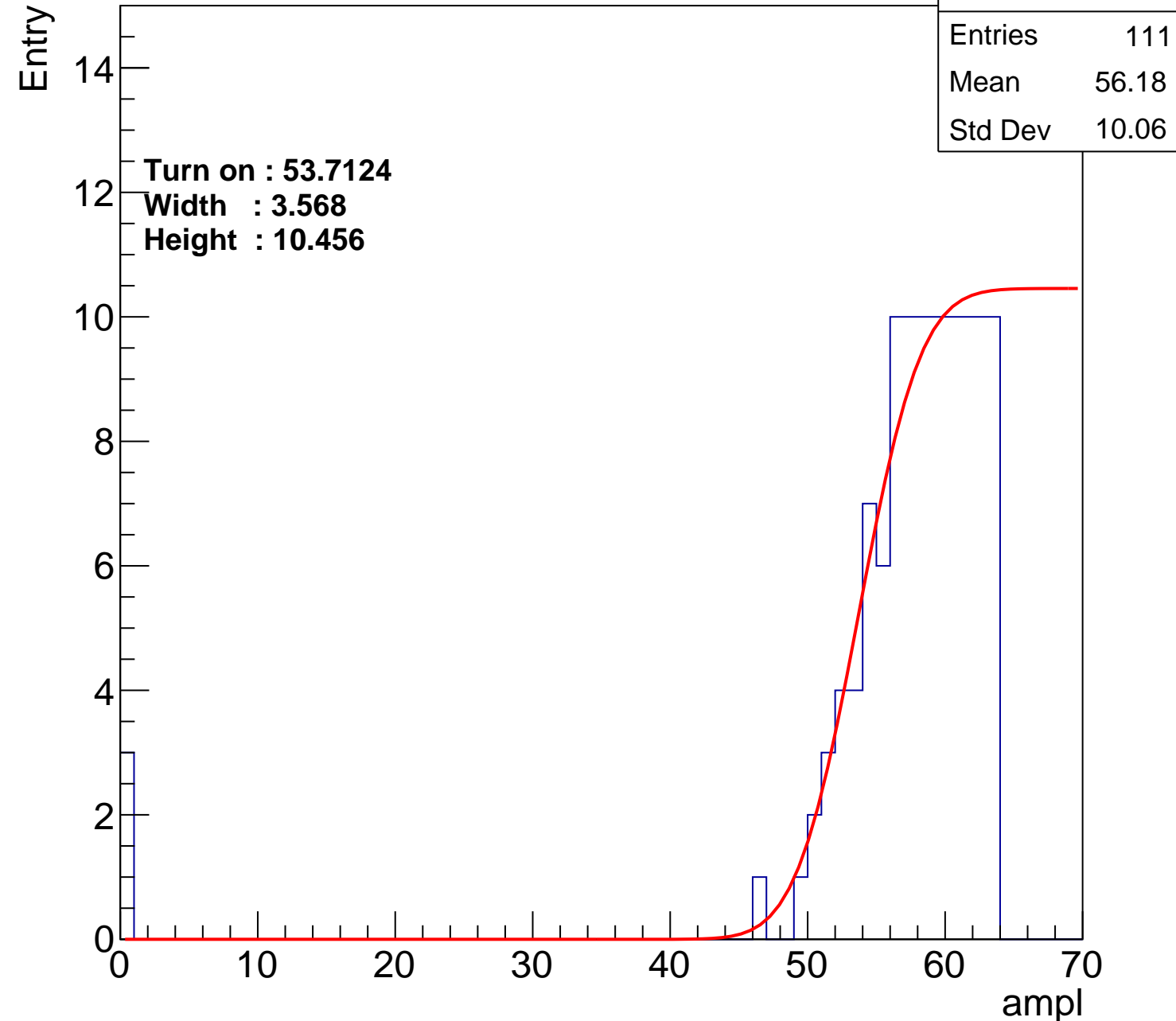
Width : 3.568

Height : 10.456

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch10

calib_packv5_040323_1717.root, FC#2, port C3

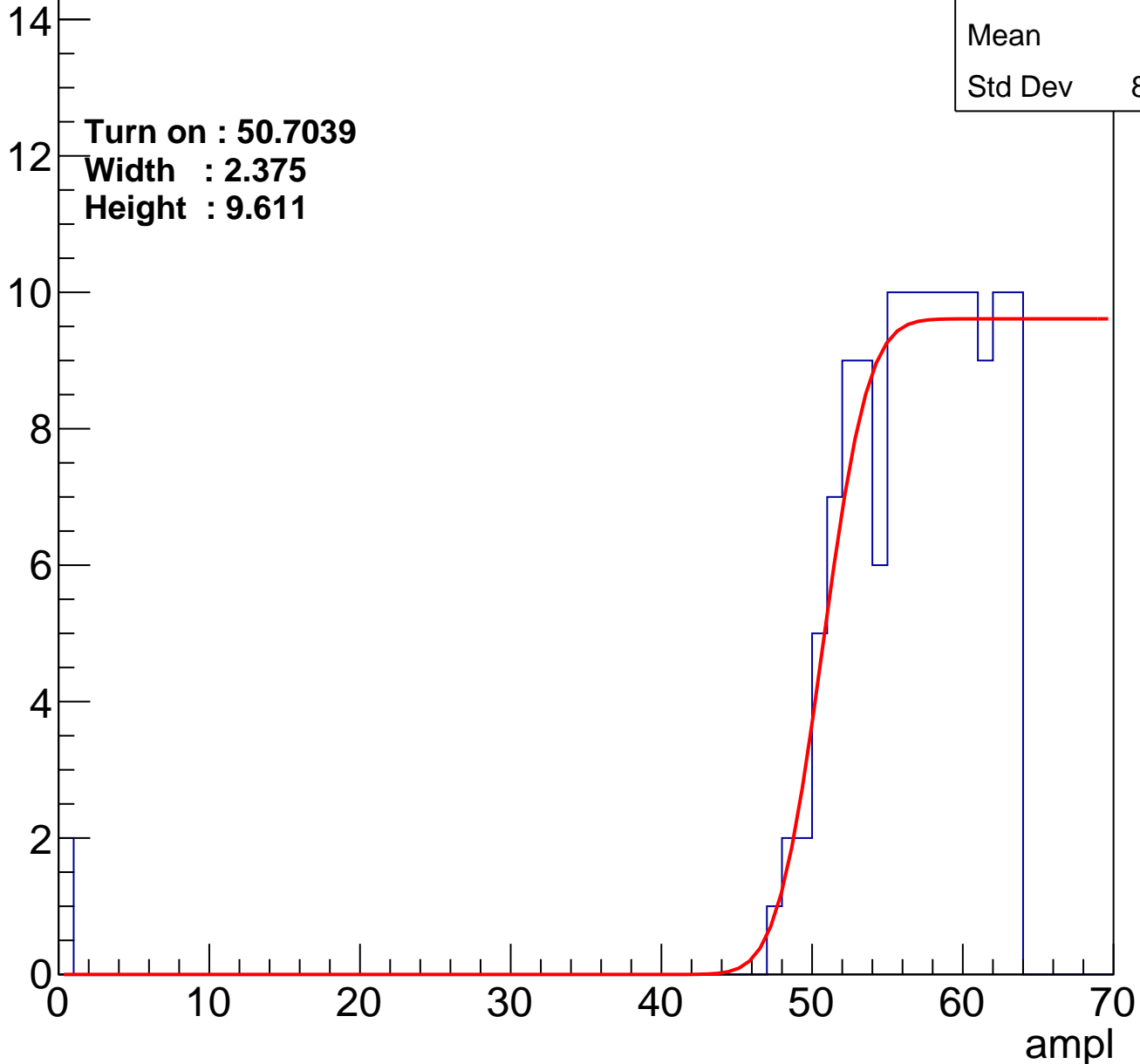
Entries	132
Mean	55.8
Std Dev	8.054

Turn on : 50.7039

Width : 2.375

Height : 9.611

Entry



B0L103S, U10-ch11

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.61
Std Dev	8.063

Turn on : 50.8066

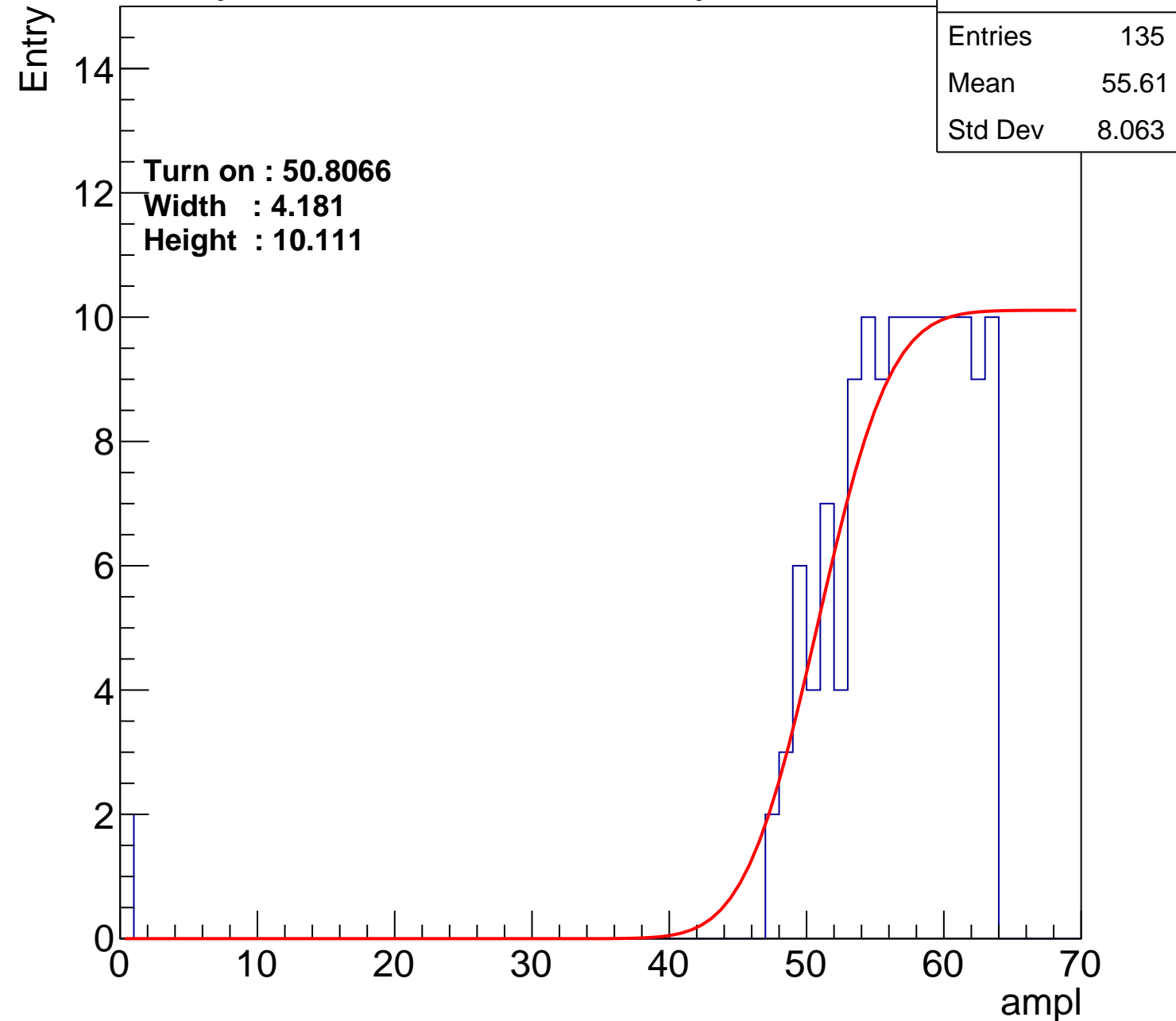
Width : 4.181

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch12

calib_packv5_040323_1717.root, FC#2, port C3

Entries	102
Mean	56.34
Std Dev	10.36

Turn on : 53.4629

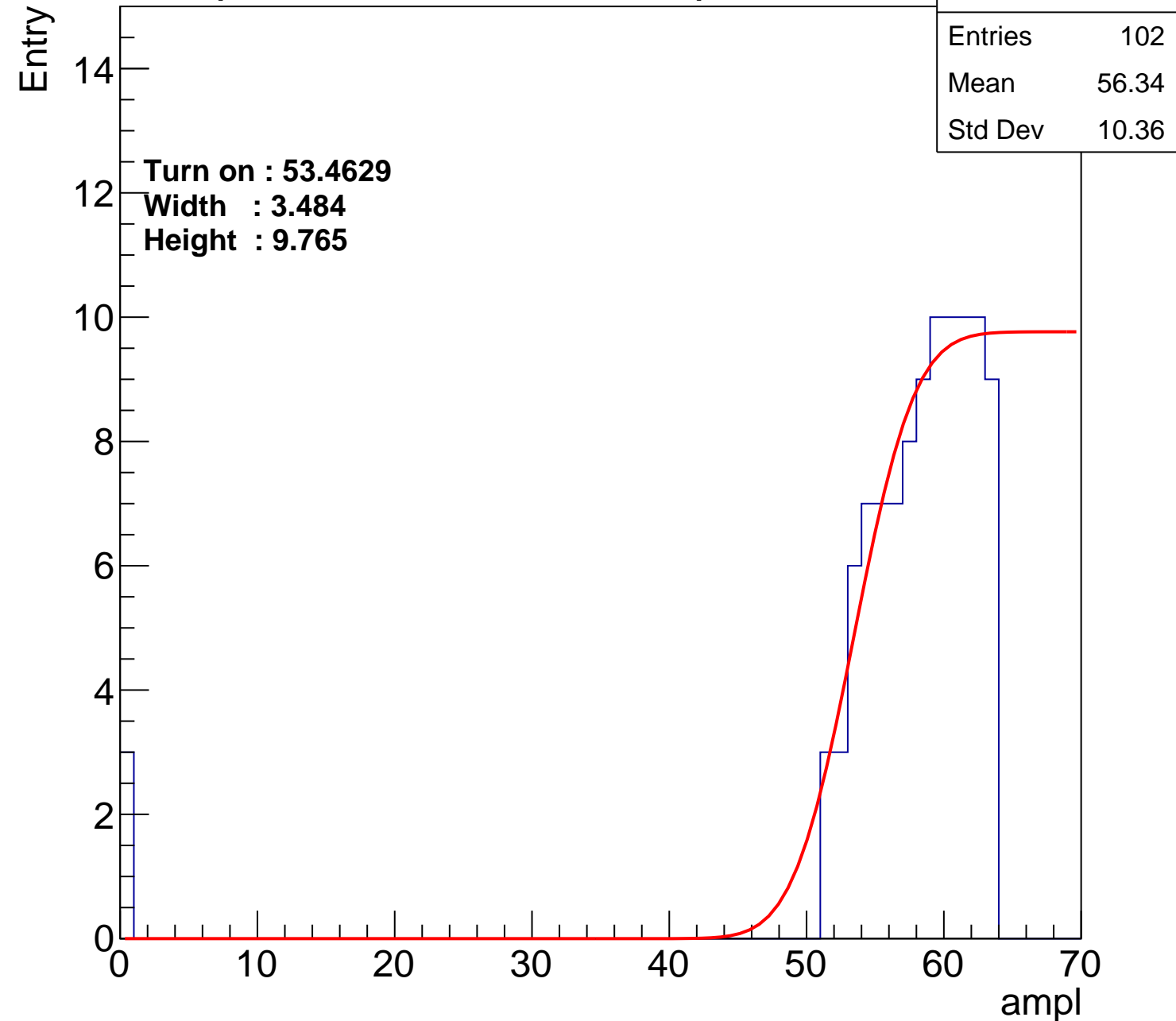
Width : 3.484

Height : 9.765

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch13

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.81
Std Dev	6.475

Turn on : 52.8221

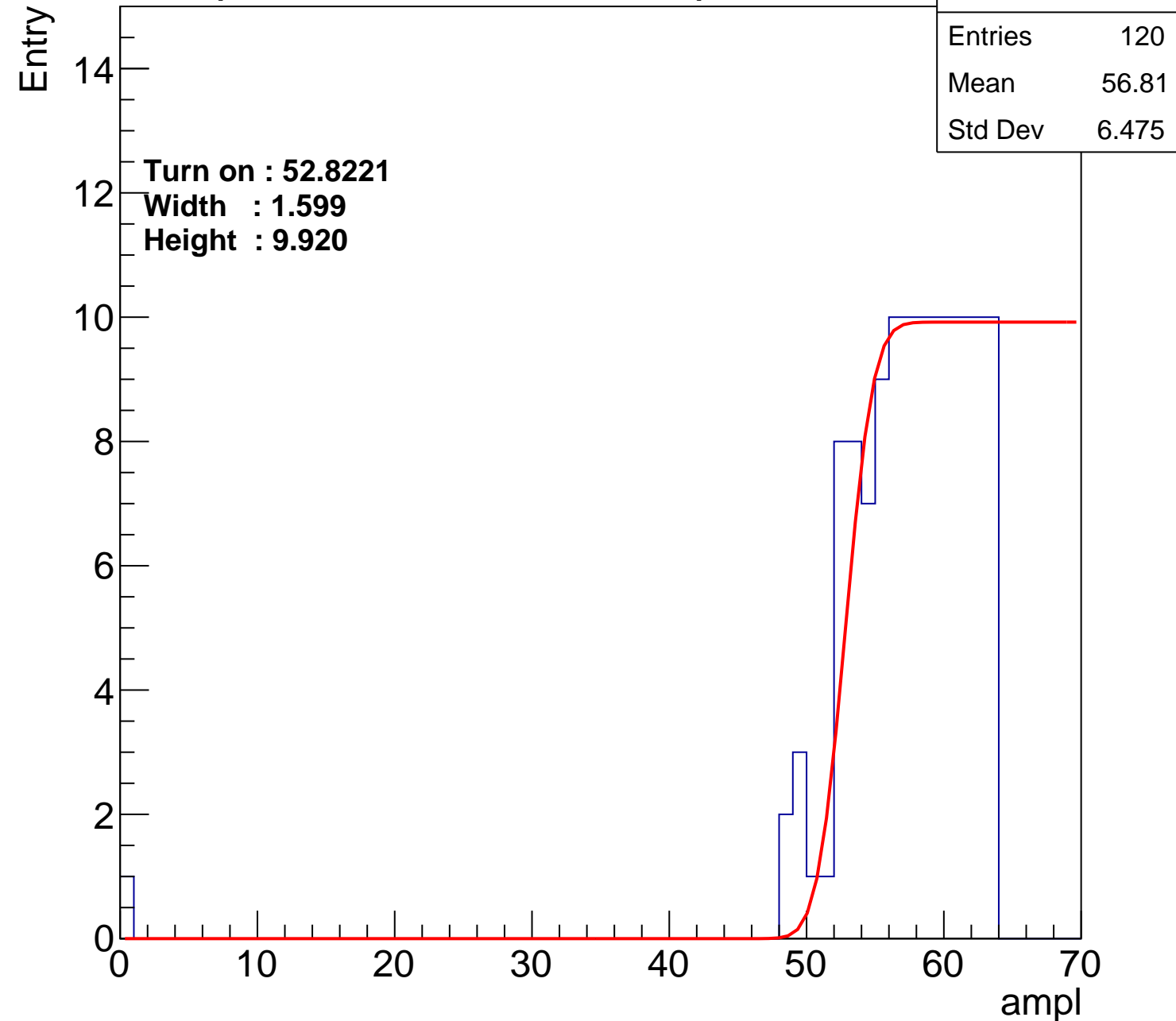
Width : 1.599

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch14

calib_packv5_040323_1717.root, FC#2, port C3

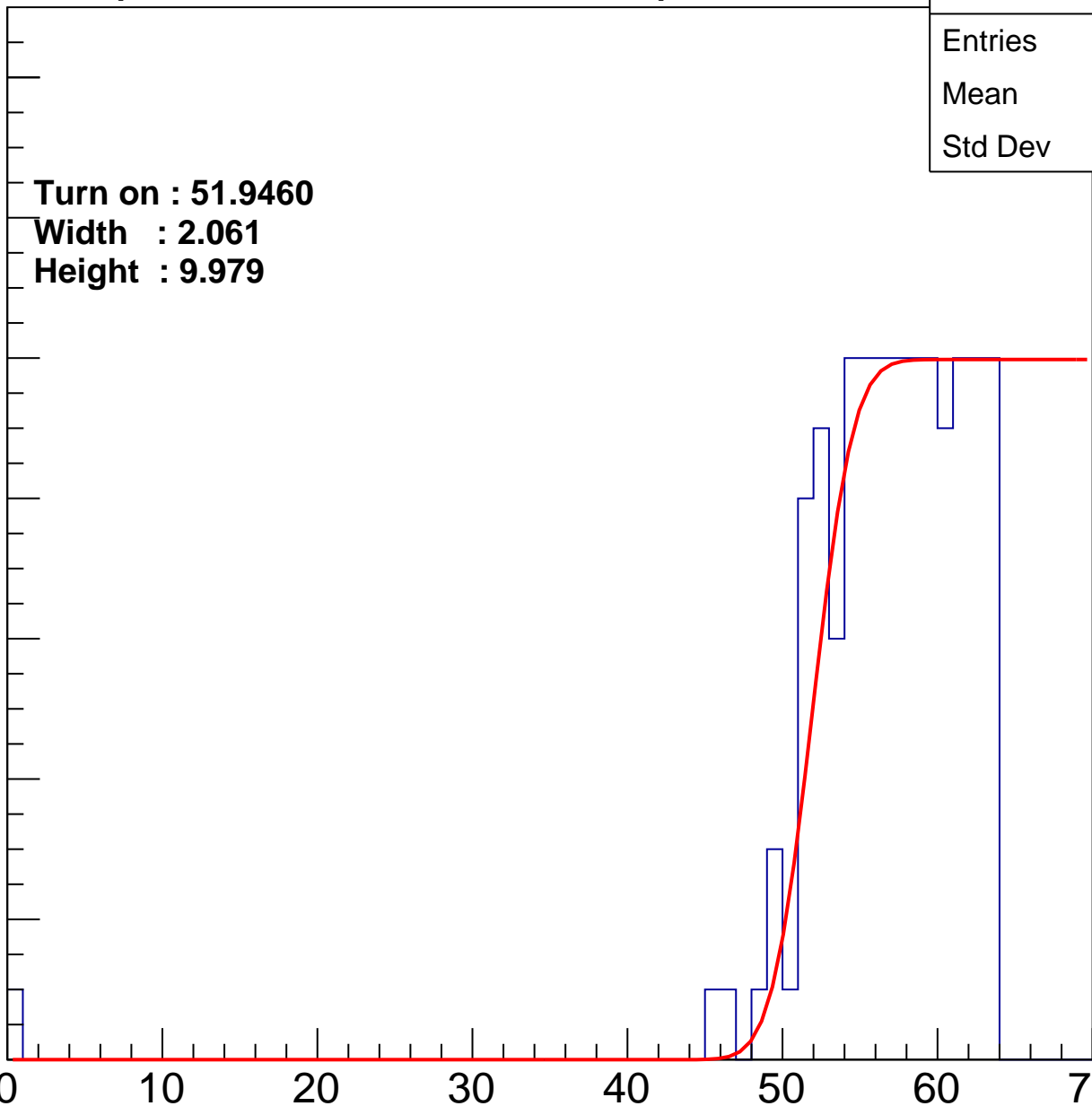
Entry

14
12
10
8
6
4
2
0

Turn on : 51.9460
Width : 2.061
Height : 9.979

Entries	130
Mean	56.31
Std Dev	6.459

ampl



B0L103S, U10-ch15

calib_packv5_040323_1717.root, FC#2, port C3

Entries	107
Mean	56.73
Std Dev	8.601

Turn on : 53.4543

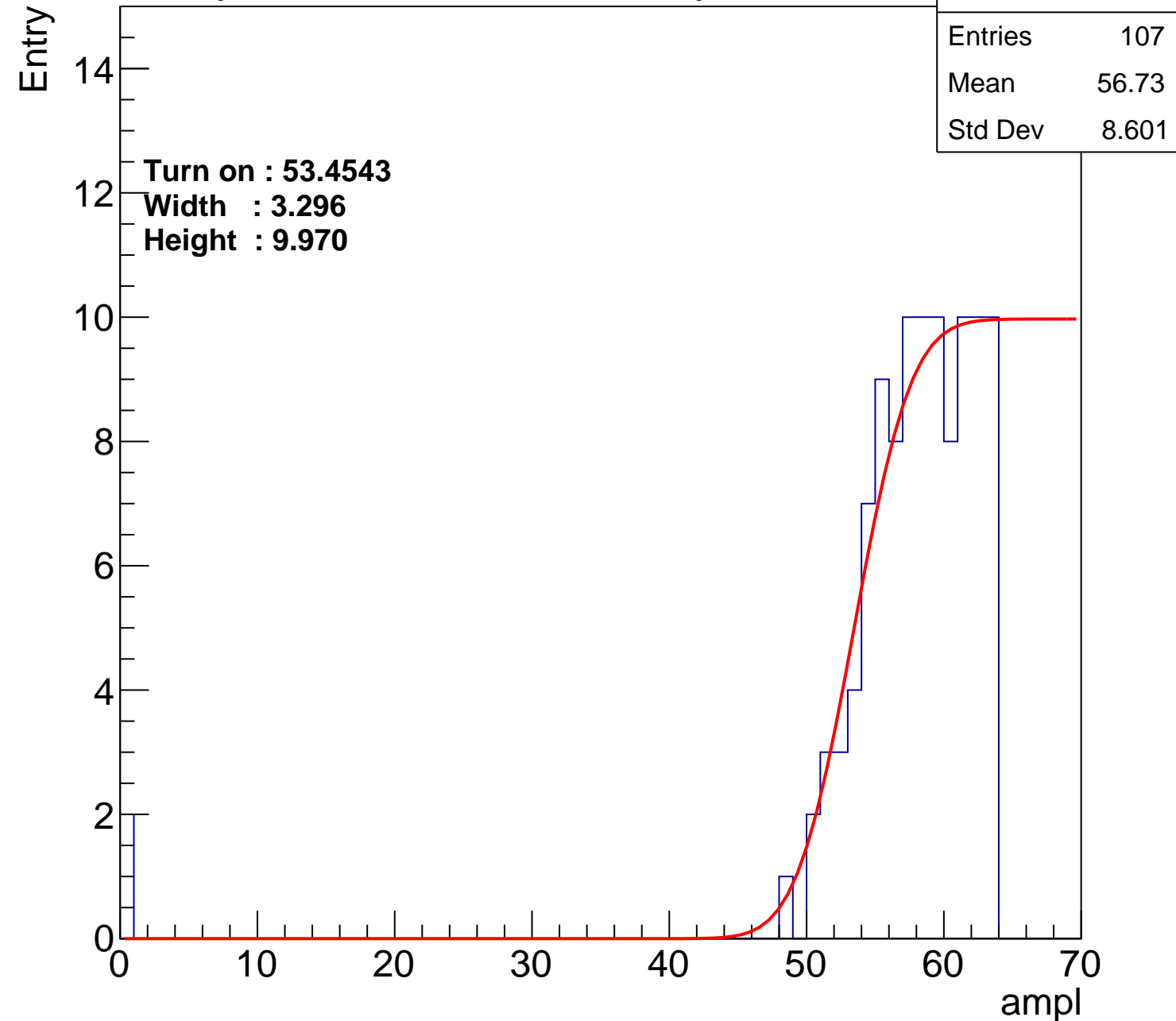
Width : 3.296

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch16

calib_packv5_040323_1717.root, FC#2, port C3

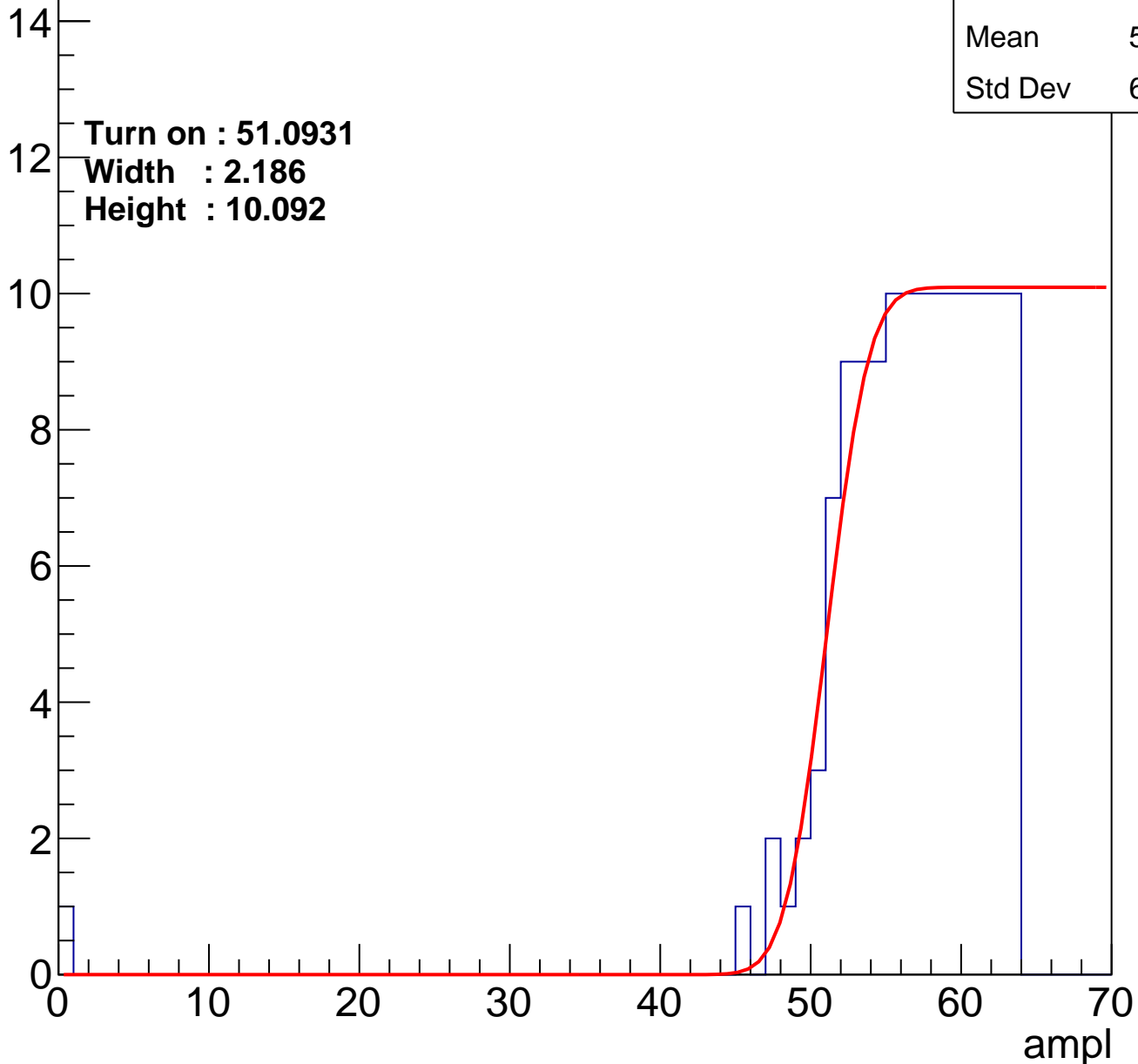
Entries	134
Mean	56.22
Std Dev	6.423

Turn on : 51.0931

Width : 2.186

Height : 10.092

Entry



B0L103S, U10-ch17

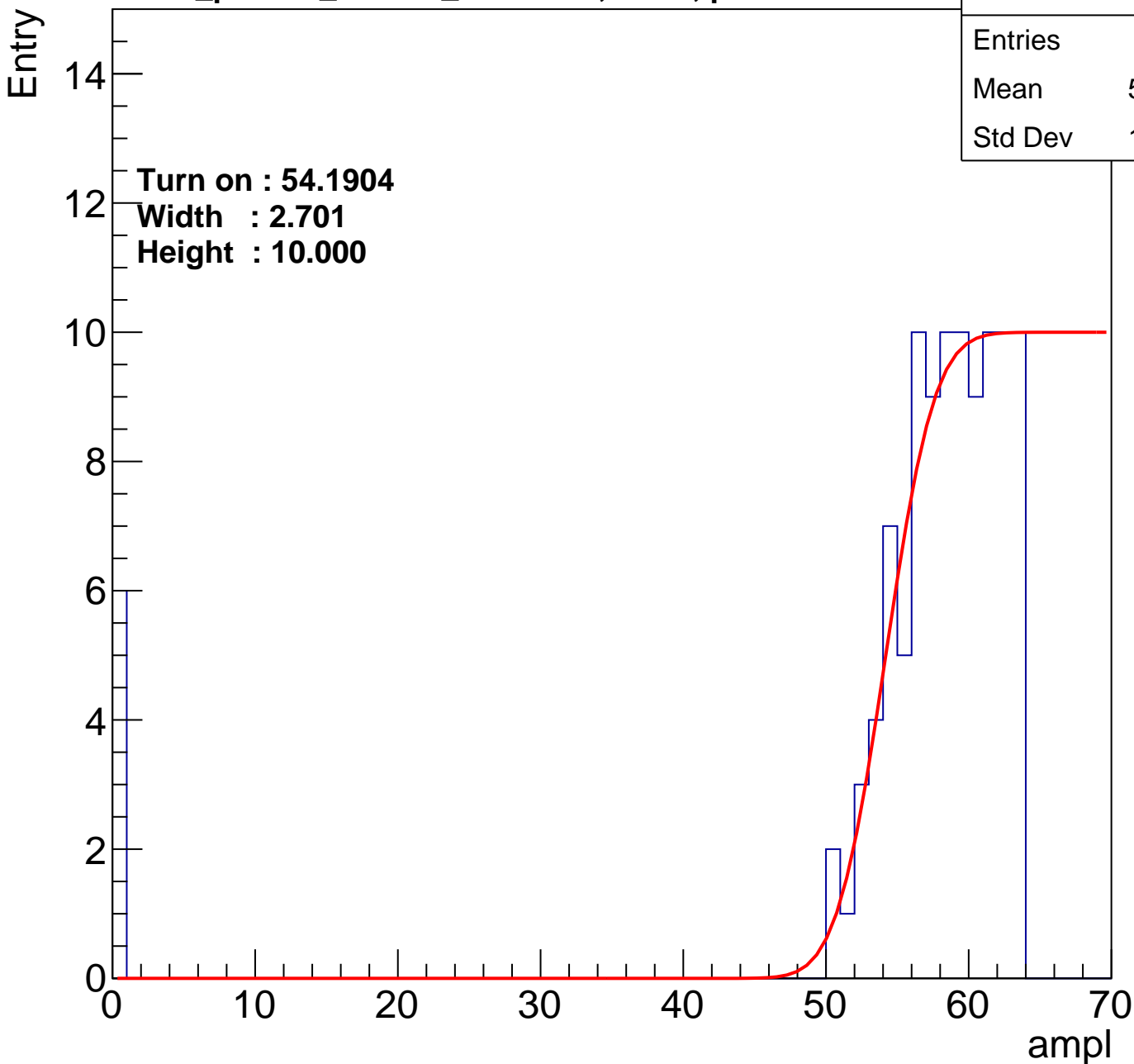
calib_packv5_040323_1717.root, FC#2, port C3

Entries	106
Mean	54.86
Std Dev	13.83

Turn on : 54.1904

Width : 2.701

Height : 10.000



B0L103S, U10-ch18

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.93
Std Dev	9.136

Turn on : 49.7408

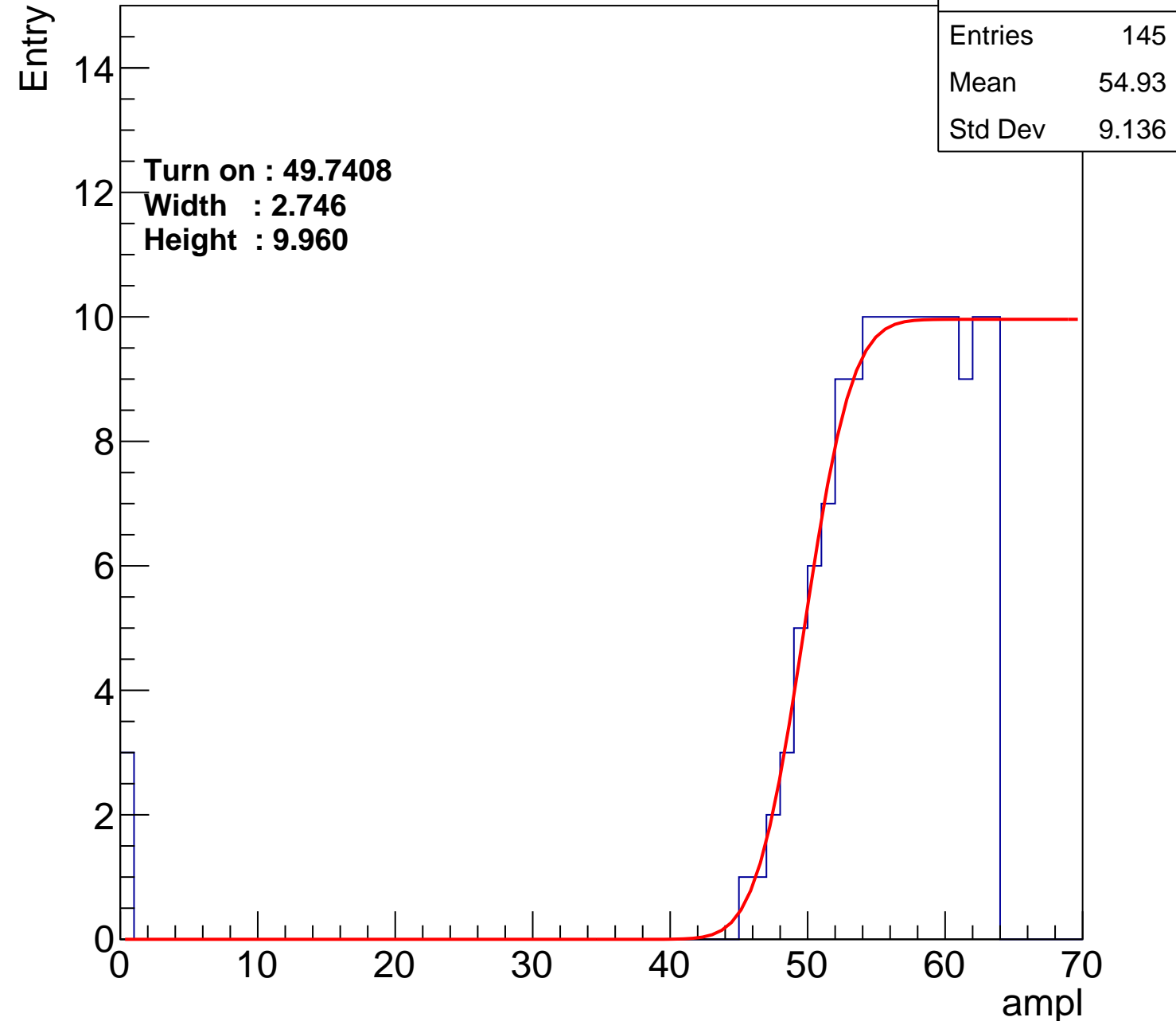
Width : 2.746

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch19

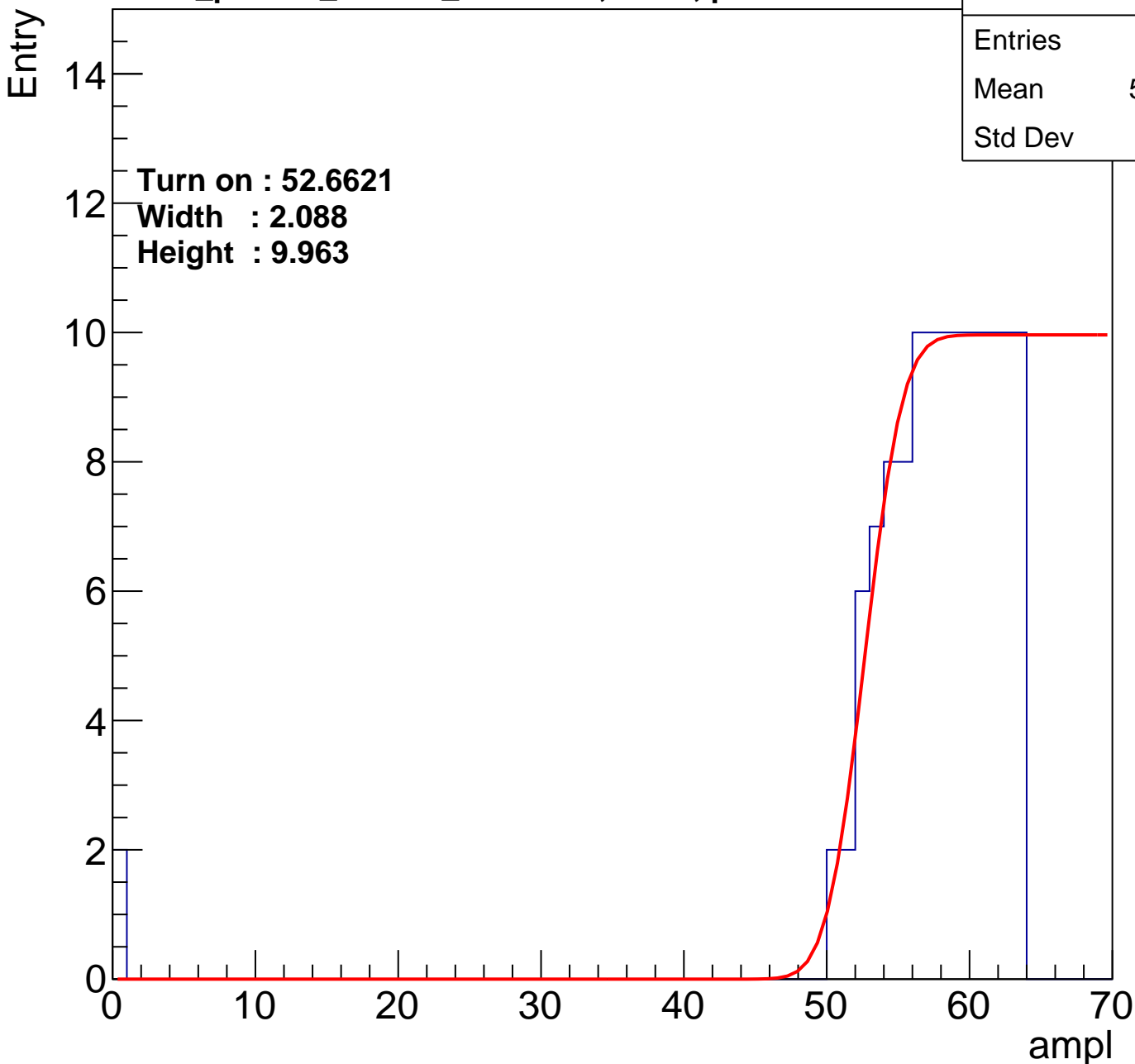
calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	56.67
Std Dev	8.31

Turn on : 52.6621

Width : 2.088

Height : 9.963



B0L103S, U10-ch20

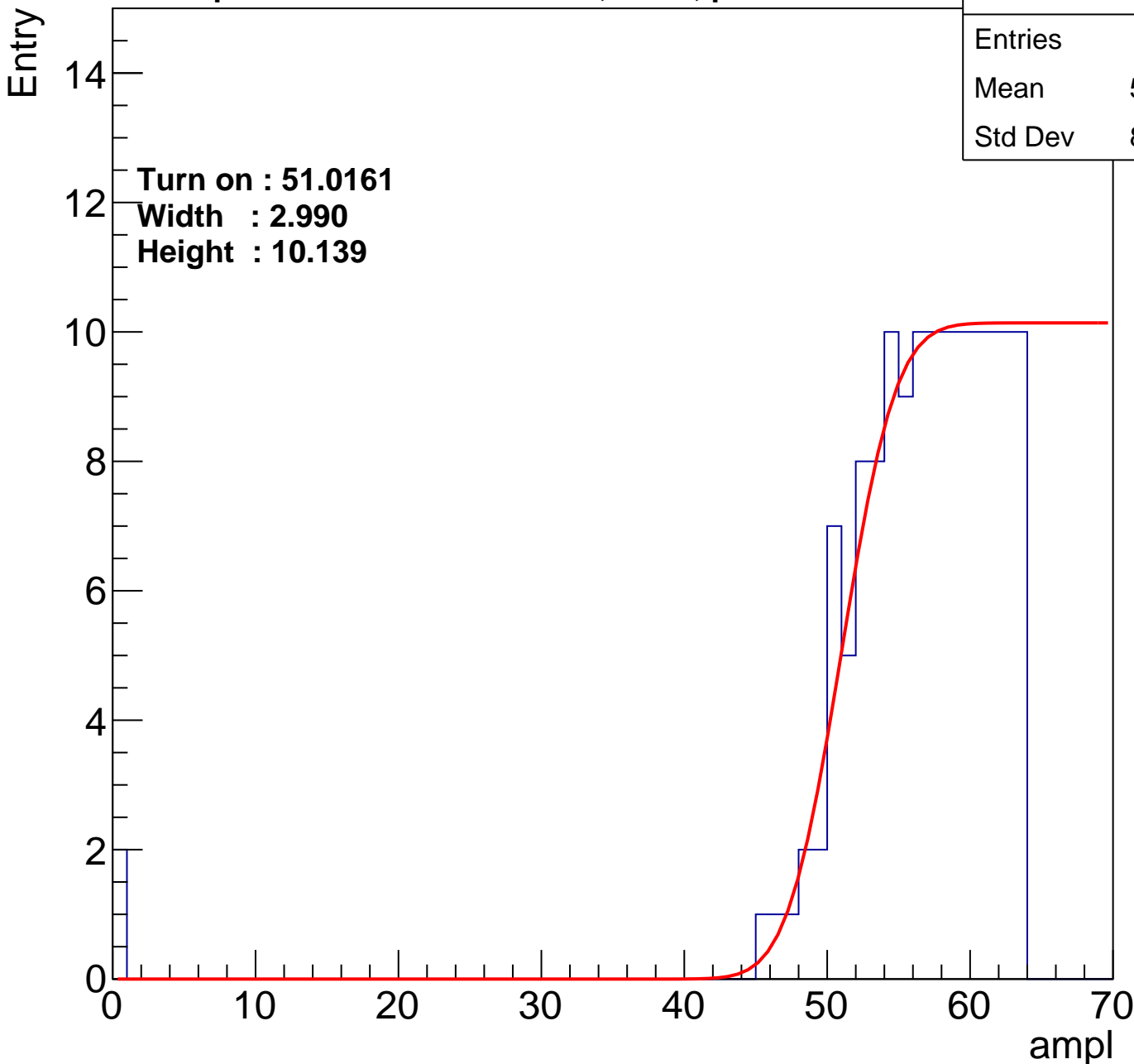
calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	55.68
Std Dev	8.049

Turn on : 51.0161

Width : 2.990

Height : 10.139



B0L103S, U10-ch21

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.74
Std Dev	6.456

Turn on : 52.4431

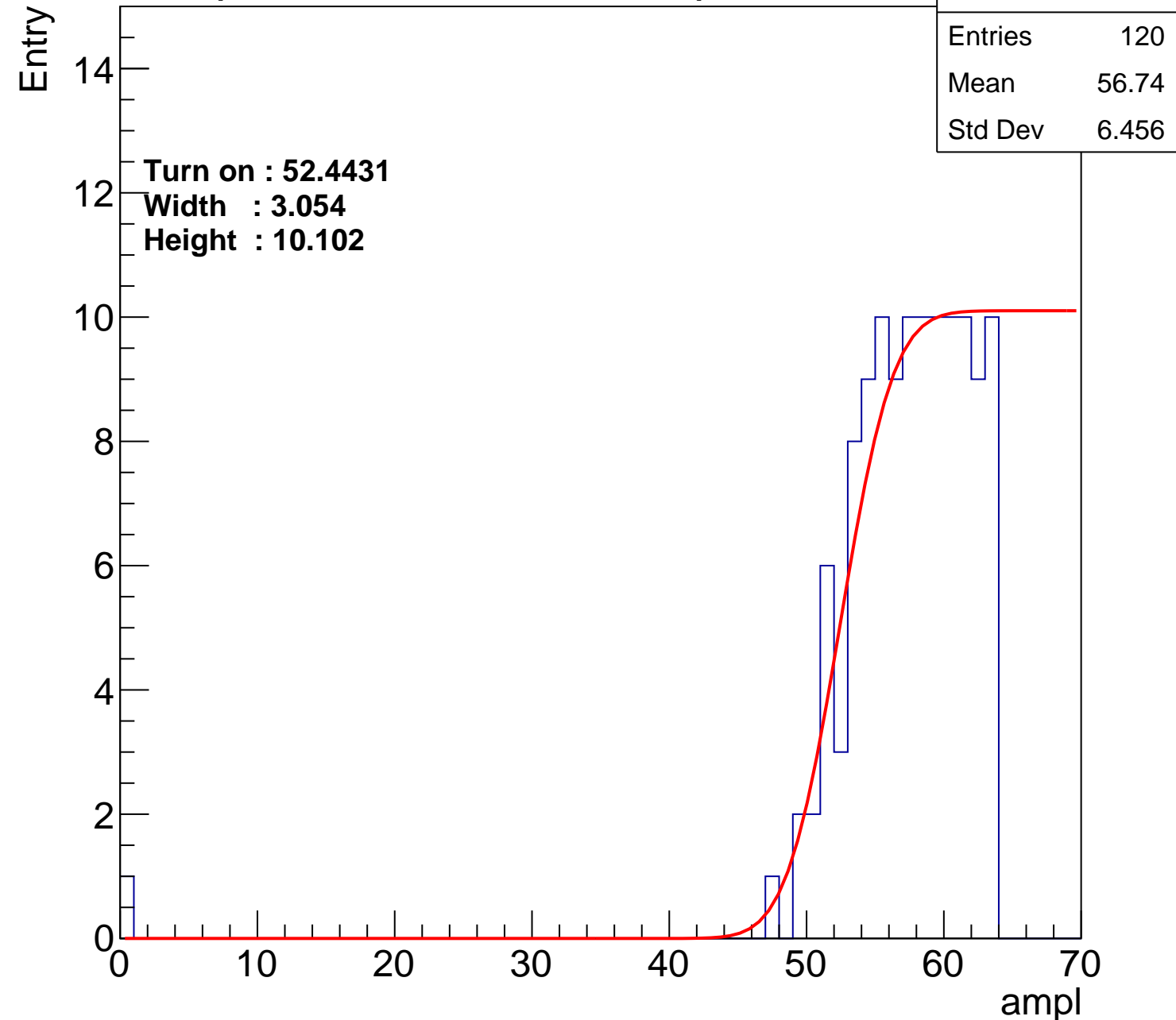
Width : 3.054

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch22

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.95
Std Dev	9.615

Turn on : 52.1863

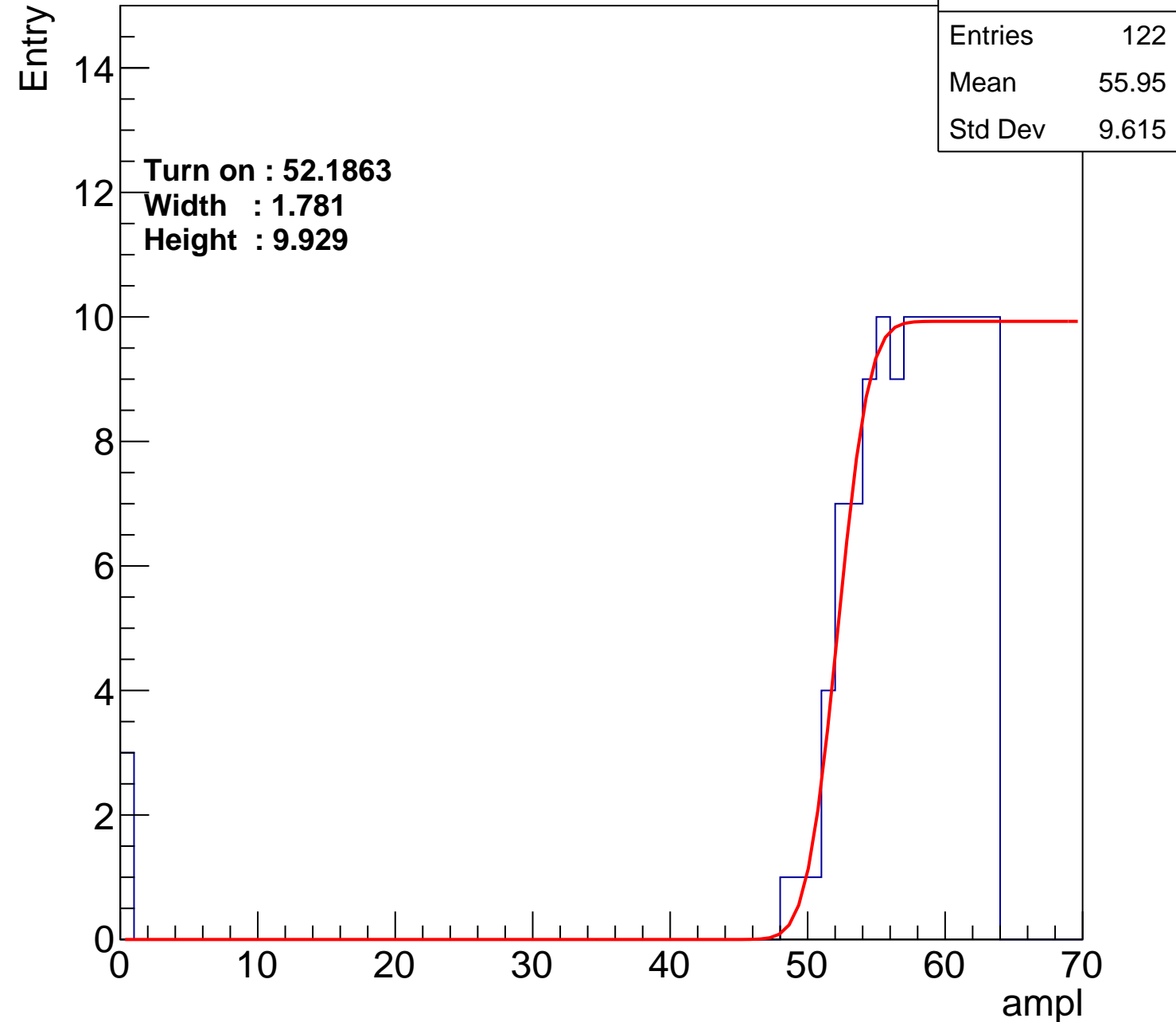
Width : 1.781

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch23

calib_packv5_040323_1717.root, FC#2, port C3

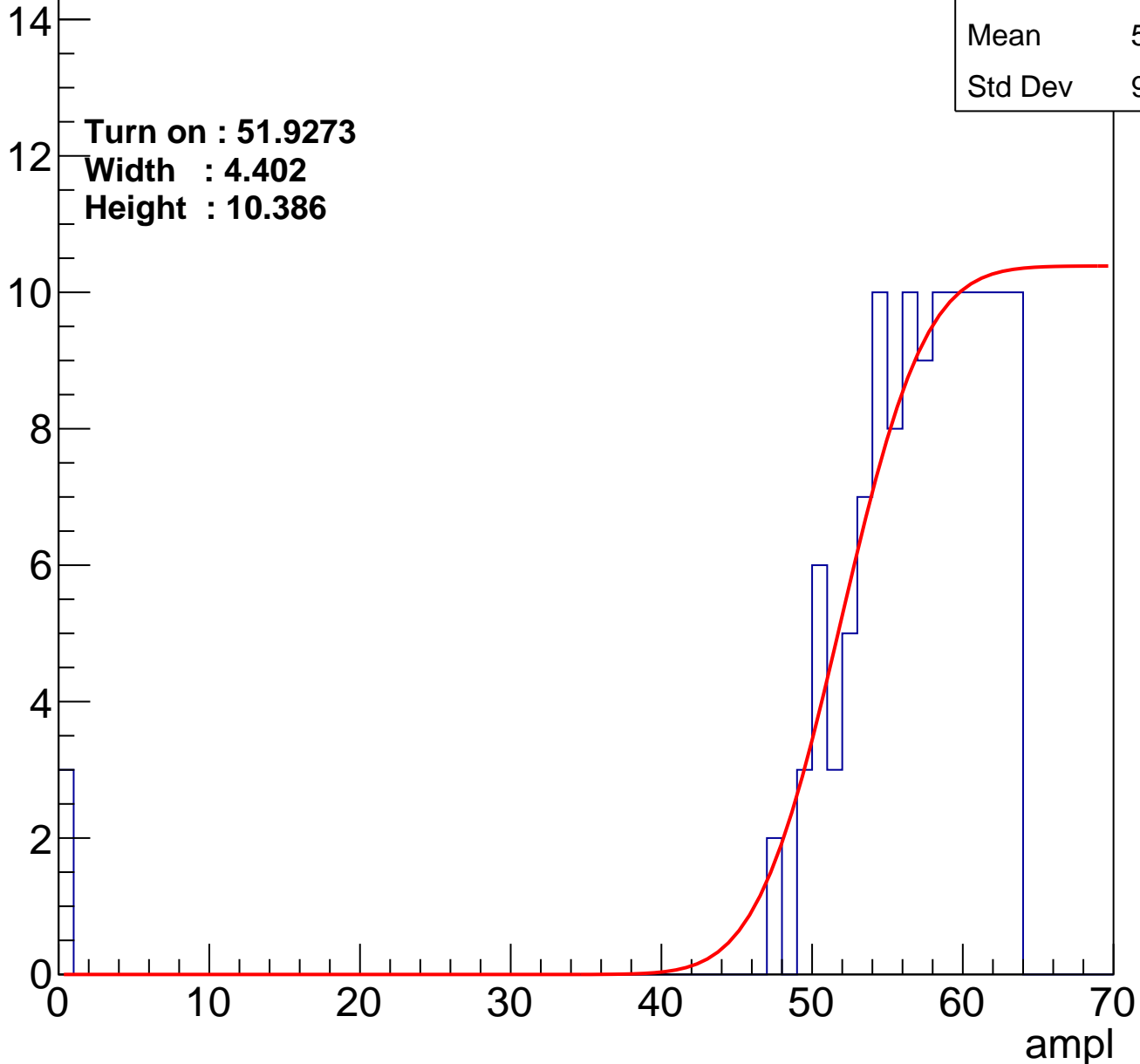
Entries	126
Mean	55.62
Std Dev	9.587

Turn on : 51.9273

Width : 4.402

Height : 10.386

Entry



B0L103S, U10-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.77
Std Dev	10.22

Turn on : 50.2375

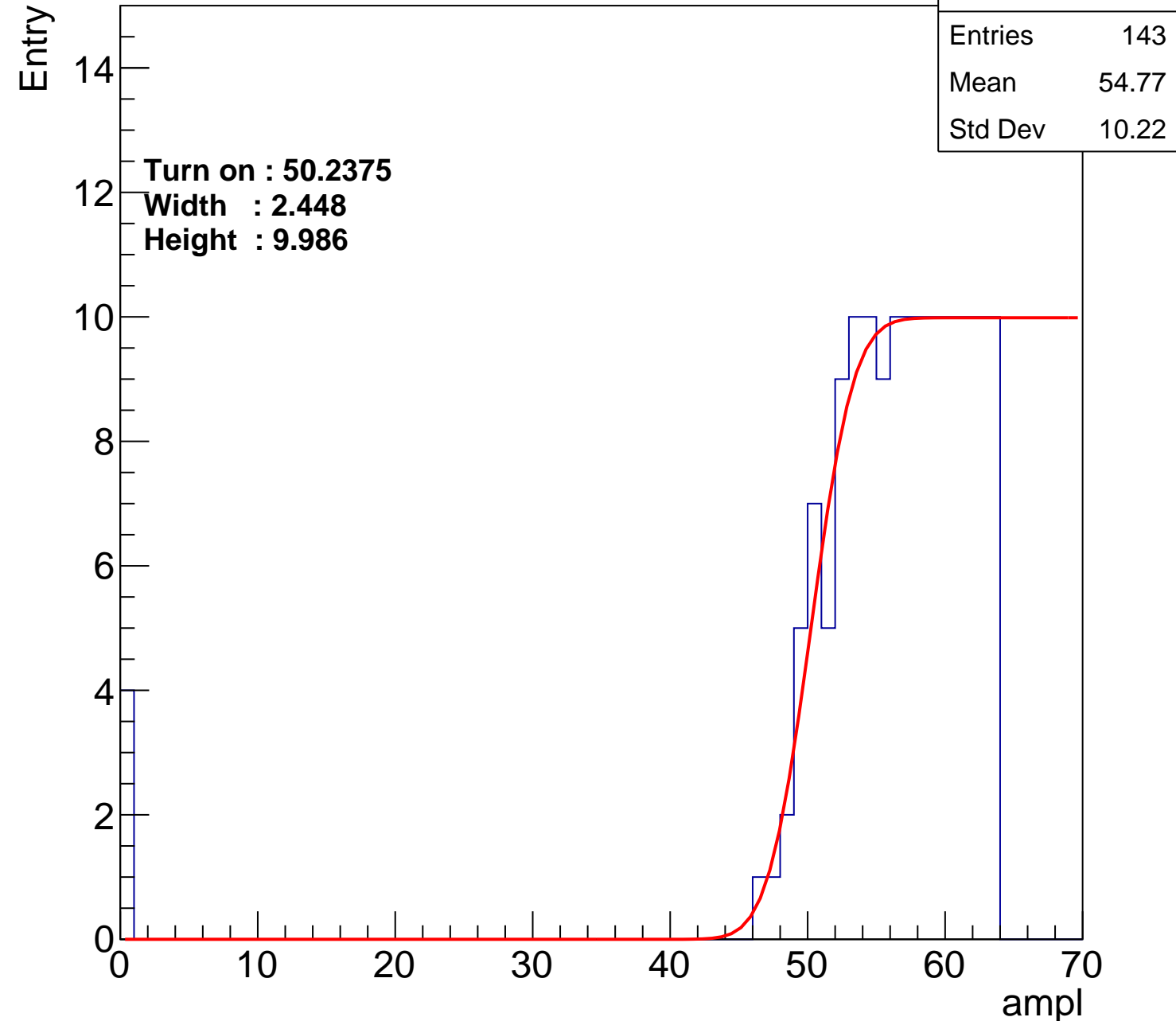
Width : 2.448

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch25

calib_packv5_040323_1717.root, FC#2, port C3

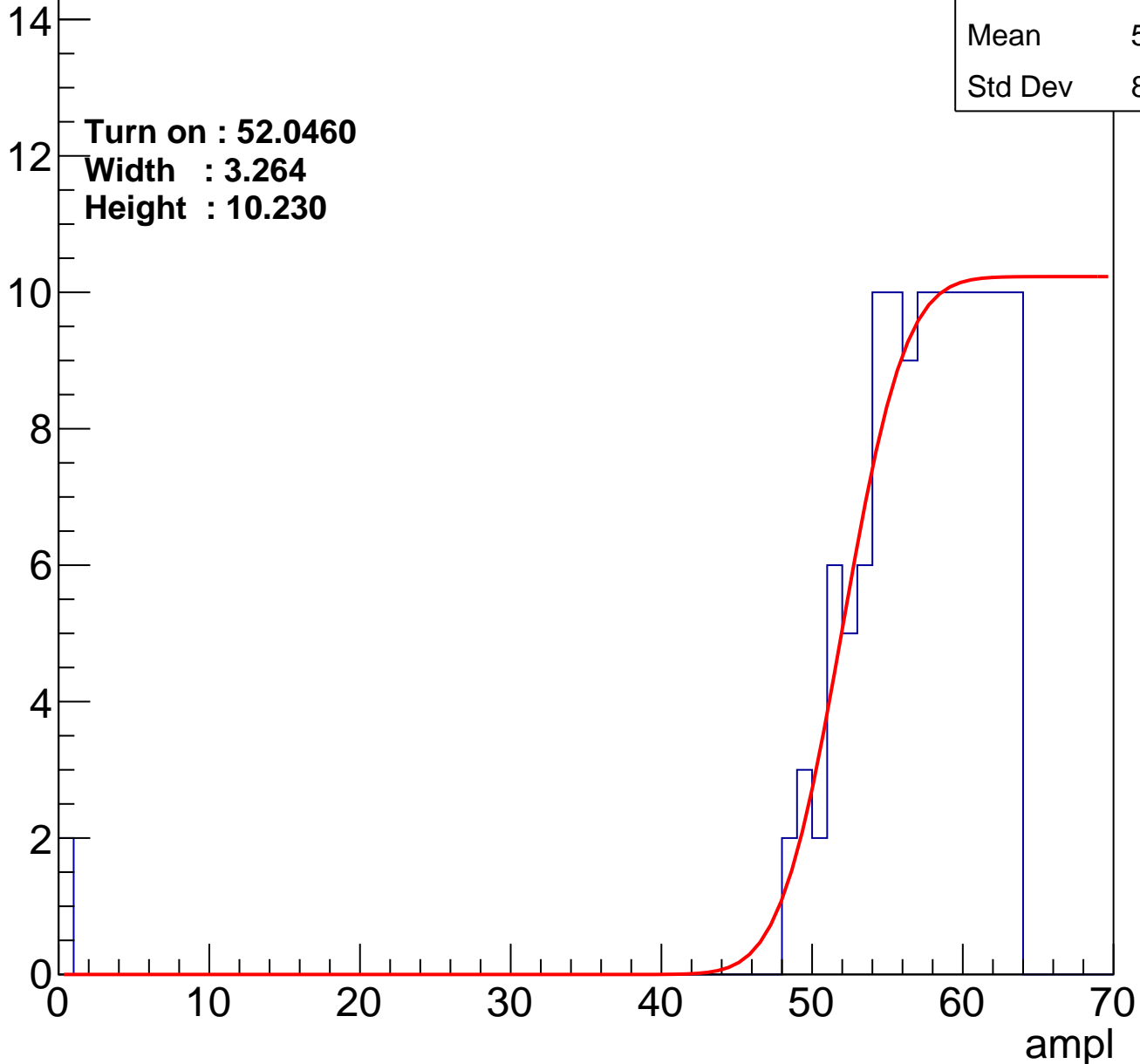
Entries	125
Mean	56.17
Std Dev	8.173

Turn on : 52.0460

Width : 3.264

Height : 10.230

Entry



B0L103S, U10-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.33
Std Dev	9.38

Turn on : 50.3157

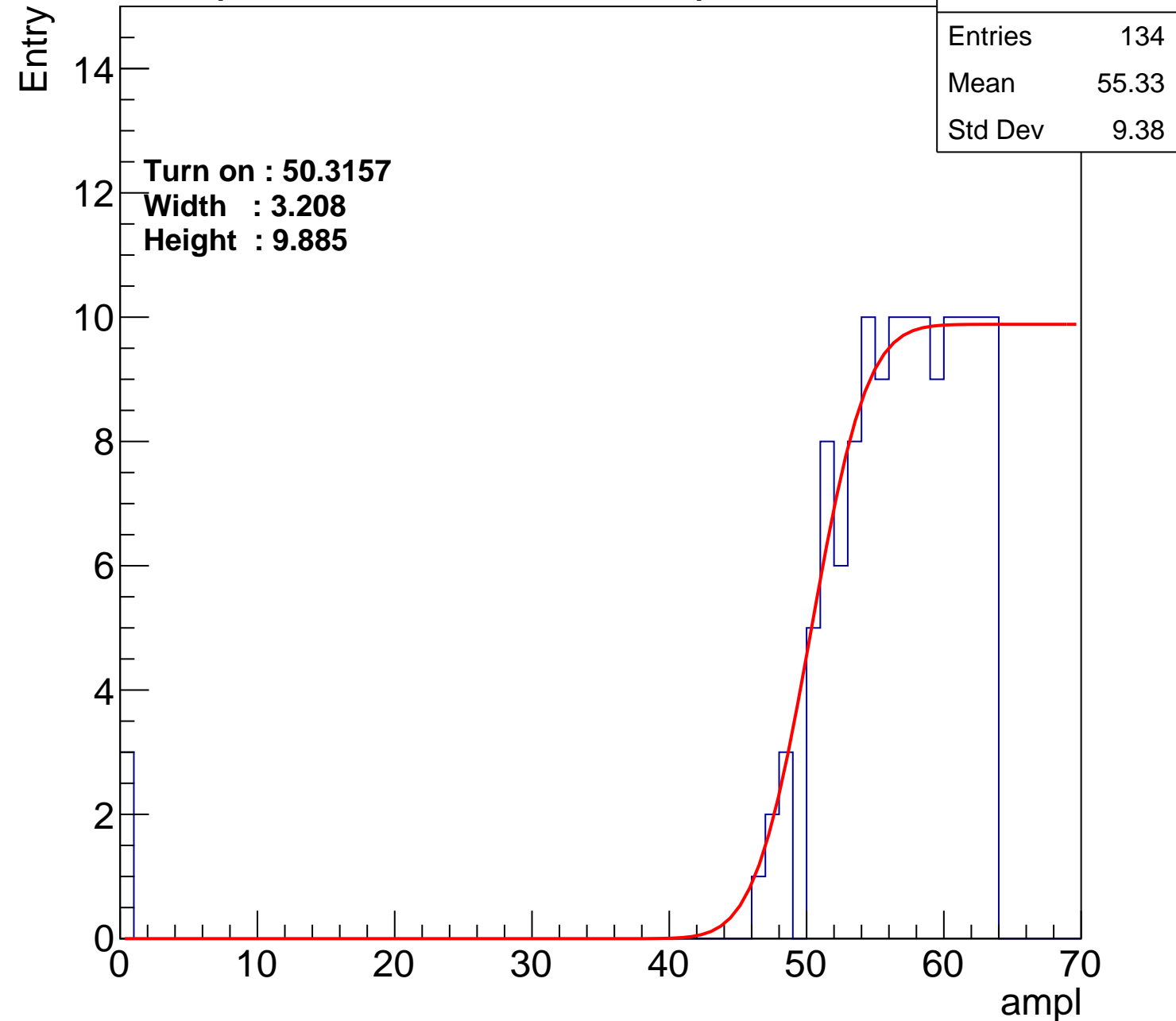
Width : 3.208

Height : 9.885

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	104
Mean	56.99
Std Dev	8.673

Turn on : 54.1894

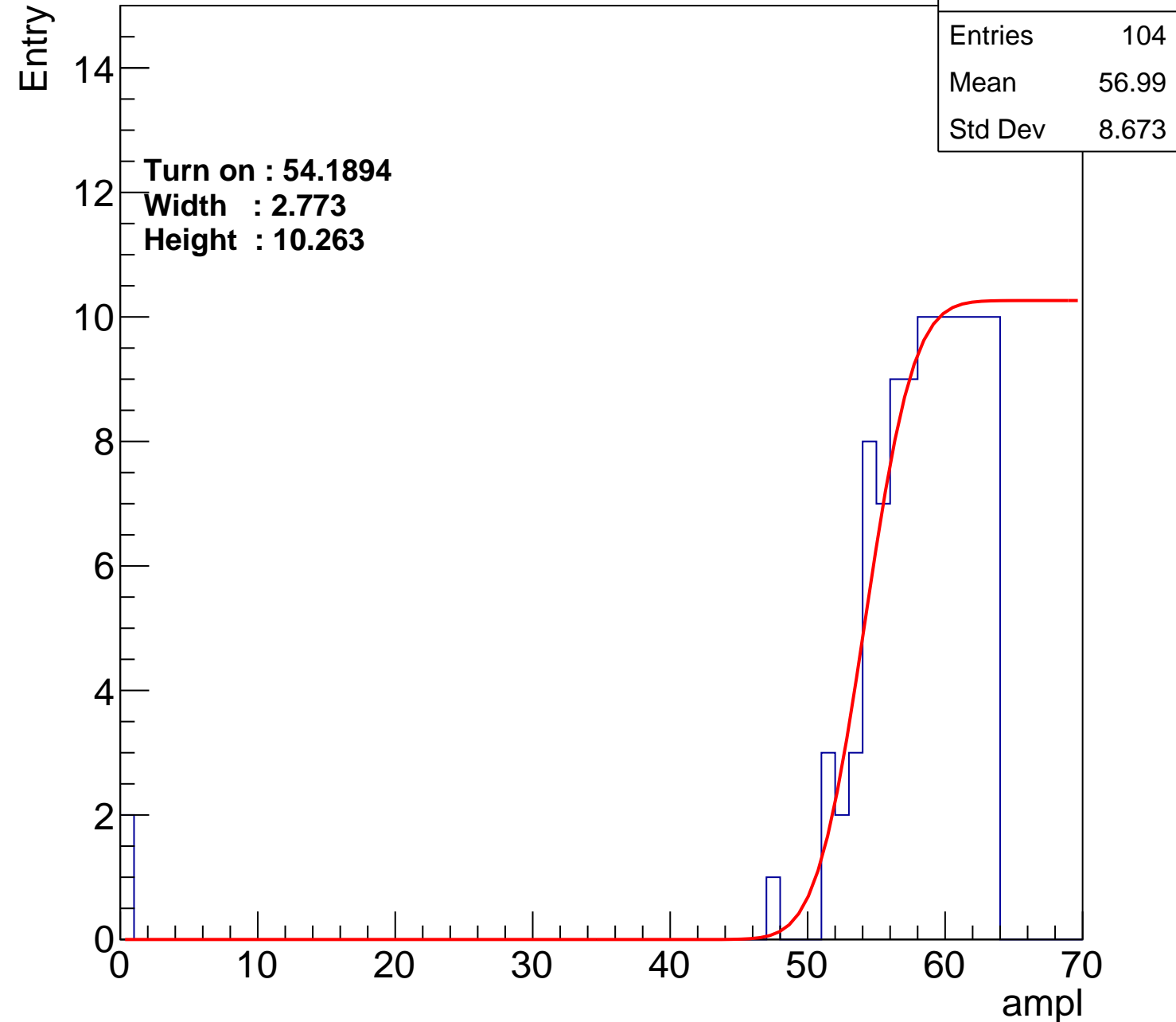
Width : 2.773

Height : 10.263

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch28

calib_packv5_040323_1717.root, FC#2, port C3

Entries	152
Mean	52.97
Std Dev	13.27

Turn on : 49.6837

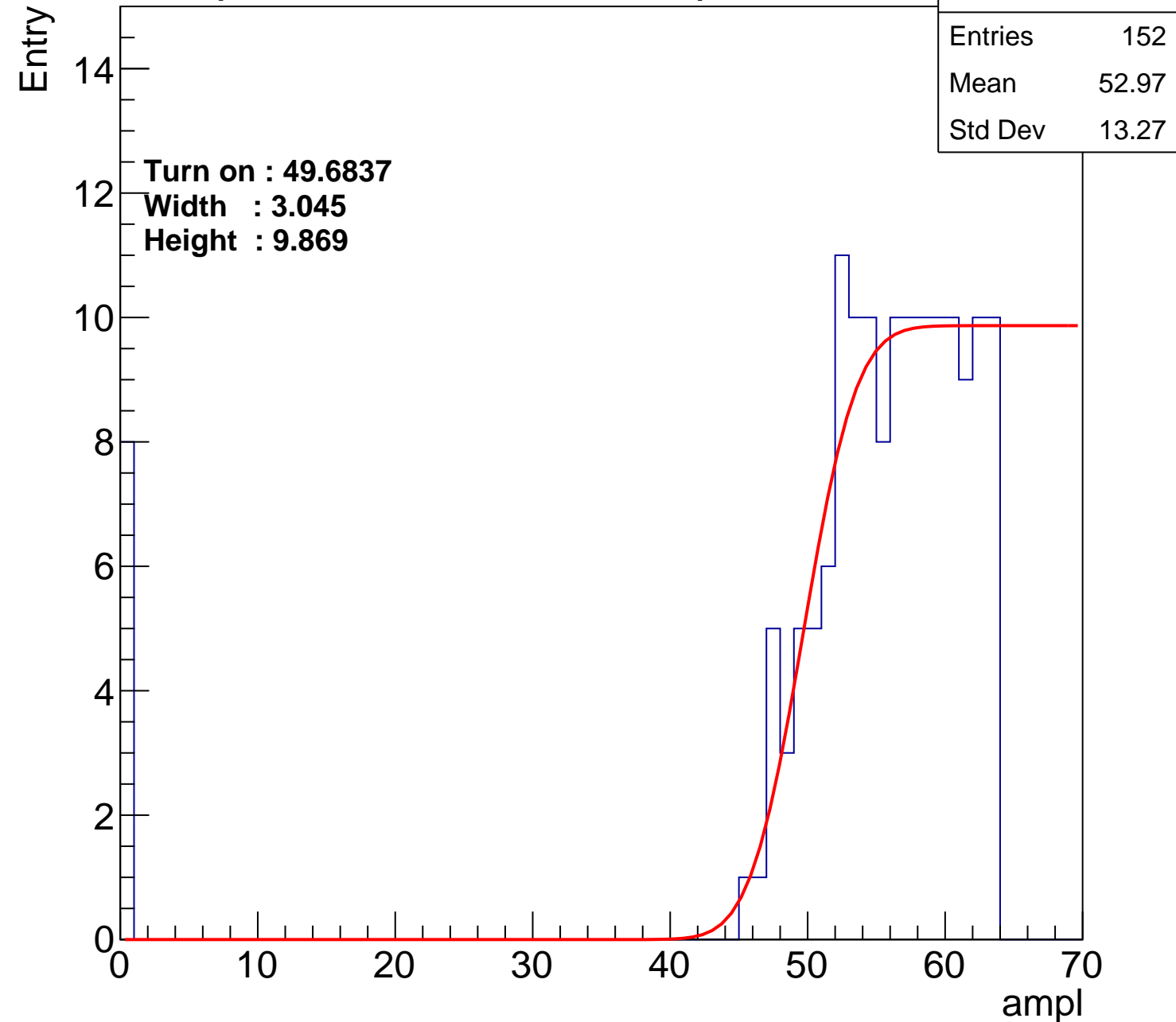
Width : 3.045

Height : 9.869

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch29

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	54.84
Std Dev	11.7

Turn on : 52.4515

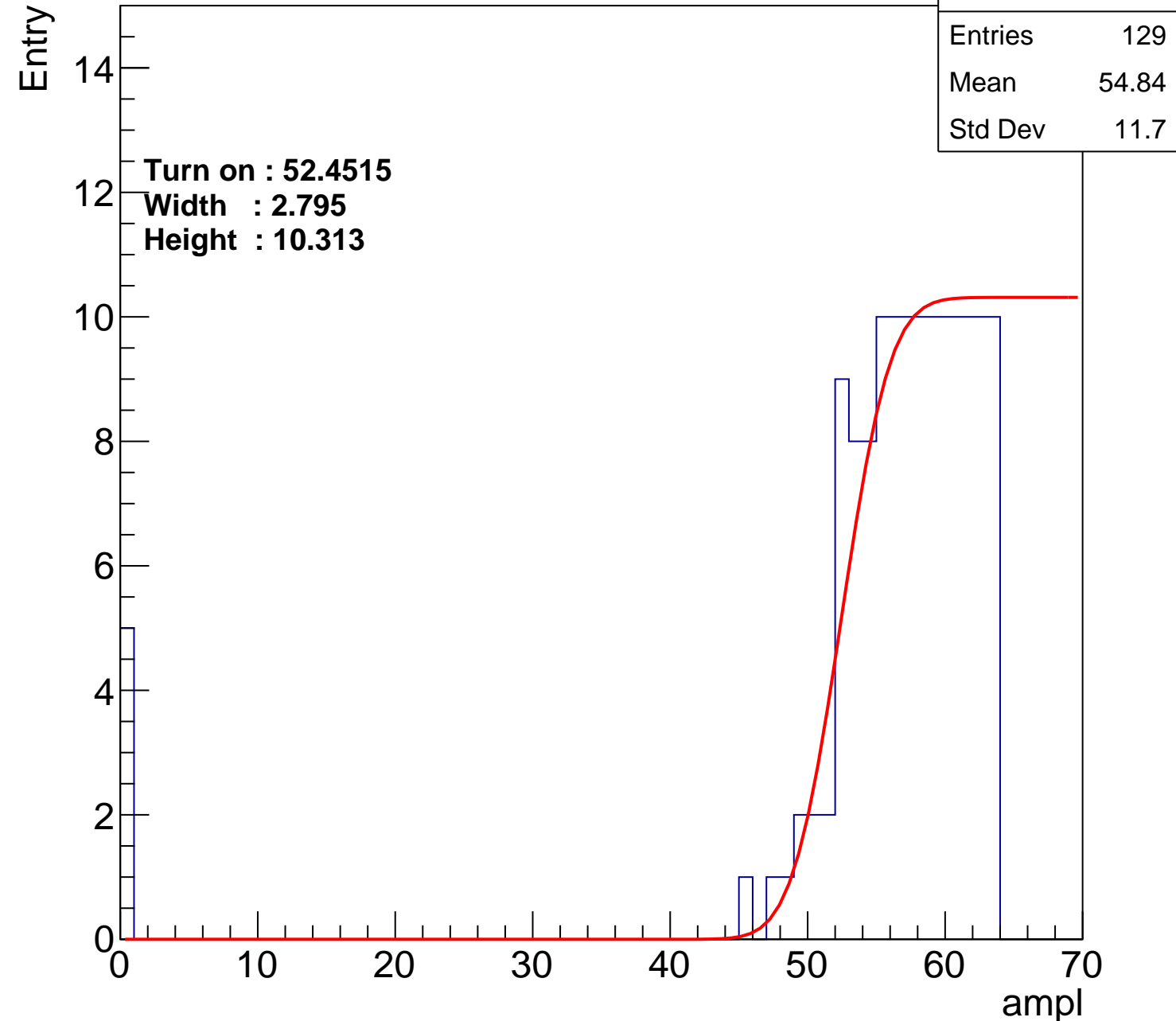
Width : 2.795

Height : 10.313

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch30

calib_packv5_040323_1717.root, FC#2, port C3

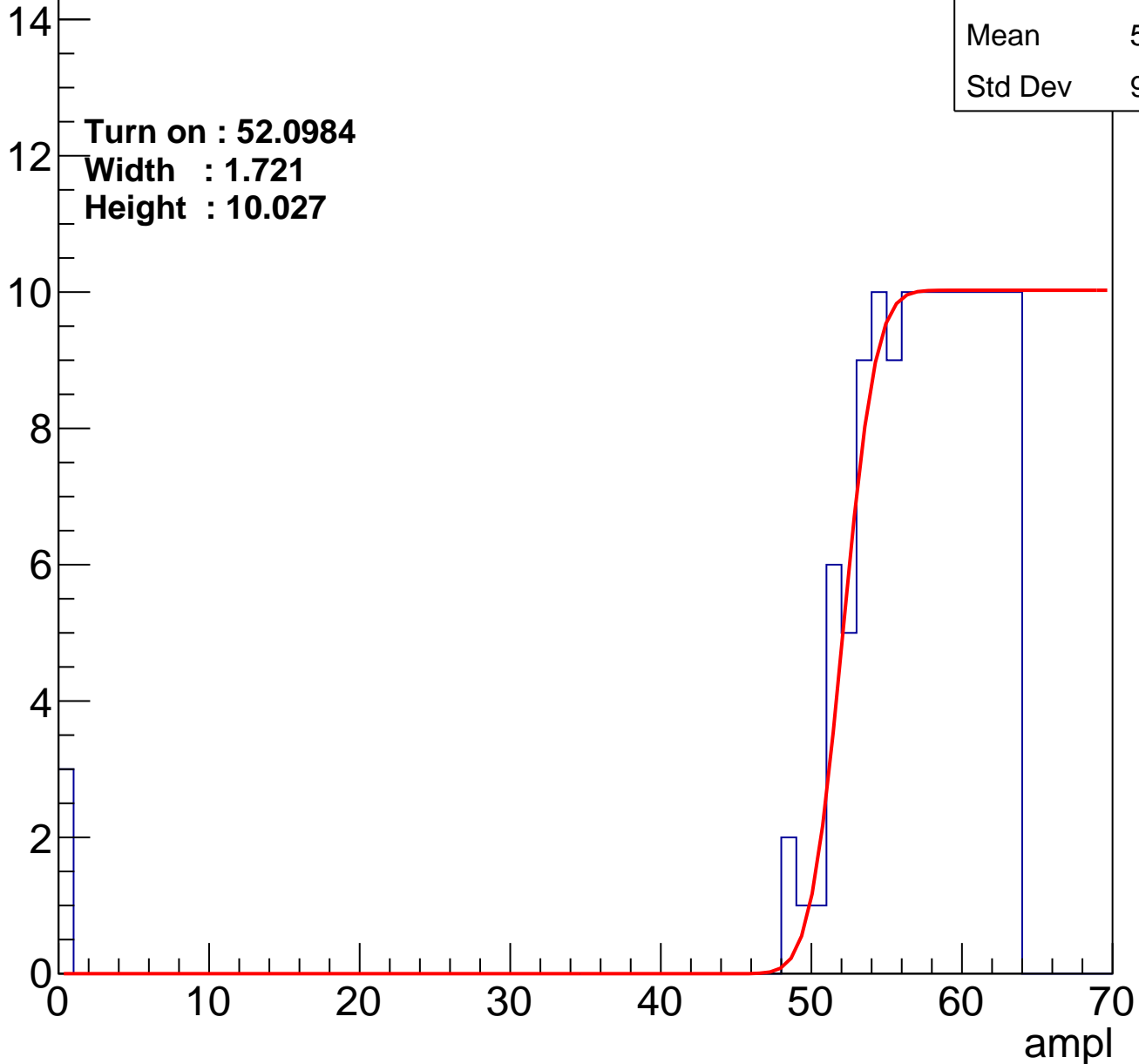
Entries	126
Mean	55.82
Std Dev	9.502

Turn on : 52.0984

Width : 1.721

Height : 10.027

Entry



B0L103S, U10-ch31

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.92
Std Dev	8.133

Turn on : 51.5600

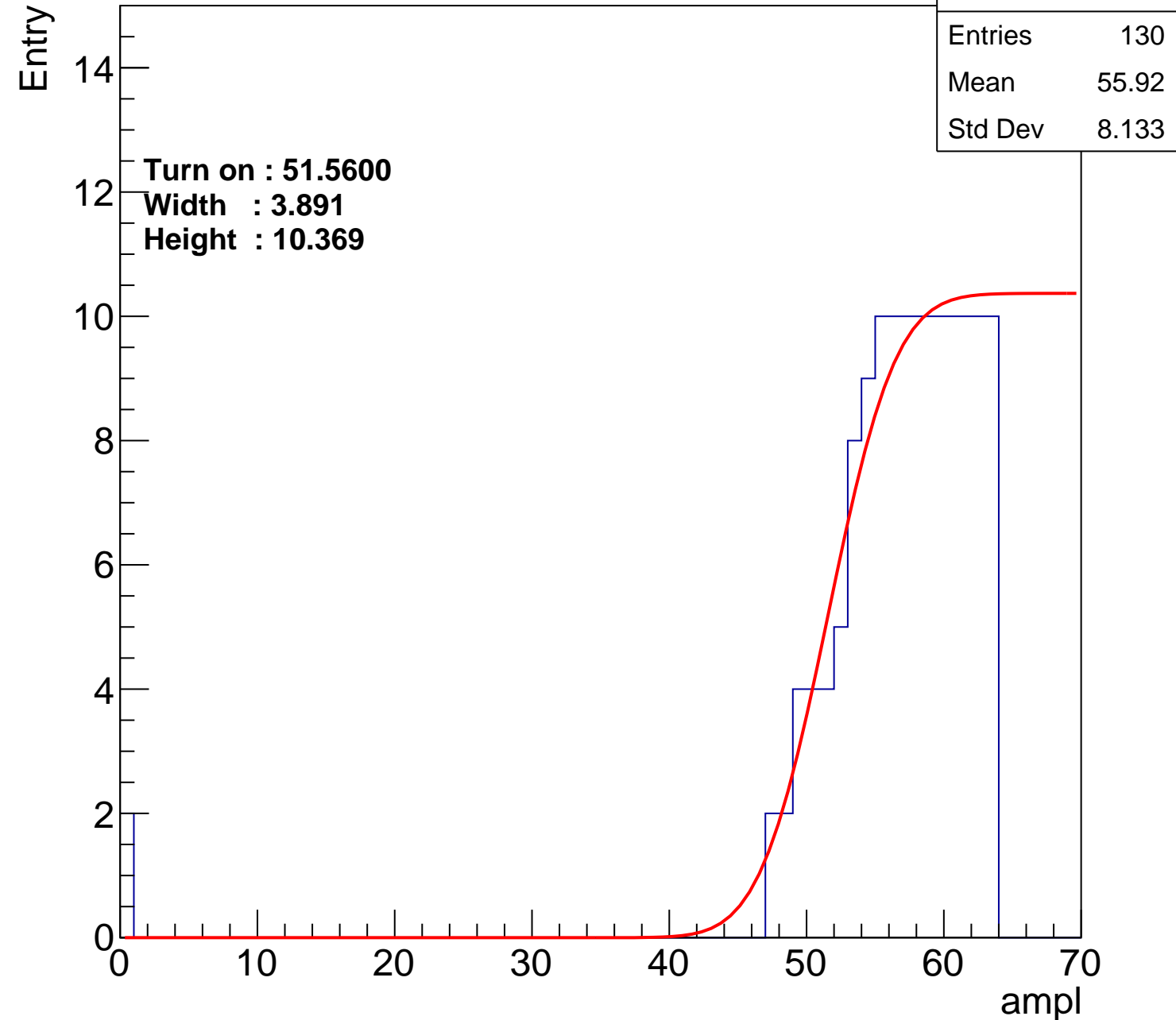
Width : 3.891

Height : 10.369

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	55.28
Std Dev	9.421

Turn on : 50.8250

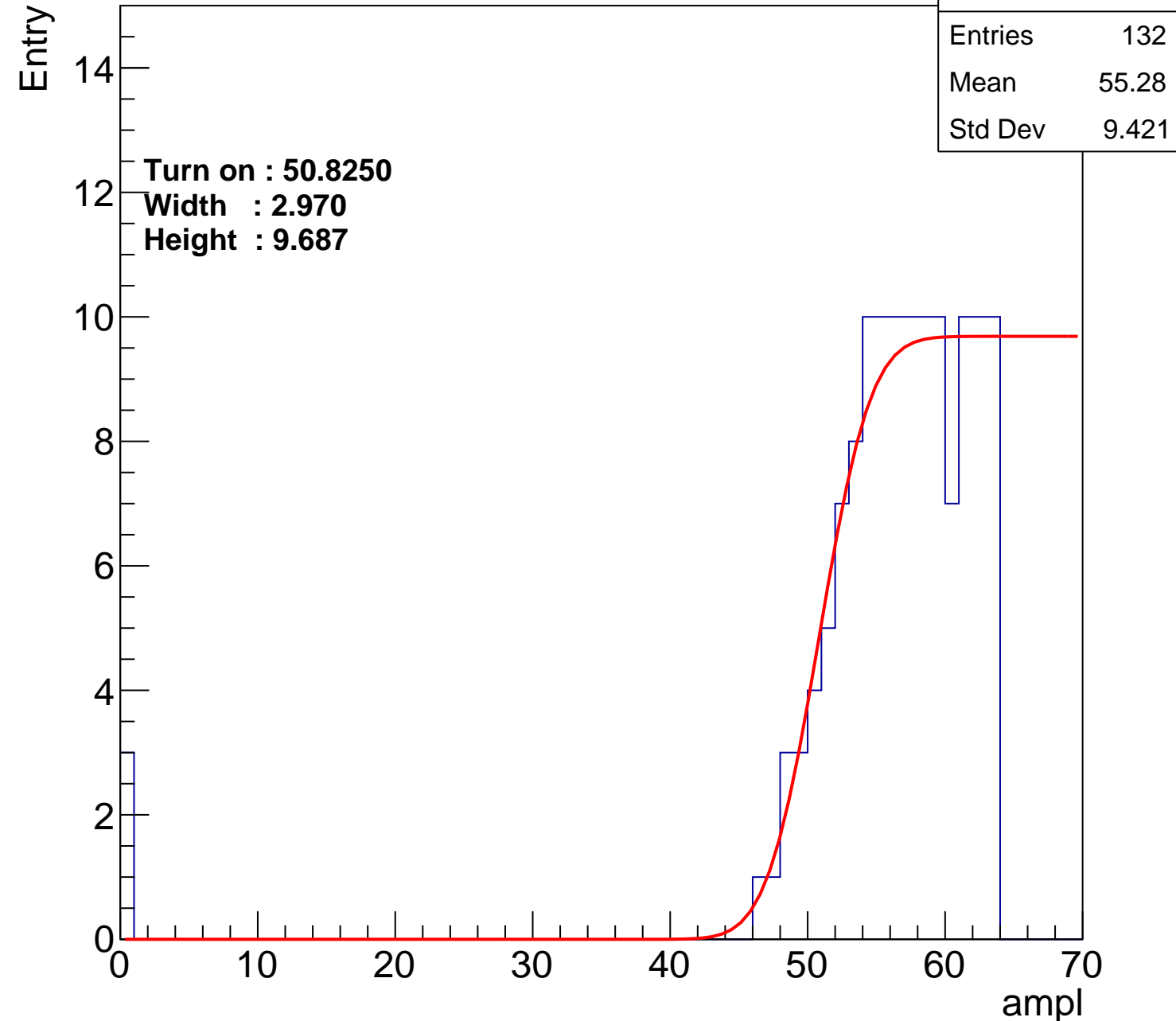
Width : 2.970

Height : 9.687

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.54
Std Dev	11.55

Turn on : 51.3006

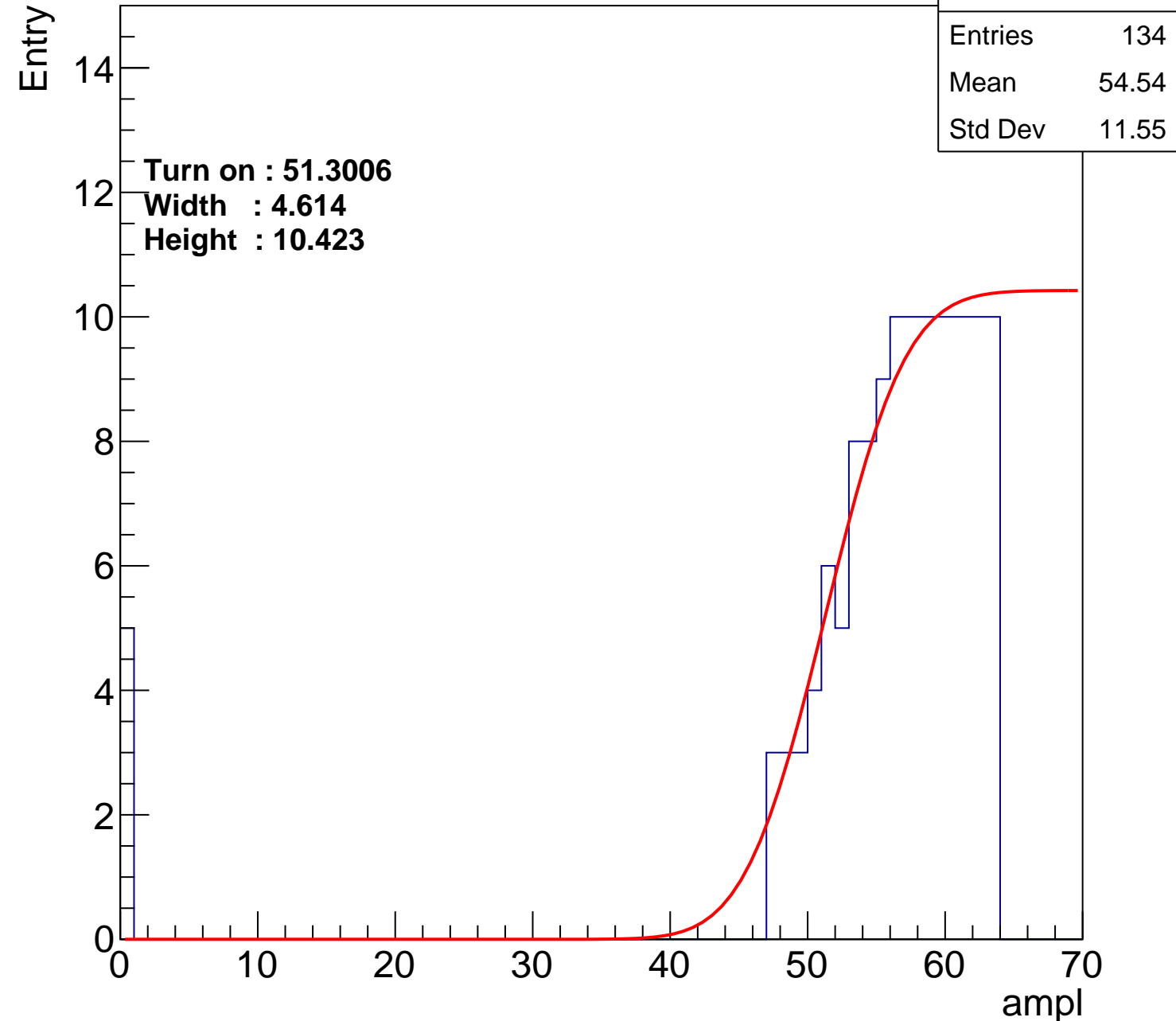
Width : 4.614

Height : 10.423

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.44
Std Dev	8.32

Turn on : 52.6417

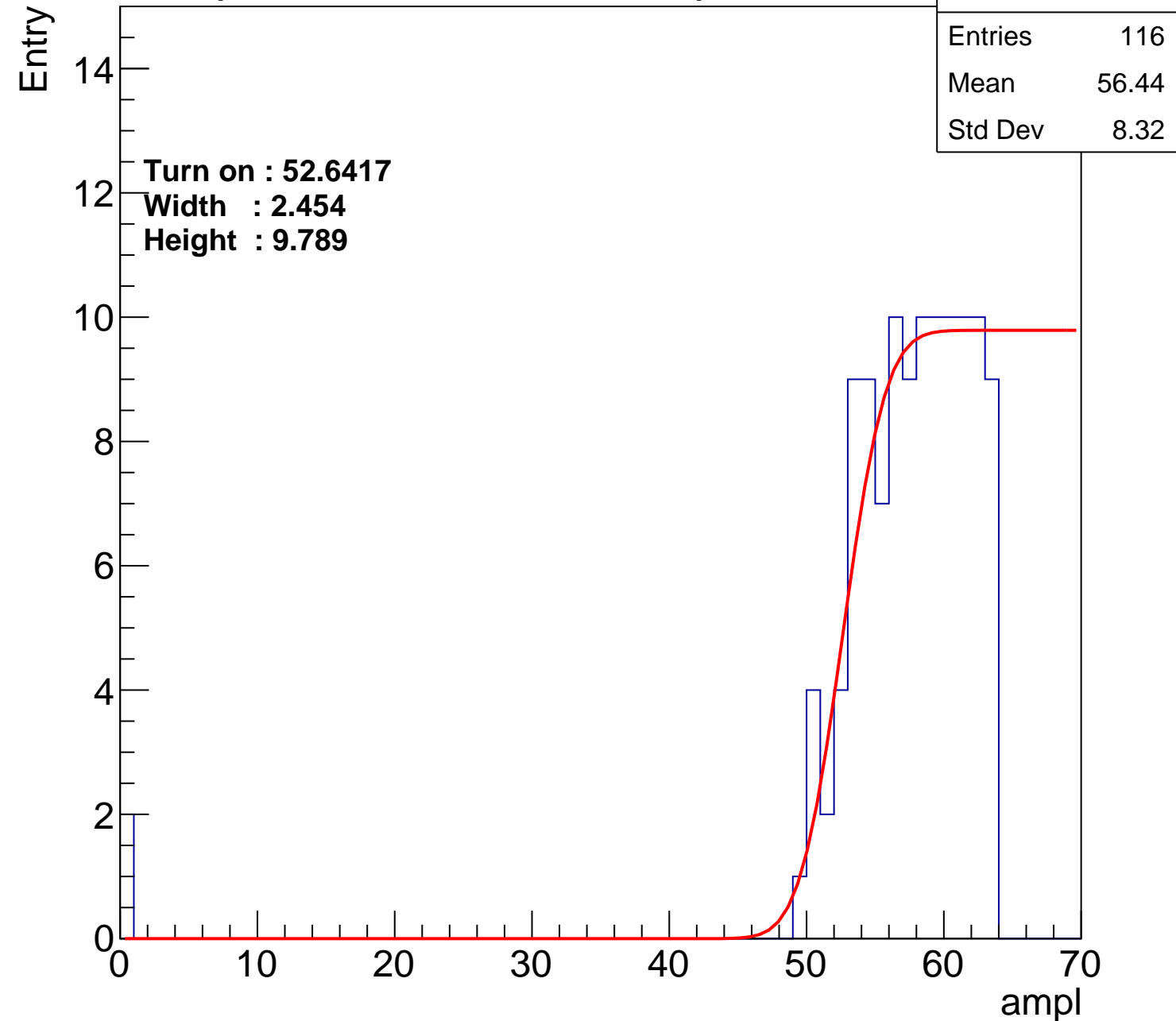
Width : 2.454

Height : 9.789

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_040323_1717.root, FC#2, port C3

calib_packv5_040323_1717.root, FC#2, port C3

Turn on : 50.6706
Width : 2.980
Height : 10.115



B0L103S, U10-ch36

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	55.97
Std Dev	9.7

Turn on : 52.3009

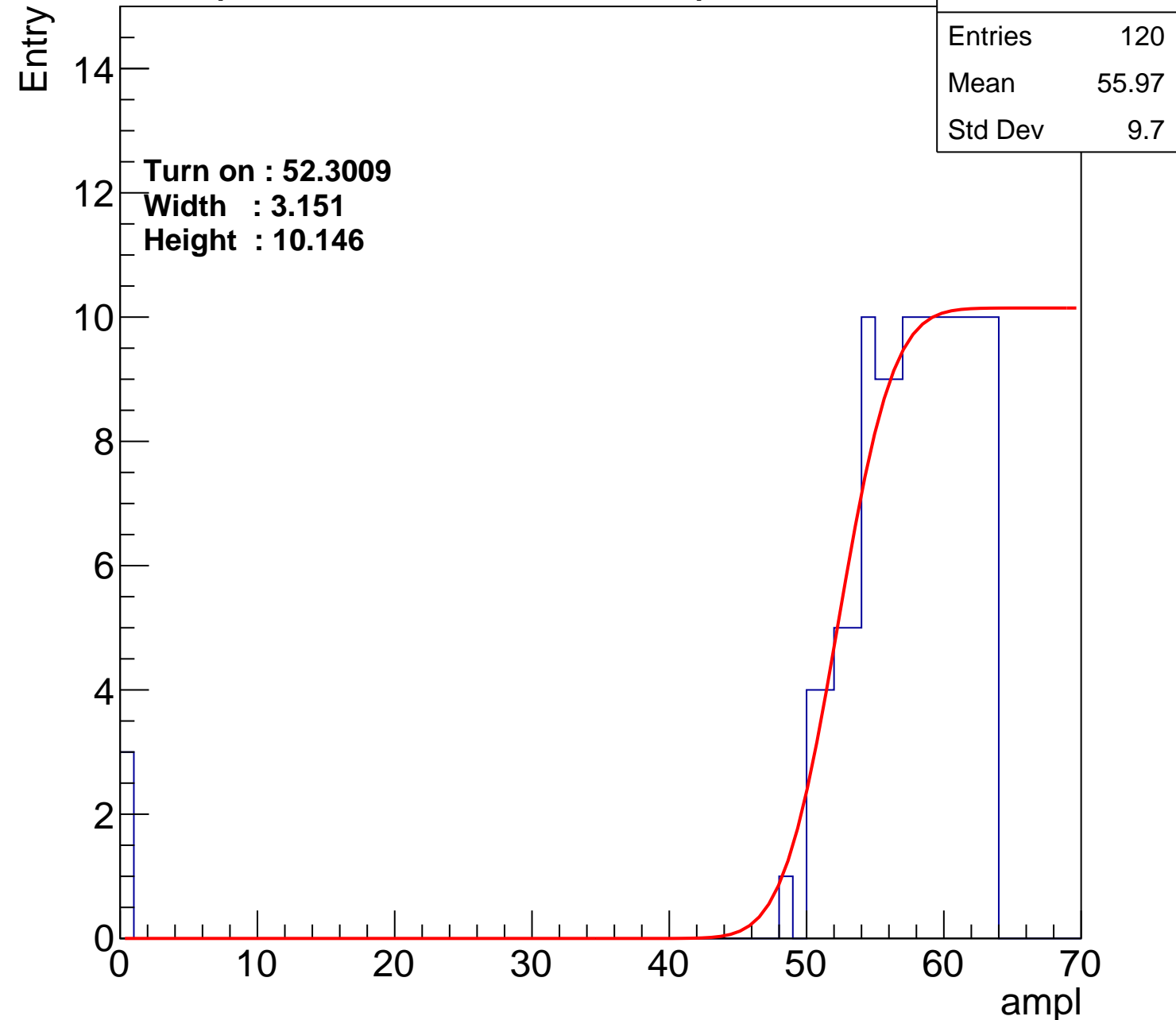
Width : 3.151

Height : 10.146

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch37

calib_packv5_040323_1717.root, FC#2, port C3

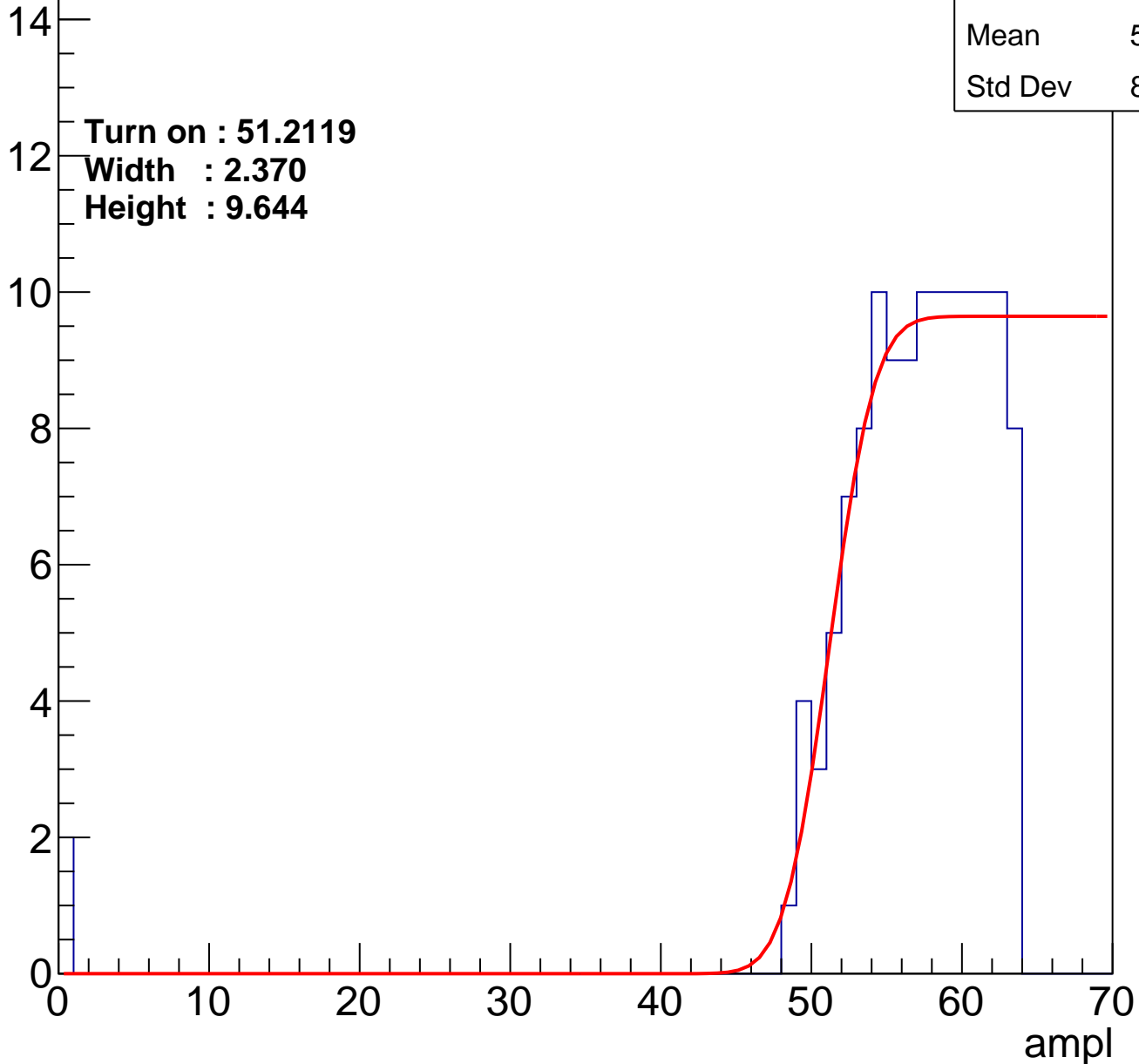
Entries	126
Mean	55.95
Std Dev	8.116

Turn on : 51.2119

Width : 2.370

Height : 9.644

Entry



B0L103S, U10-ch38

calib_packv5_040323_1717.root, FC#2, port C3

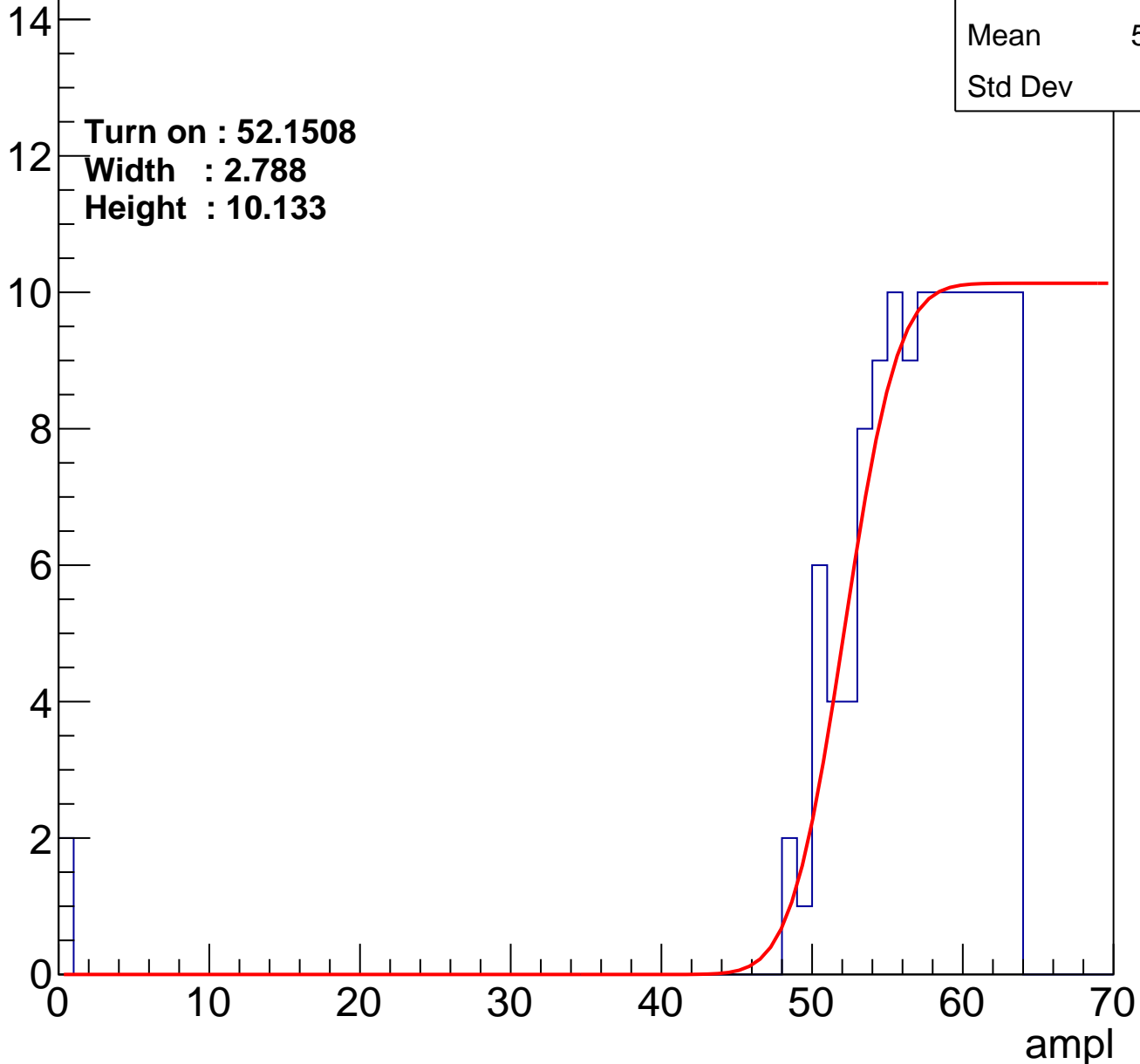
Entries	125
Mean	56.17
Std Dev	8.17

Turn on : 52.1508

Width : 2.788

Height : 10.133

Entry



B0L103S, U10-ch39

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.81
Std Dev	6.482

Turn on : 49.7953

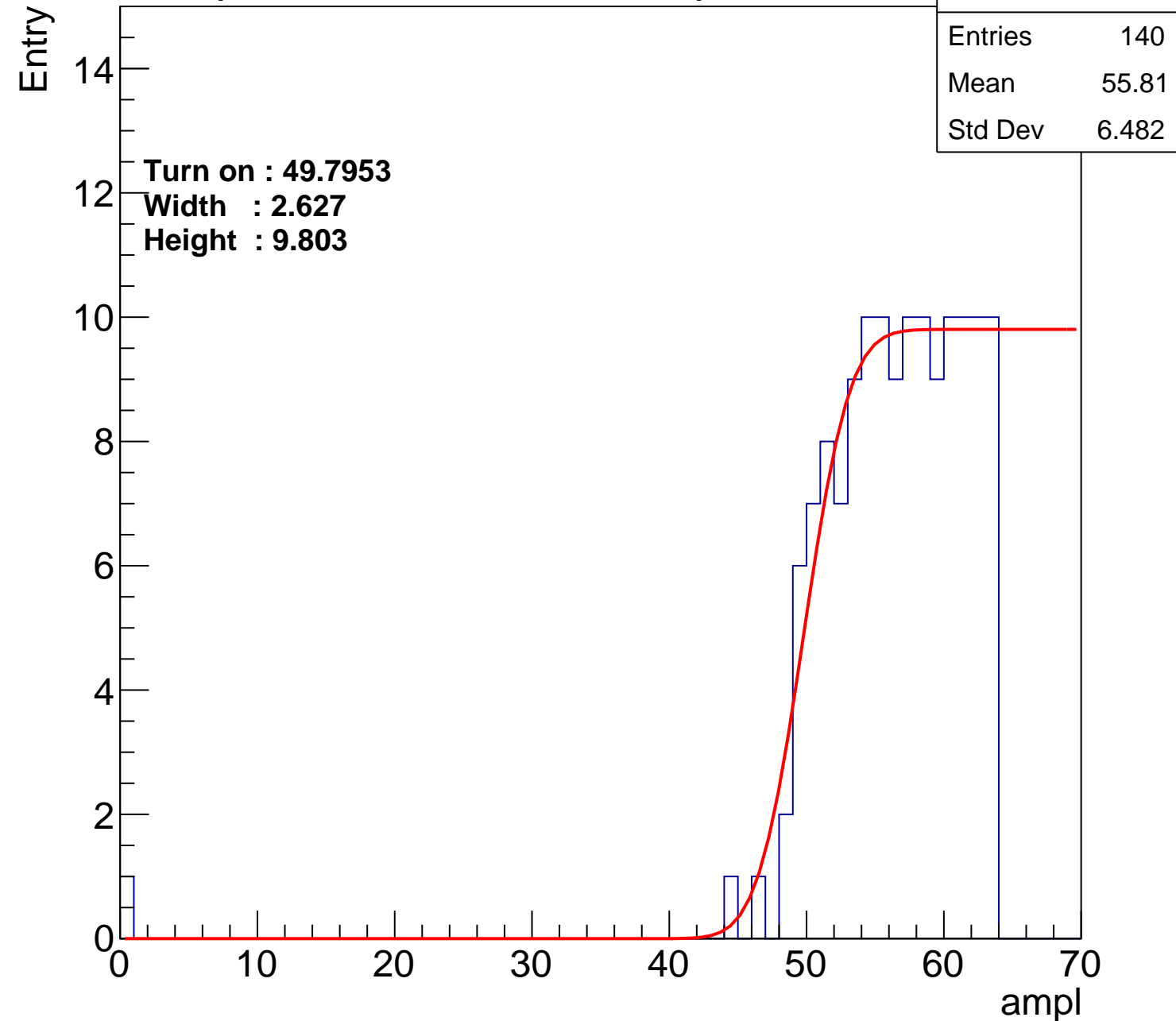
Width : 2.627

Height : 9.803

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch40

calib_packv5_040323_1717.root, FC#2, port C3

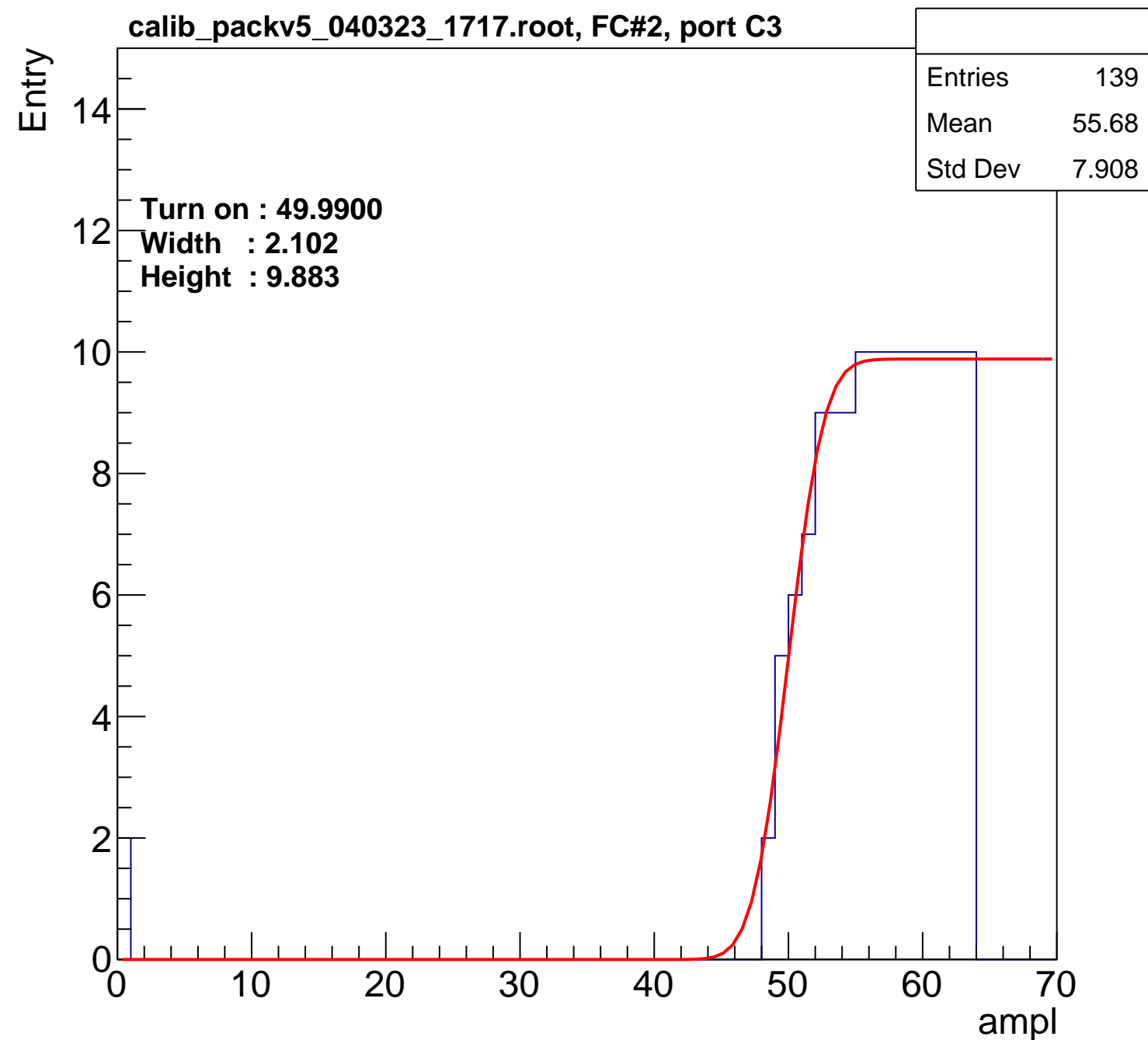
Entry

14
12
10
8
6
4
2
0

Turn on : 49.9900
Width : 2.102
Height : 9.883

Entries	139
Mean	55.68
Std Dev	7.908

ampl



B0L103S, U10-ch41

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	55.59
Std Dev	11.23

Turn on : 52.6693

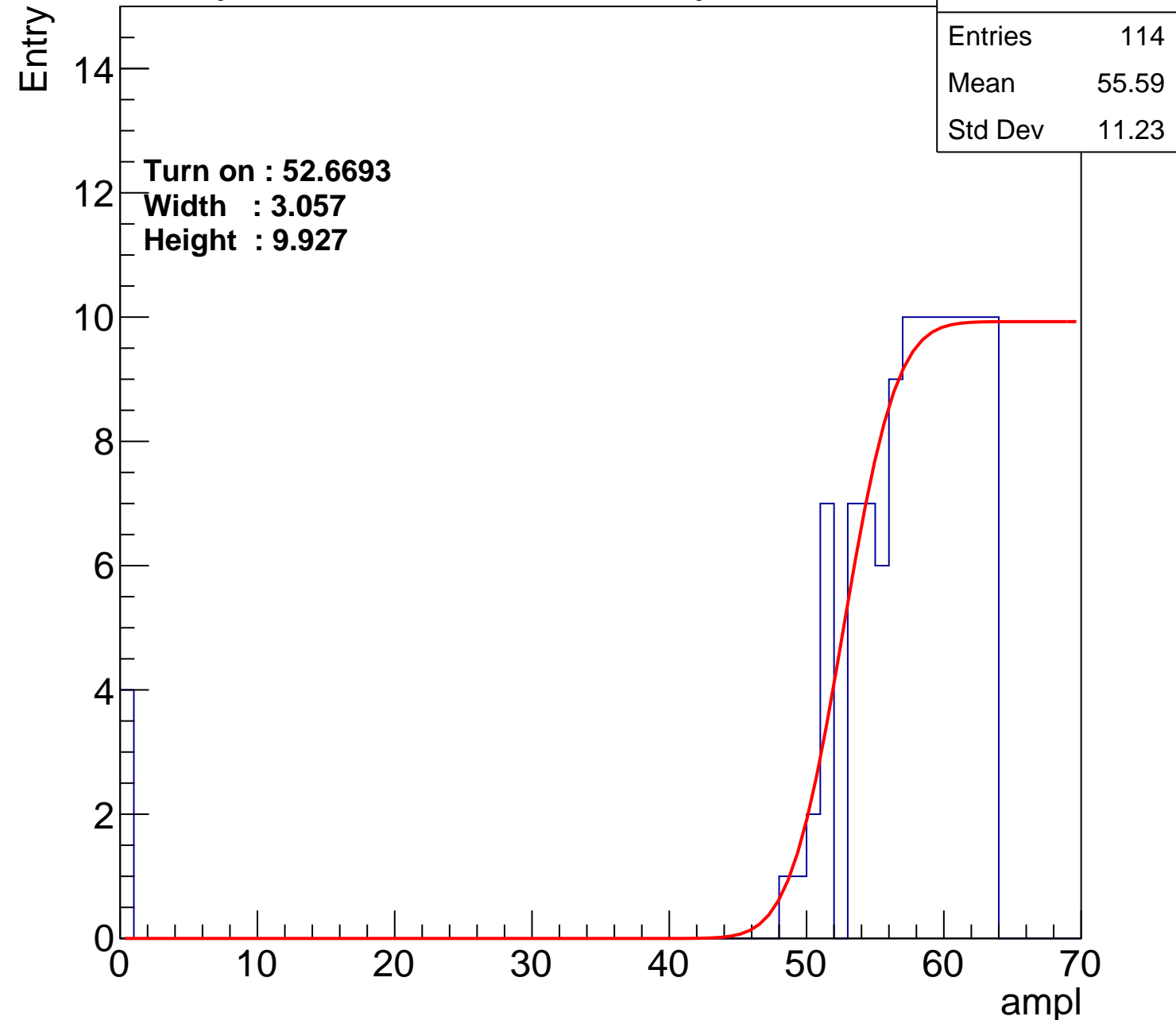
Width : 3.057

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch42

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.46
Std Dev	9.452

Turn on : 51.3506

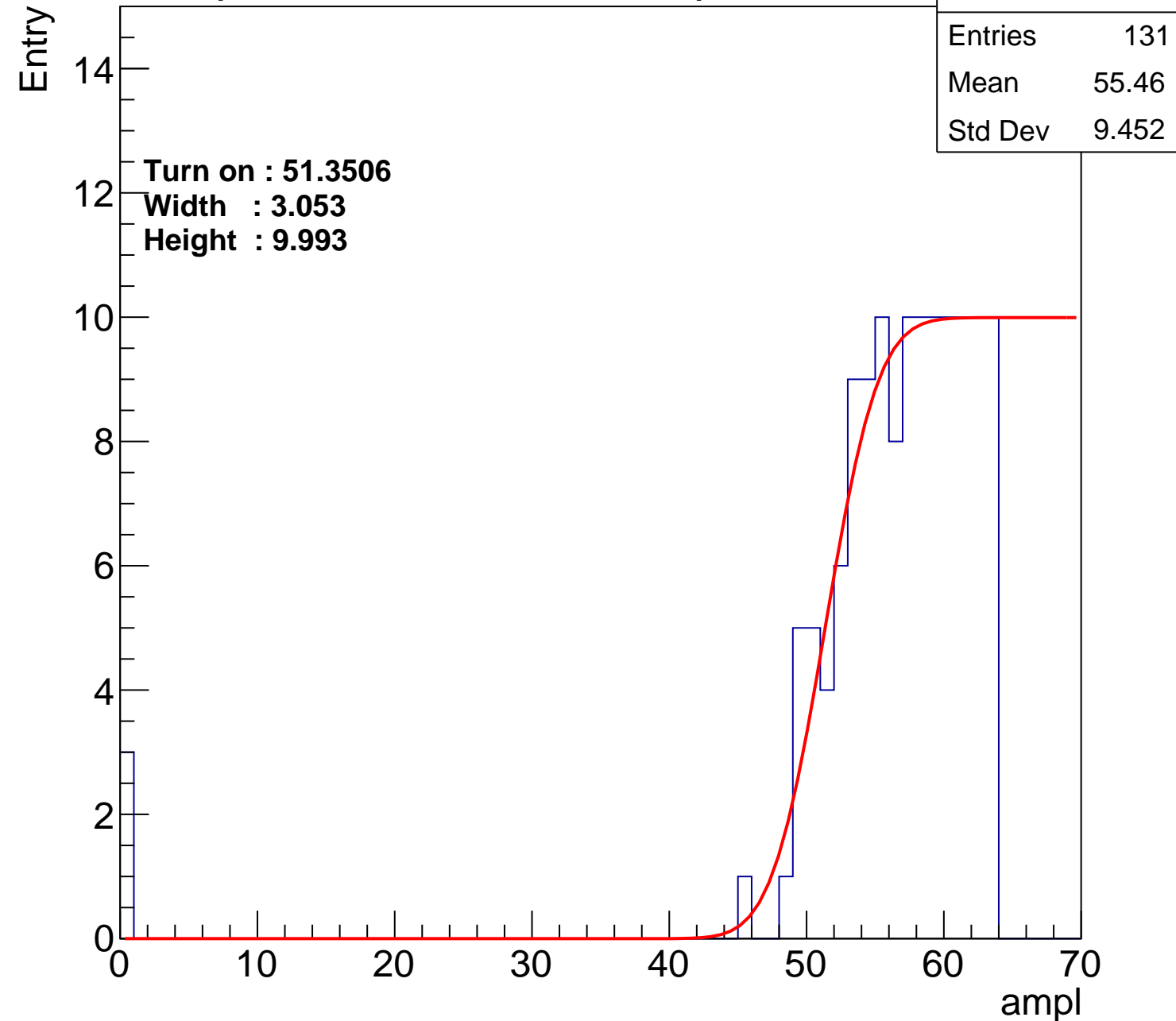
Width : 3.053

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch43

calib_packv5_040323_1717.root, FC#2, port C3

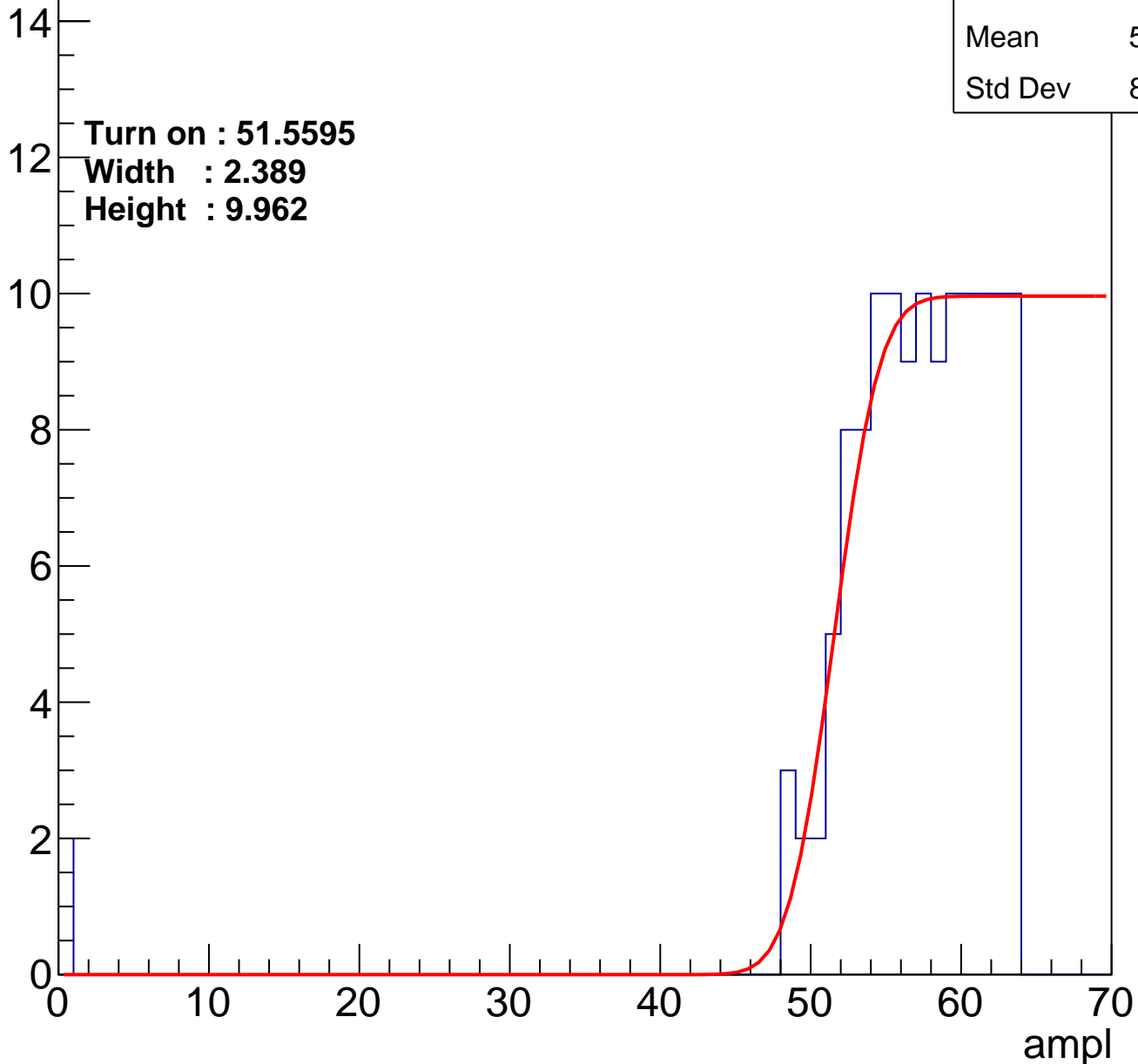
Entries	128
Mean	56.04
Std Dev	8.103

Turn on : 51.5595

Width : 2.389

Height : 9.962

Entry



B0L103S, U10-ch44

calib_packv5_040323_1717.root, FC#2, port C3

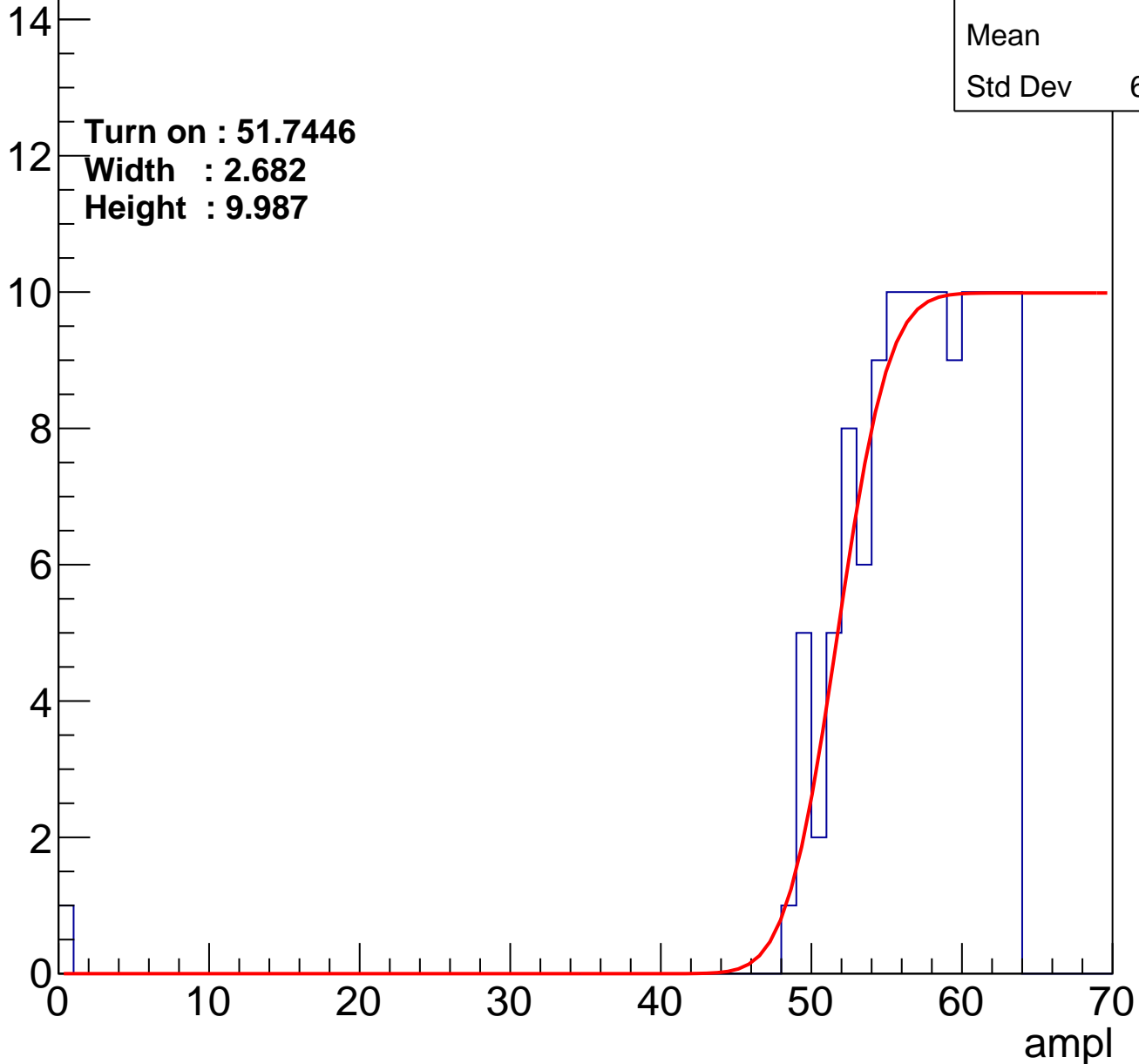
Entries	126
Mean	56.5
Std Dev	6.442

Turn on : 51.7446

Width : 2.682

Height : 9.987

Entry



B0L103S, U10-ch45

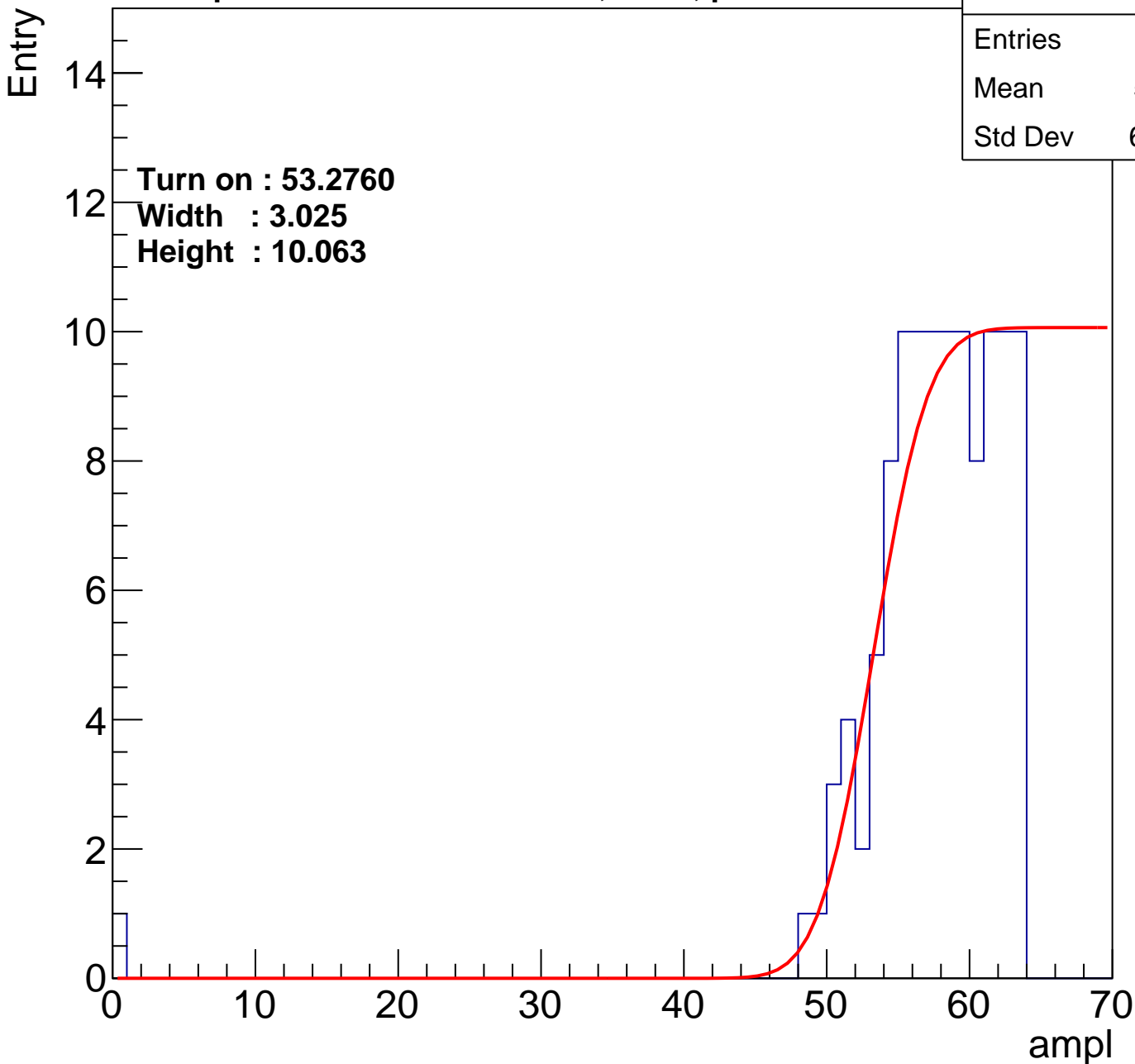
calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	57.01
Std Dev	6.537

Turn on : 53.2760

Width : 3.025

Height : 10.063



B0L103S, U10-ch46

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.69
Std Dev	6.523

Turn on : 52.0375

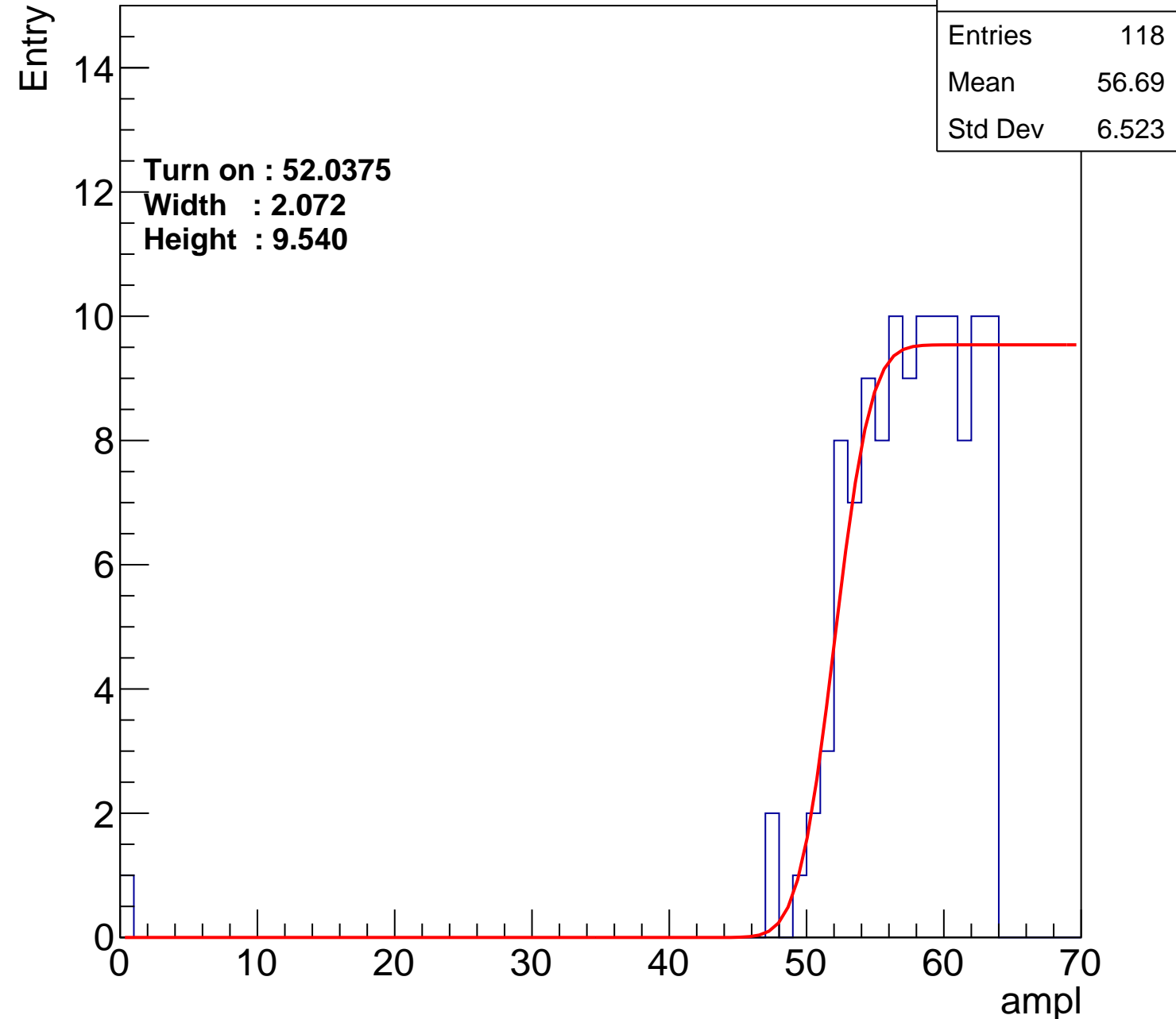
Width : 2.072

Height : 9.540

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch47

calib_packv5_040323_1717.root, FC#2, port C3

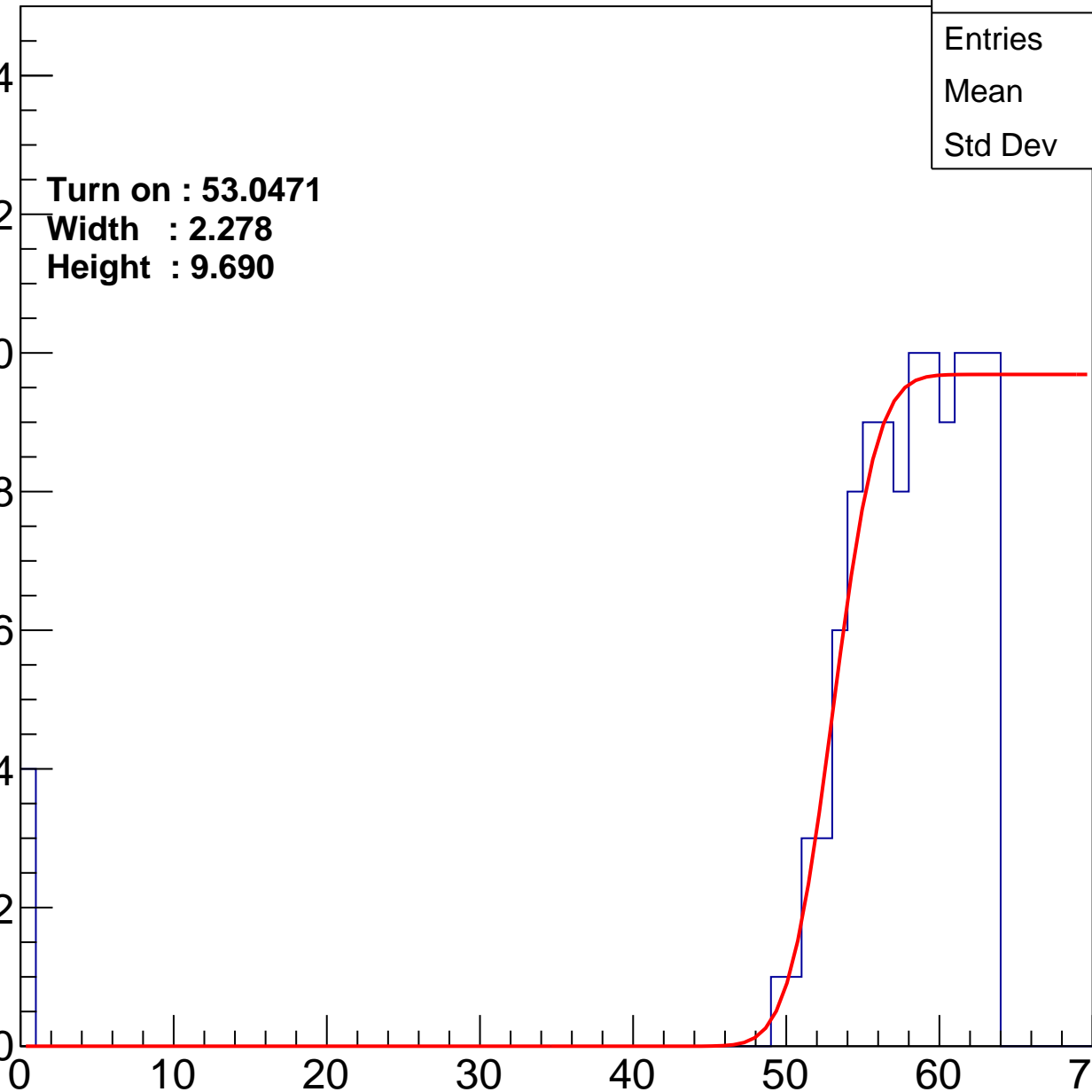
Entry

14
12
10
8
6
4
2
0

Turn on : 53.0471
Width : 2.278
Height : 9.690

Entries	111
Mean	55.7
Std Dev	11.32

ampl



B0L103S, U10-ch48

calib_packv5_040323_1717.root, FC#2, port C3

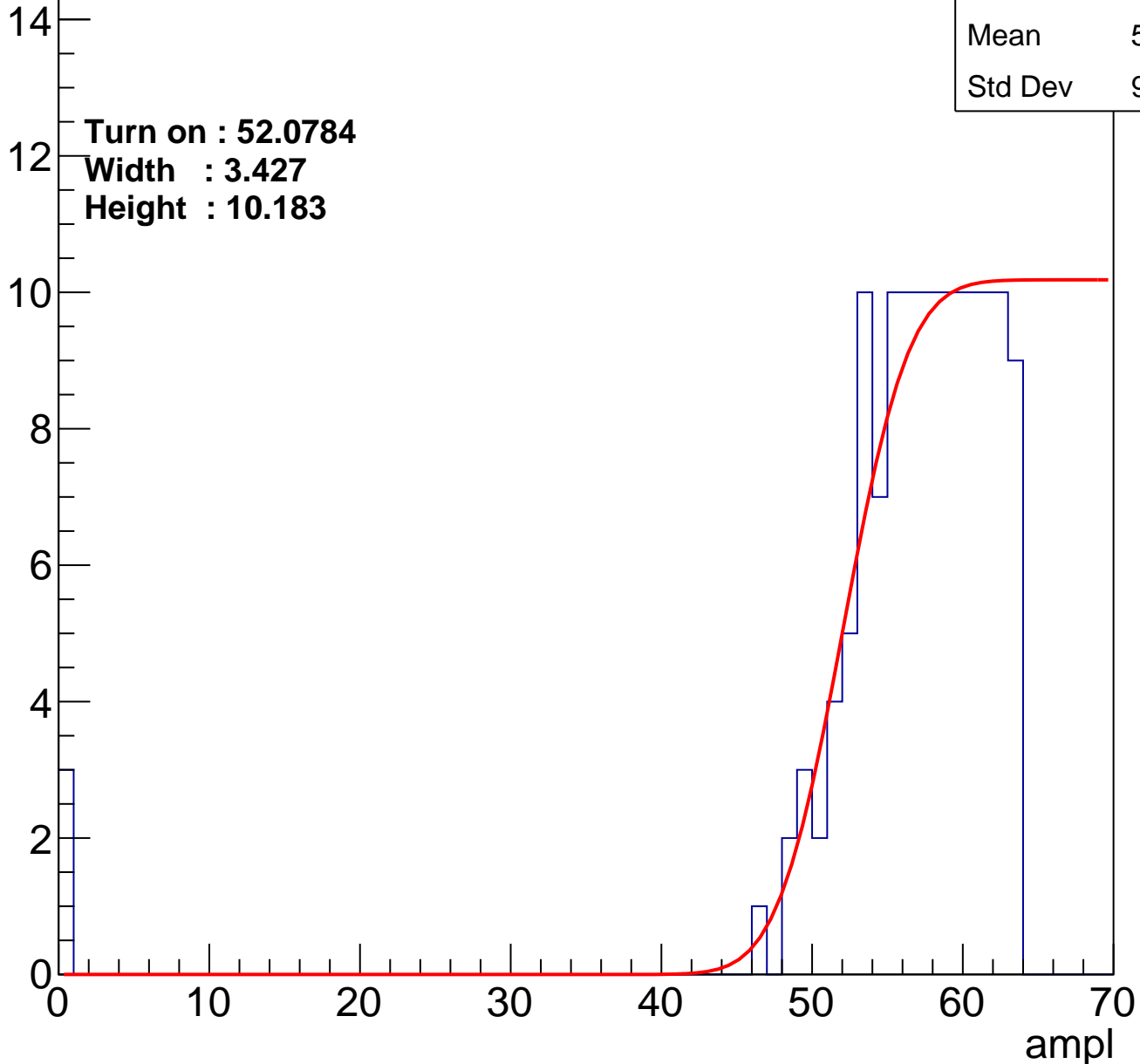
Entries	126
Mean	55.62
Std Dev	9.552

Turn on : 52.0784

Width : 3.427

Height : 10.183

Entry



B0L103S, U10-ch49

calib_packv5_040323_1717.root, FC#2, port C3

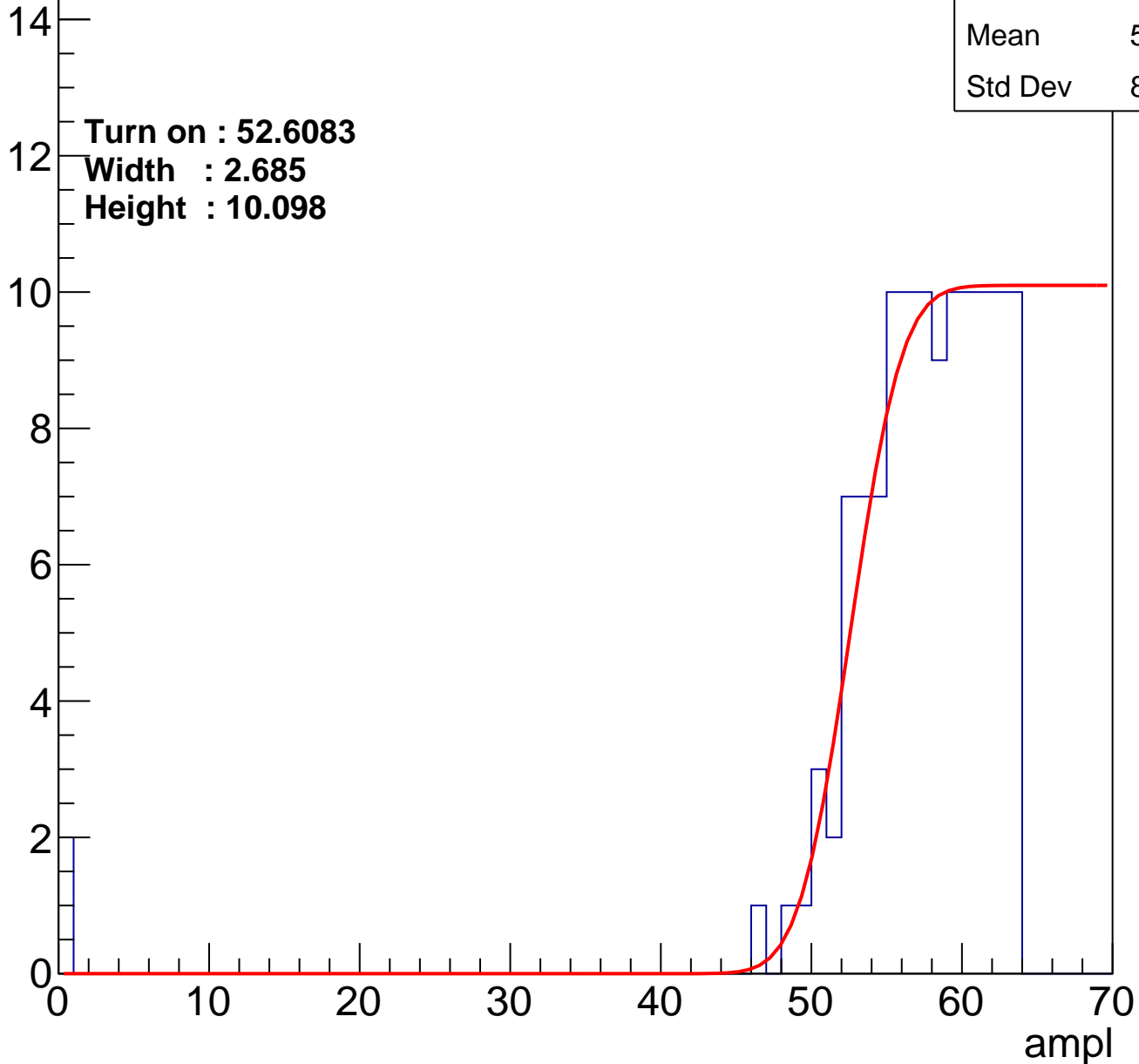
Entries	120
Mean	56.33
Std Dev	8.286

Turn on : 52.6083

Width : 2.685

Height : 10.098

Entry



B0L103S, U10-ch50

calib_packv5_040323_1717.root, FC#2, port C3

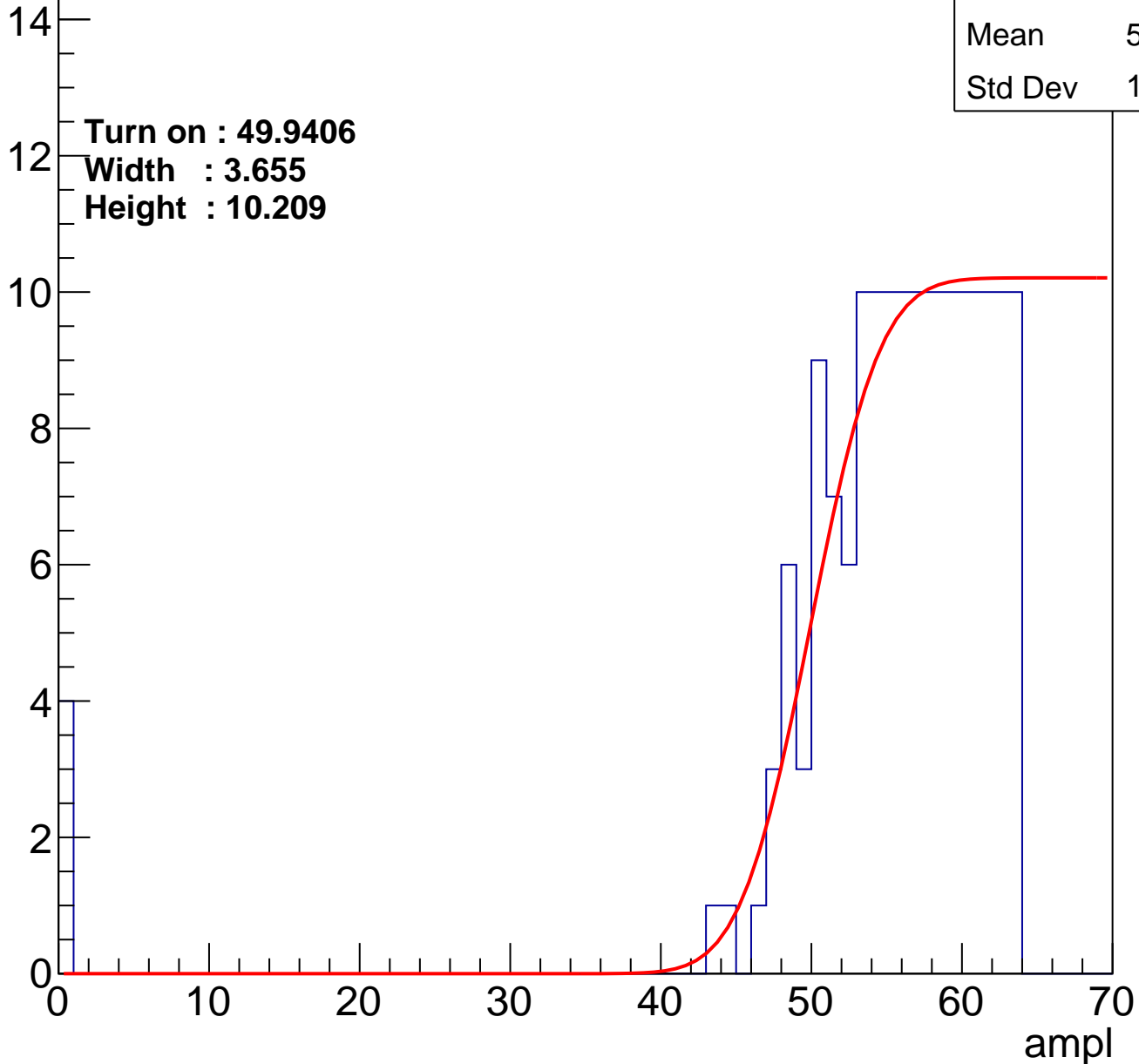
Entries	151
Mean	54.36
Std Dev	10.12

Turn on : 49.9406

Width : 3.655

Height : 10.209

Entry



B0L103S, U10-ch51

calib_packv5_040323_1717.root, FC#2, port C3

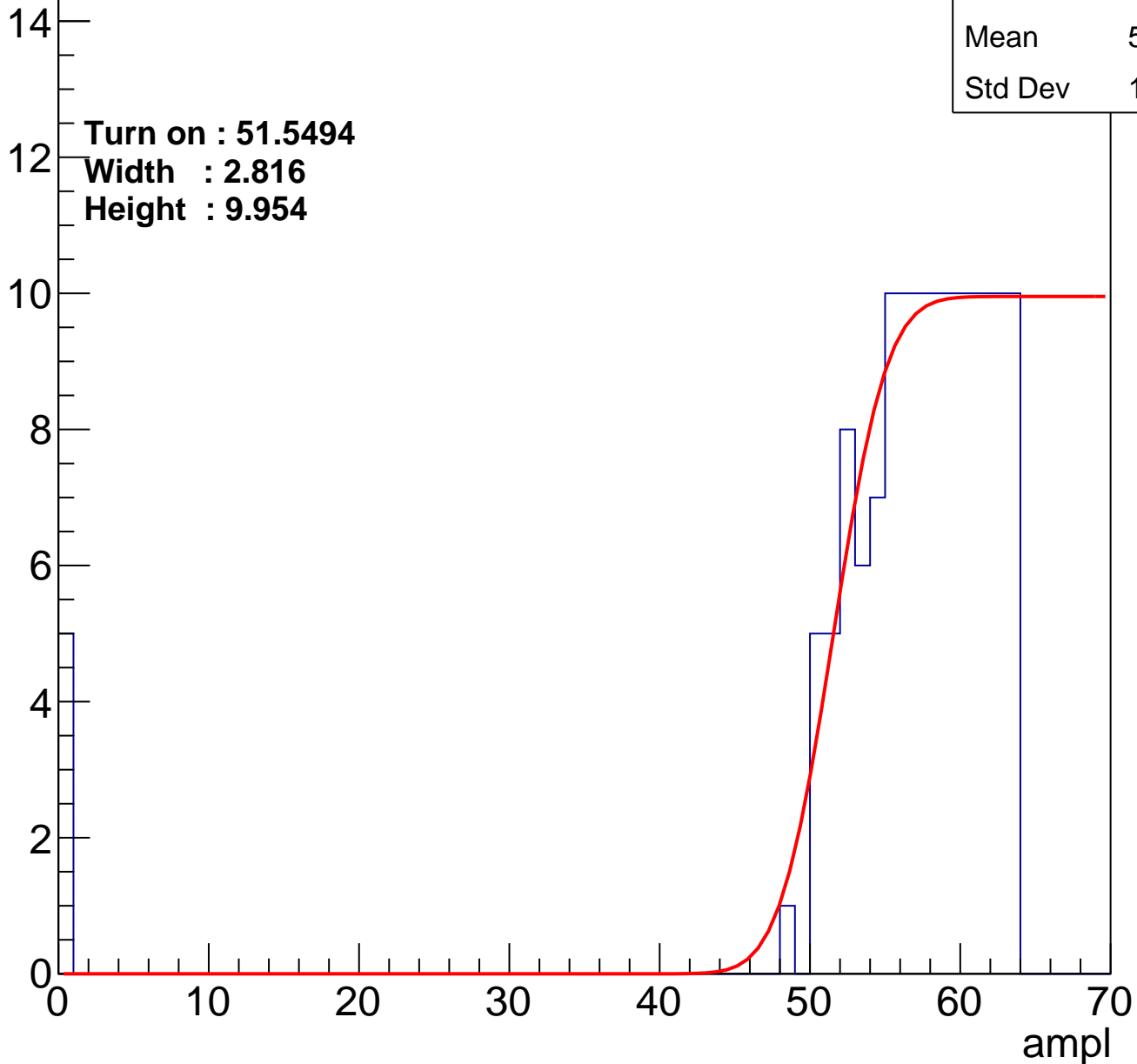
Entries	127
Mean	54.92
Std Dev	11.75

Turn on : 51.5494

Width : 2.816

Height : 9.954

Entry



B0L103S, U10-ch52

calib_packv5_040323_1717.root, FC#2, port C3

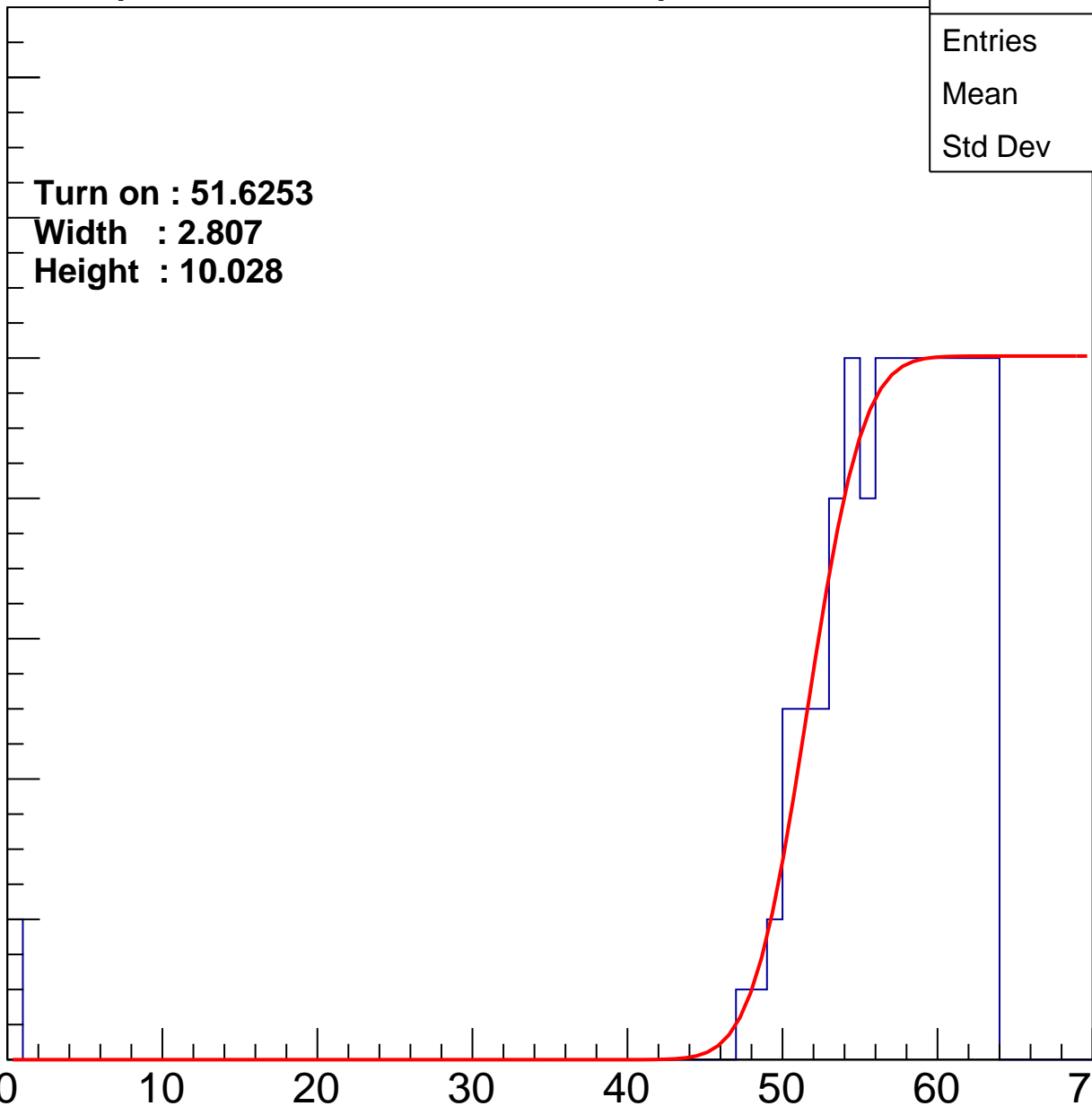
Entry

14
12
10
8
6
4
2
0

Turn on : 51.6253
Width : 2.807
Height : 10.028

Entries	127
Mean	56.08
Std Dev	8.142

ampl



B0L103S, U10-ch53

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.8
Std Dev	6.485

Turn on : 49.8048

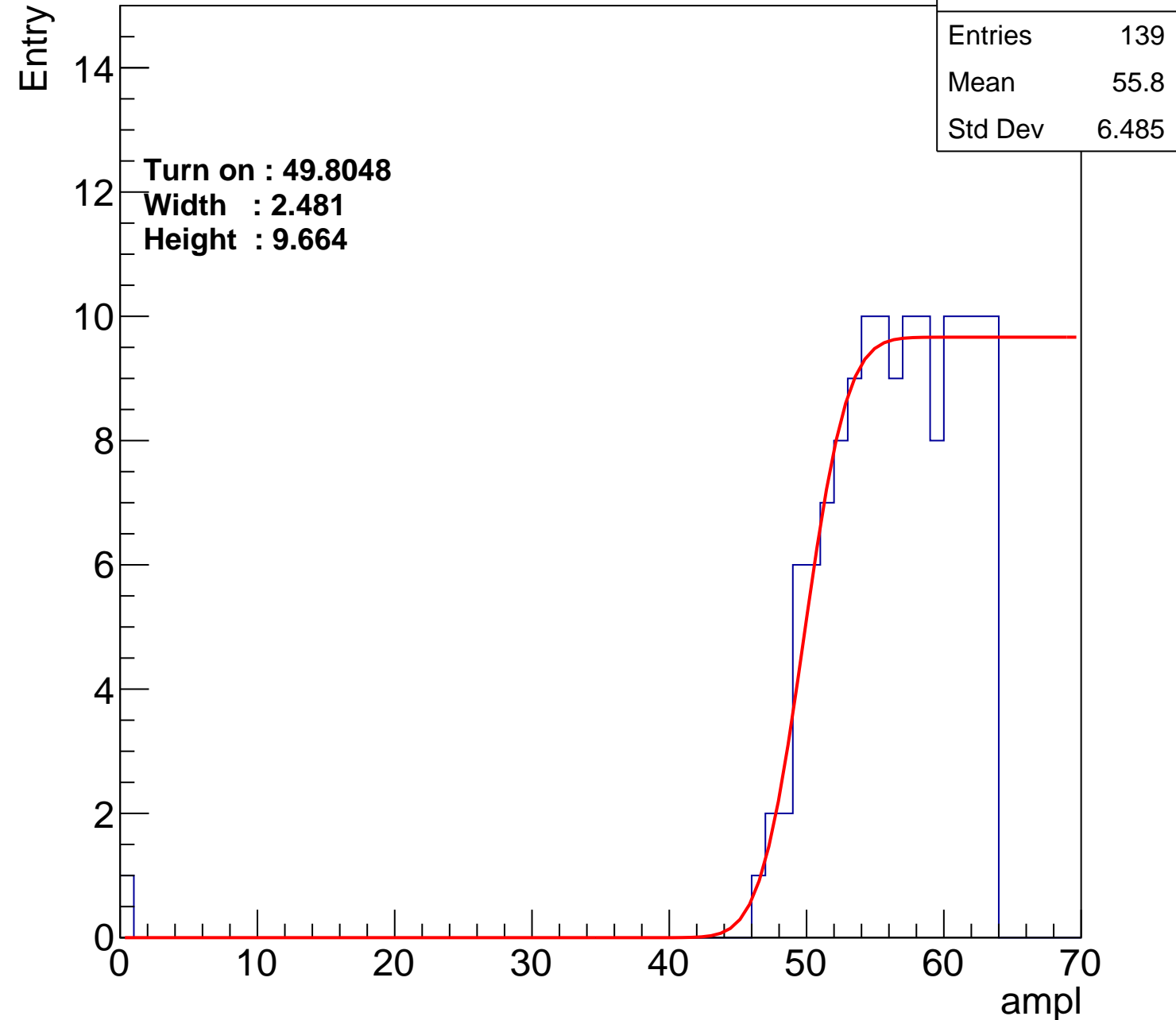
Width : 2.481

Height : 9.664

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	55.94
Std Dev	9.933

Turn on : 54.3435

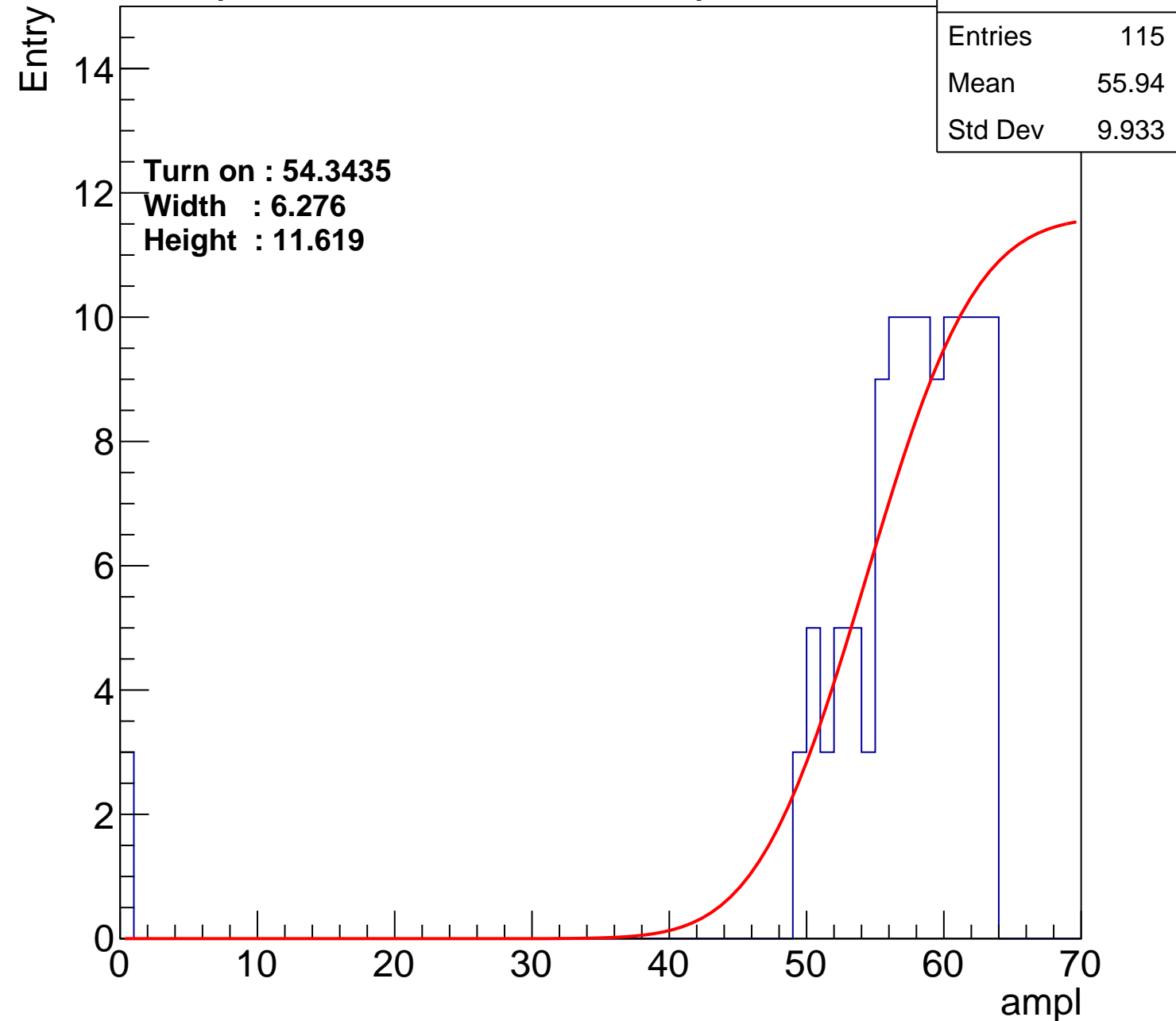
Width : 6.276

Height : 11.619

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch55

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.05
Std Dev	12.01

Turn on : 52.3917

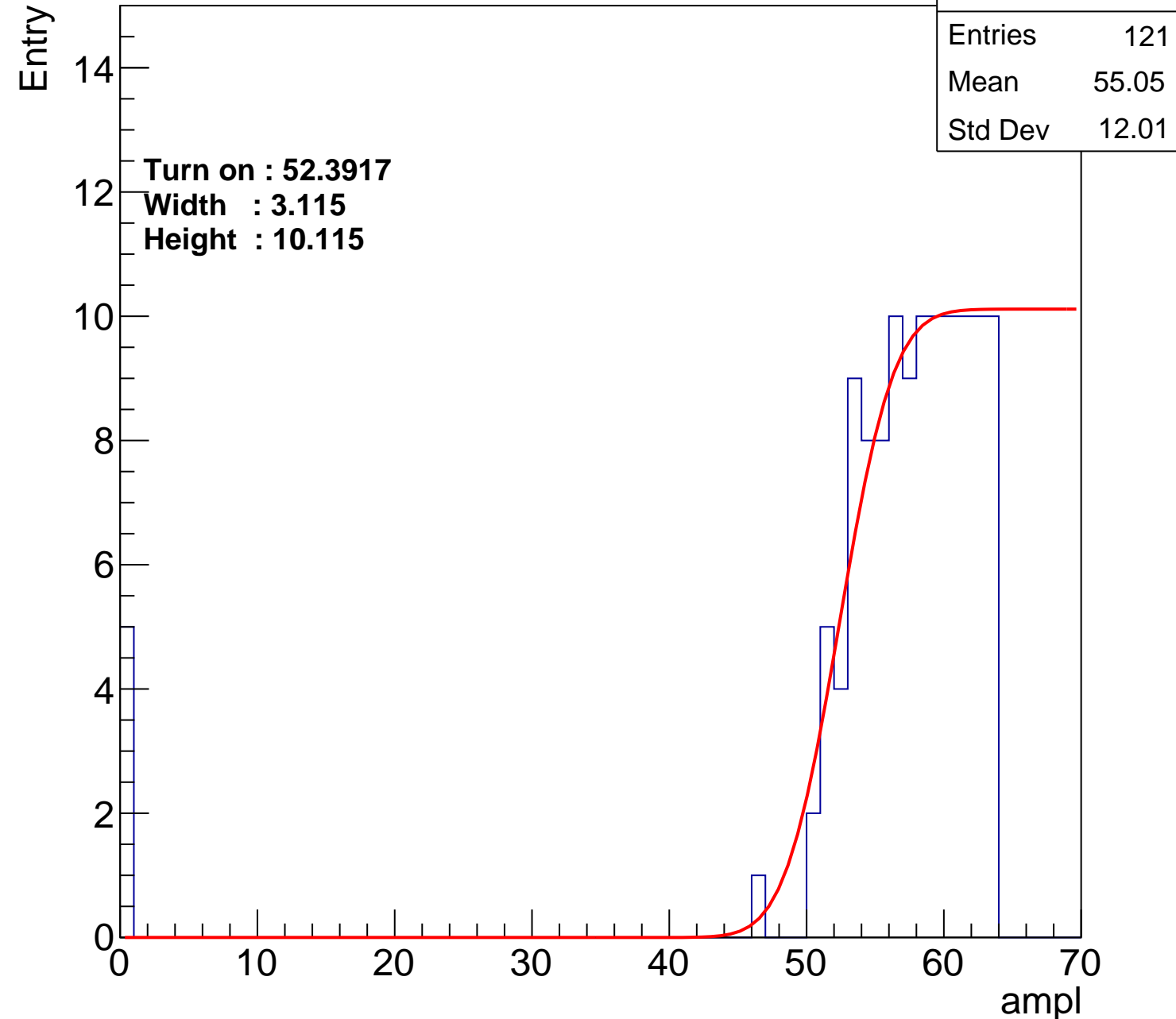
Width : 3.115

Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch56

calib_packv5_040323_1717.root, FC#2, port C3

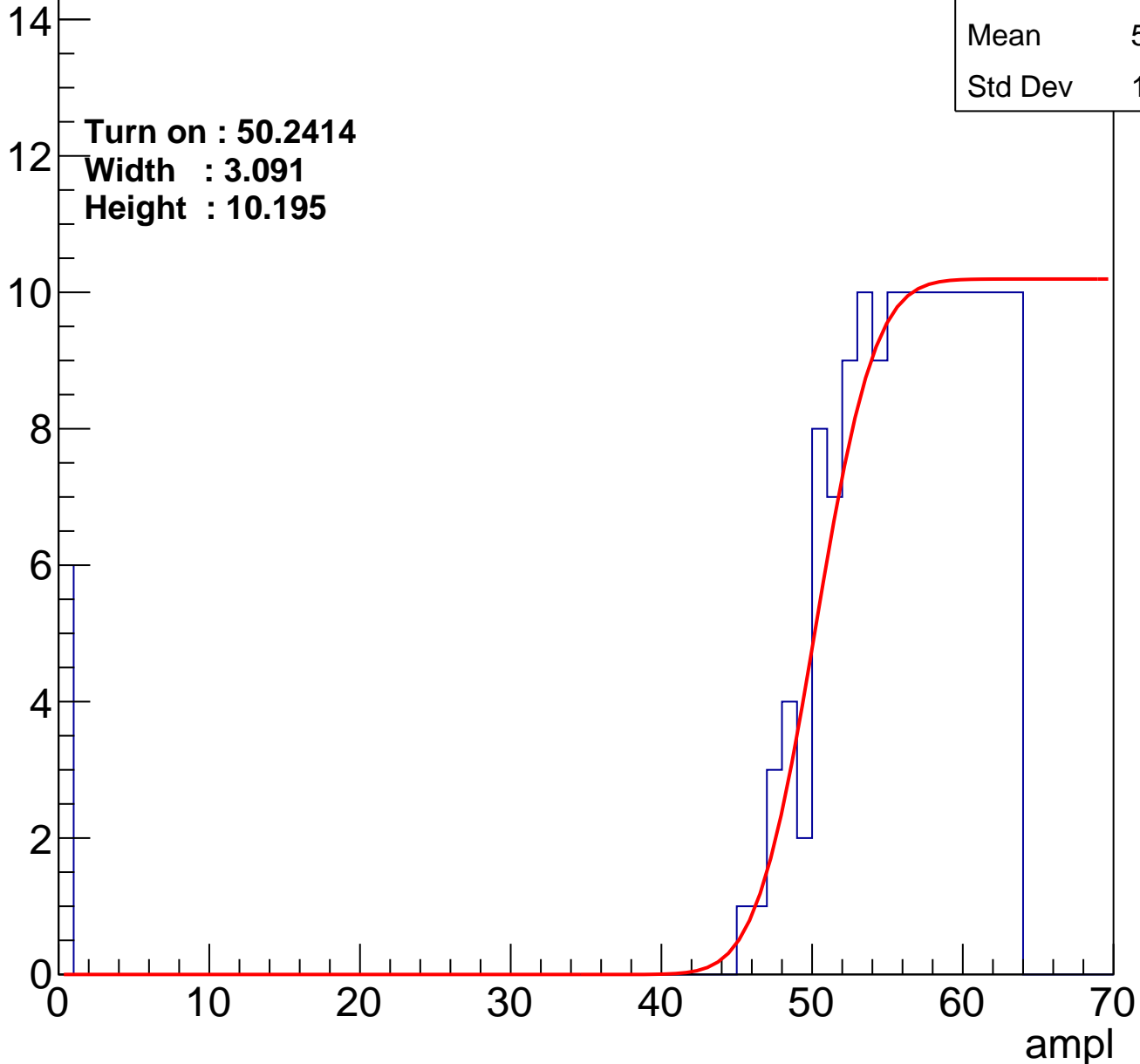
Entries	150
Mean	53.82
Std Dev	11.85

Turn on : 50.2414

Width : 3.091

Height : 10.195

Entry



B0L103S, U10-ch57

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	53.66
Std Dev	11.75

Turn on : 49.7008

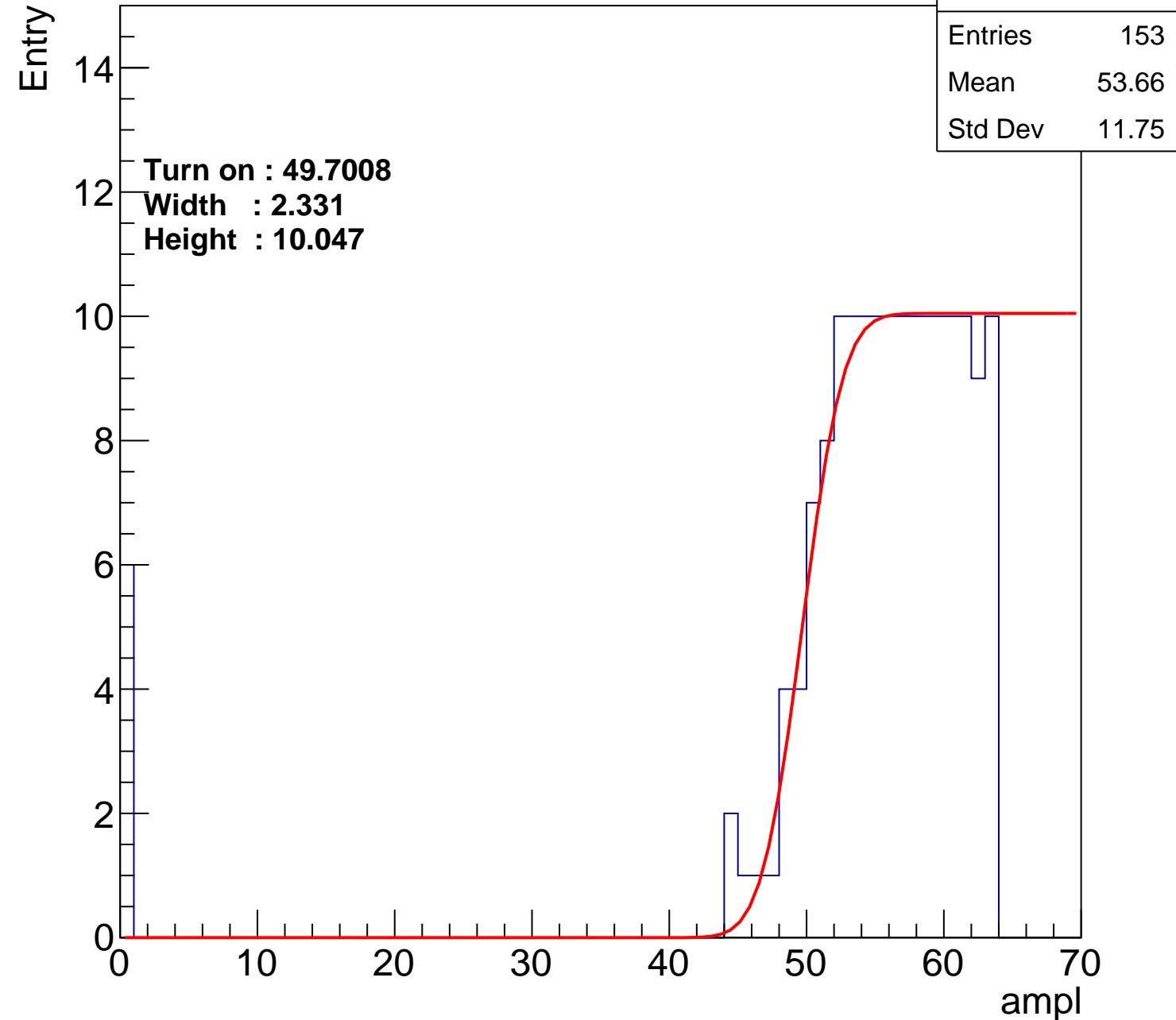
Width : 2.331

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch58

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.77
Std Dev	8.058

Turn on : 50.9841

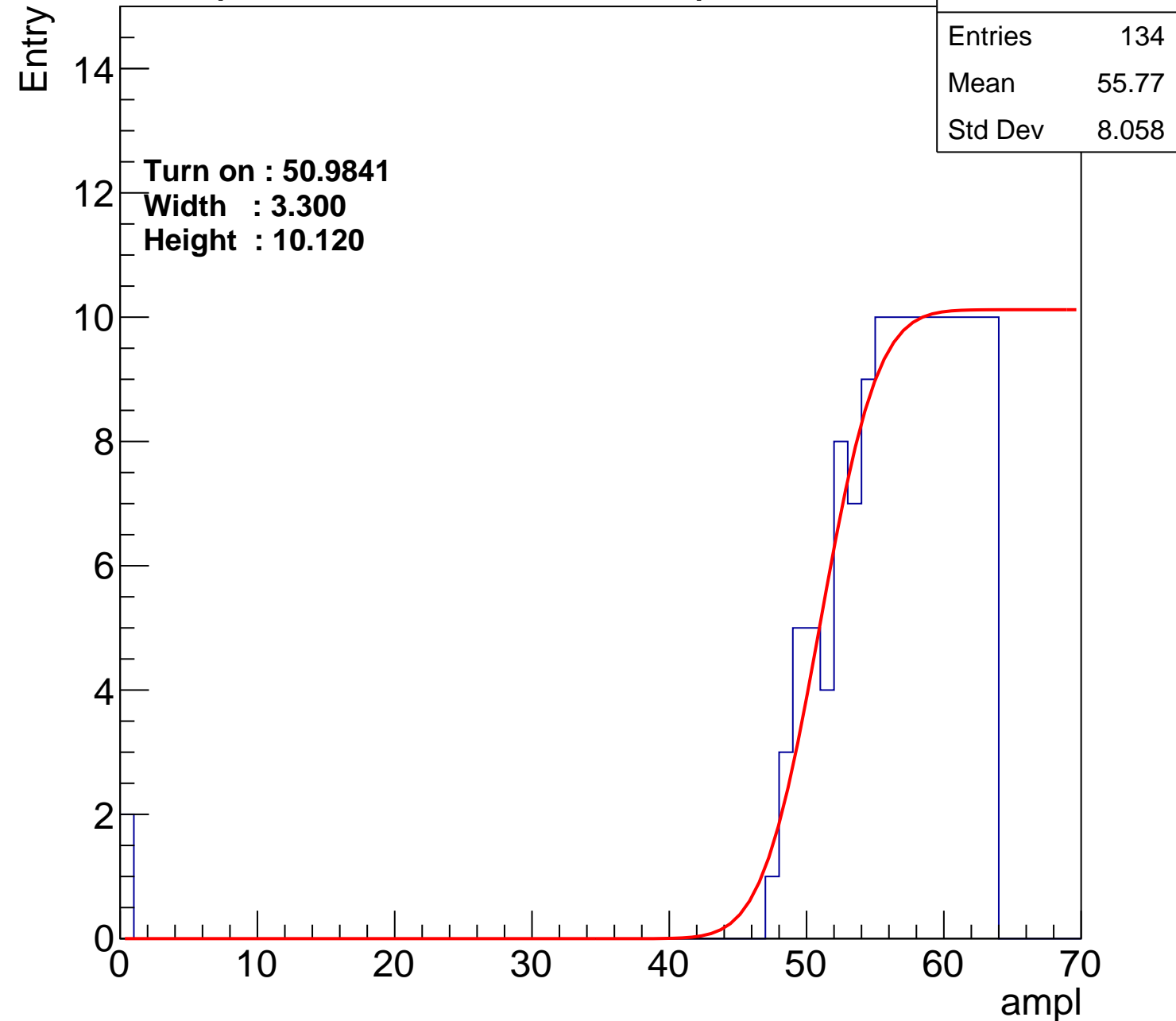
Width : 3.300

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch59

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.3
Std Dev	9.306

Turn on : 50.9033

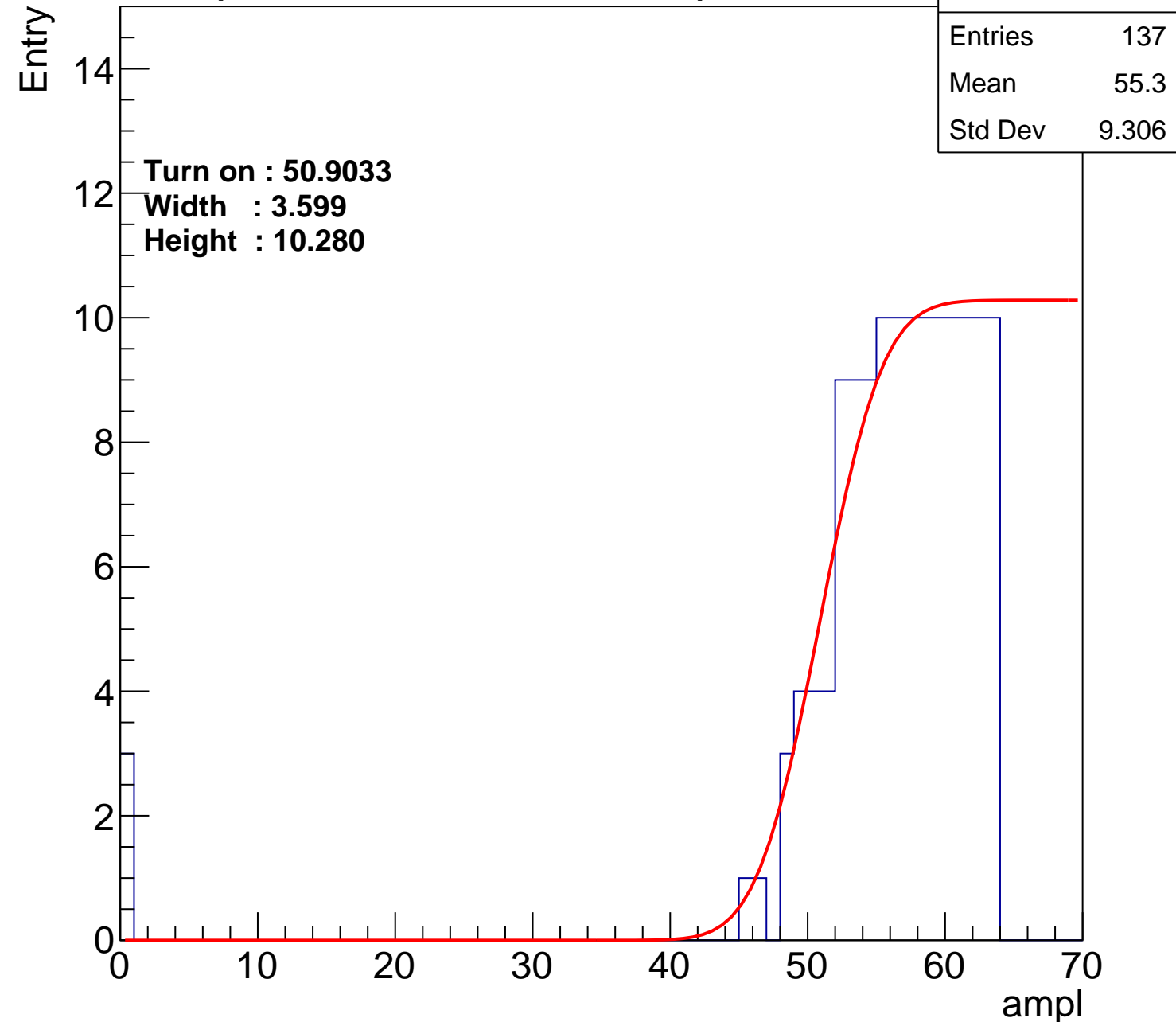
Width : 3.599

Height : 10.280

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch60

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	55.29
Std Dev	9.212

Turn on : 50.3025

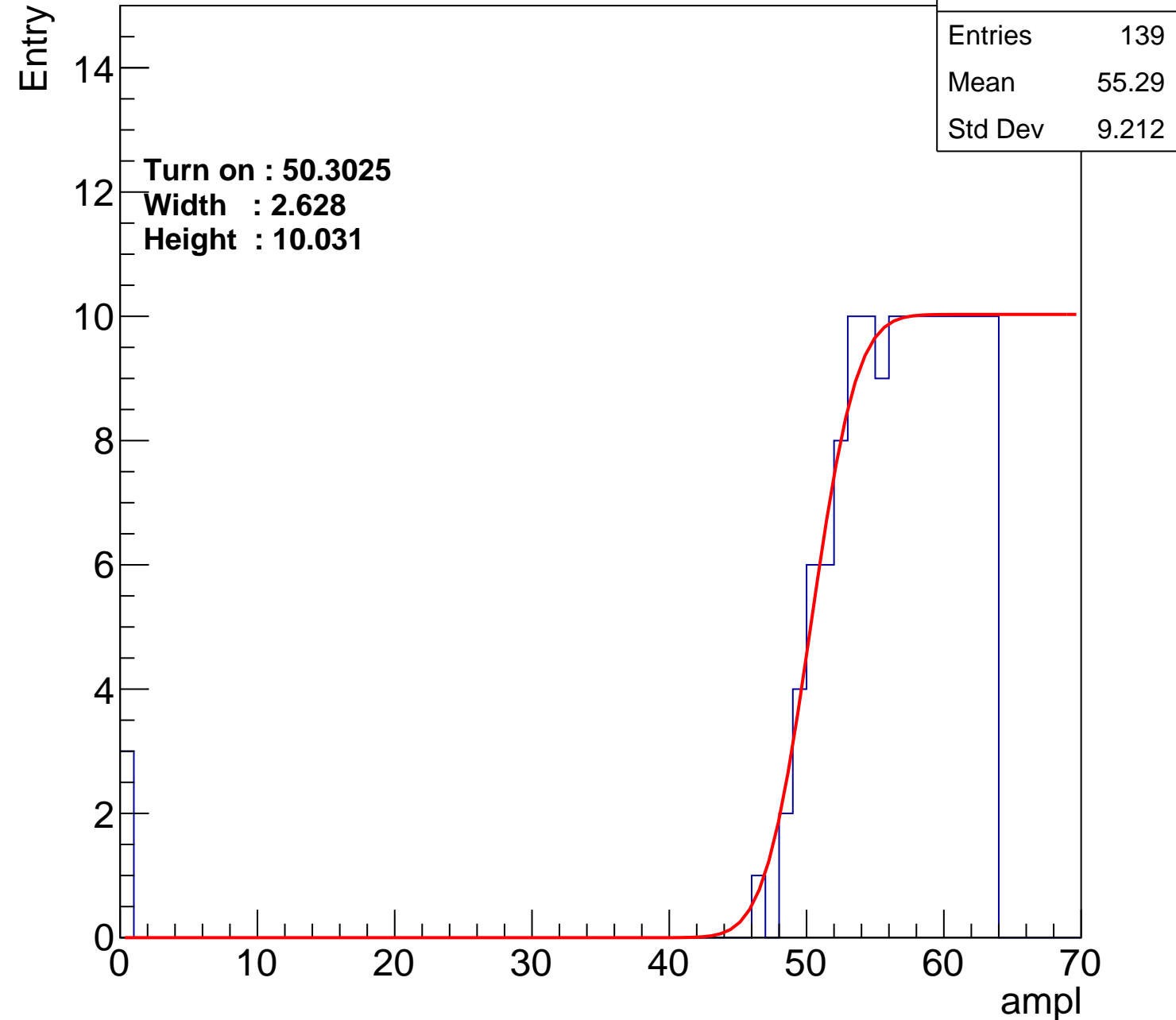
Width : 2.628

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch61

calib_packv5_040323_1717.root, FC#2, port C3

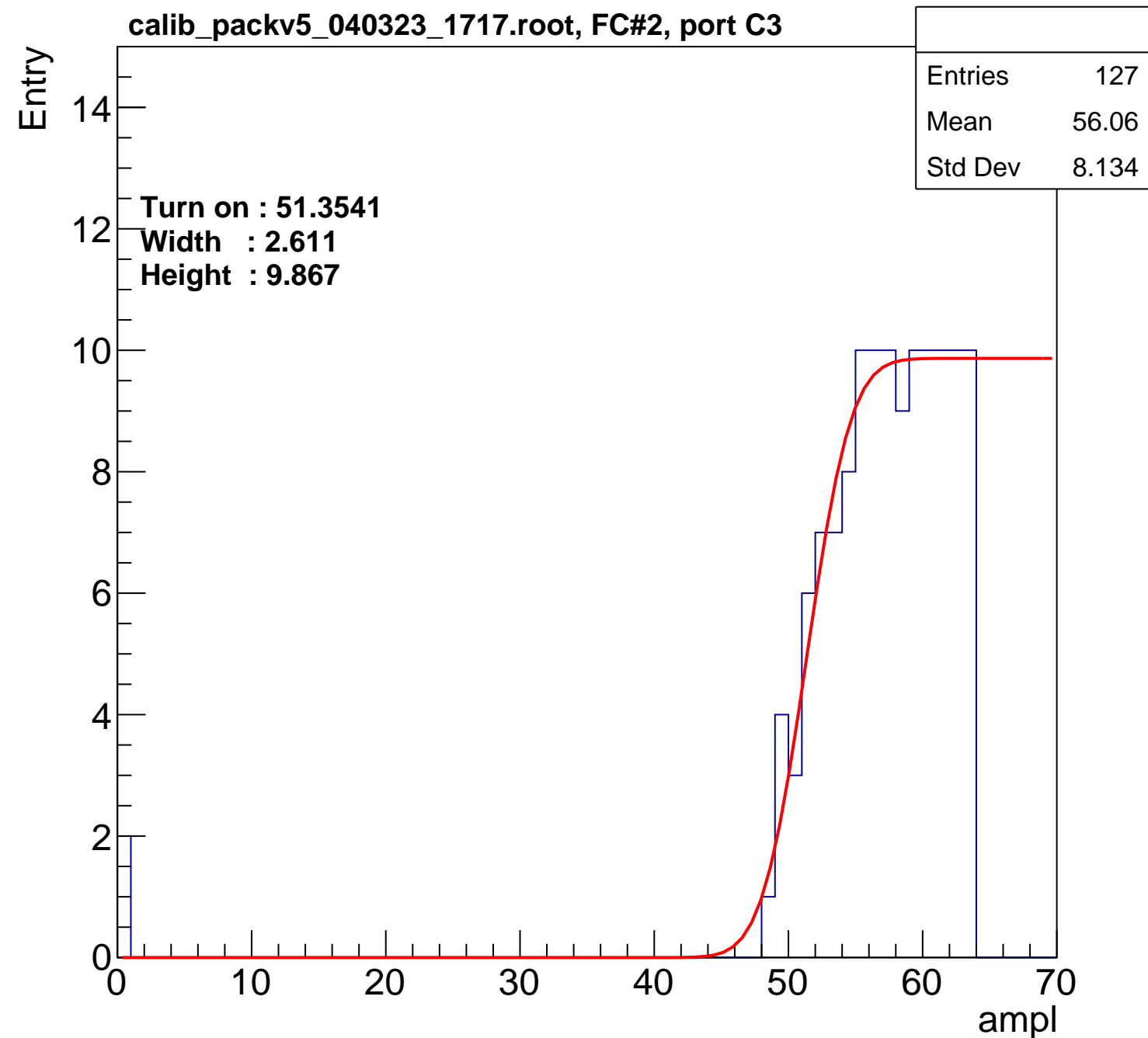
Entry

14
12
10
8
6
4
2
0

Turn on : 51.3541
Width : 2.611
Height : 9.867

Entries	127
Mean	56.06
Std Dev	8.134

ampl



B0L103S, U10-ch62

calib_packv5_040323_1717.root, FC#2, port C3

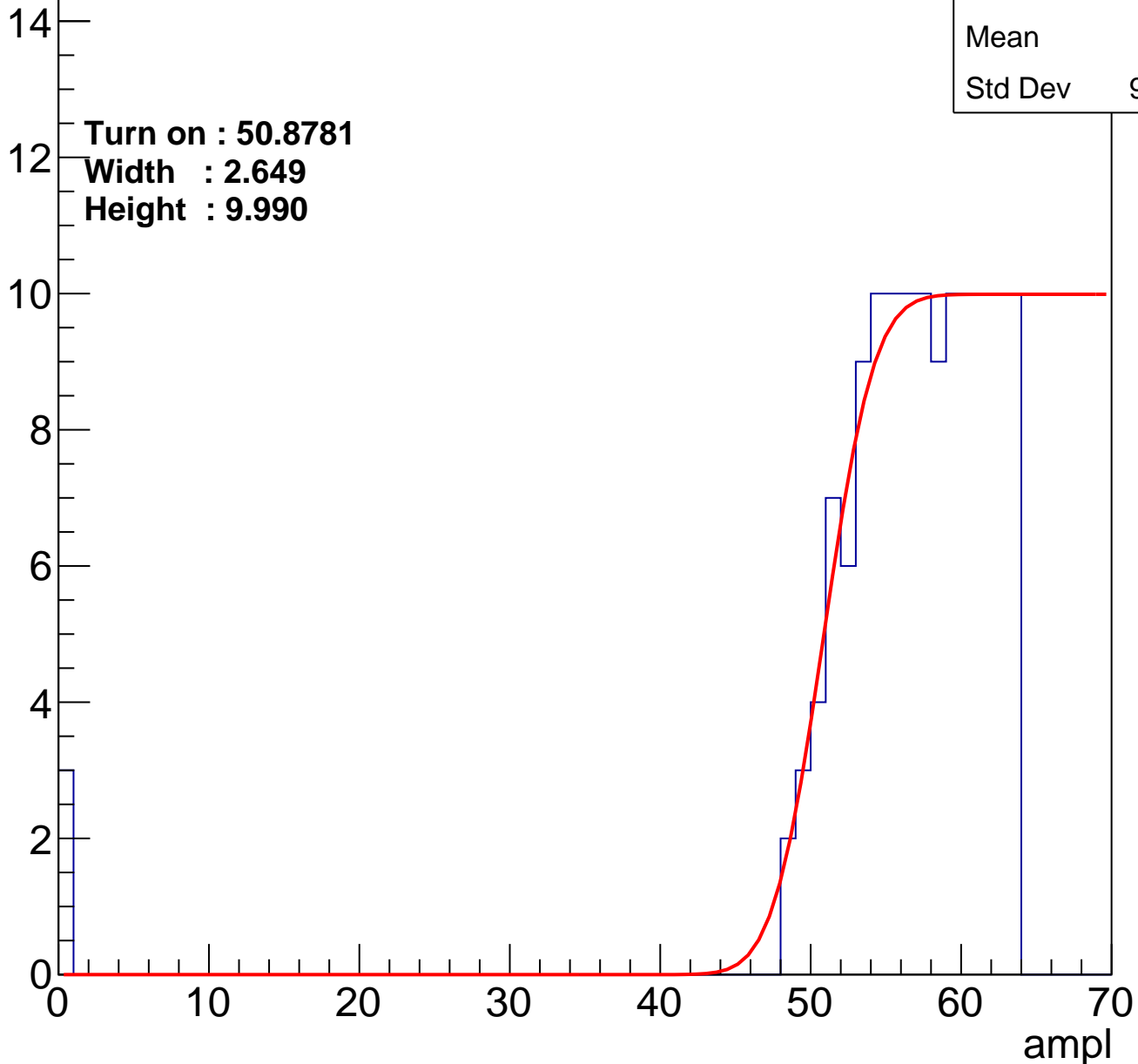
Entries	133
Mean	55.5
Std Dev	9.336

Turn on : 50.8781

Width : 2.649

Height : 9.990

Entry



B0L103S, U10-ch63

calib_packv5_040323_1717.root, FC#2, port C3

Entries	100
Mean	57.5
Std Dev	6.718

Turn on : 53.8228

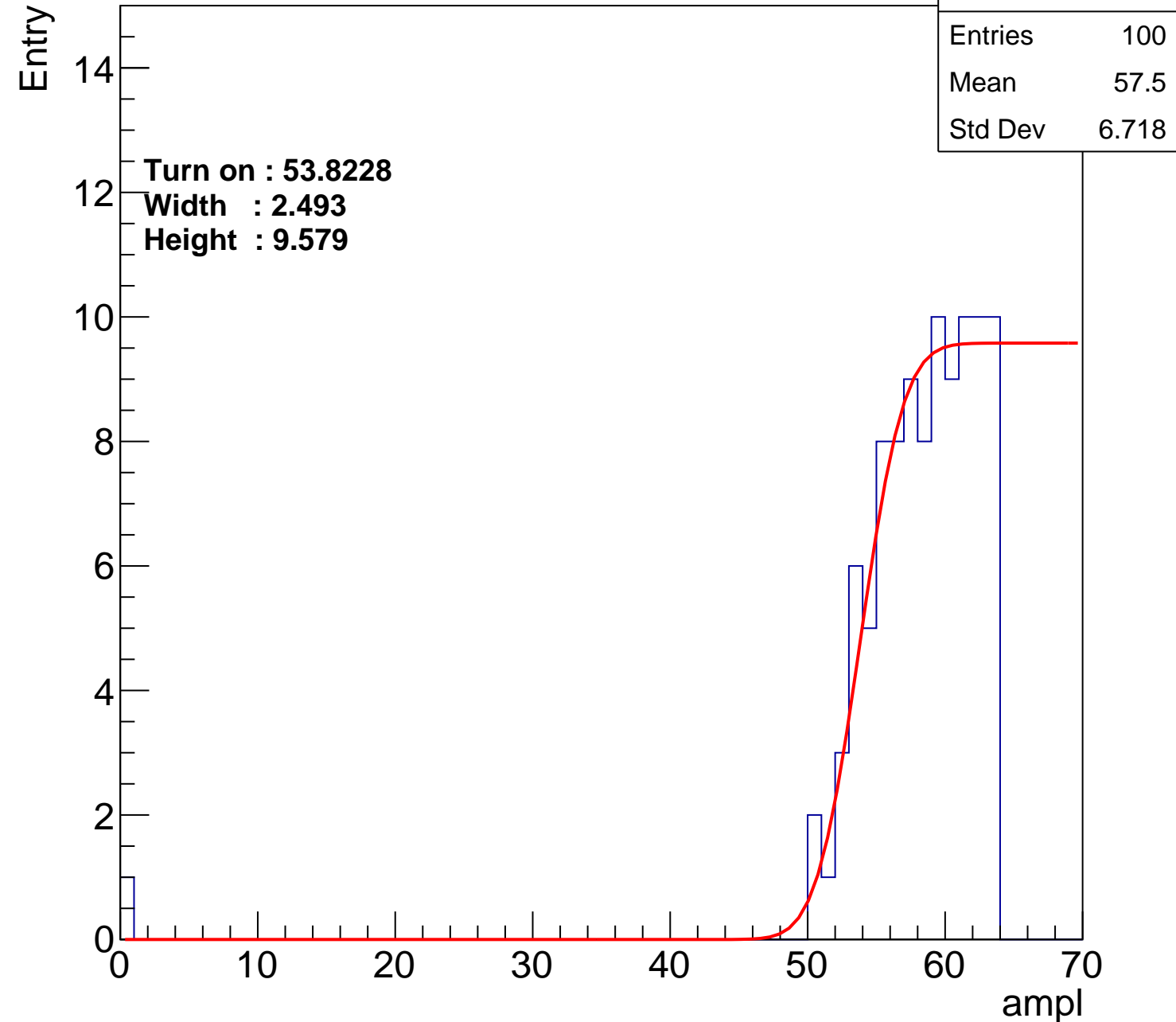
Width : 2.493

Height : 9.579

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch64

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.96
Std Dev	6.544

Turn on : 50.9673

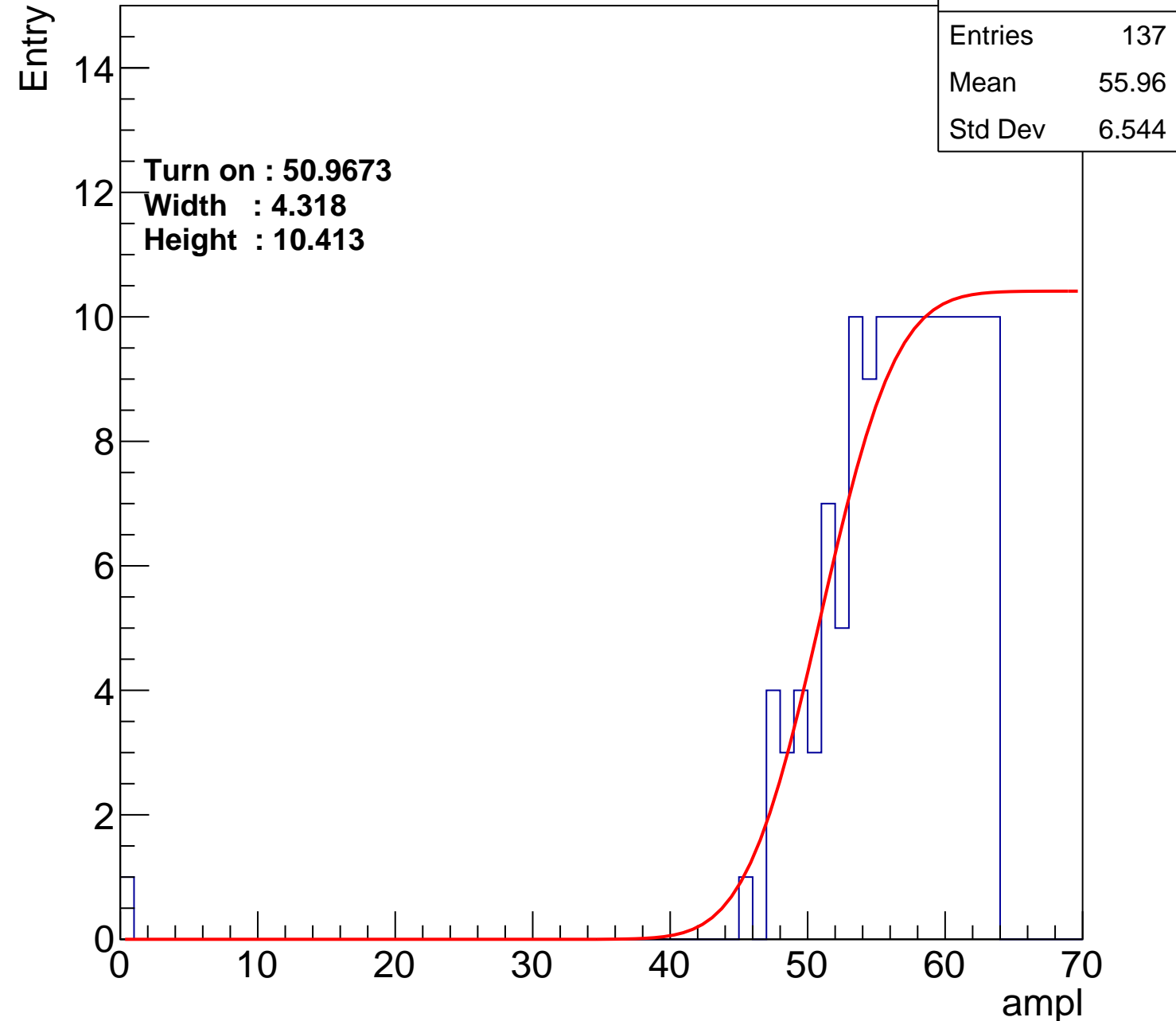
Width : 4.318

Height : 10.413

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch65

calib_packv5_040323_1717.root, FC#2, port C3

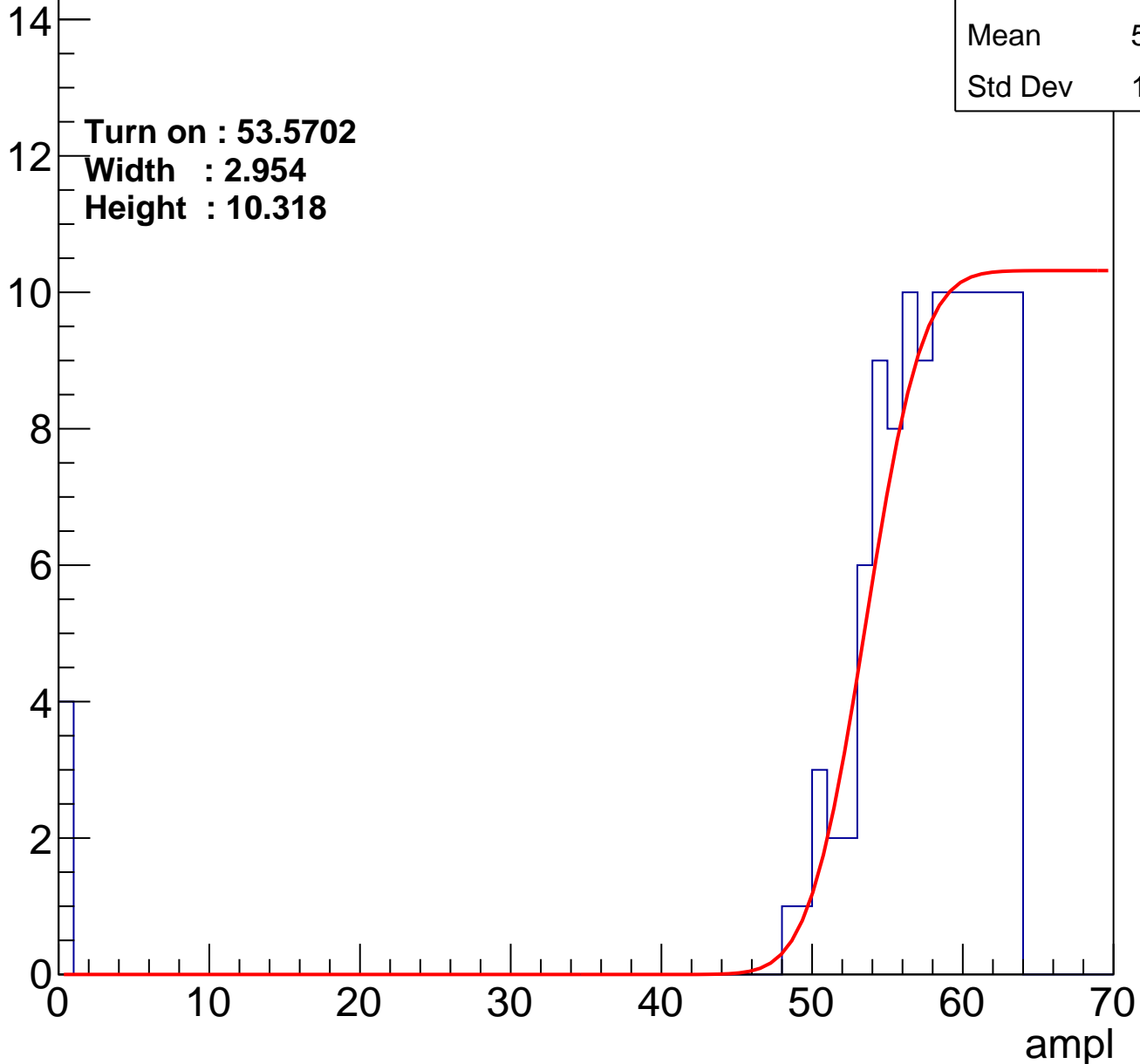
Entries	115
Mean	55.65
Std Dev	11.16

Turn on : 53.5702

Width : 2.954

Height : 10.318

Entry



B0L103S, U10-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.86
Std Dev	9.099

Turn on : 49.6978

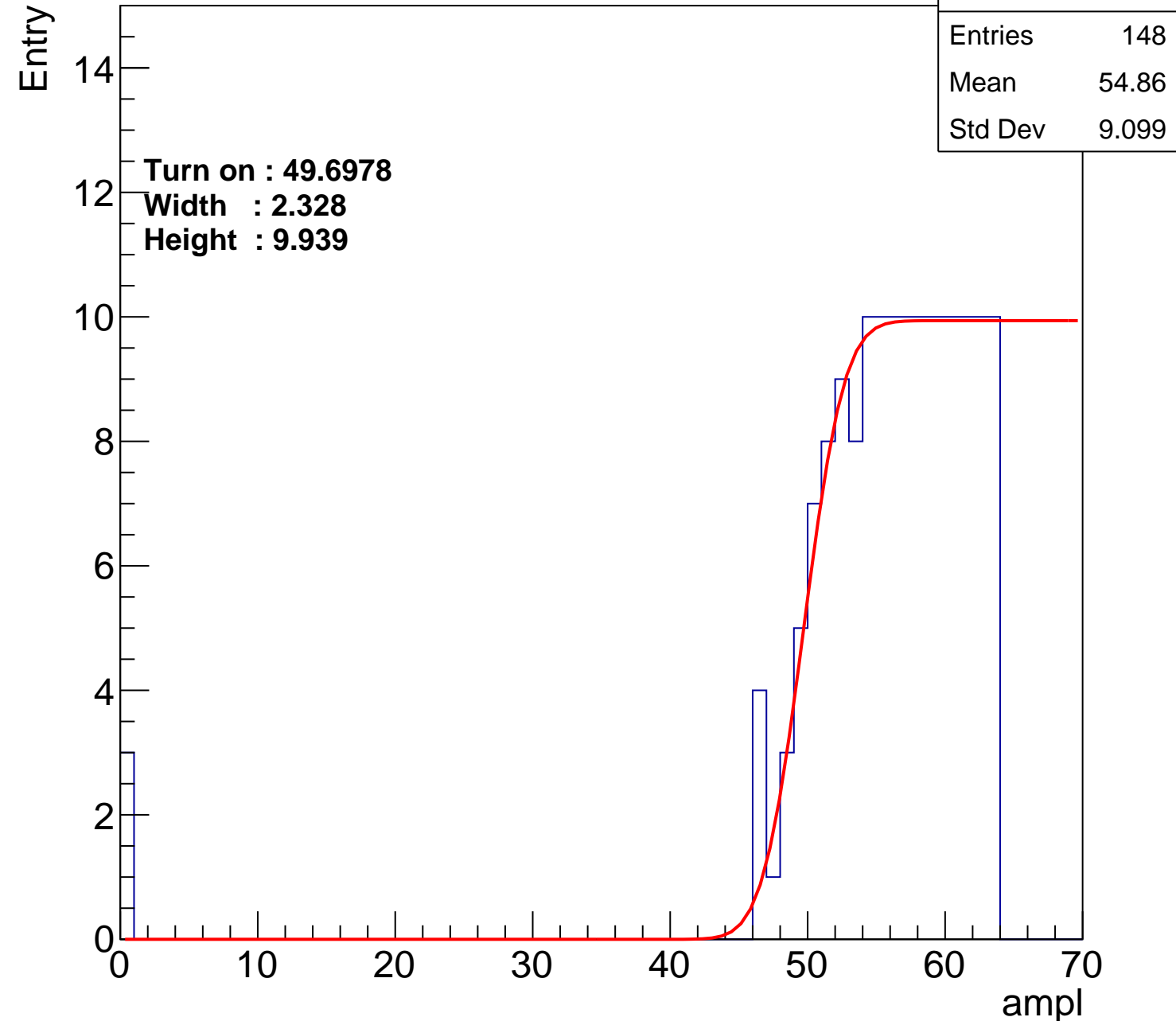
Width : 2.328

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.61
Std Dev	9.458

Turn on : 51.0605

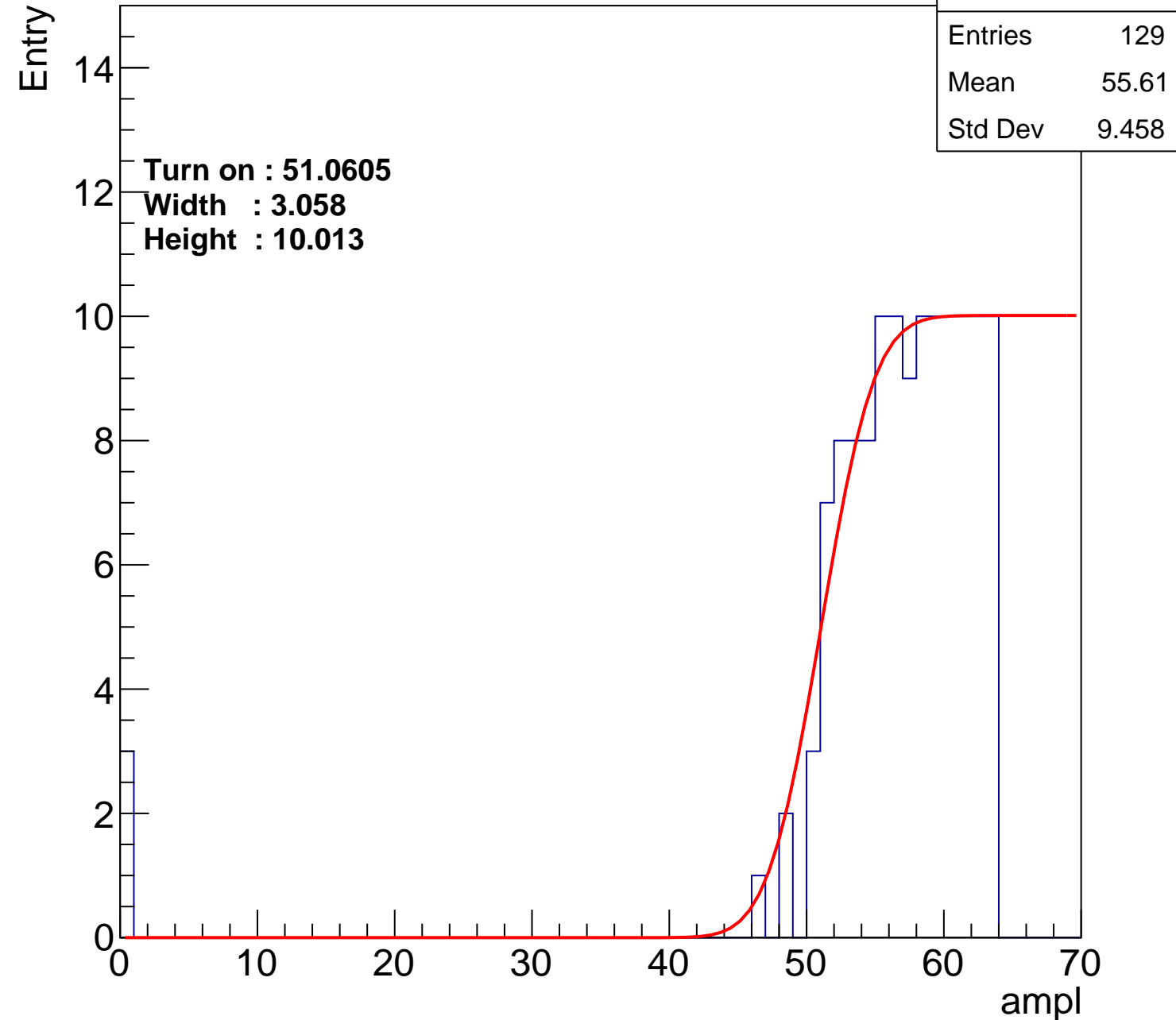
Width : 3.058

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	56.43
Std Dev	6.558

Turn on : 51.7666

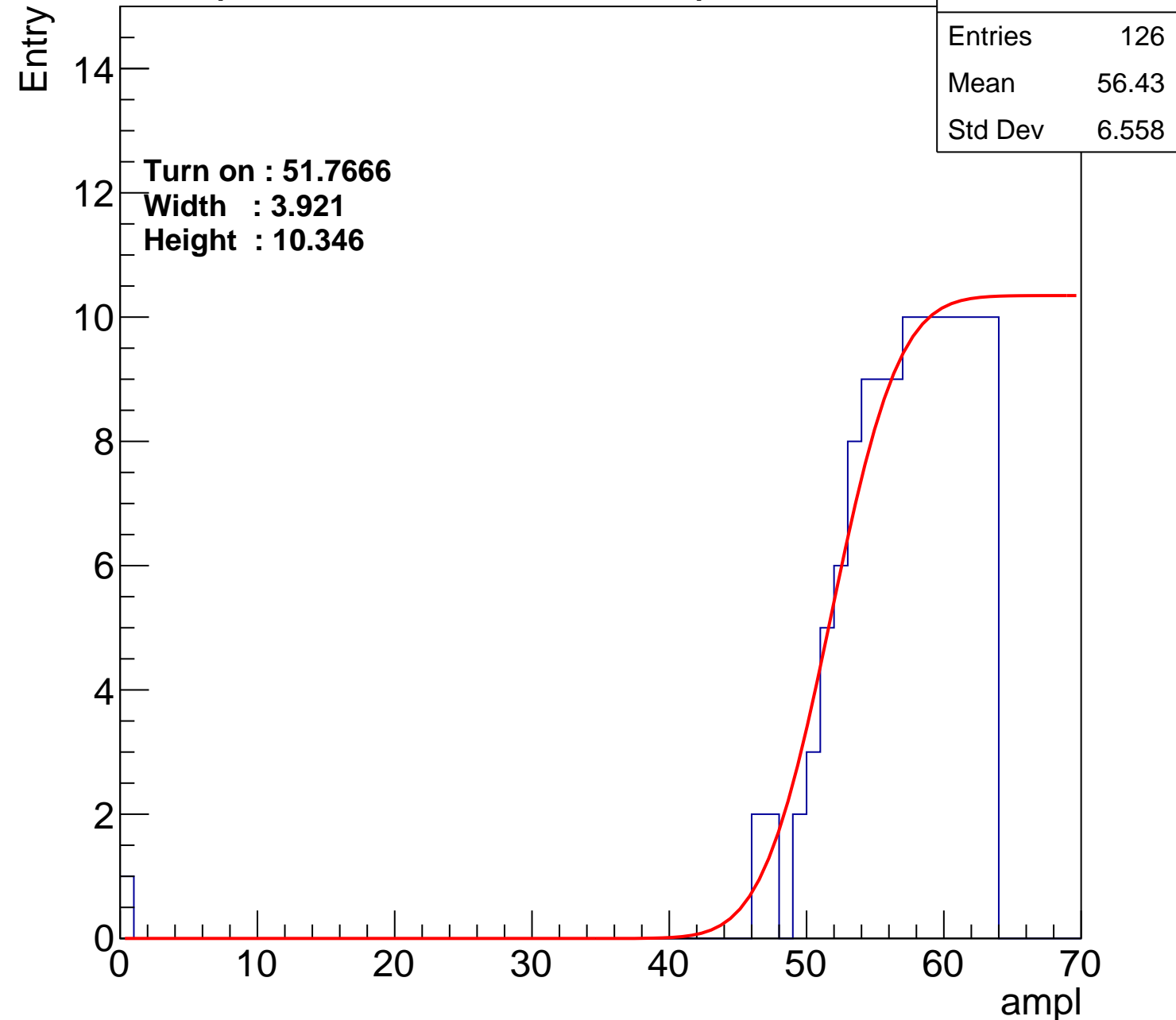
Width : 3.921

Height : 10.346

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch69

calib_packv5_040323_1717.root, FC#2, port C3

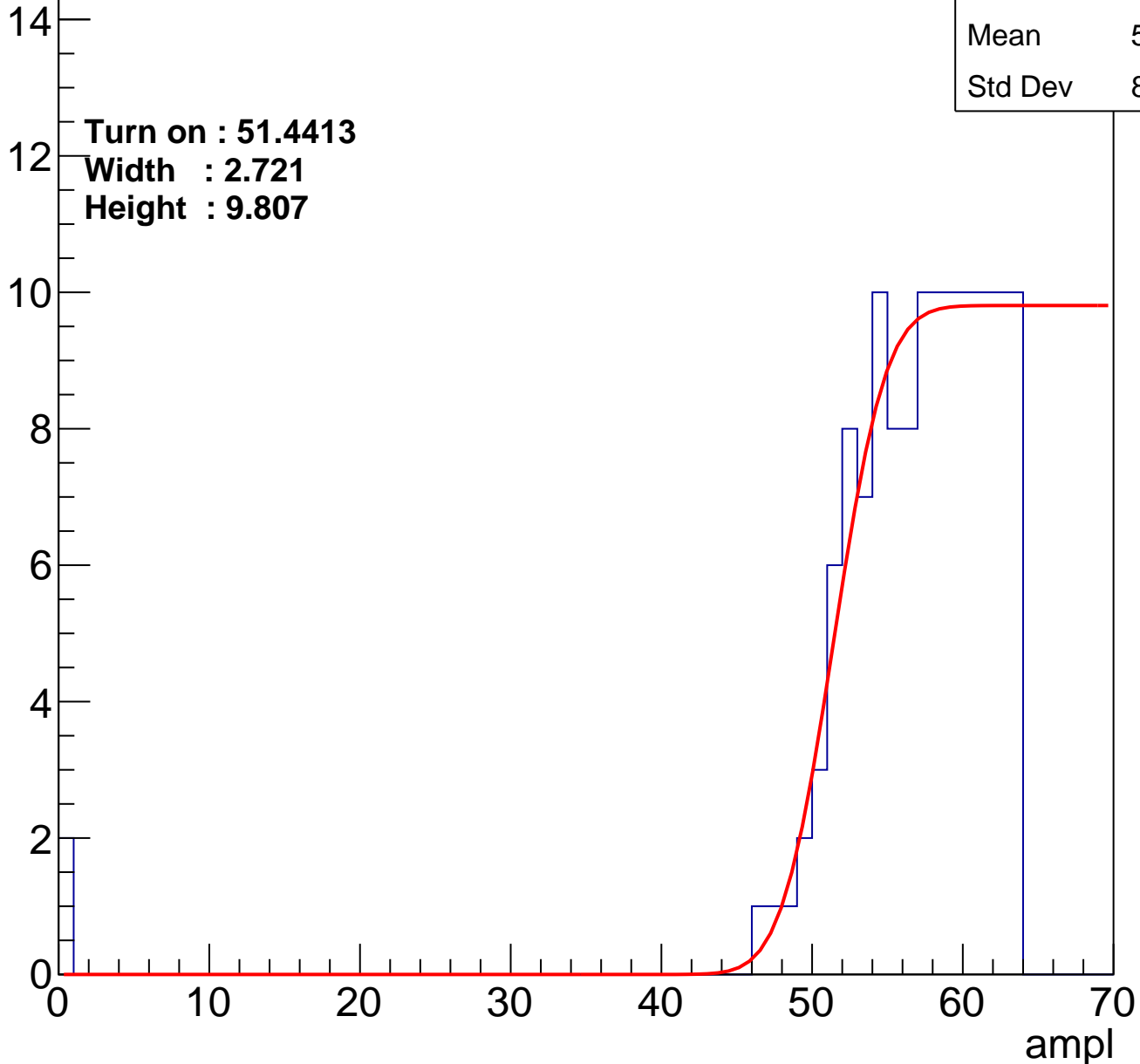
Entries	127
Mean	55.98
Std Dev	8.187

Turn on : 51.4413

Width : 2.721

Height : 9.807

Entry



B0L103S, U10-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.13
Std Dev	9.369

Turn on : 50.3219

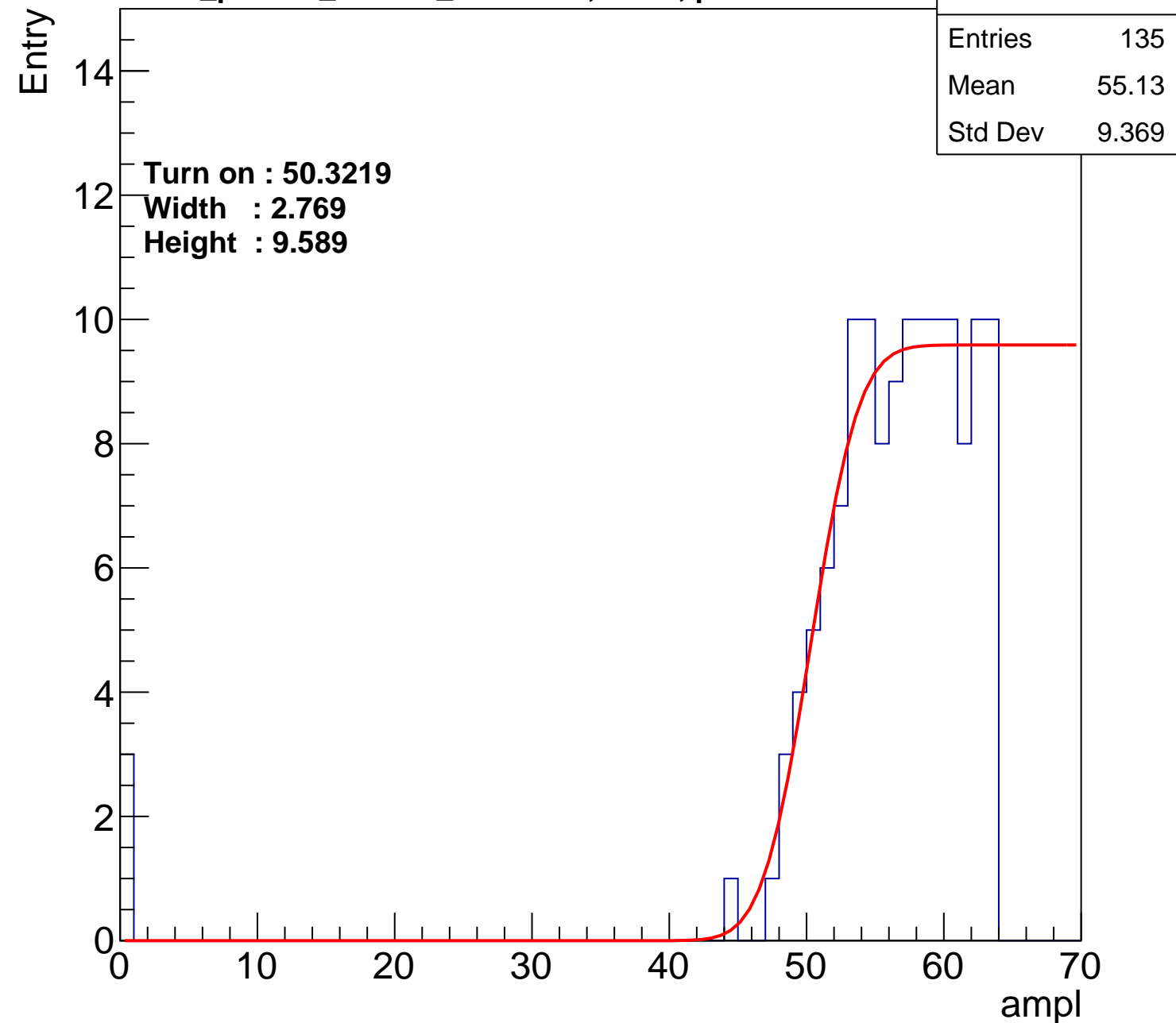
Width : 2.769

Height : 9.589

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch71

calib_packv5_040323_1717.root, FC#2, port C3

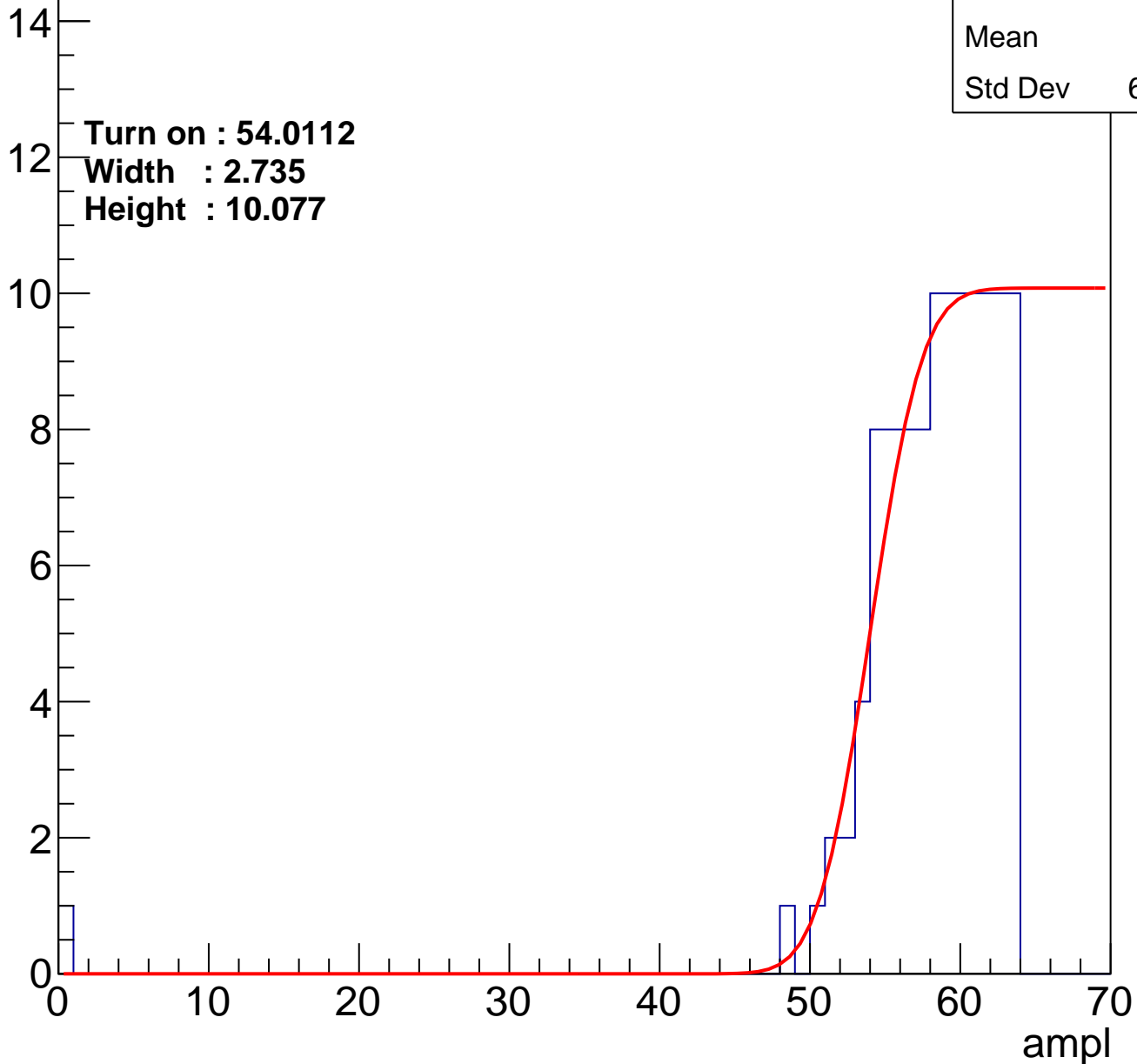
Entries	103
Mean	57.5
Std Dev	6.655

Turn on : 54.0112

Width : 2.735

Height : 10.077

Entry



B0L103S, U10-ch72

calib_packv5_040323_1717.root, FC#2, port C3

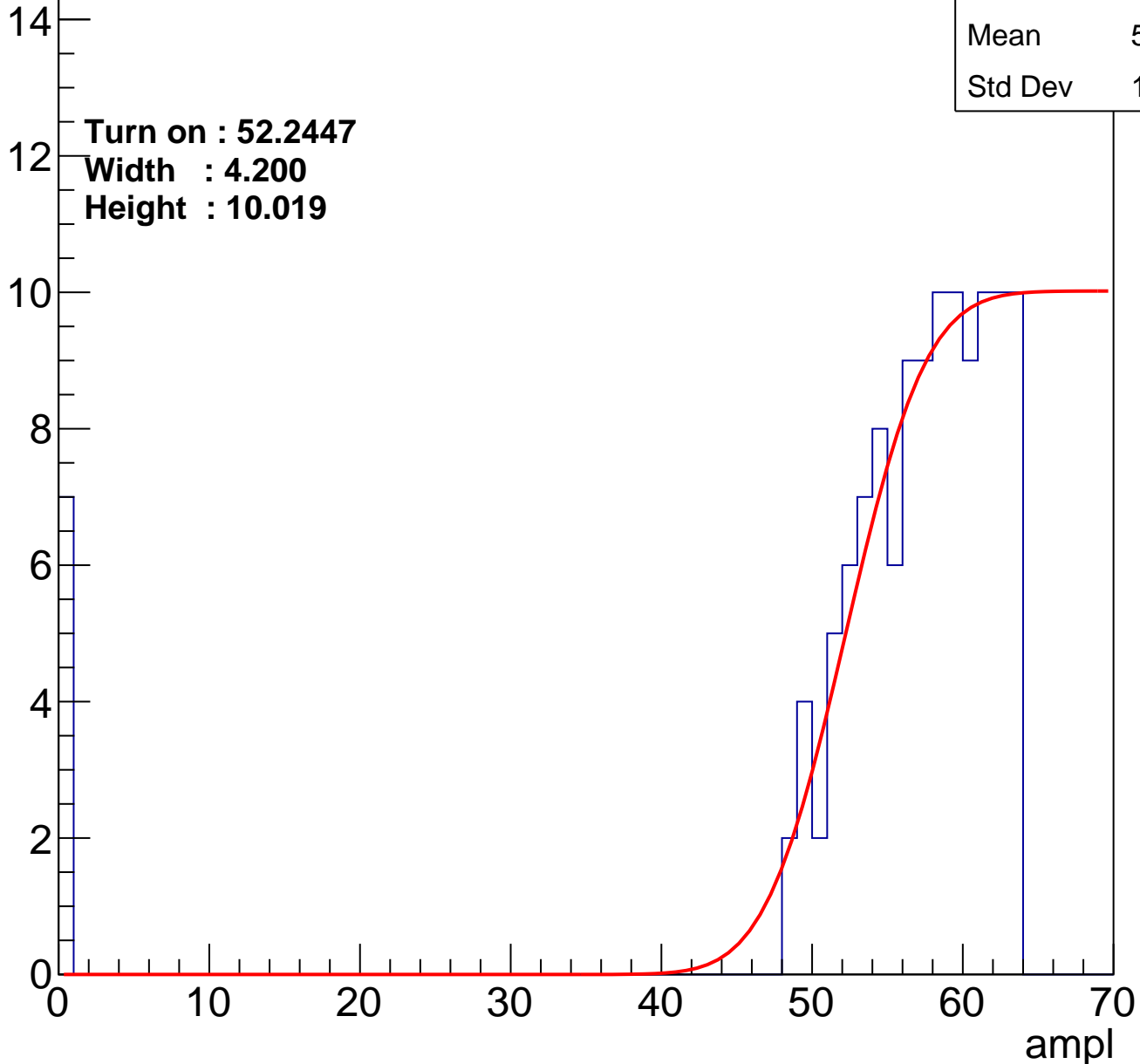
Entries	124
Mean	53.86
Std Dev	13.76

Turn on : 52.2447

Width : 4.200

Height : 10.019

Entry



B0L103S, U10-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	106
Mean	57.25
Std Dev	6.752

Turn on : 54.0988

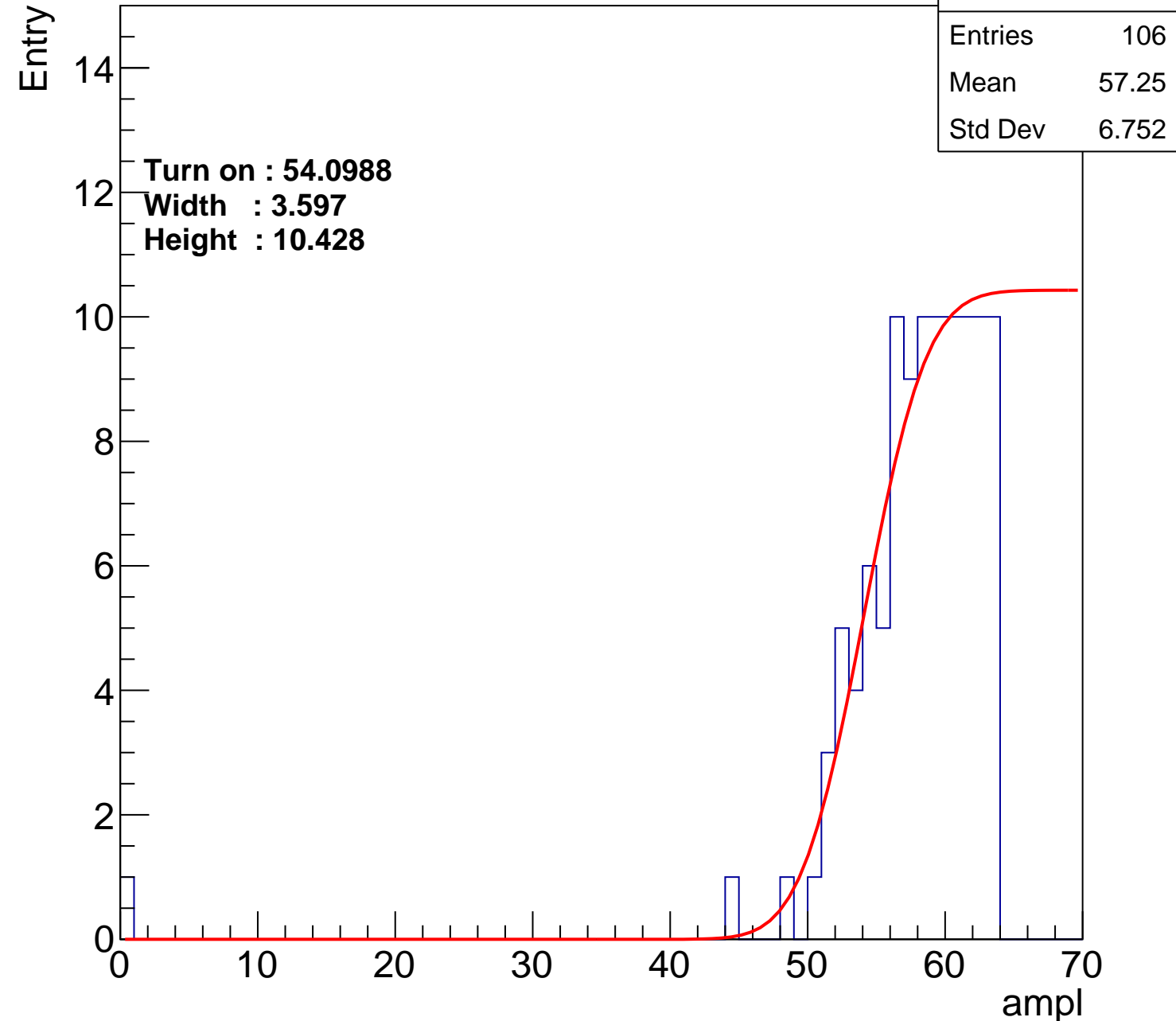
Width : 3.597

Height : 10.428

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch74

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.69
Std Dev	6.539

Turn on : 51.7334

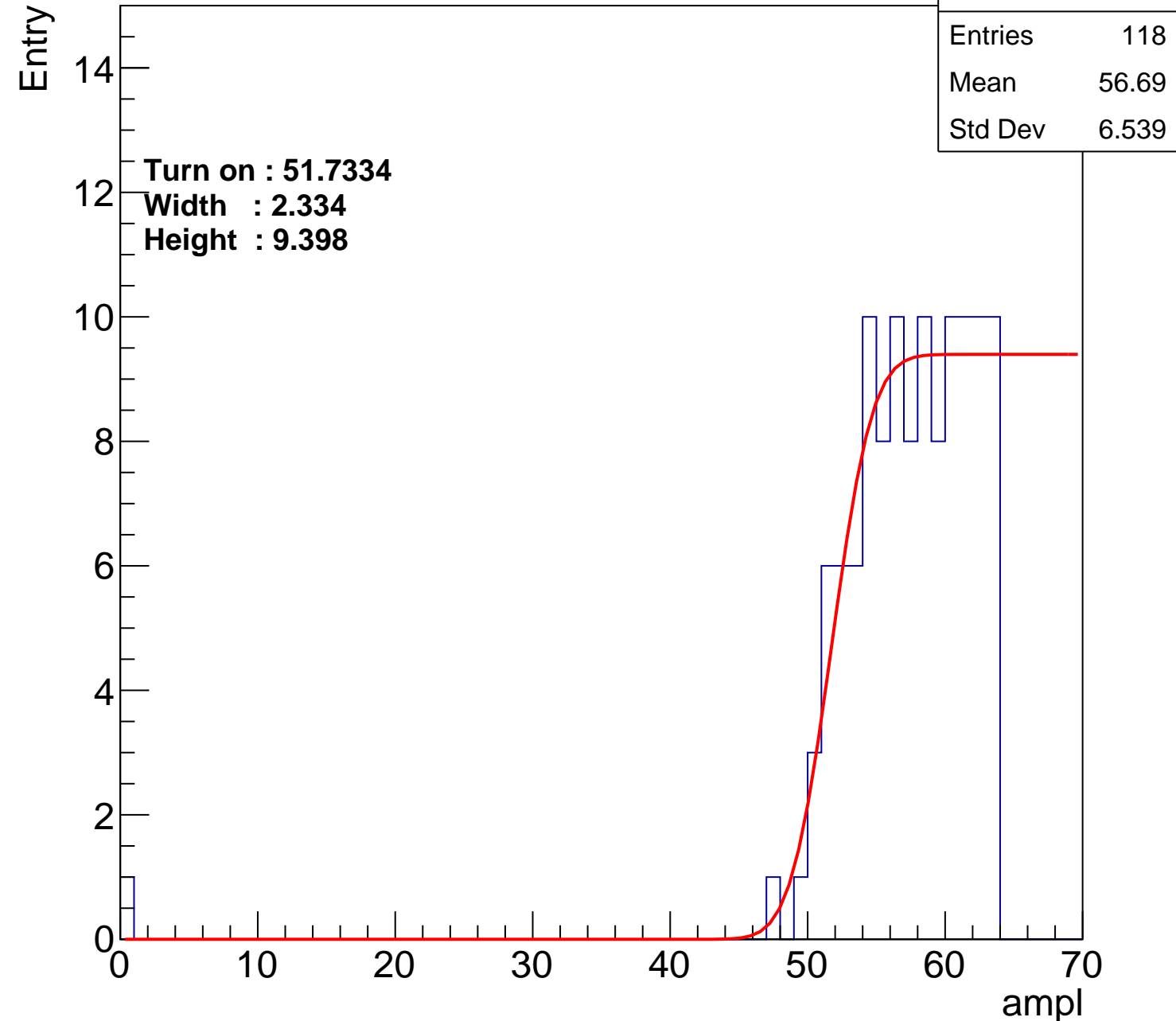
Width : 2.334

Height : 9.398

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	54.85
Std Dev	9.306

Turn on : 50.9826

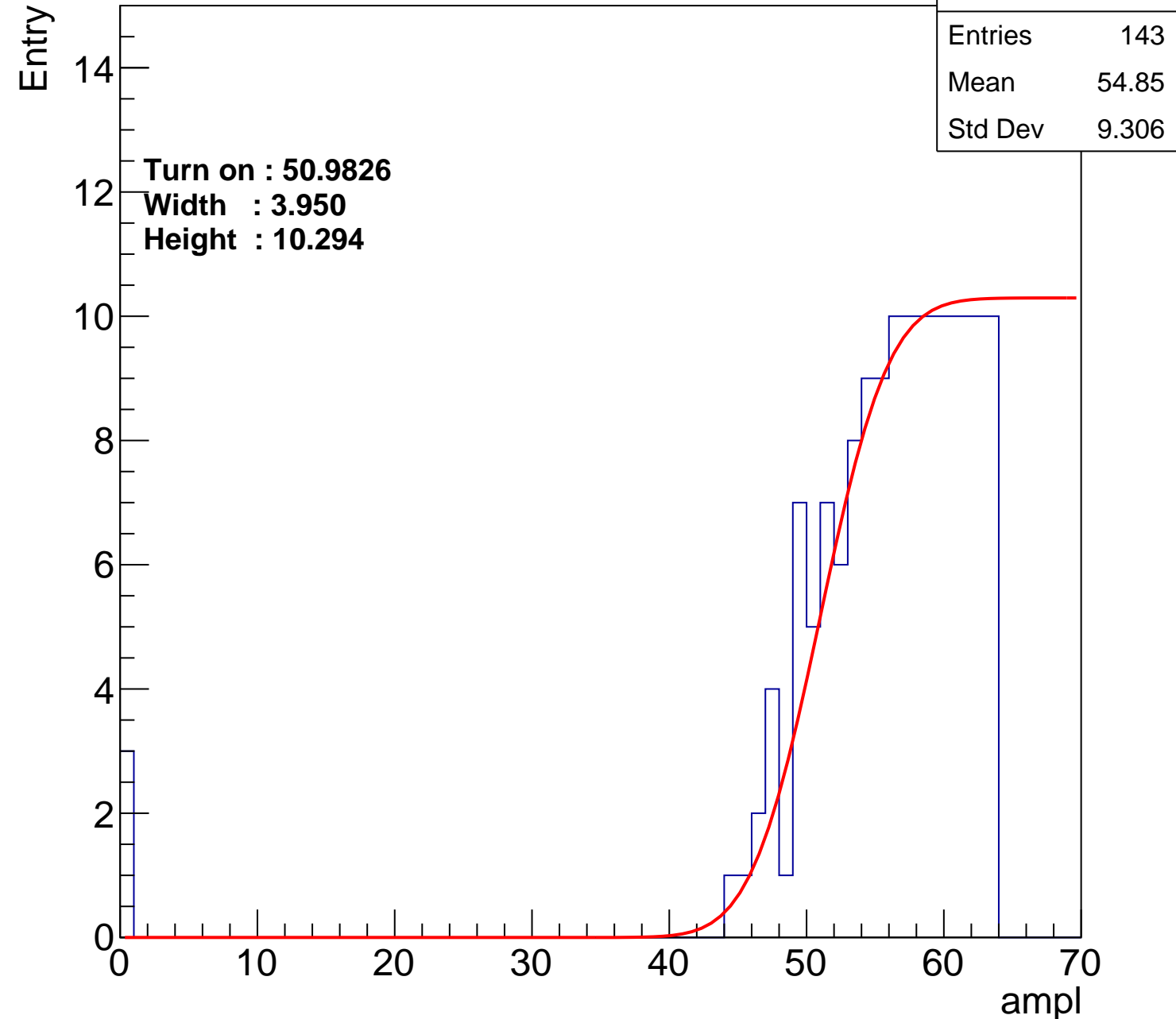
Width : 3.950

Height : 10.294

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch76

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.95
Std Dev	10.61

Turn on : 51.6321

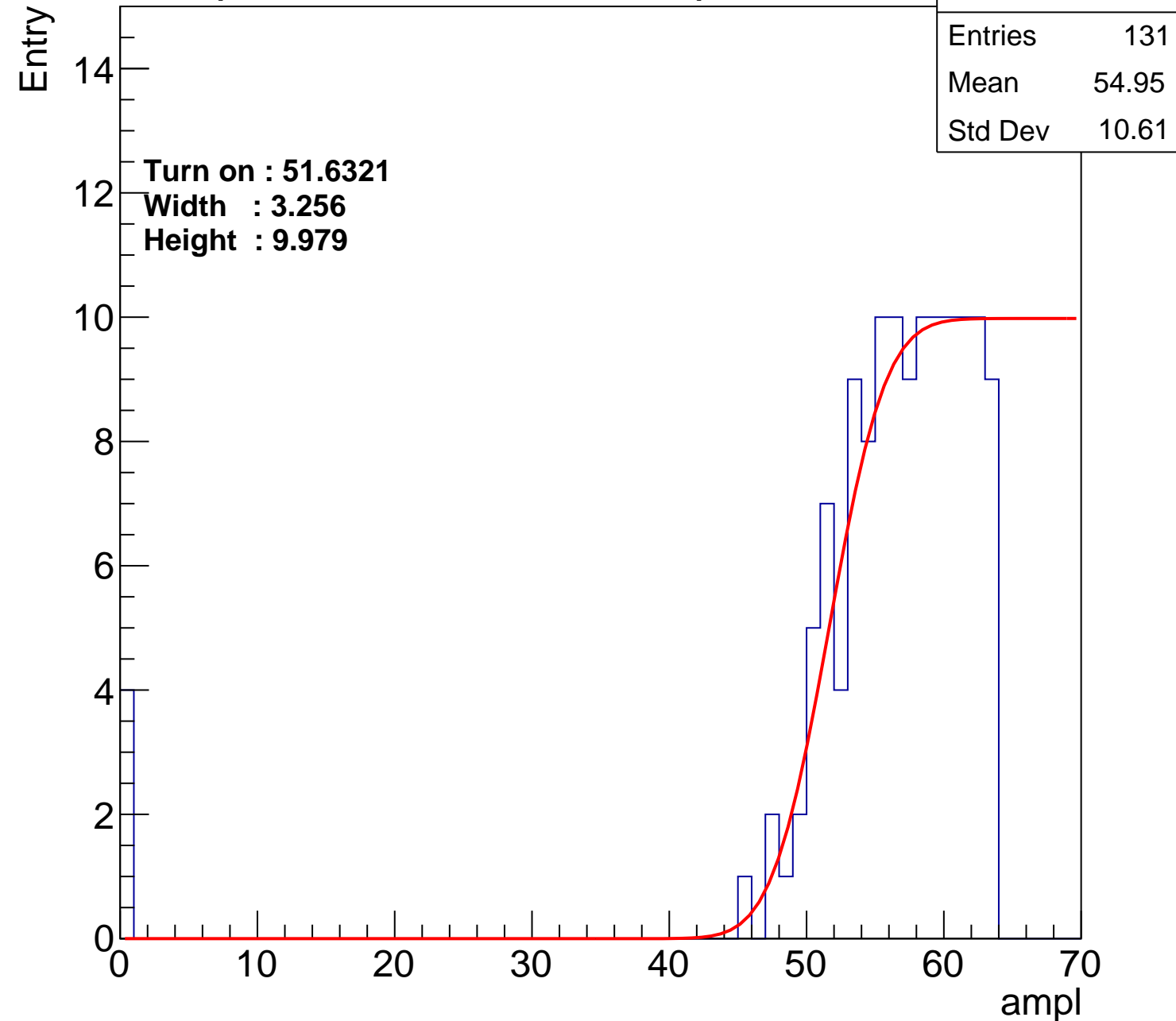
Width : 3.256

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch77

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.88
Std Dev	10.57

Turn on : 51.3936

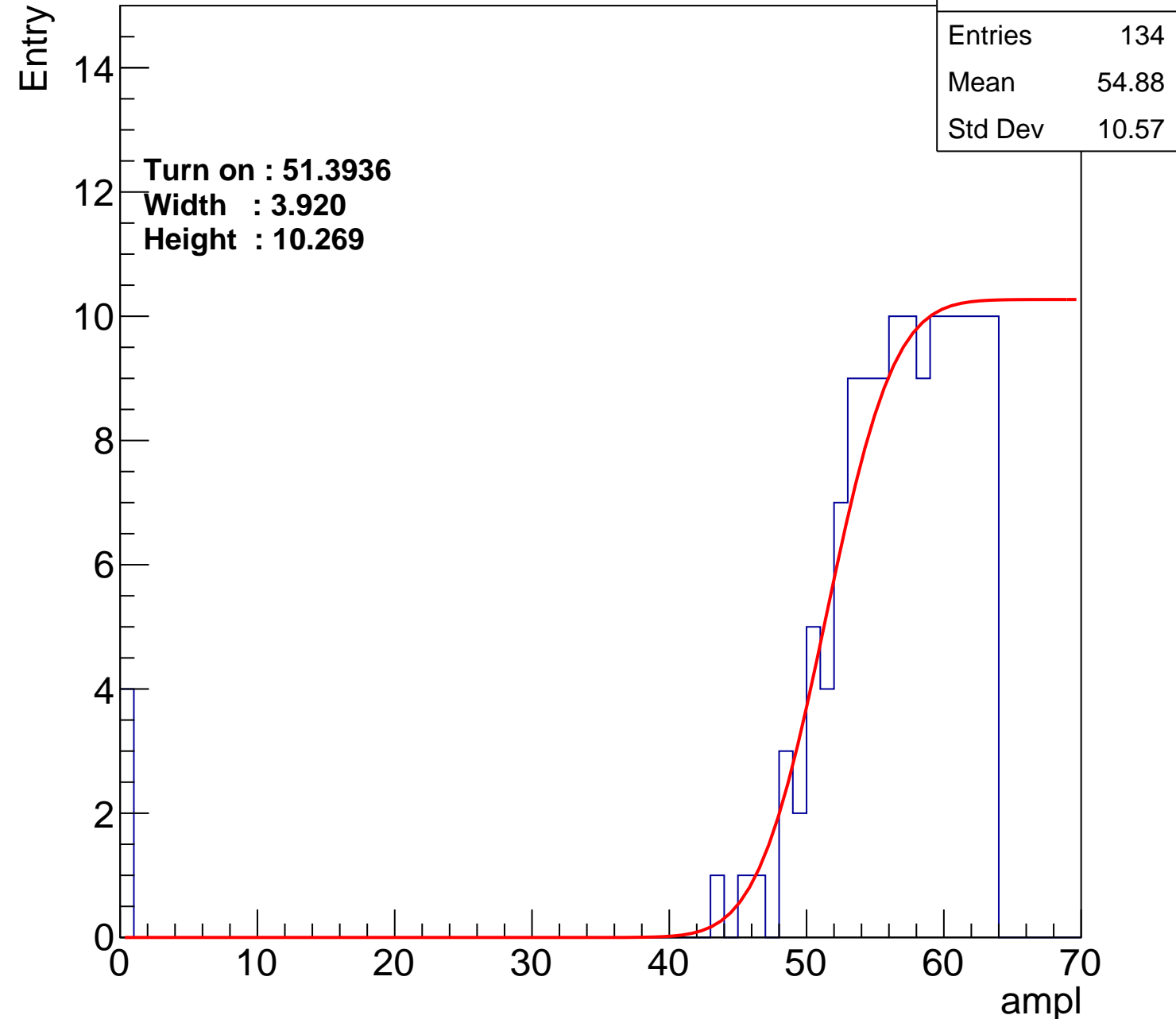
Width : 3.920

Height : 10.269

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch78

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.9
Std Dev	9.073

Turn on : 49.6182

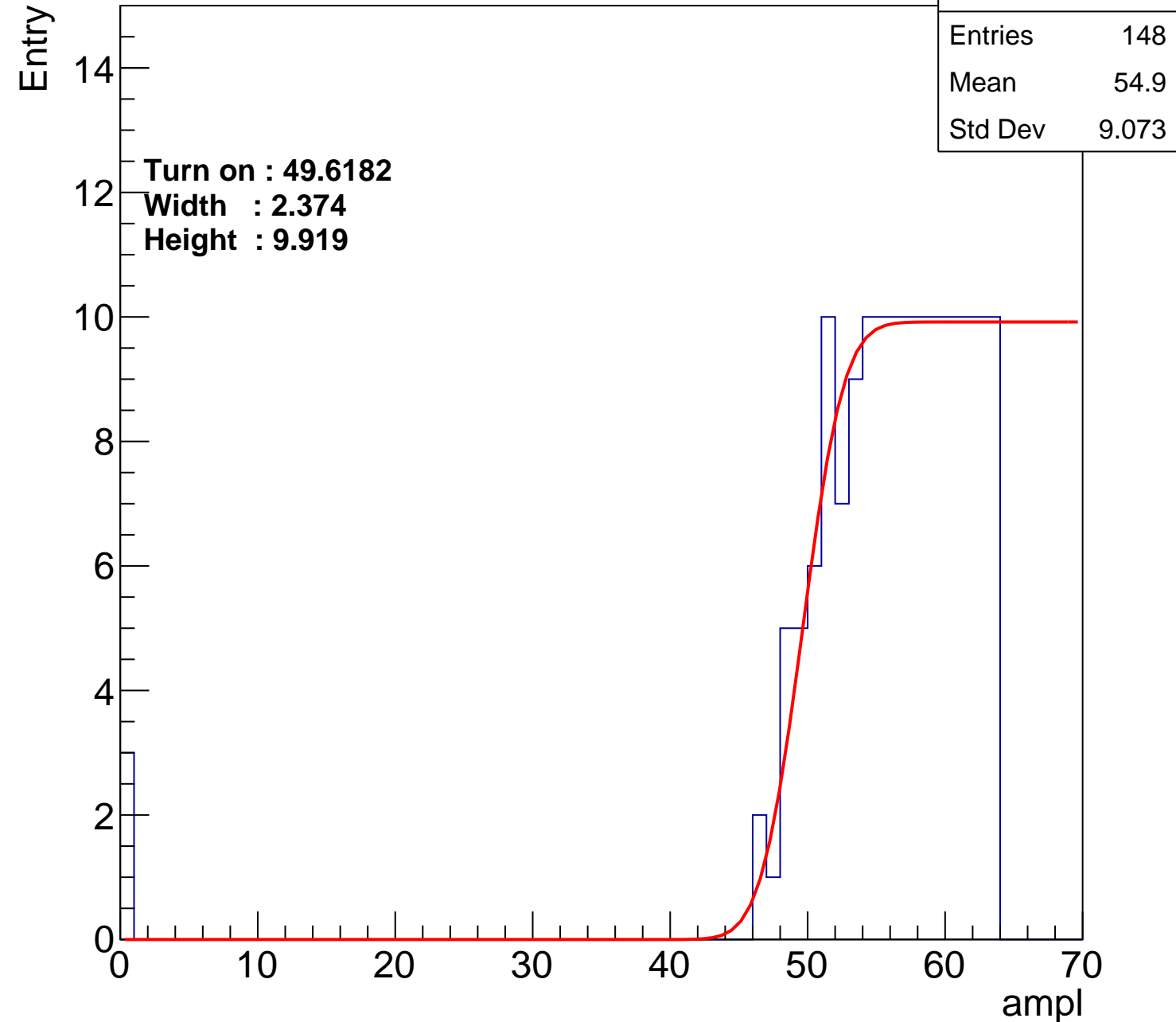
Width : 2.374

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	55.73
Std Dev	11.23

Turn on : 53.1837

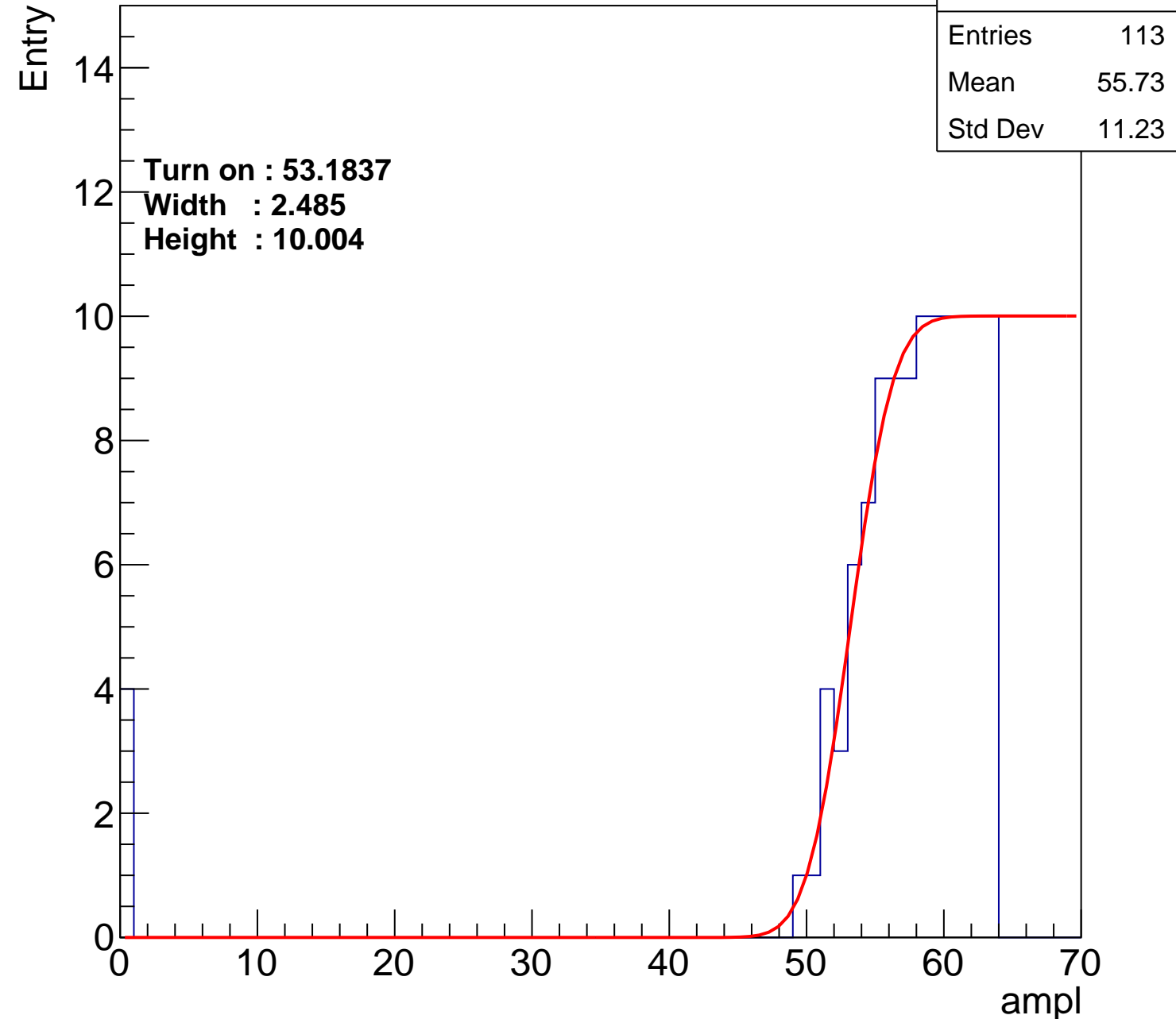
Width : 2.485

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch80

calib_packv5_040323_1717.root, FC#2, port C3

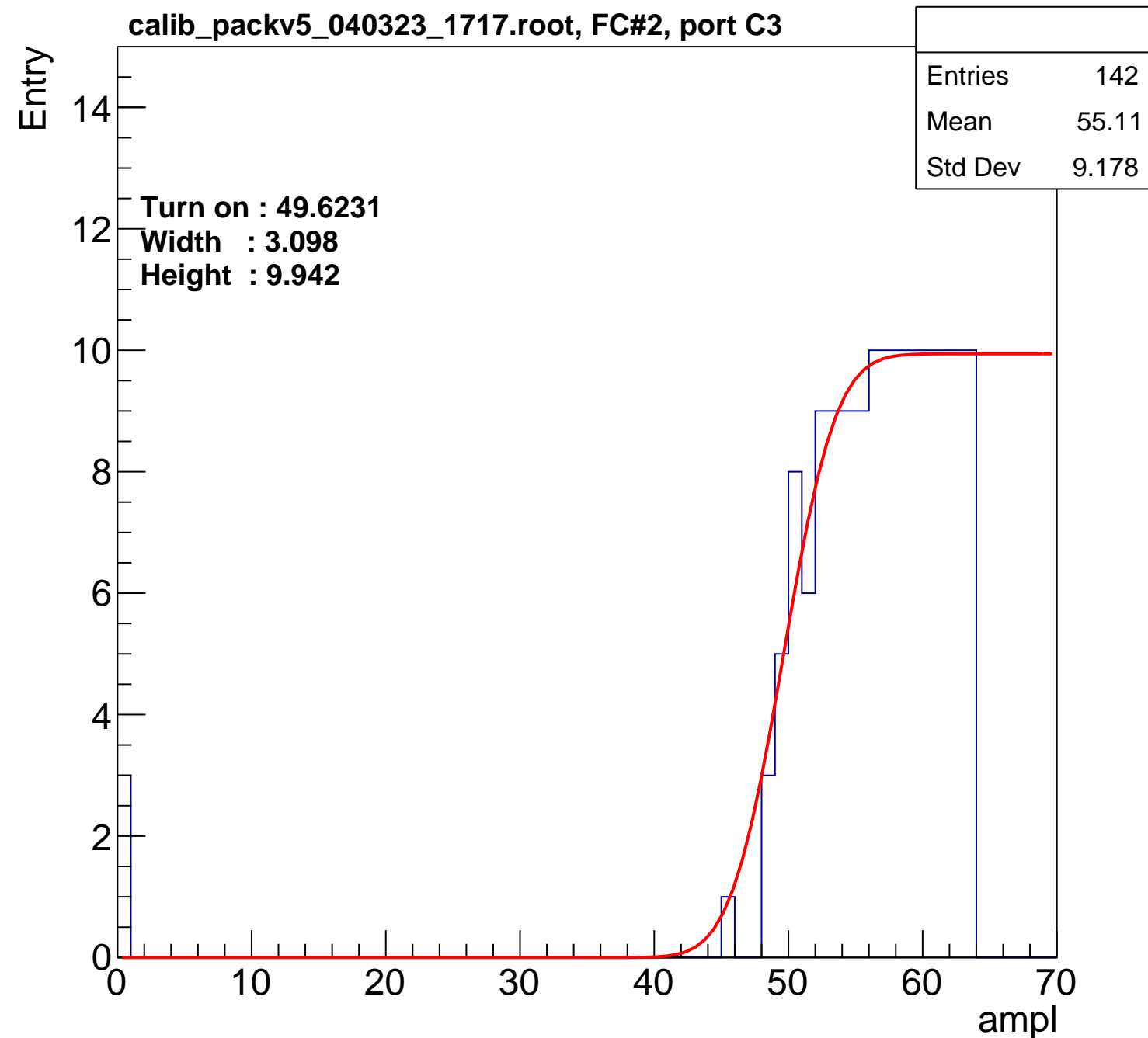
Entry

14
12
10
8
6
4
2
0

Turn on : 49.6231
Width : 3.098
Height : 9.942

Entries	142
Mean	55.11
Std Dev	9.178

ampl



B0L103S, U10-ch81

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.35
Std Dev	10.82

Turn on : 51.9756

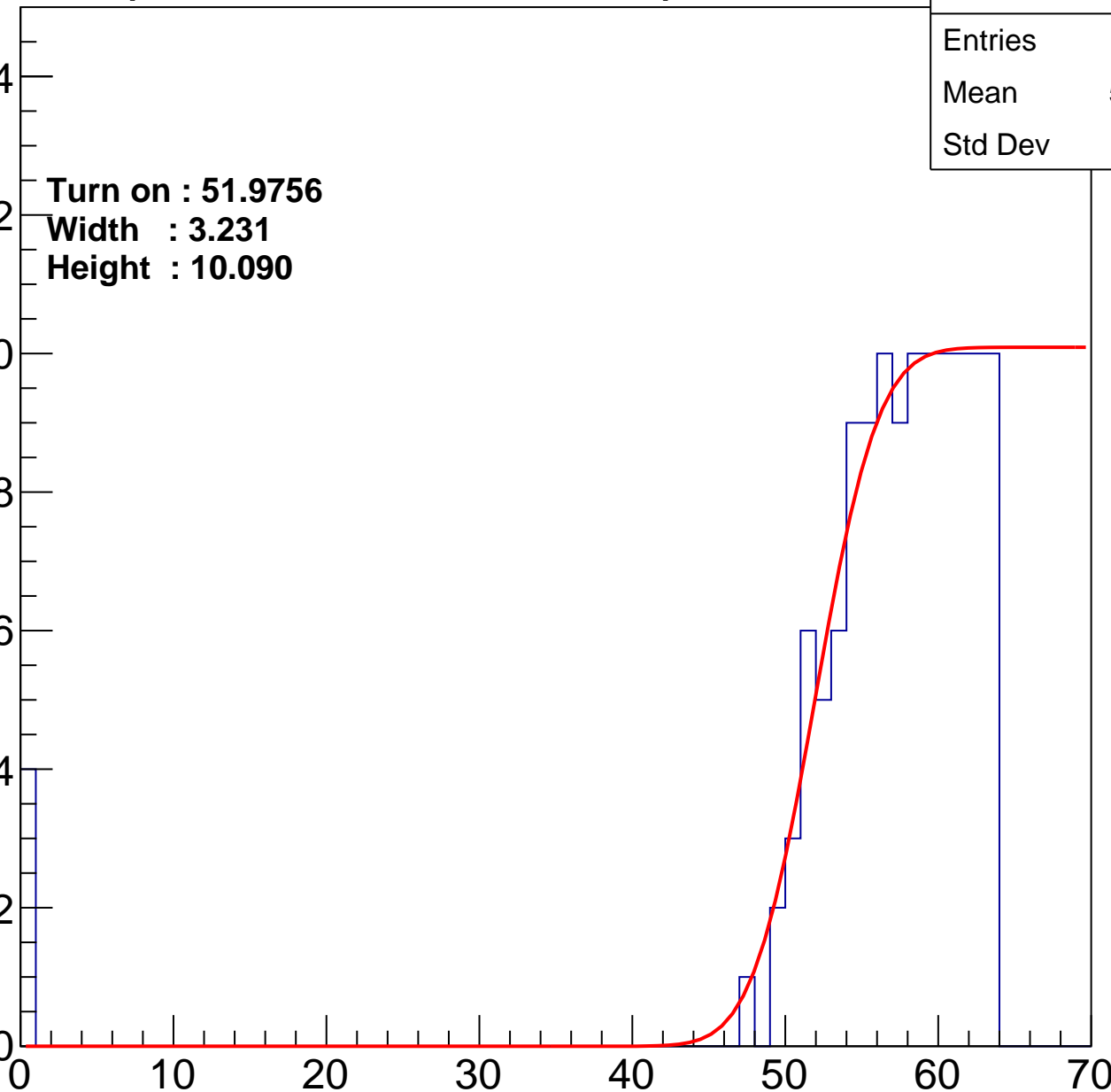
Width : 3.231

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.77
Std Dev	6.488

Turn on : 52.1485

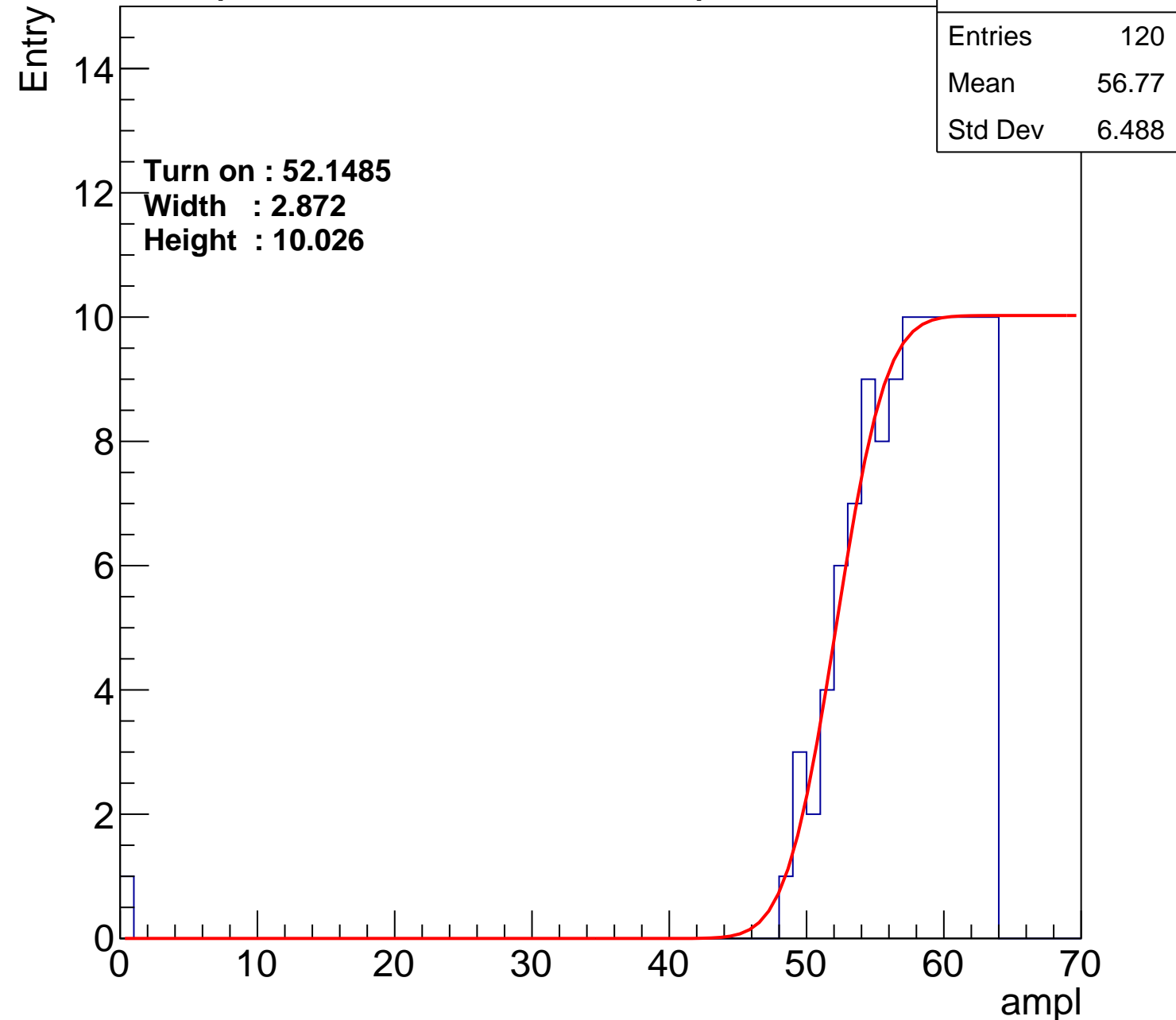
Width : 2.872

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch83

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.95
Std Dev	8.037

Turn on : 51.1330

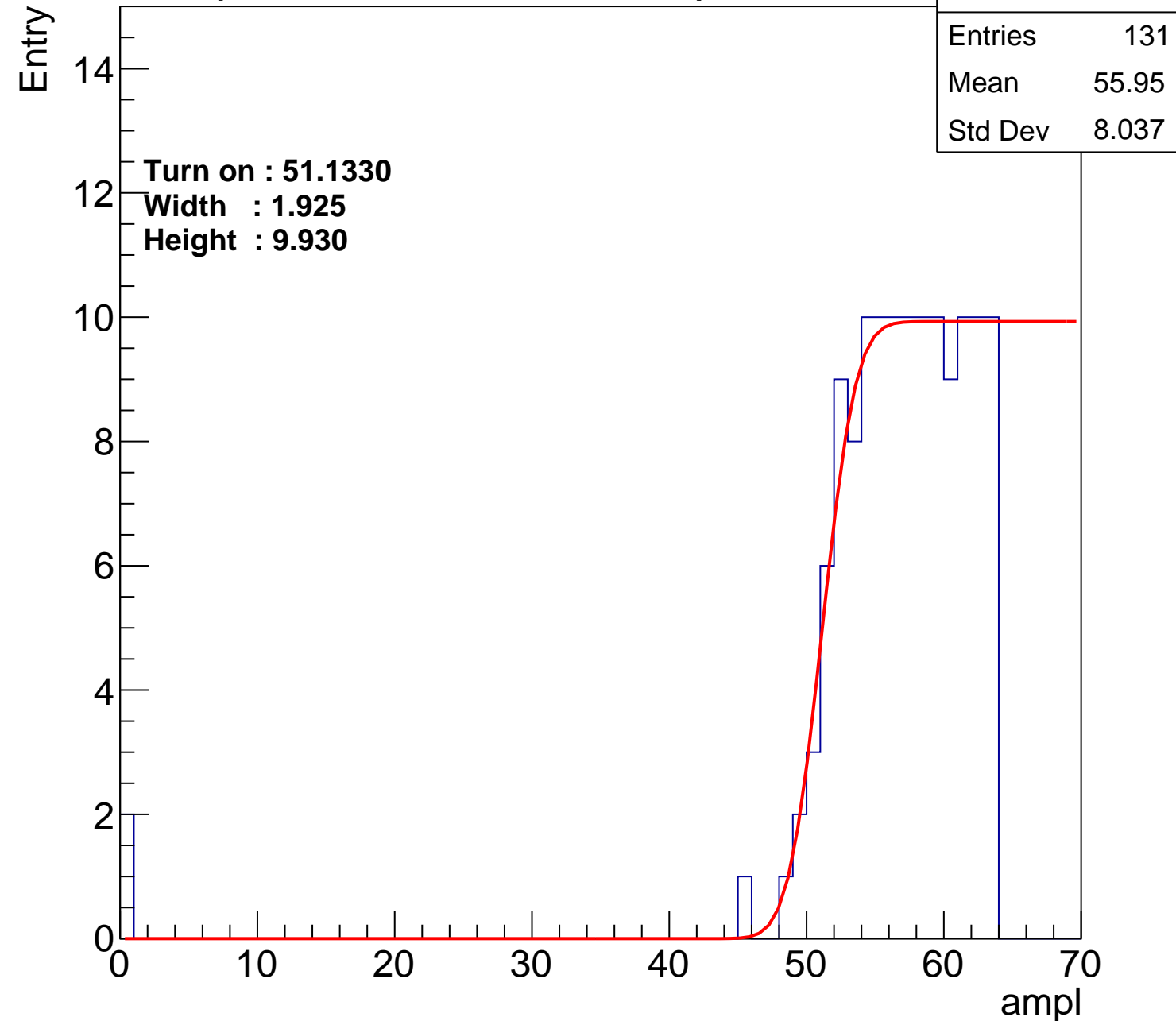
Width : 1.925

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch84

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.27
Std Dev	12.38

Turn on : 50.4181

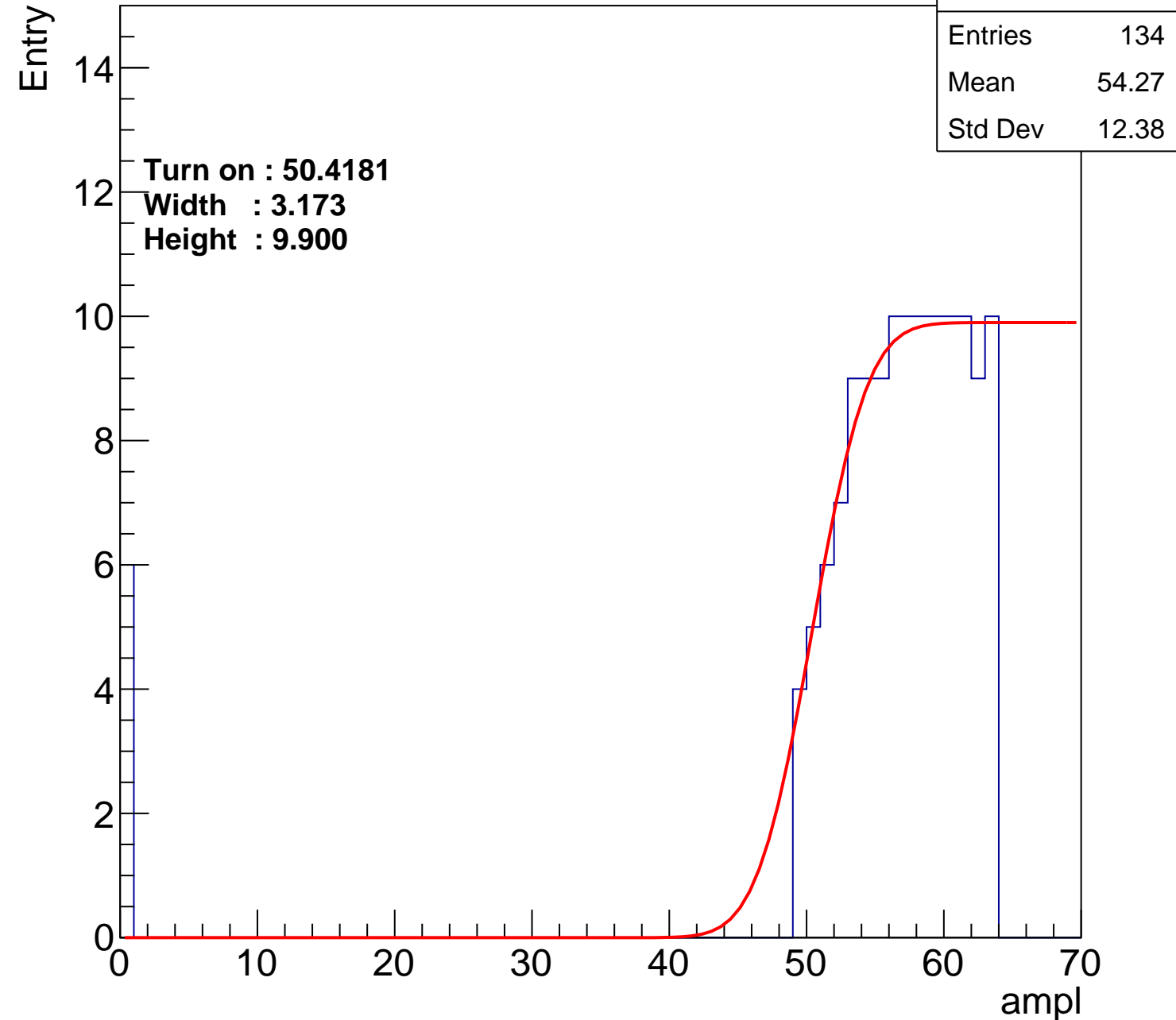
Width : 3.173

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.45
Std Dev	9.489

Turn on : 52.1221

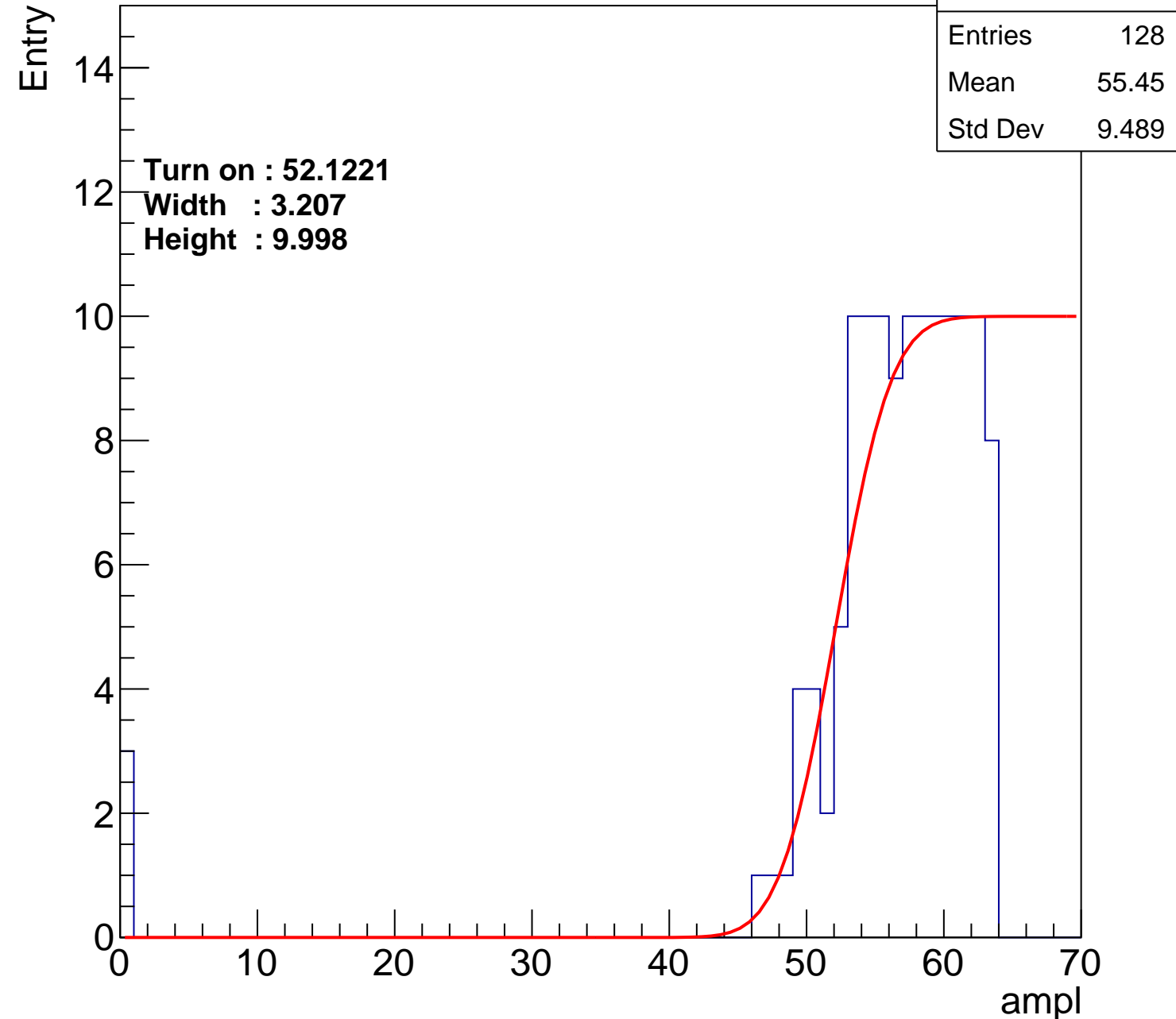
Width : 3.207

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch86

calib_packv5_040323_1717.root, FC#2, port C3

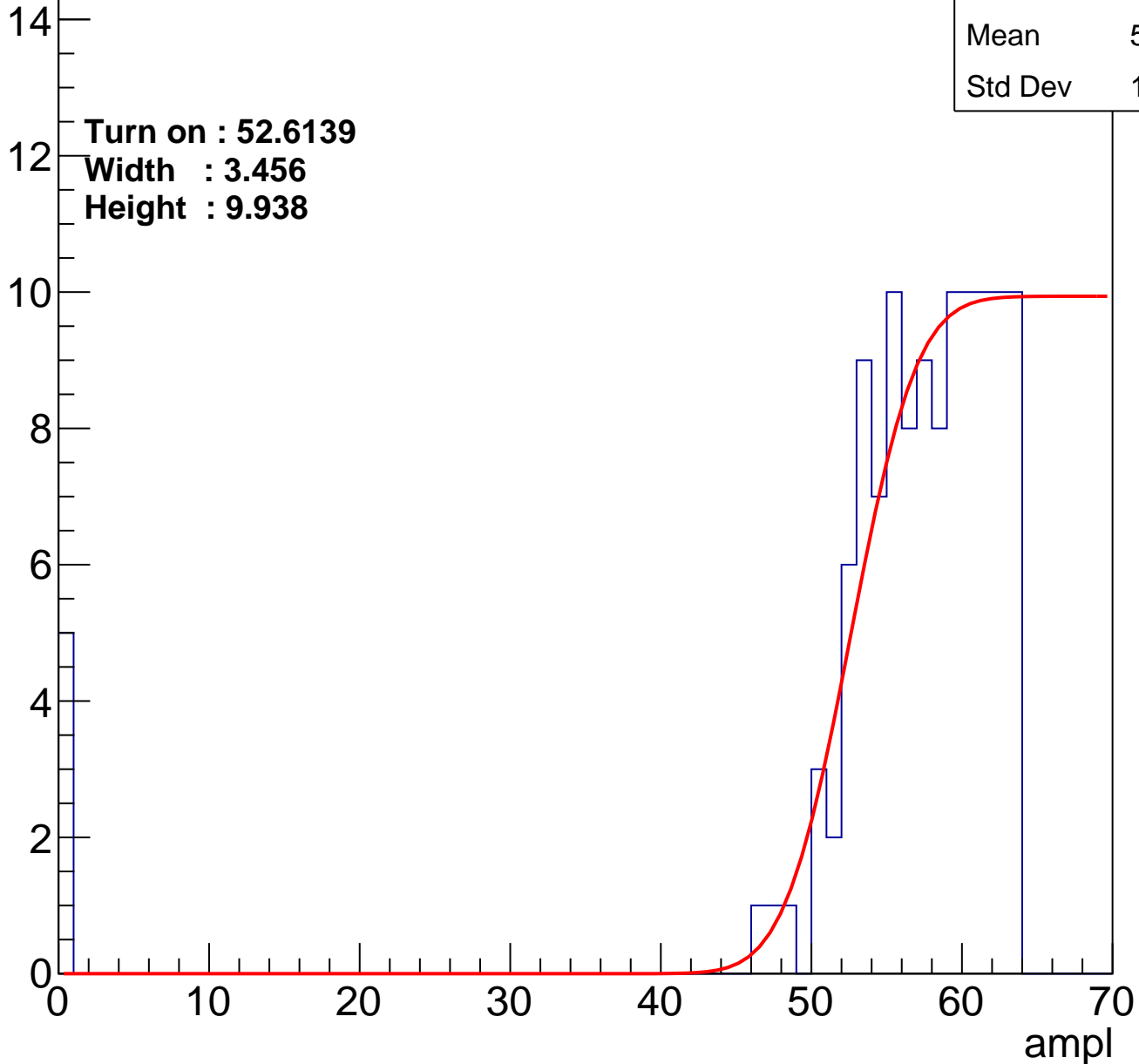
Entries	120
Mean	54.88
Std Dev	12.09

Turn on : 52.6139

Width : 3.456

Height : 9.938

Entry



B0L103S, U10-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	56.22
Std Dev	8.323

Turn on : 53.1684

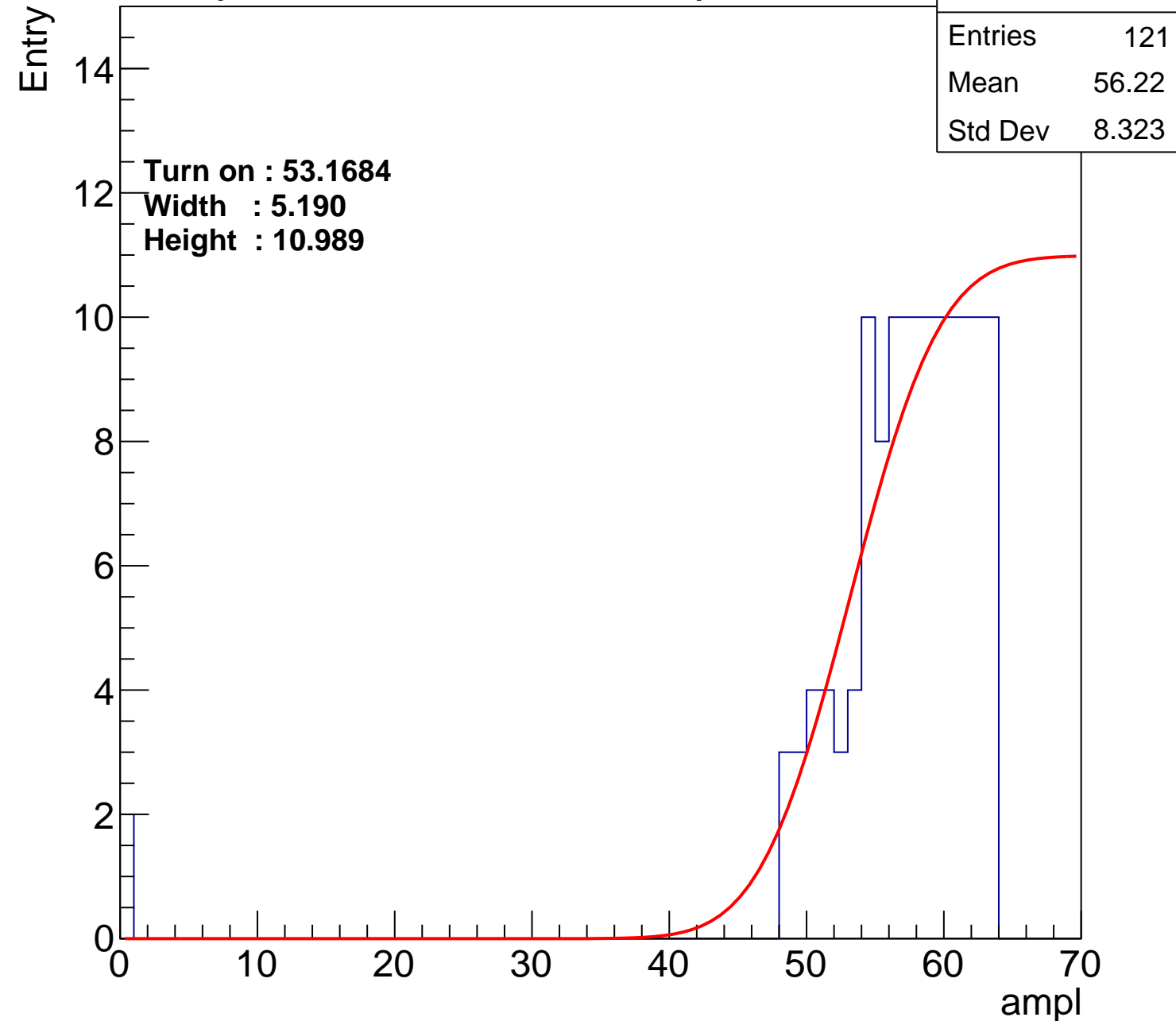
Width : 5.190

Height : 10.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	54.97
Std Dev	11.9

Turn on : 52.5194

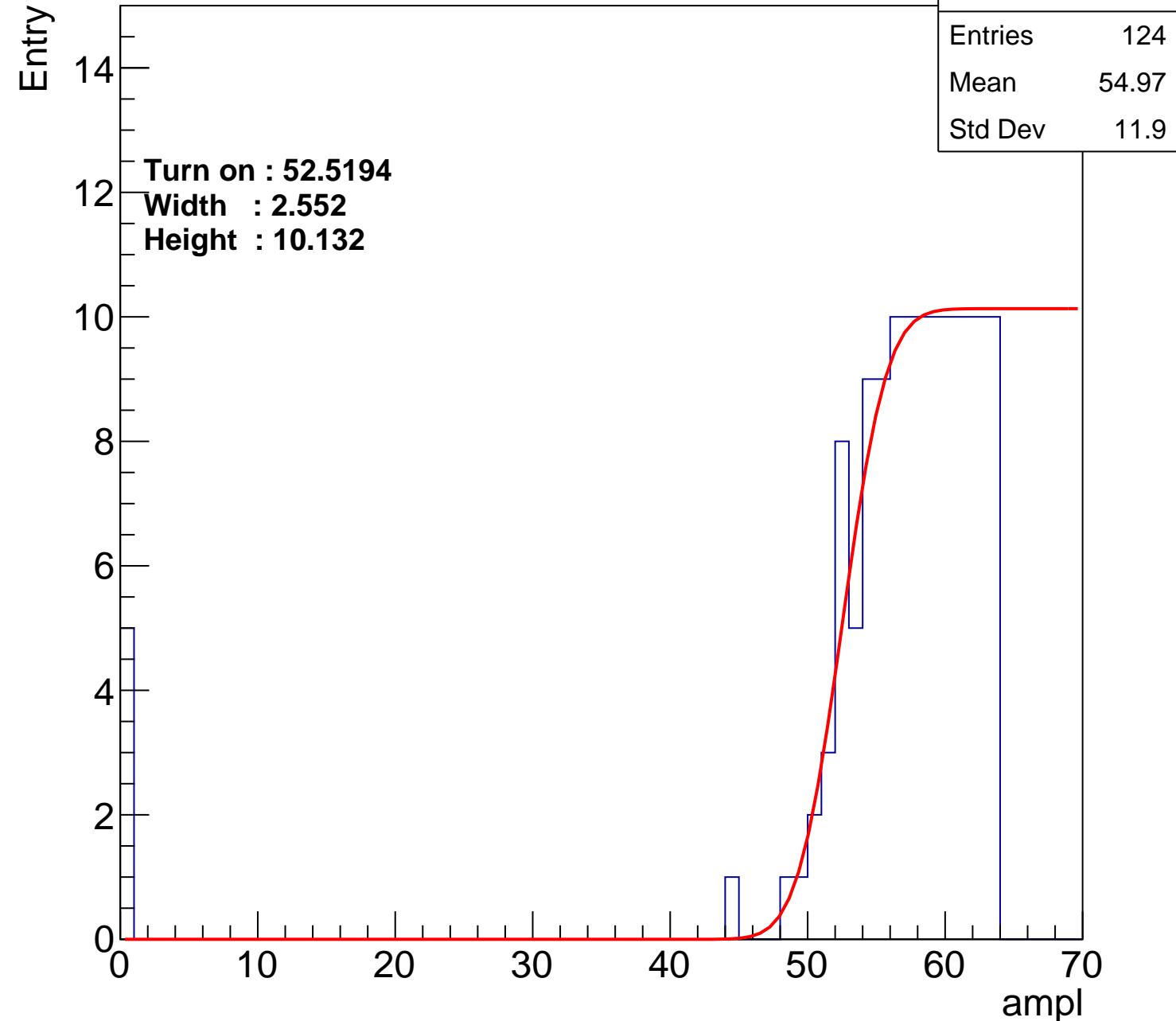
Width : 2.552

Height : 10.132

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch89

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	56.17
Std Dev	6.525

Turn on : 51.5992

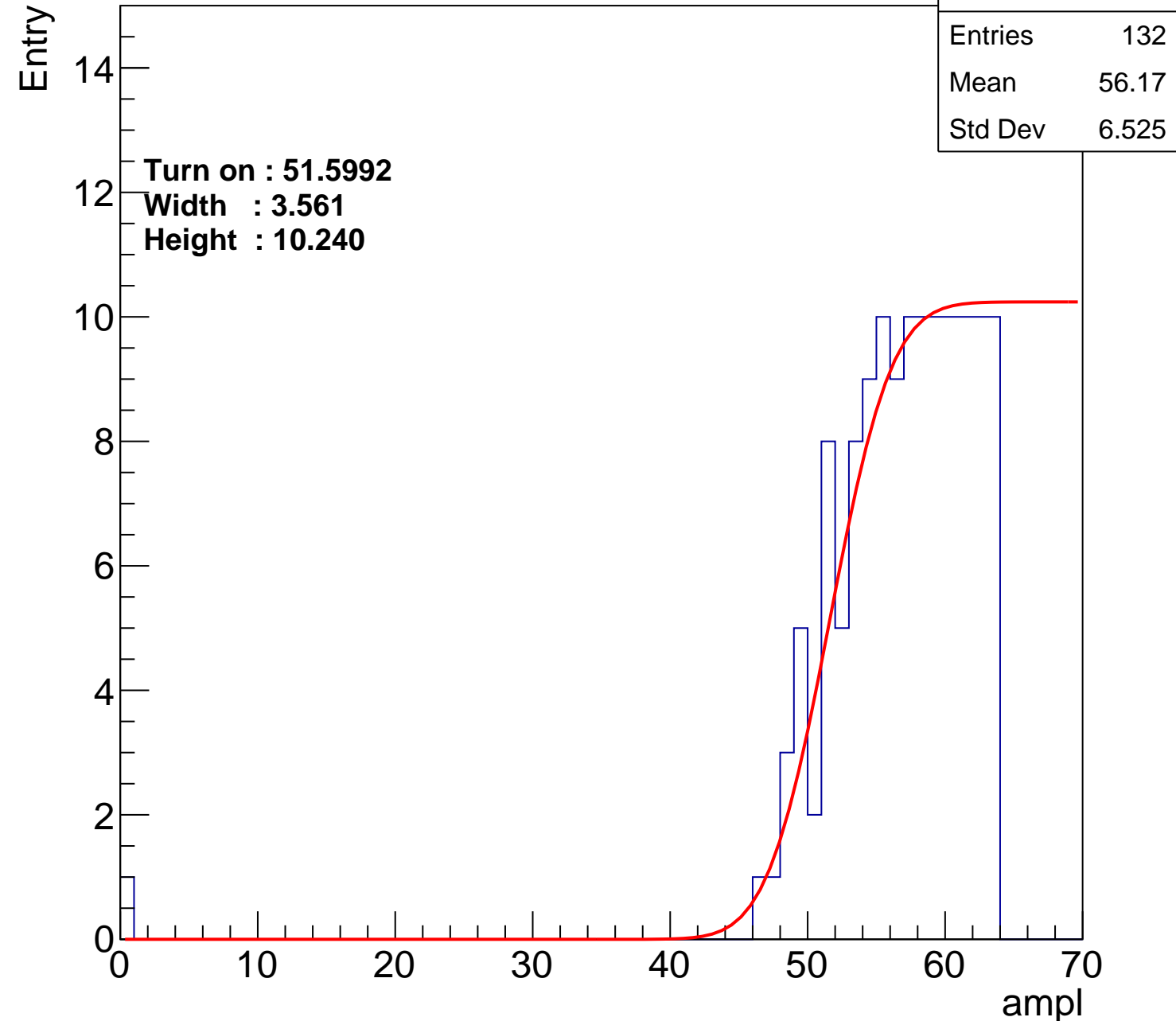
Width : 3.561

Height : 10.240

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.56
Std Dev	9.396

Turn on : 51.0460

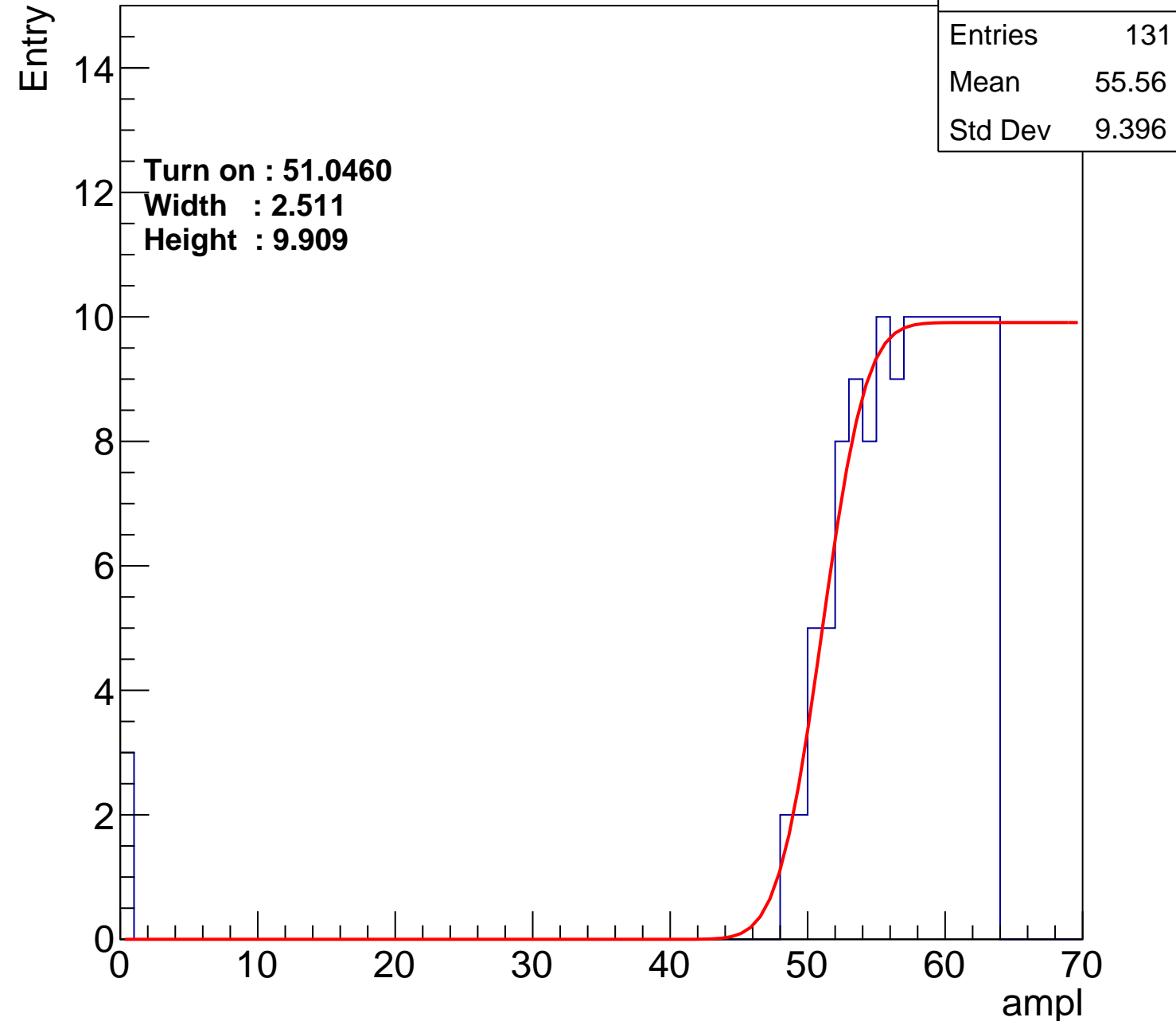
Width : 2.511

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch91

calib_packv5_040323_1717.root, FC#2, port C3

Entries	105
Mean	56.34
Std Dev	10.28

Turn on : 54.1340

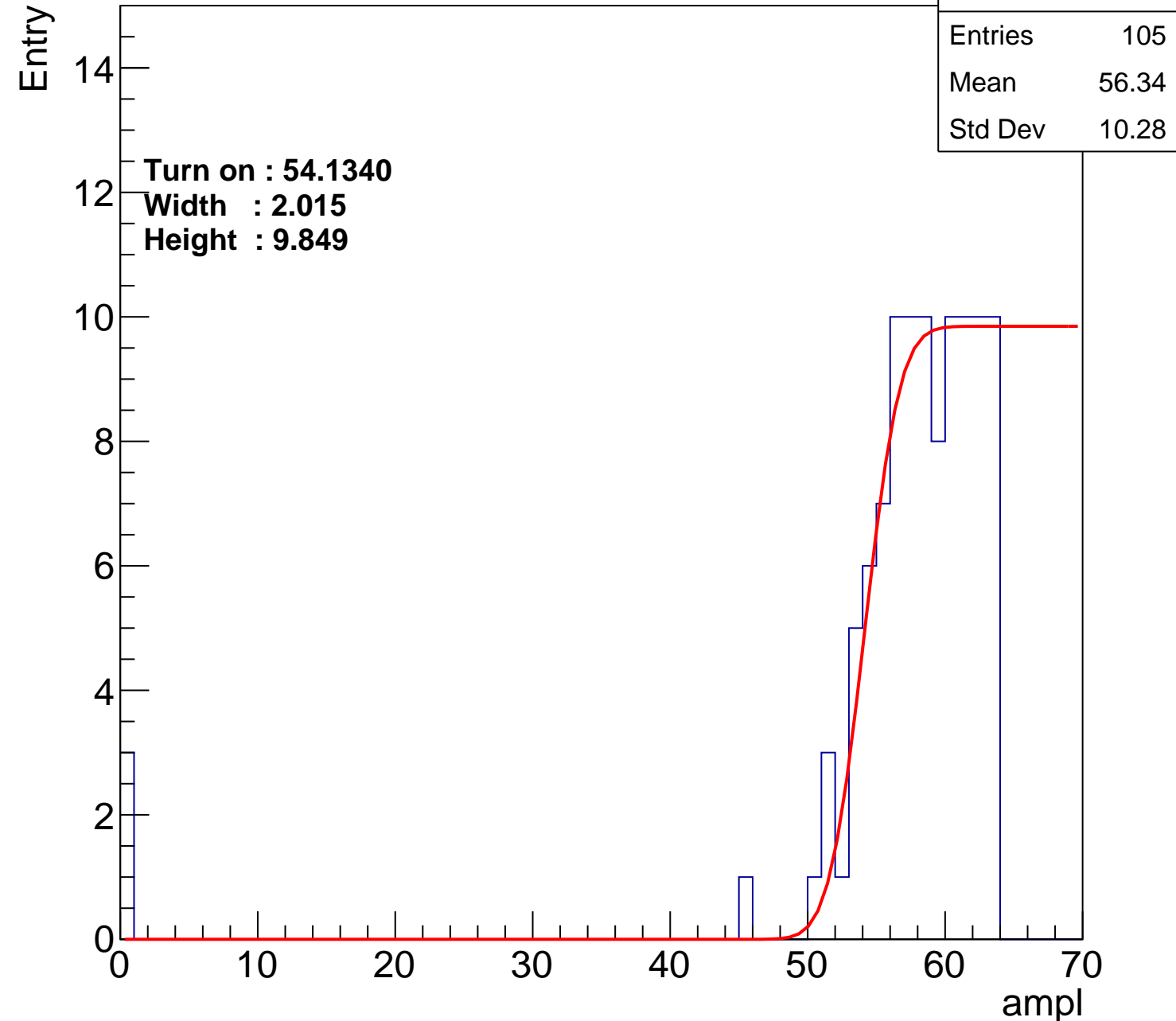
Width : 2.015

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch92

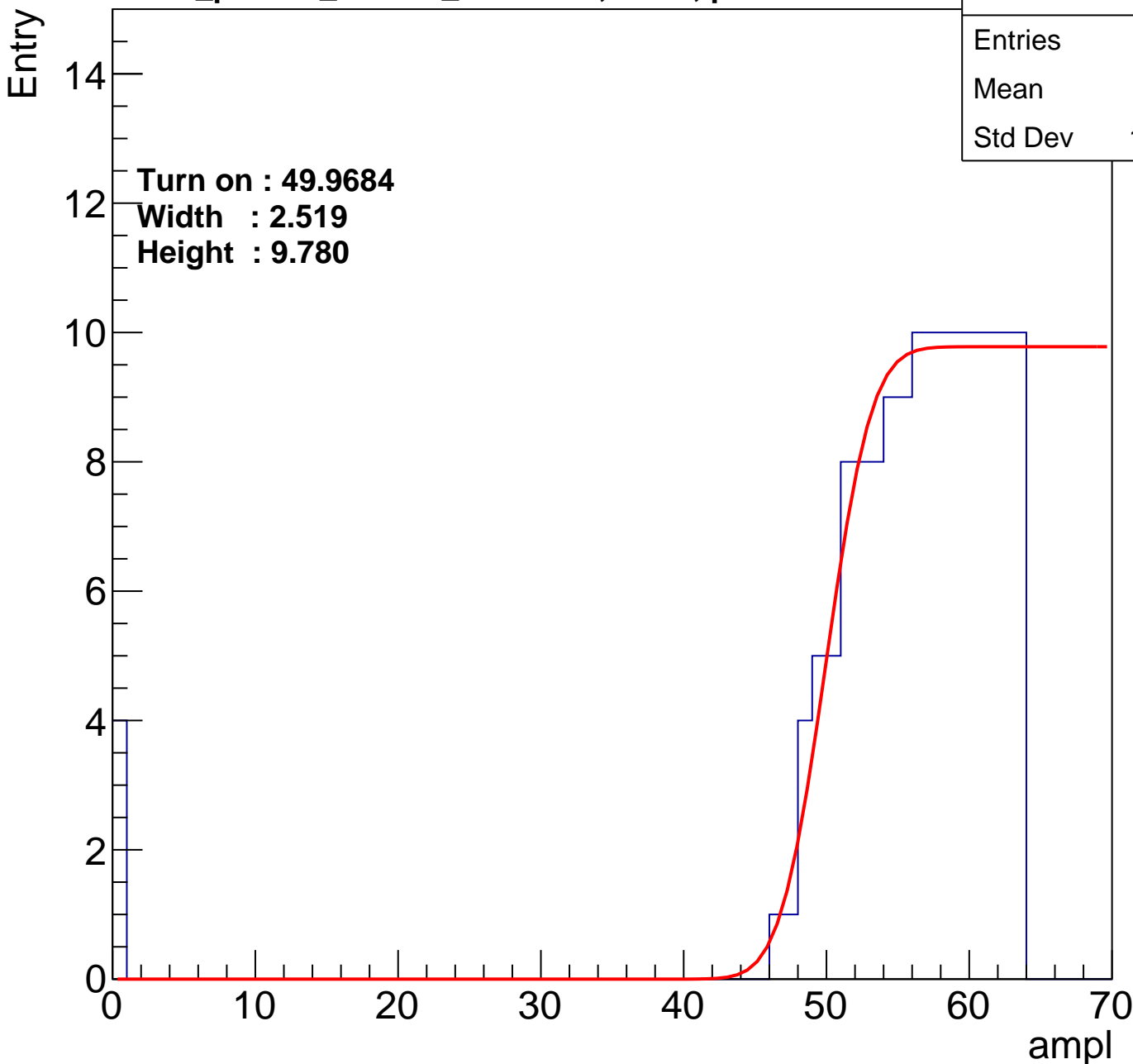
calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	54.71
Std Dev	10.28

Turn on : 49.9684

Width : 2.519

Height : 9.780



B0L103S, U10-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	56.55
Std Dev	8.273

Turn on : 52.3277

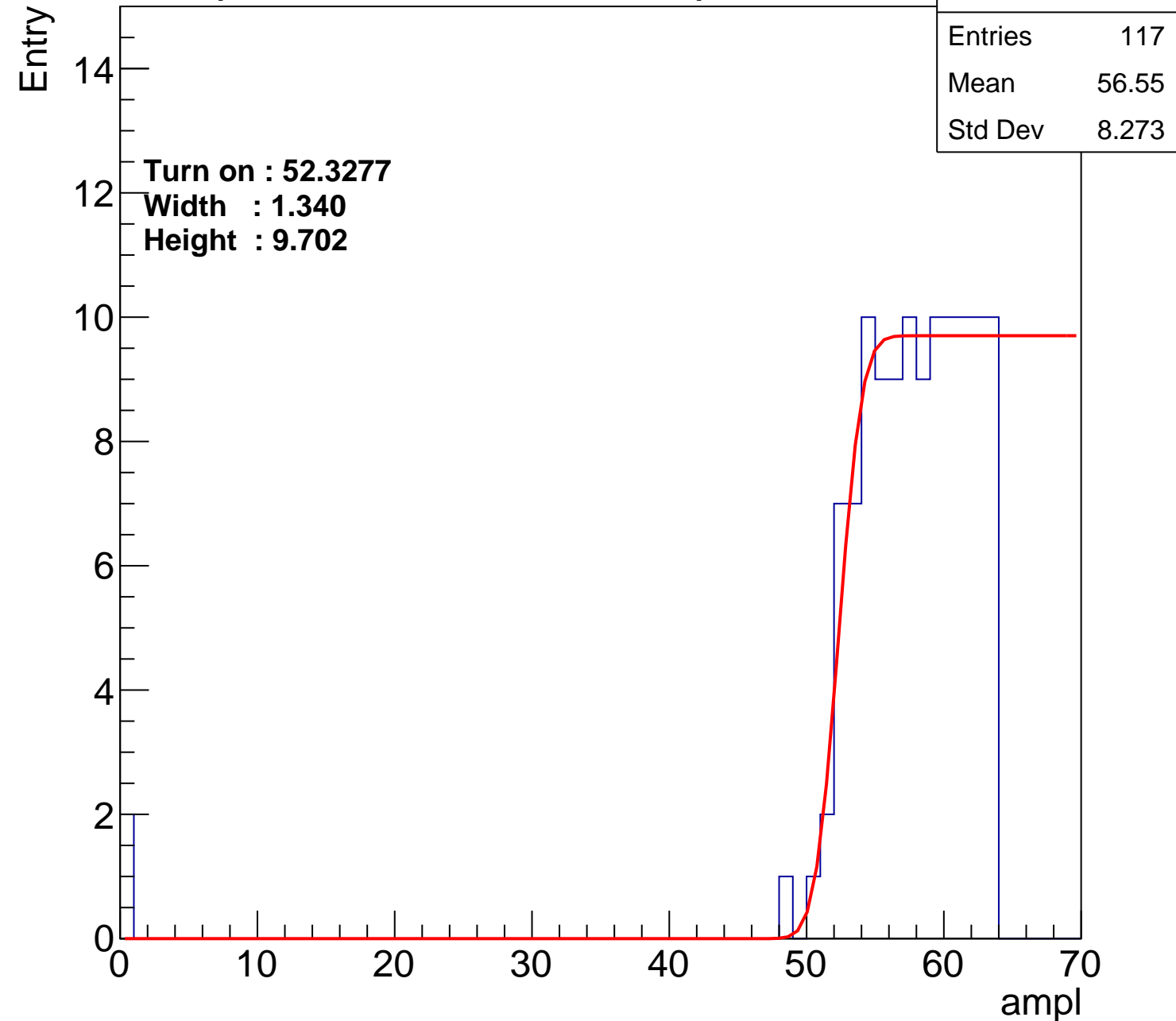
Width : 1.340

Height : 9.702

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch94

calib_packv5_040323_1717.root, FC#2, port C3

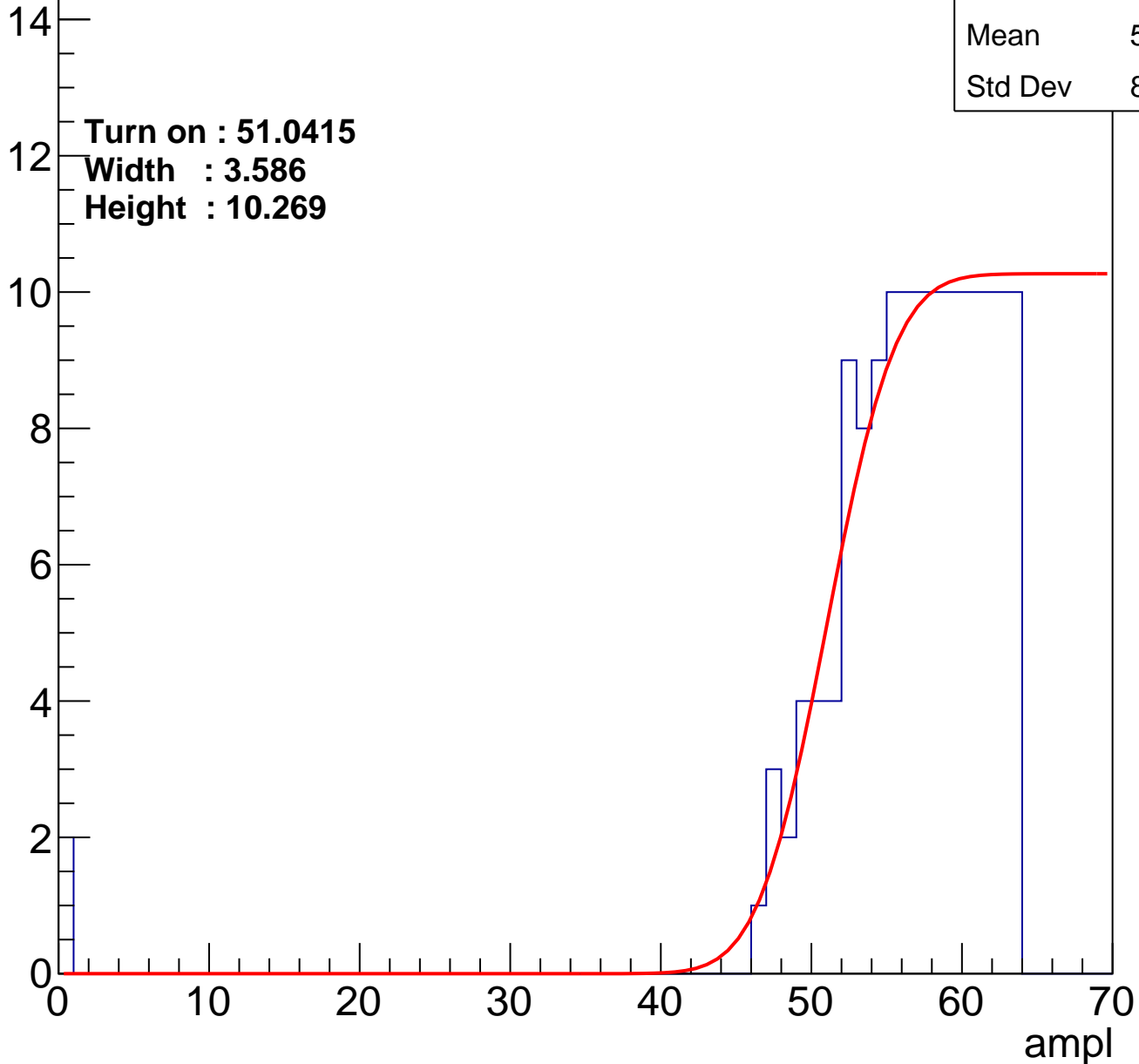
Entries	136
Mean	55.67
Std Dev	8.058

Turn on : 51.0415

Width : 3.586

Height : 10.269

Entry



B0L103S, U10-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	55.59
Std Dev	11.12

Turn on : 53.0383

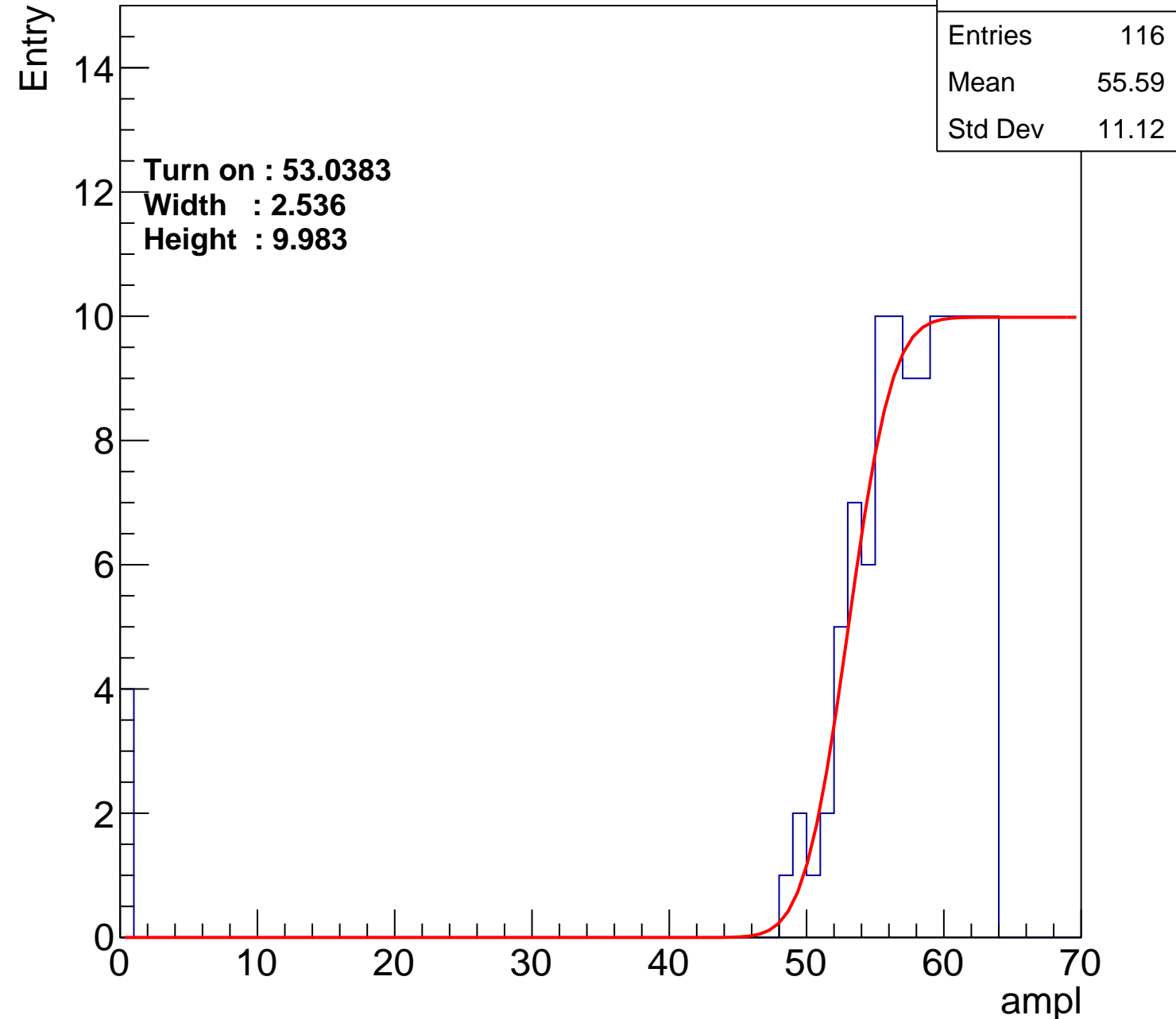
Width : 2.536

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	156
Mean	54.53
Std Dev	8.962

Turn on : 49.5993

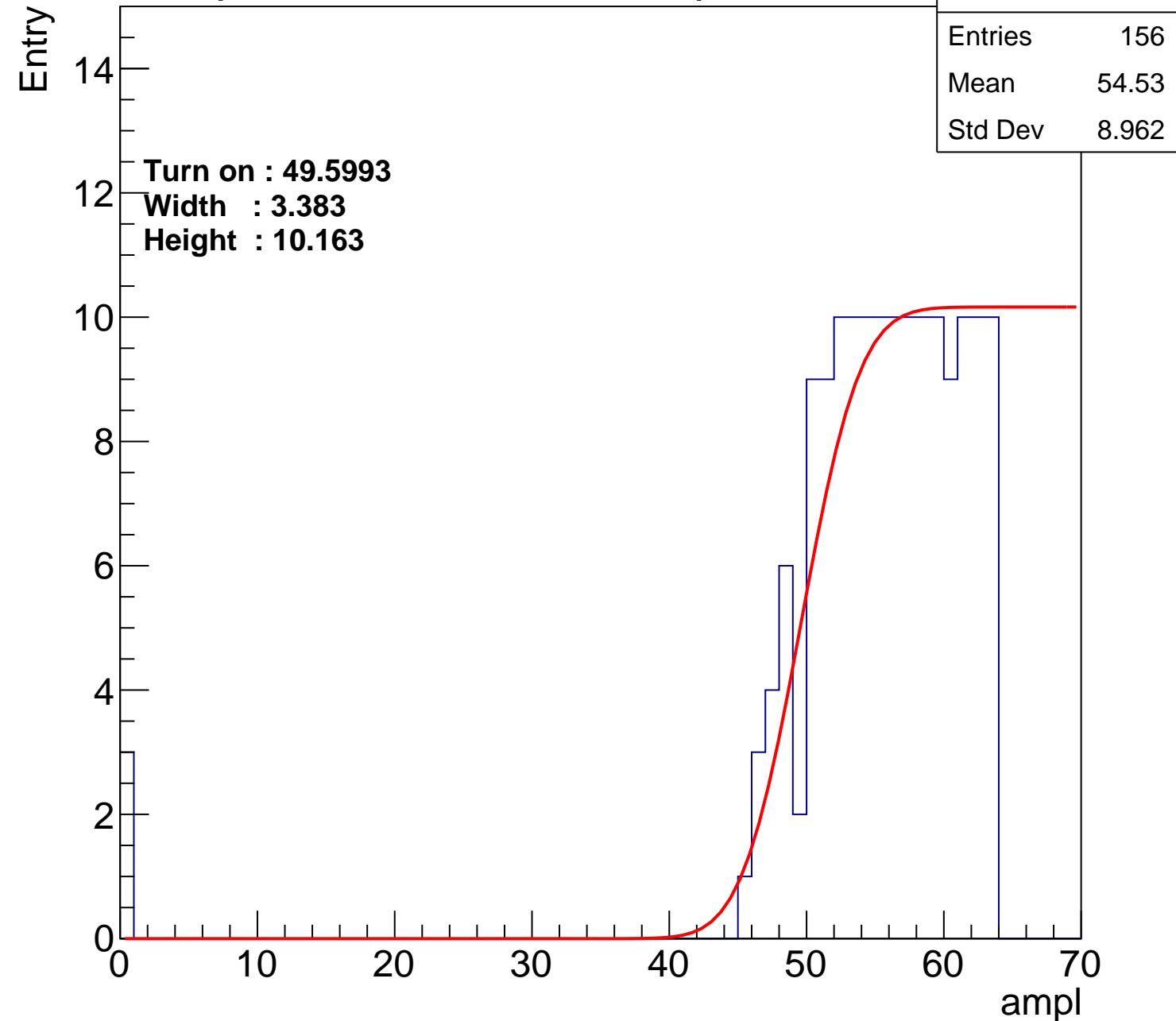
Width : 3.383

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	101
Mean	56.7
Std Dev	8.9

Turn on : 55.4869

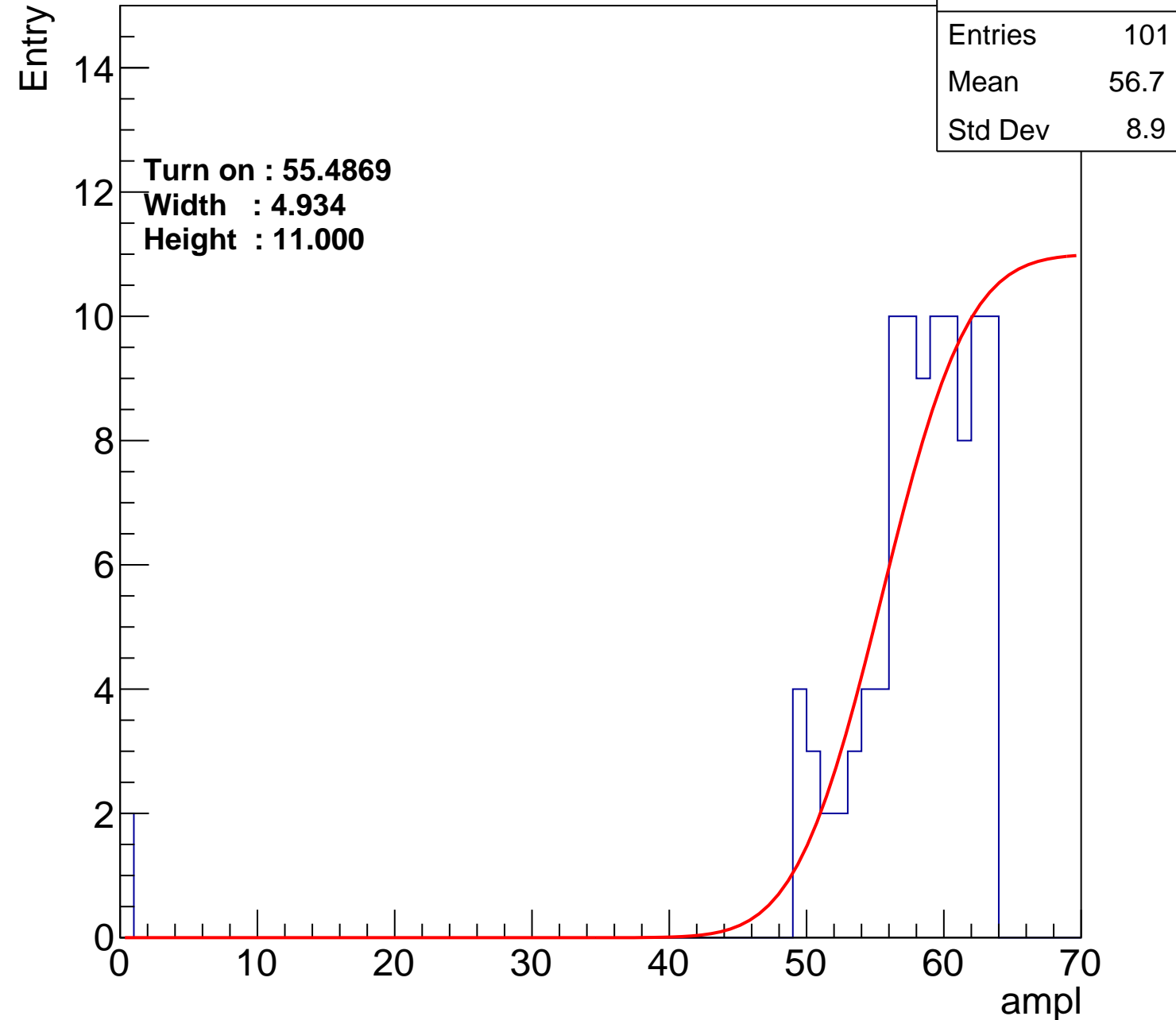
Width : 4.934

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch98

calib_packv5_040323_1717.root, FC#2, port C3

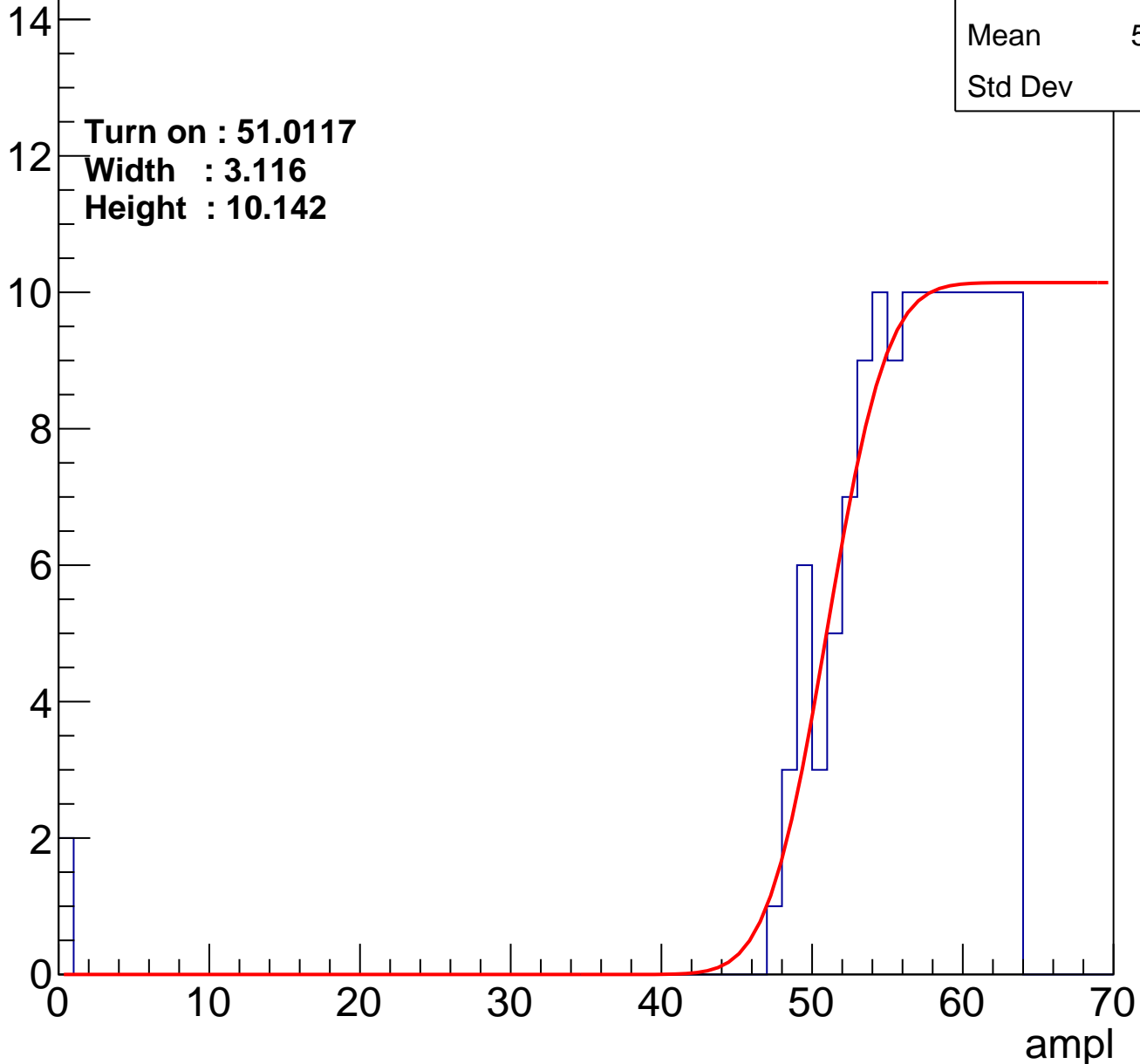
Entries	135
Mean	55.75
Std Dev	8.03

Turn on : 51.0117

Width : 3.116

Height : 10.142

Entry



B0L103S, U10-ch99

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	56.58
Std Dev	6.477

Turn on : 51.5853

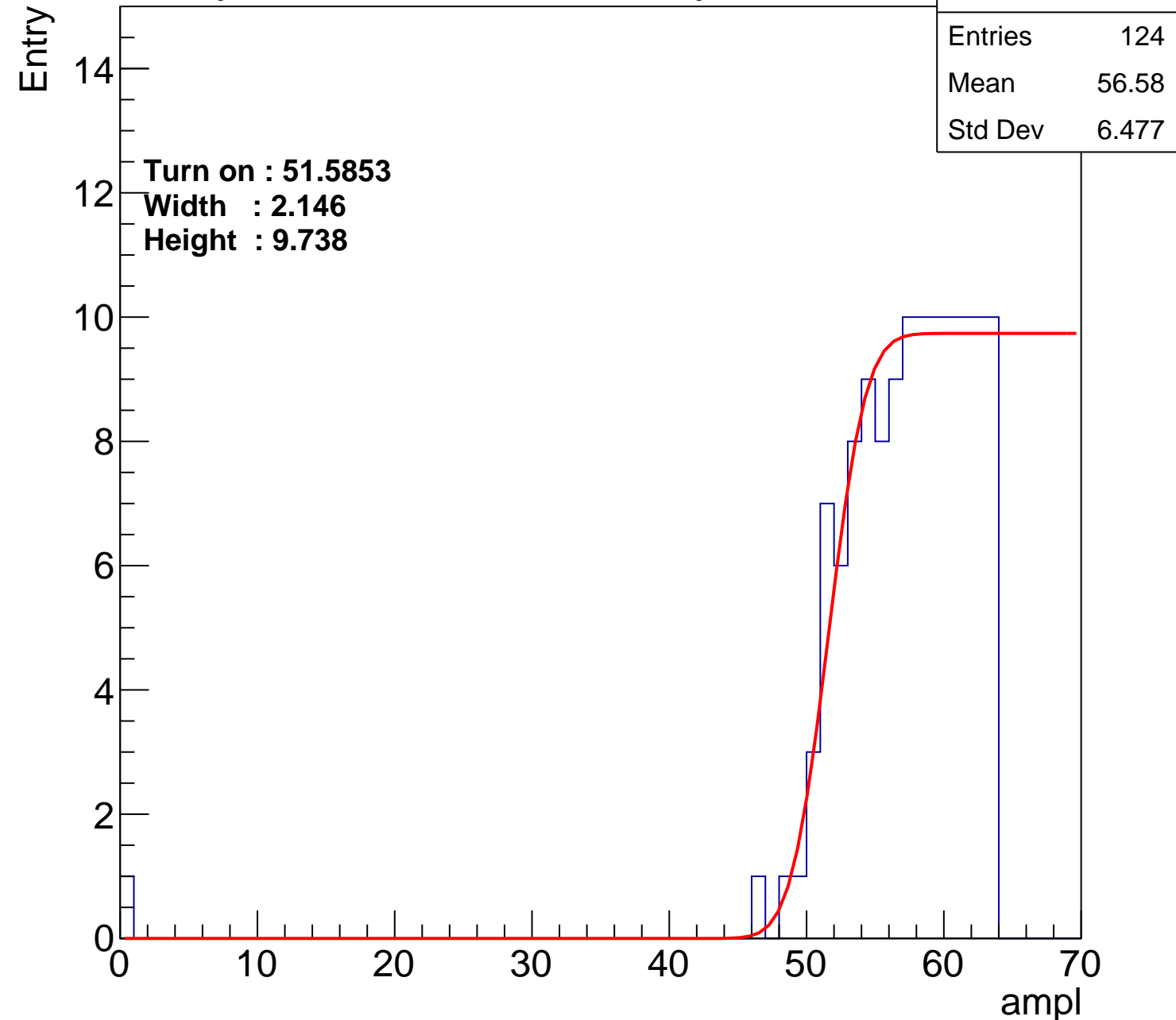
Width : 2.146

Height : 9.738

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.62
Std Dev	11.56

Turn on : 51.1603

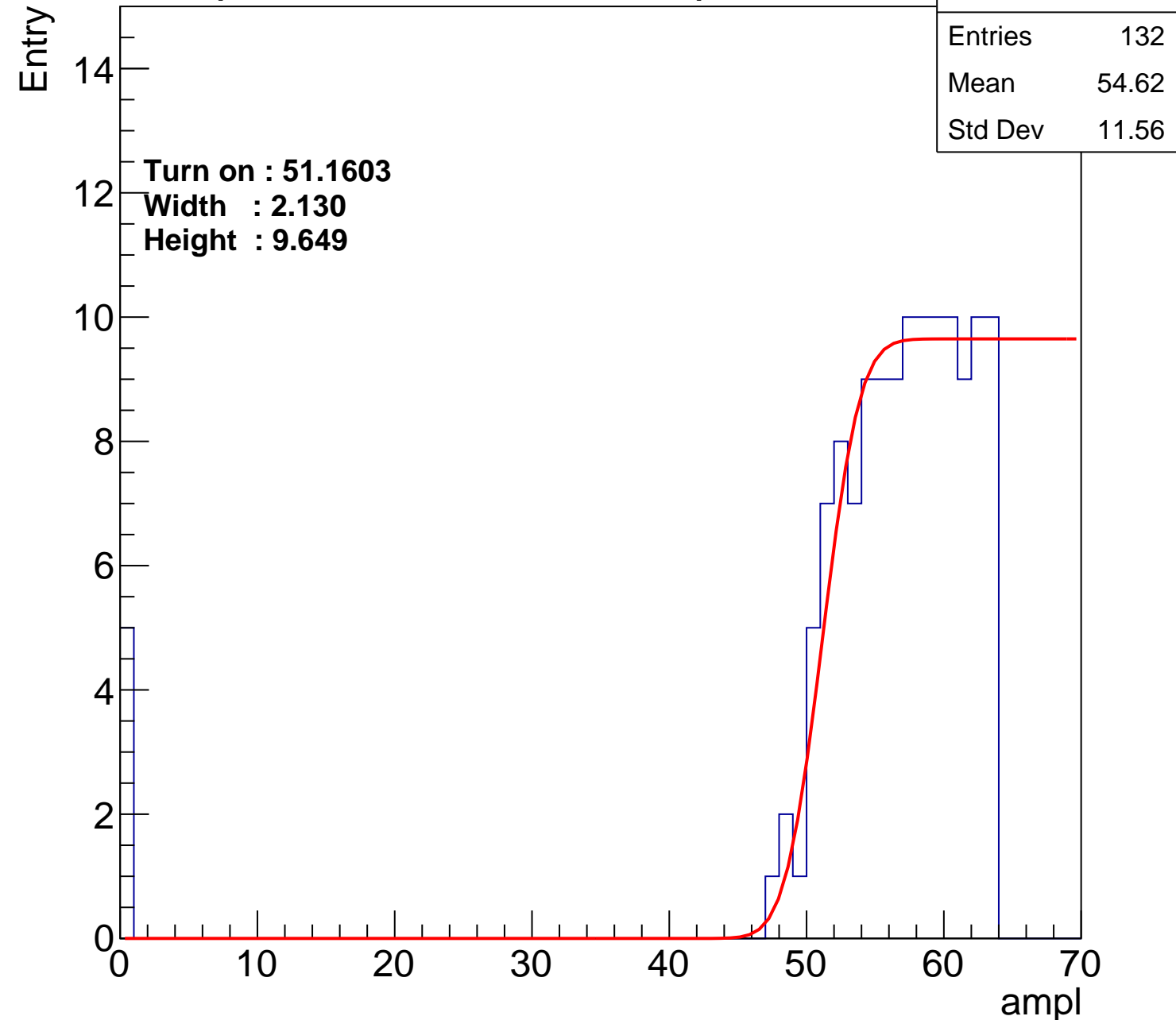
Width : 2.130

Height : 9.649

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch101

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.93
Std Dev	8.158

Turn on : 51.4466

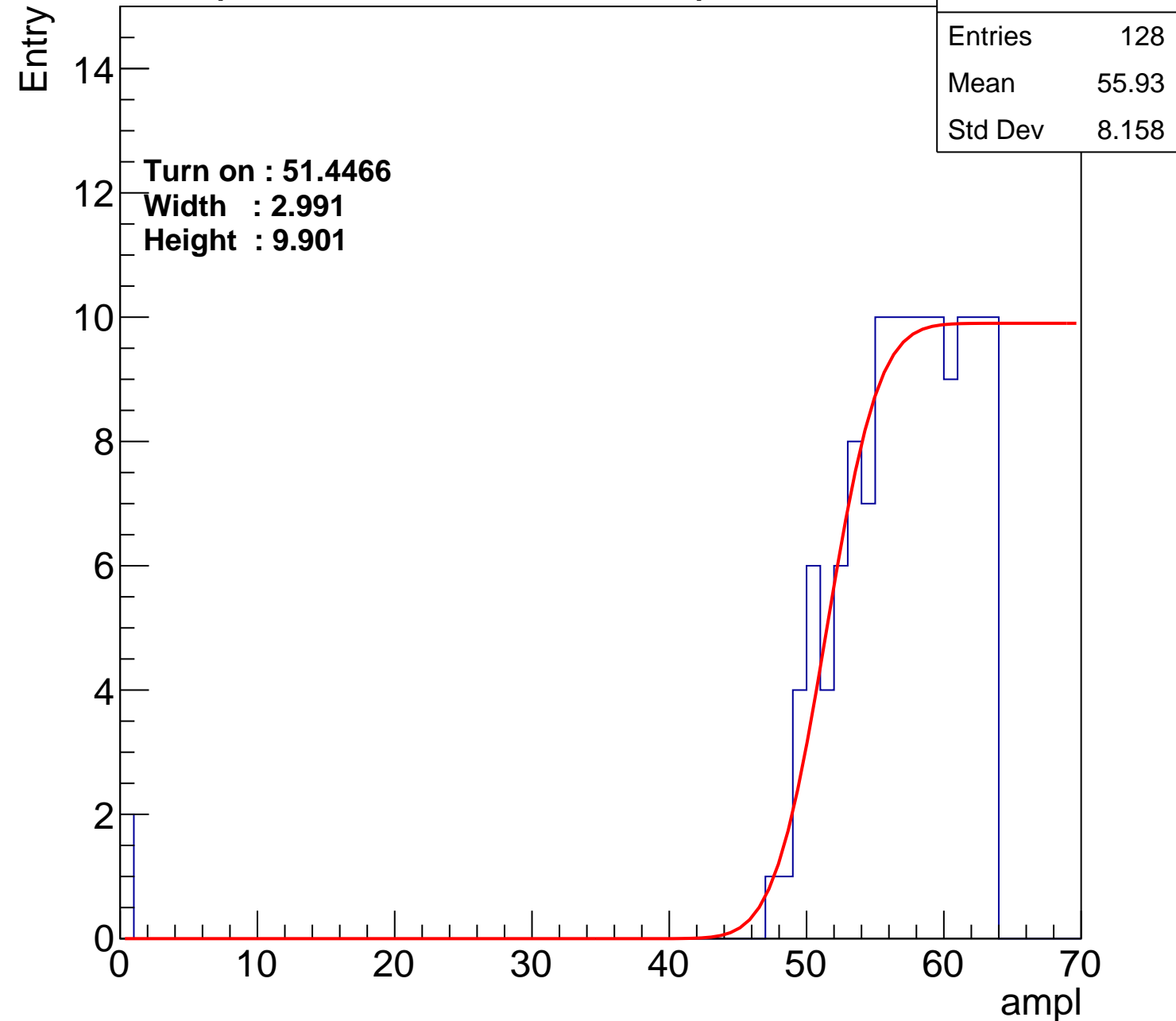
Width : 2.991

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch102

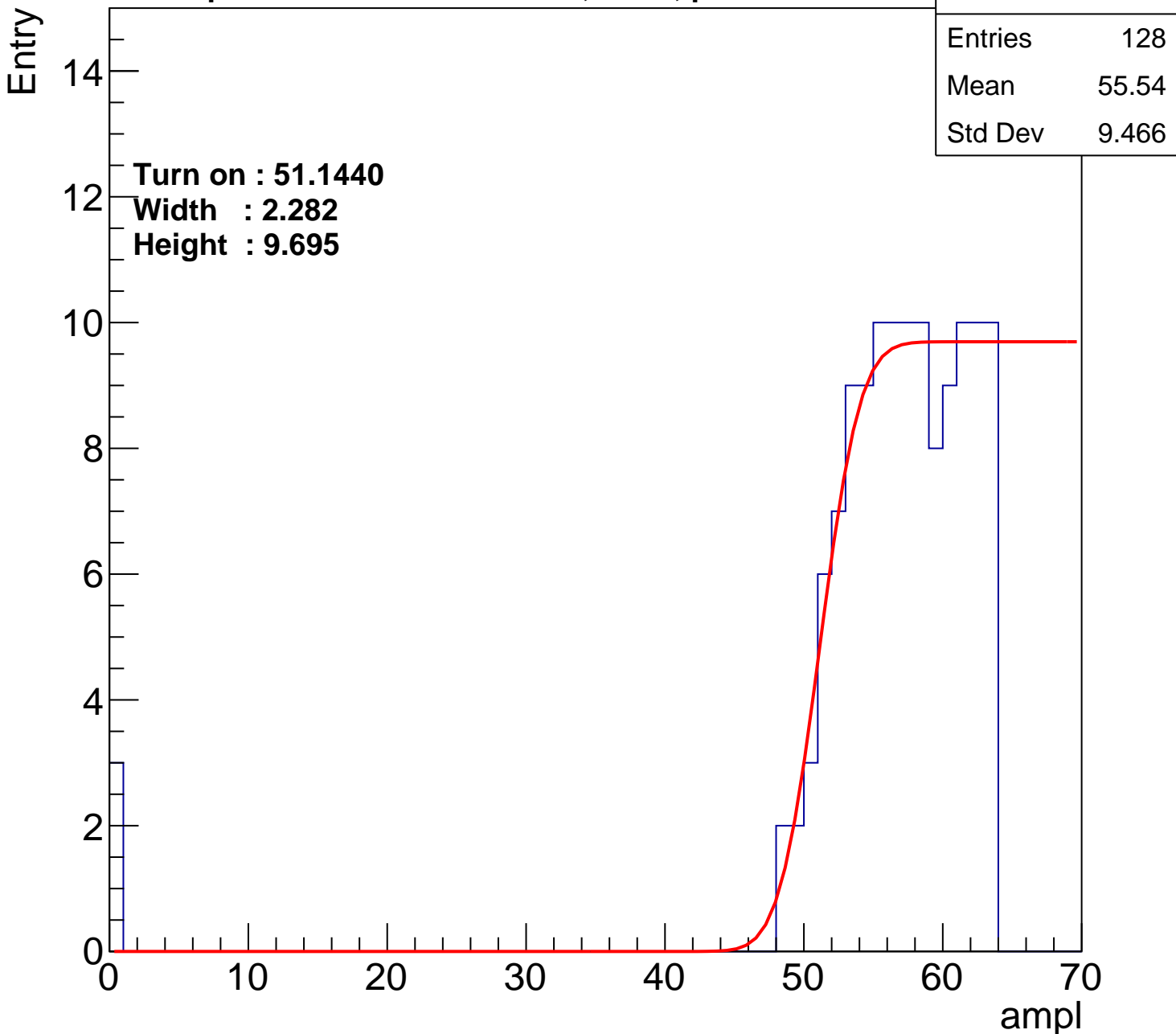
calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.54
Std Dev	9.466

Turn on : 51.1440

Width : 2.282

Height : 9.695



B0L103S, U10-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	57.39
Std Dev	6.471

Turn on : 53.4000

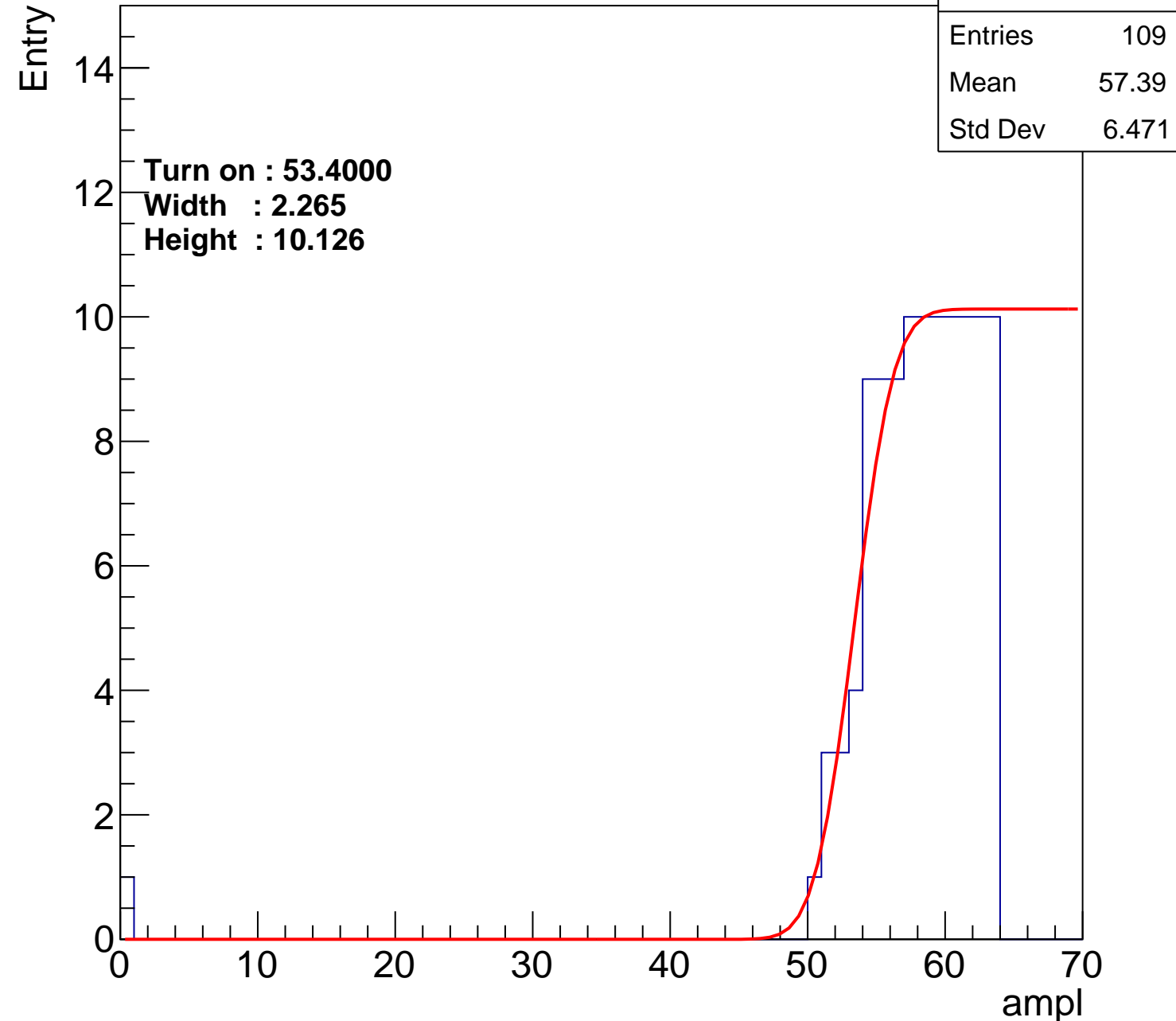
Width : 2.265

Height : 10.126

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch104

calib_packv5_040323_1717.root, FC#2, port C3

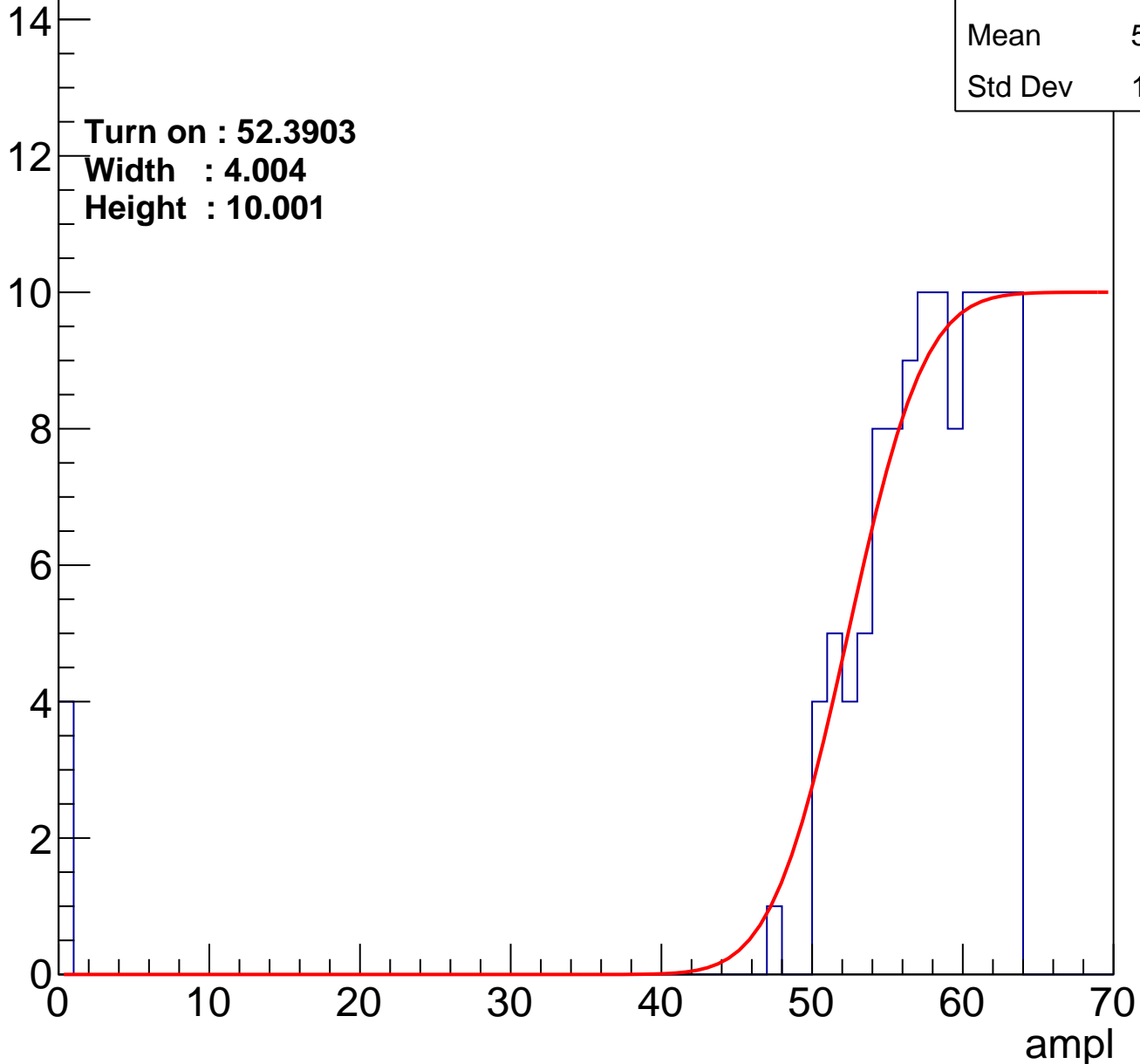
Entries	116
Mean	55.46
Std Dev	11.14

Turn on : 52.3903

Width : 4.004

Height : 10.001

Entry



B0L103S, U10-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.94
Std Dev	8.167

Turn on : 51.2900

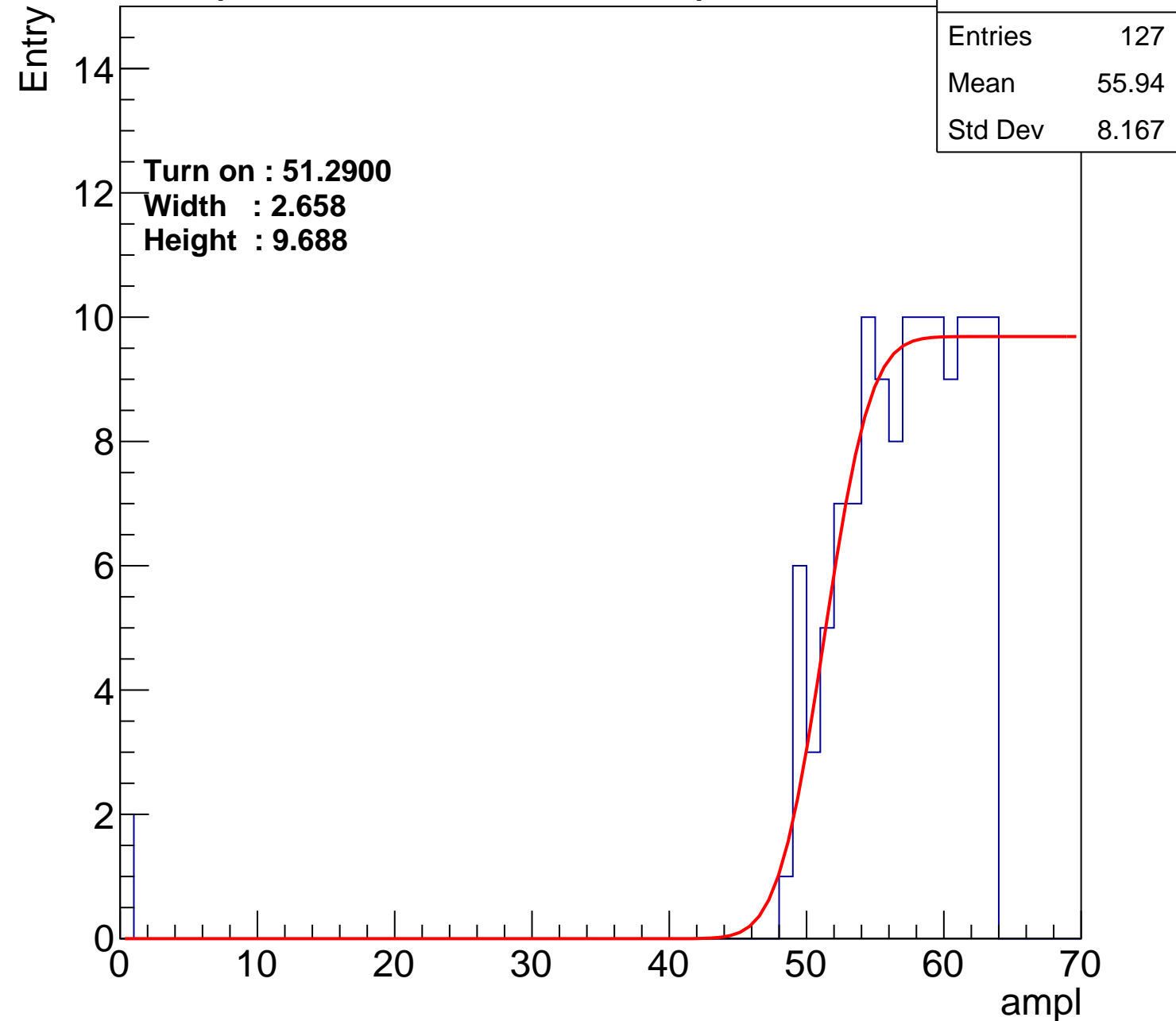
Width : 2.658

Height : 9.688

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.17
Std Dev	10.98

Turn on : 49.2168

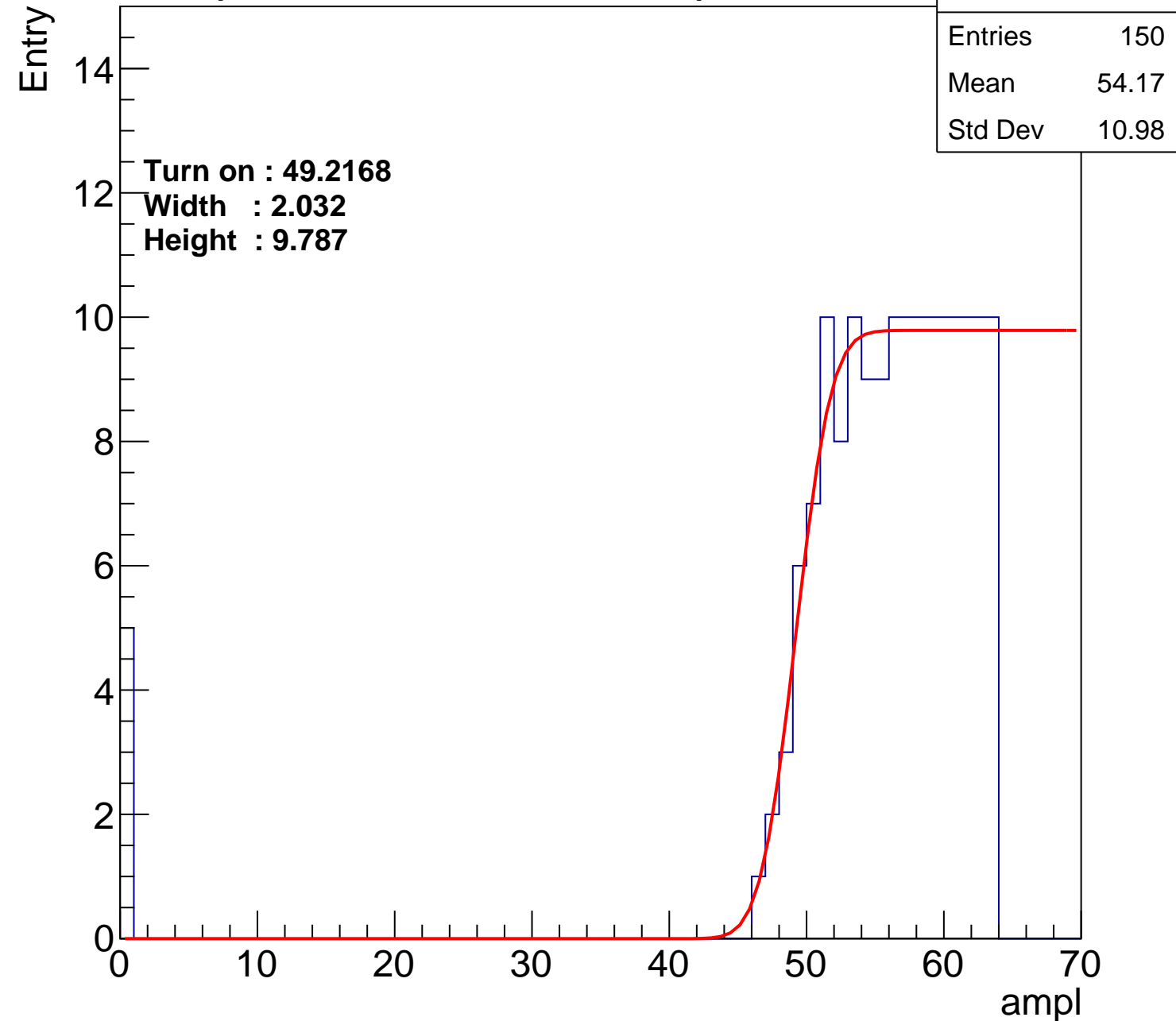
Width : 2.032

Height : 9.787

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	54.77
Std Dev	11.94

Turn on : 52.2209

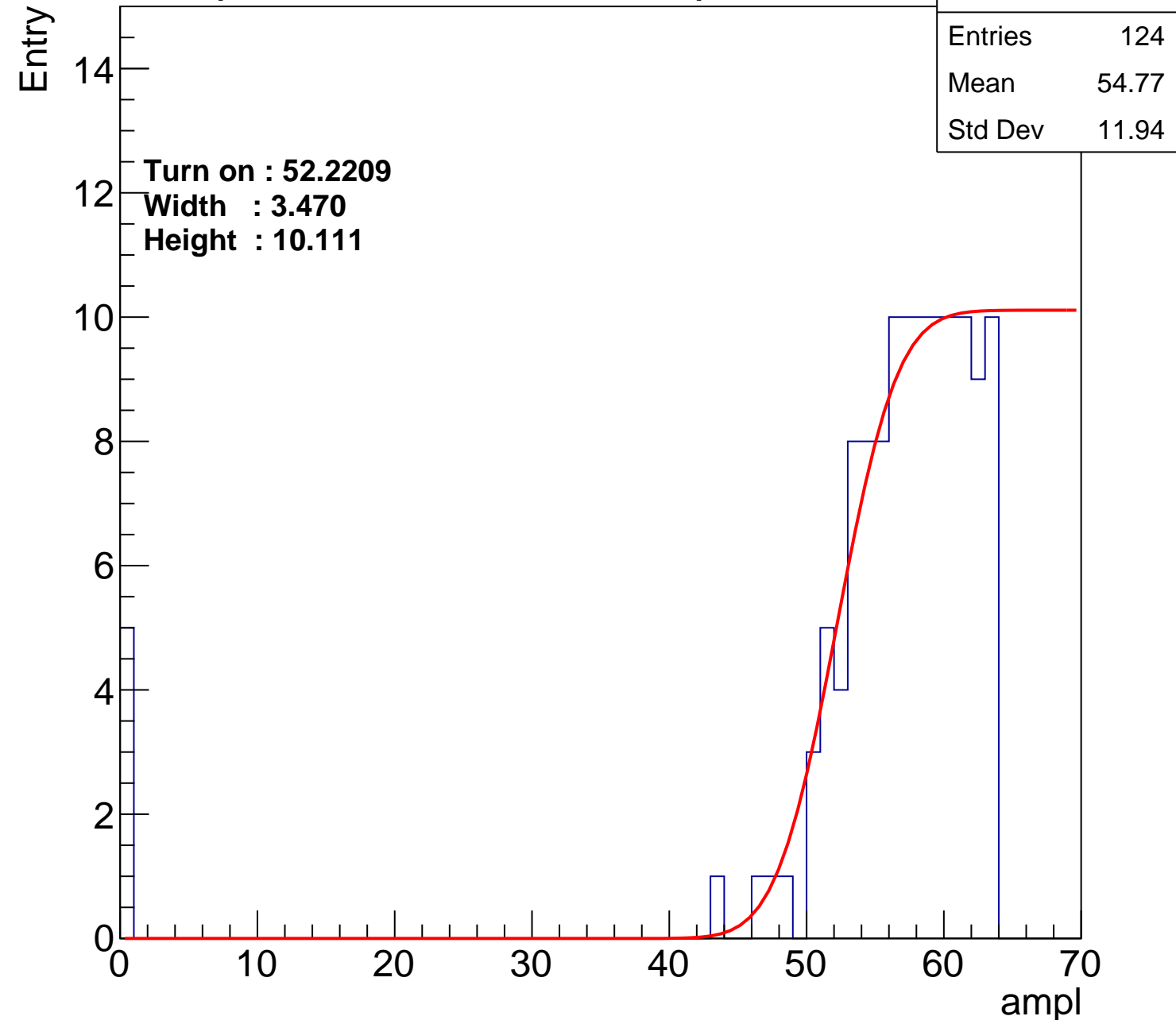
Width : 3.470

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch108

calib_packv5_040323_1717.root, FC#2, port C3

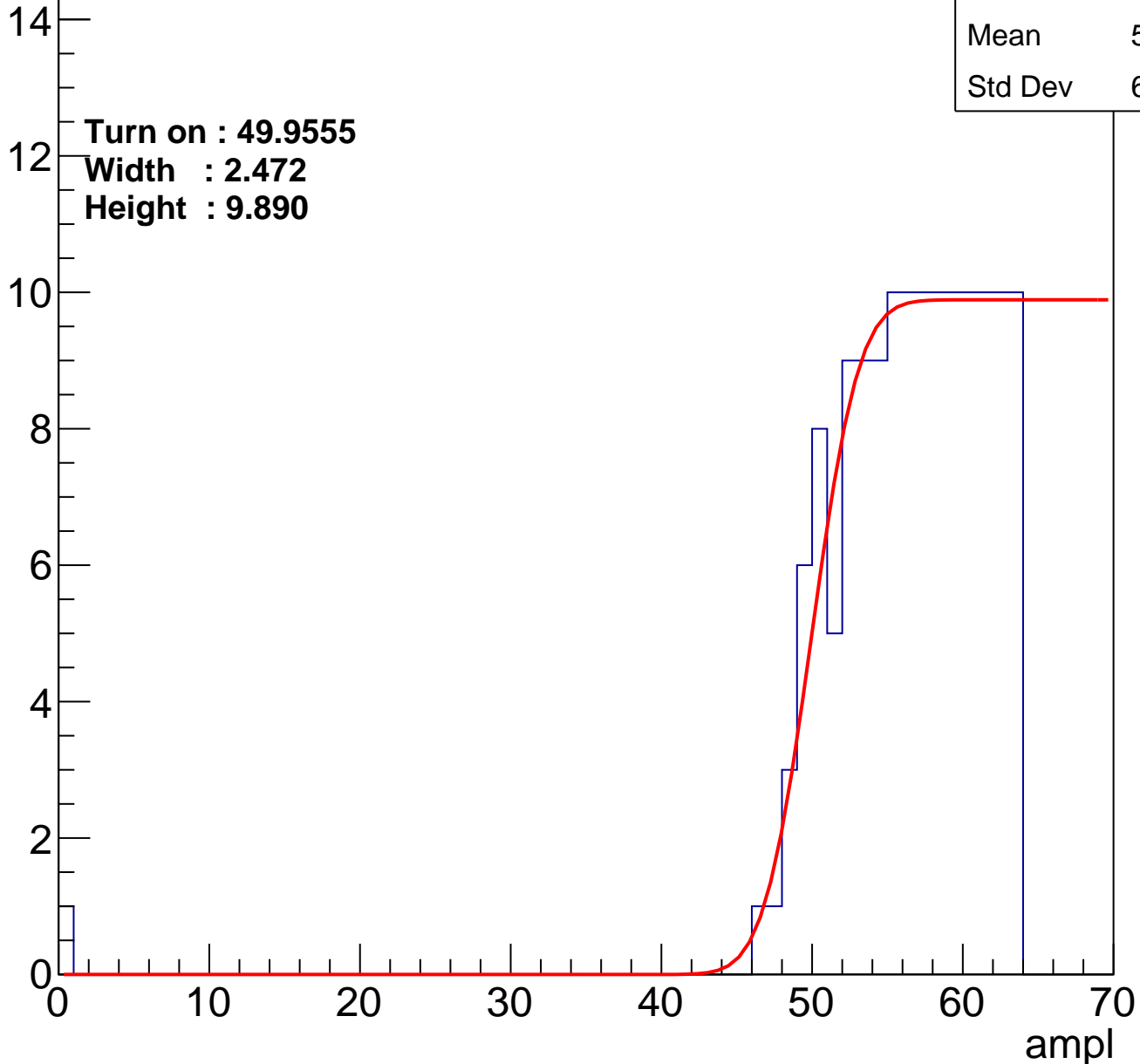
Entries	142
Mean	55.82
Std Dev	6.436

Turn on : 49.9555

Width : 2.472

Height : 9.890

Entry



B0L103S, U10-ch109

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.35
Std Dev	8.285

Turn on : 52.8155

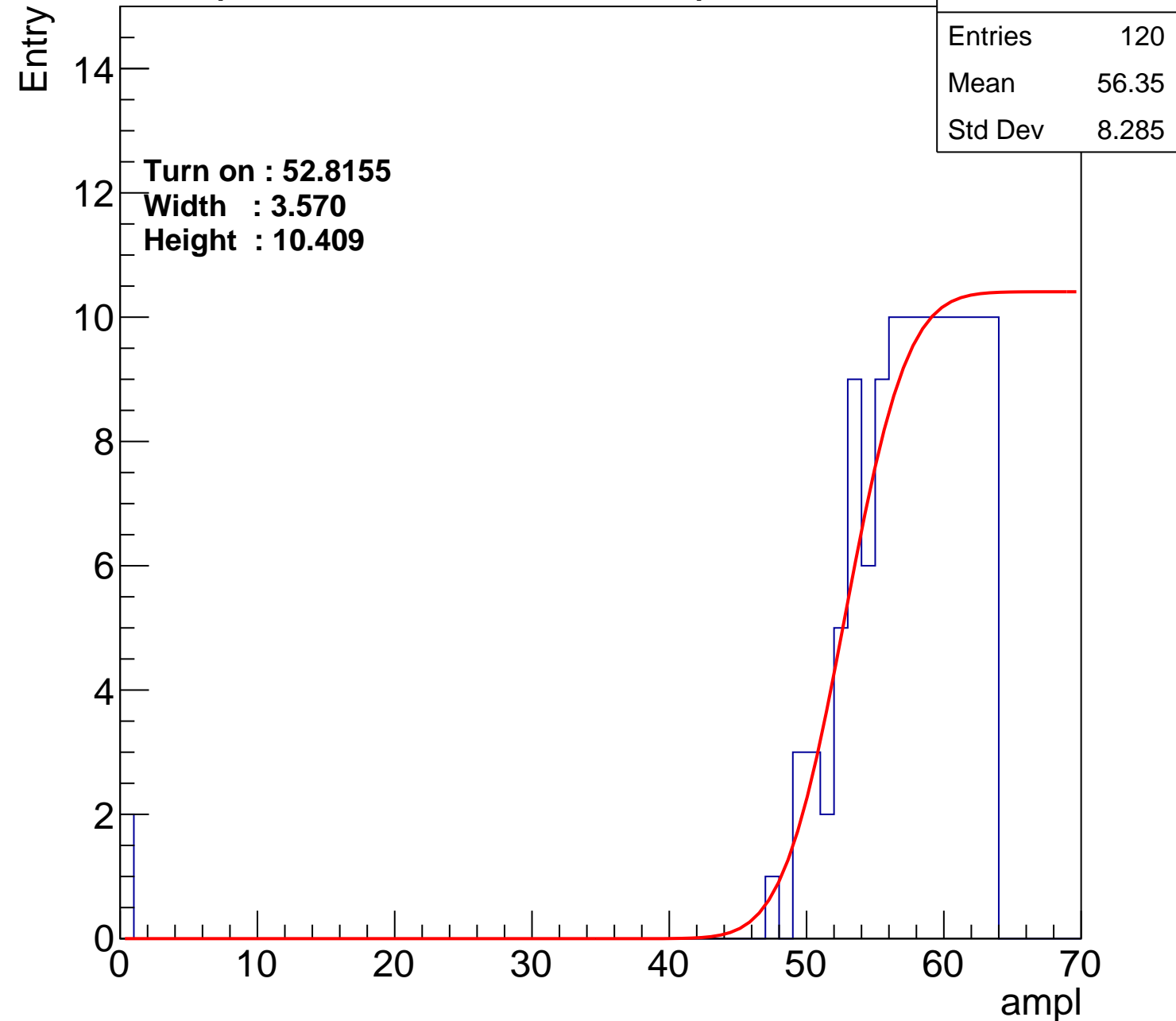
Width : 3.570

Height : 10.409

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch110

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.27
Std Dev	8.055

Turn on : 50.5388

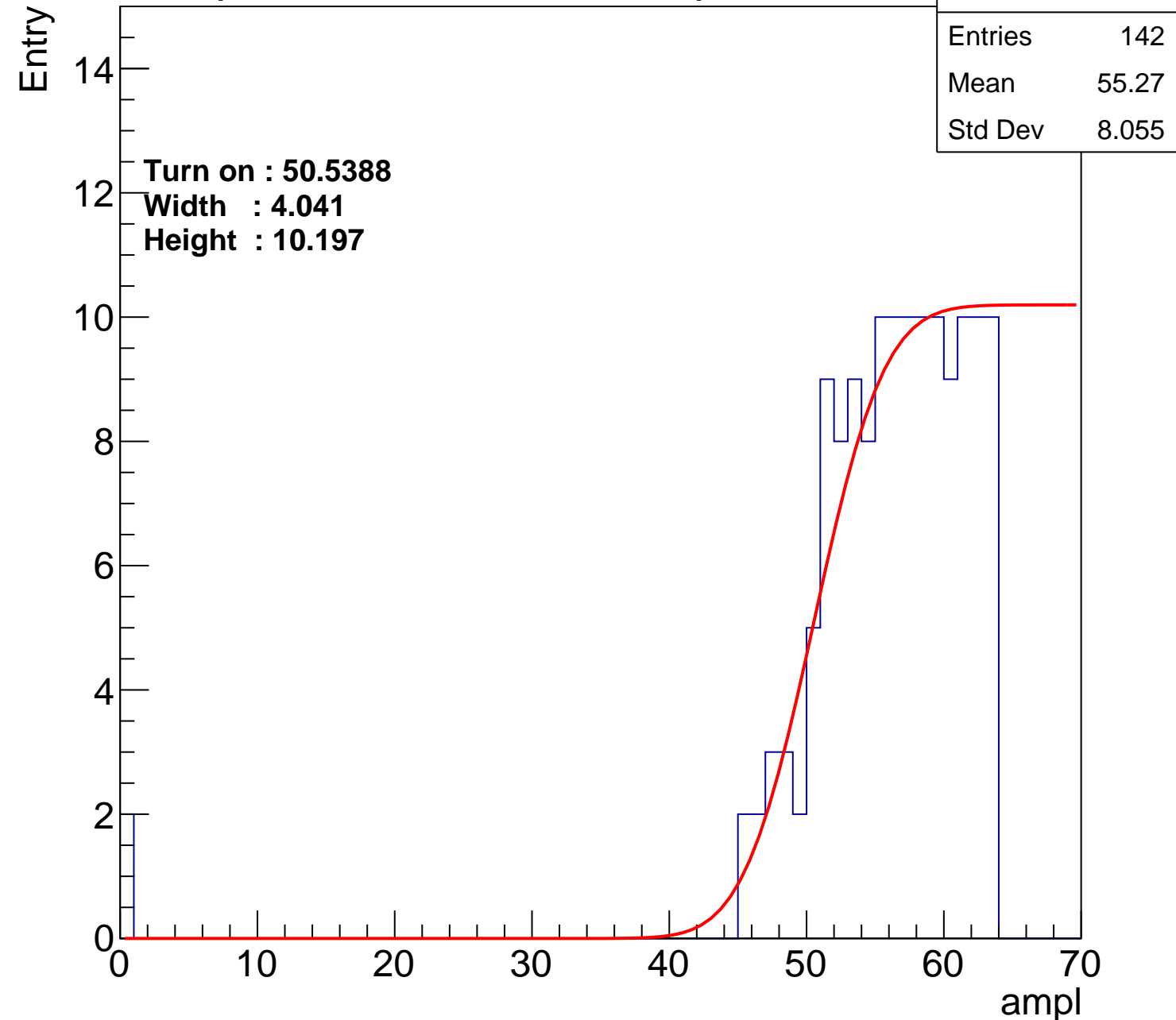
Width : 4.041

Height : 10.197

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch111

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.64
Std Dev	11.6

Turn on : 51.8112

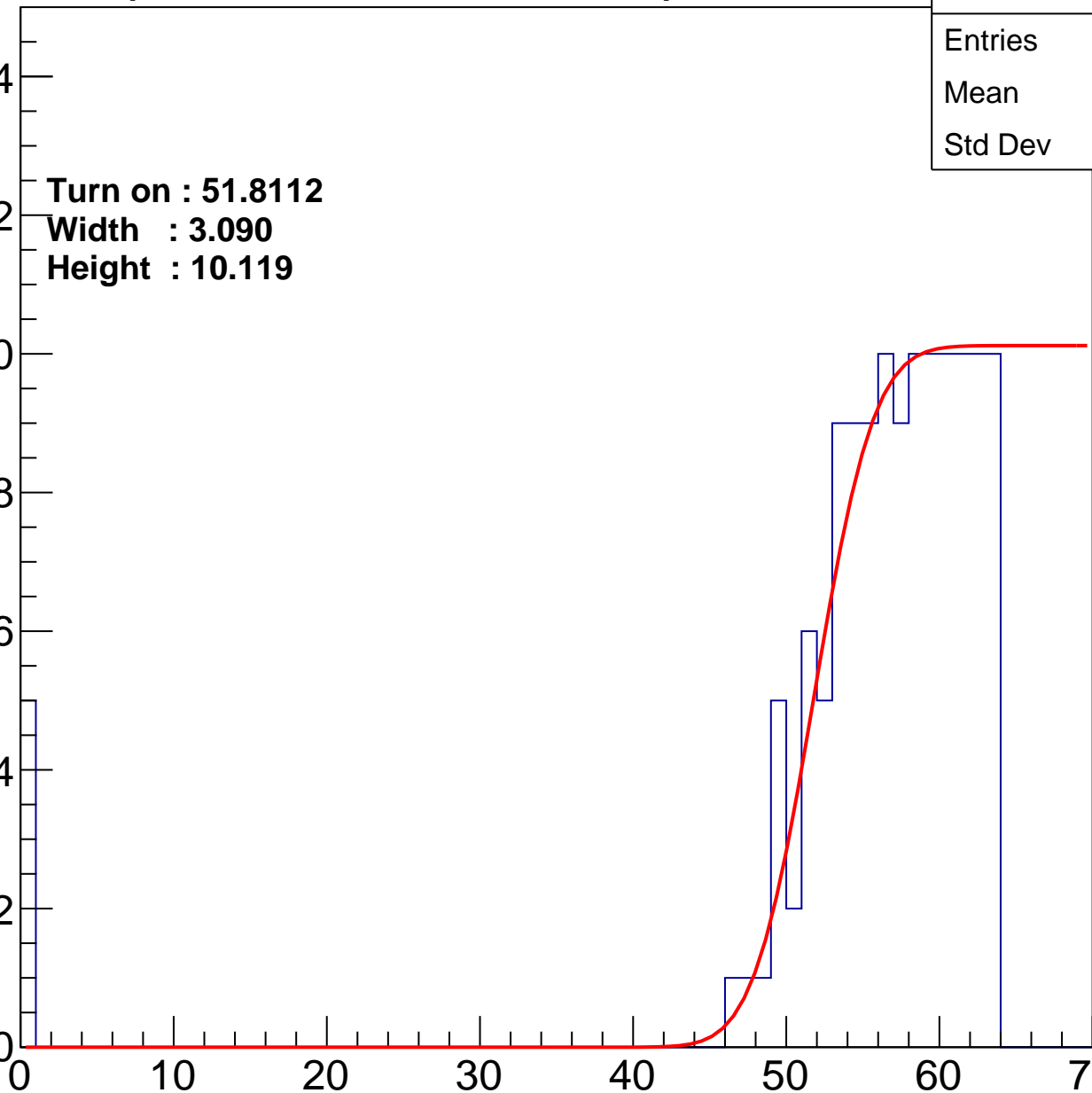
Width : 3.090

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch112

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	55.16
Std Dev	9.462

Turn on : 50.8802

Width : 5.743

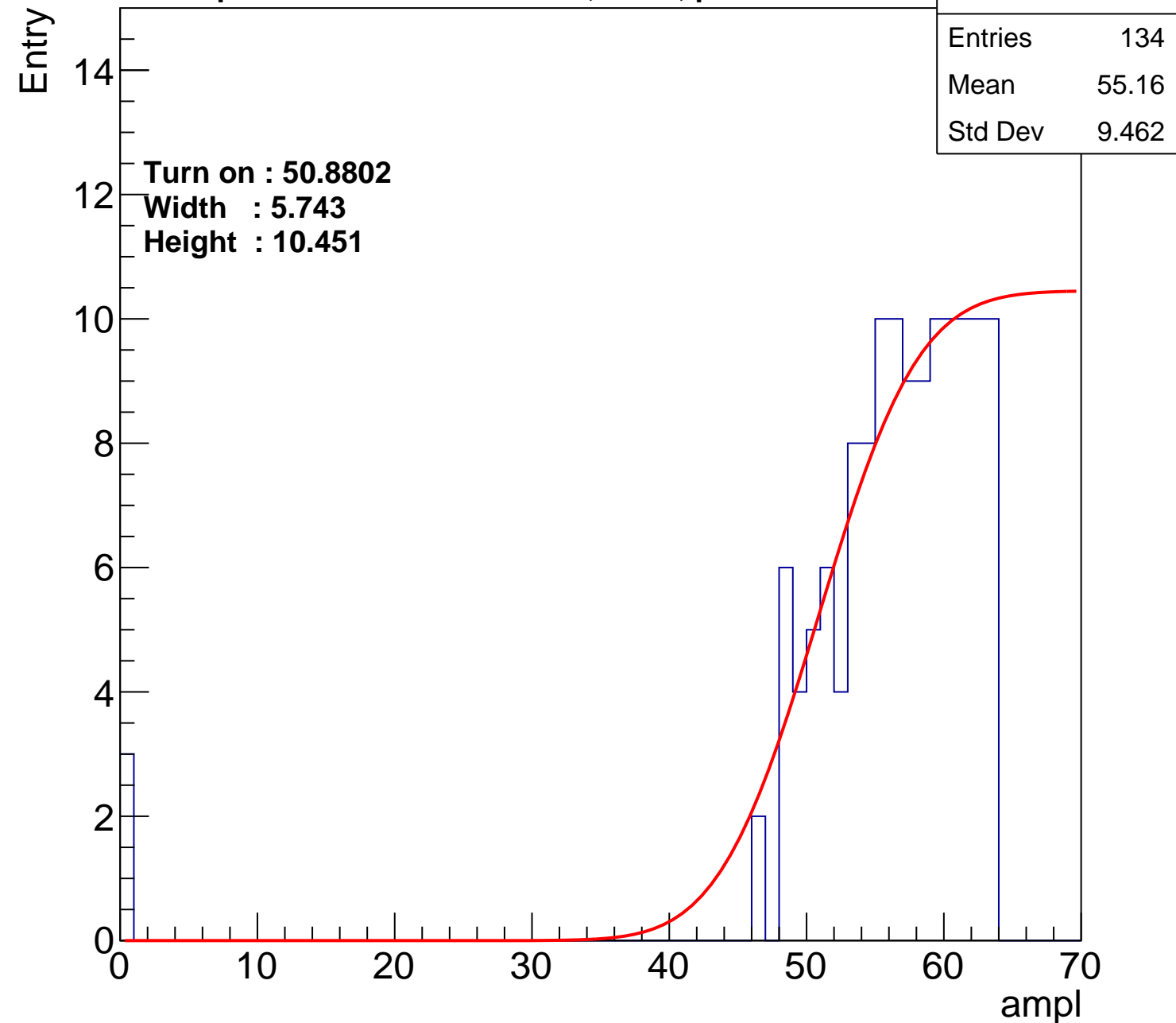
Height : 10.451

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U10-ch113

calib_packv5_040323_1717.root, FC#2, port C3

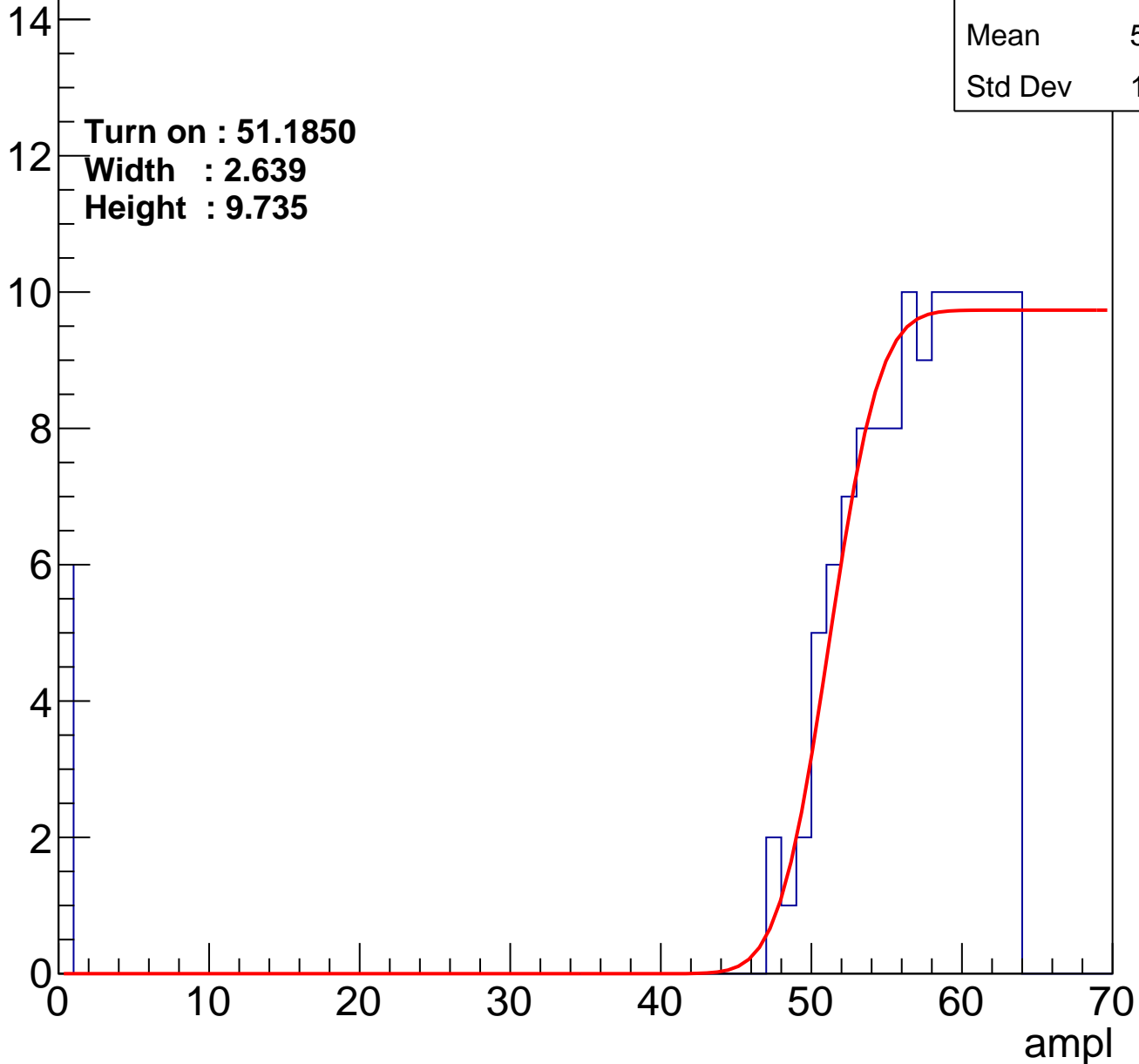
Entries	132
Mean	54.23
Std Dev	12.52

Turn on : 51.1850

Width : 2.639

Height : 9.735

Entry



B0L103S, U10-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.1
Std Dev	8.133

Turn on : 51.0236

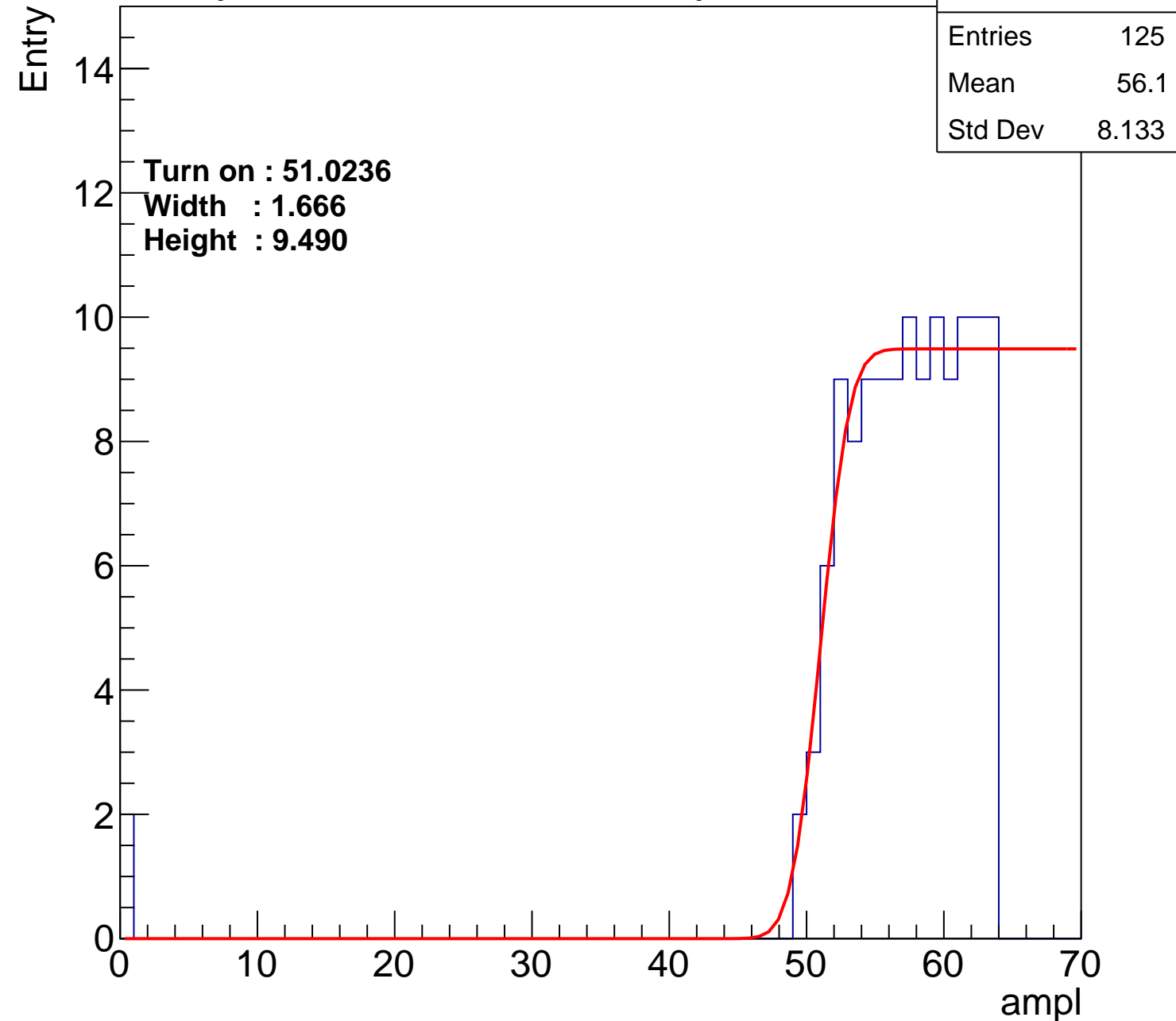
Width : 1.666

Height : 9.490

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch115

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	53.45
Std Dev	13.86

Turn on : 51.3208

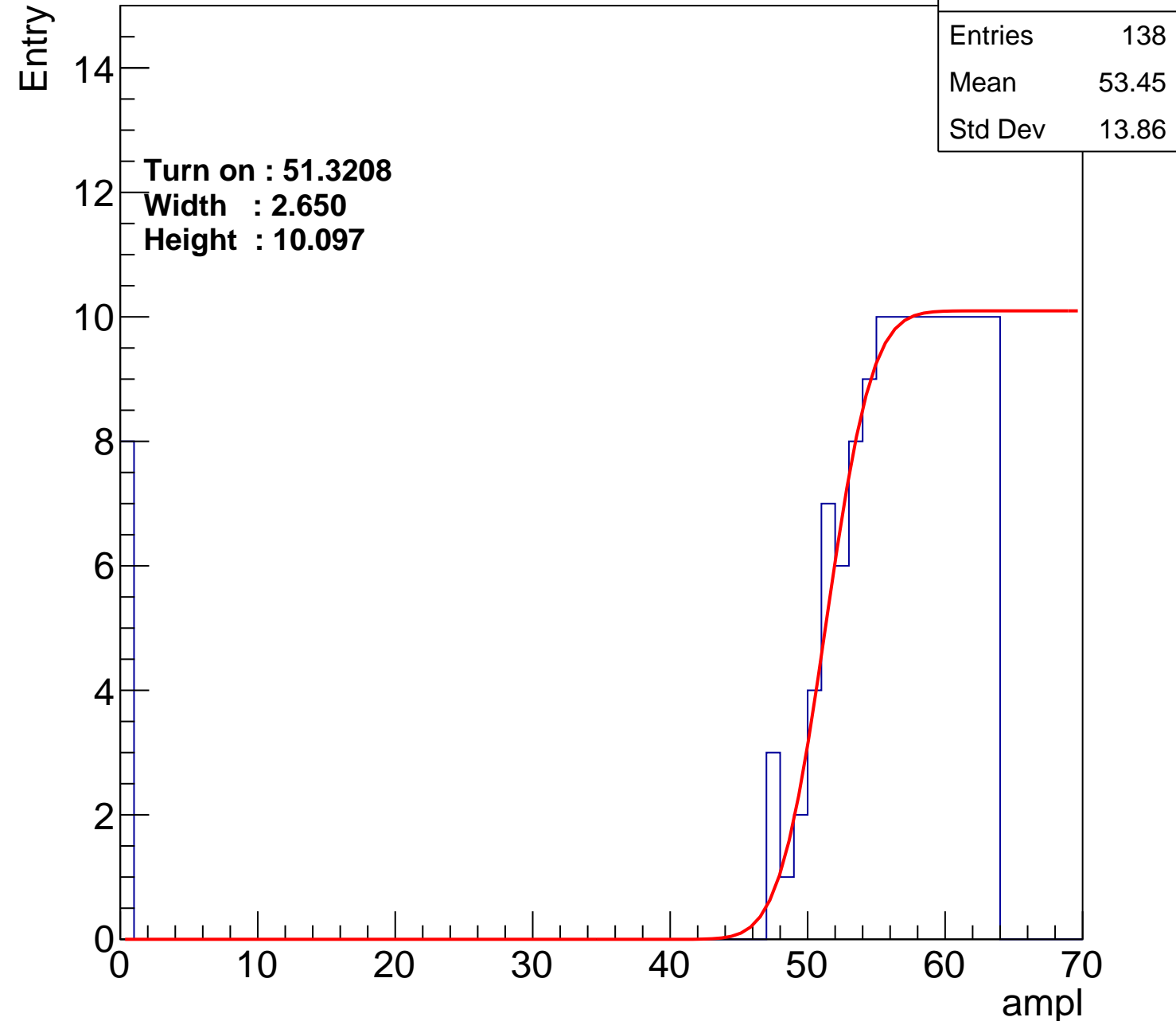
Width : 2.650

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.51
Std Dev	8.07

Turn on : 50.8563

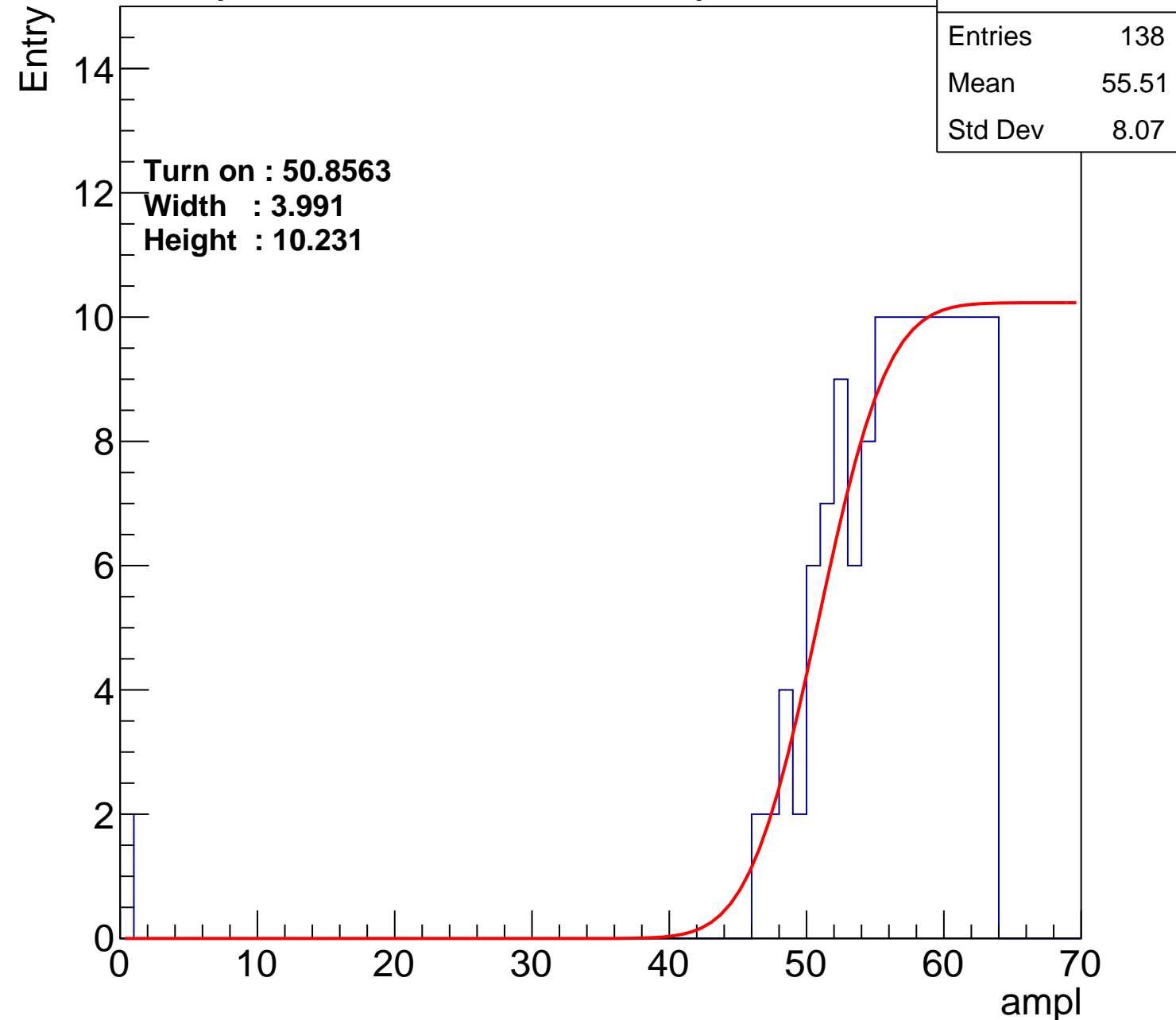
Width : 3.991

Height : 10.231

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.81
Std Dev	7.988

Turn on : 51.2825

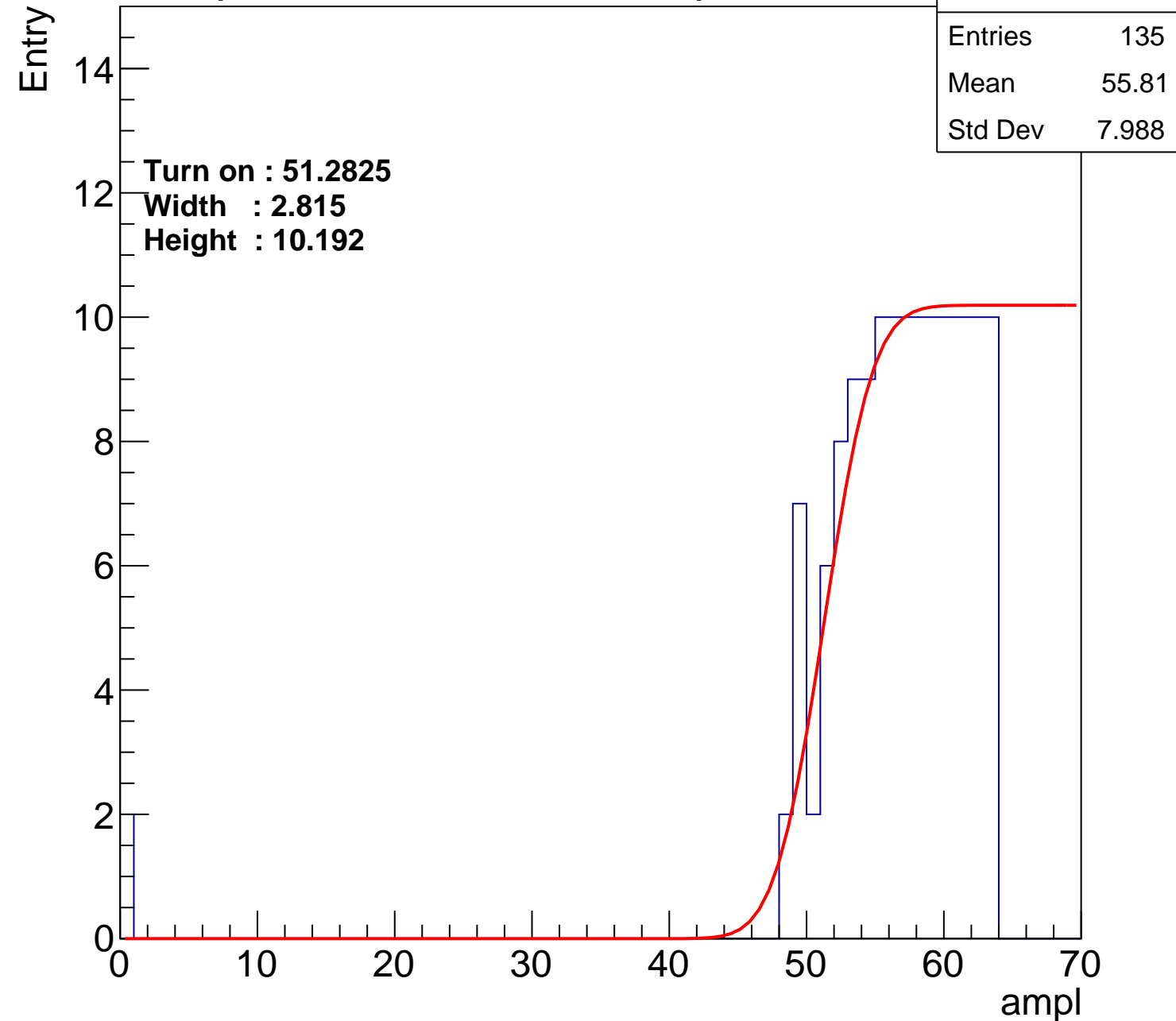
Width : 2.815

Height : 10.192

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	54.38
Std Dev	10.1

Turn on : 49.8212

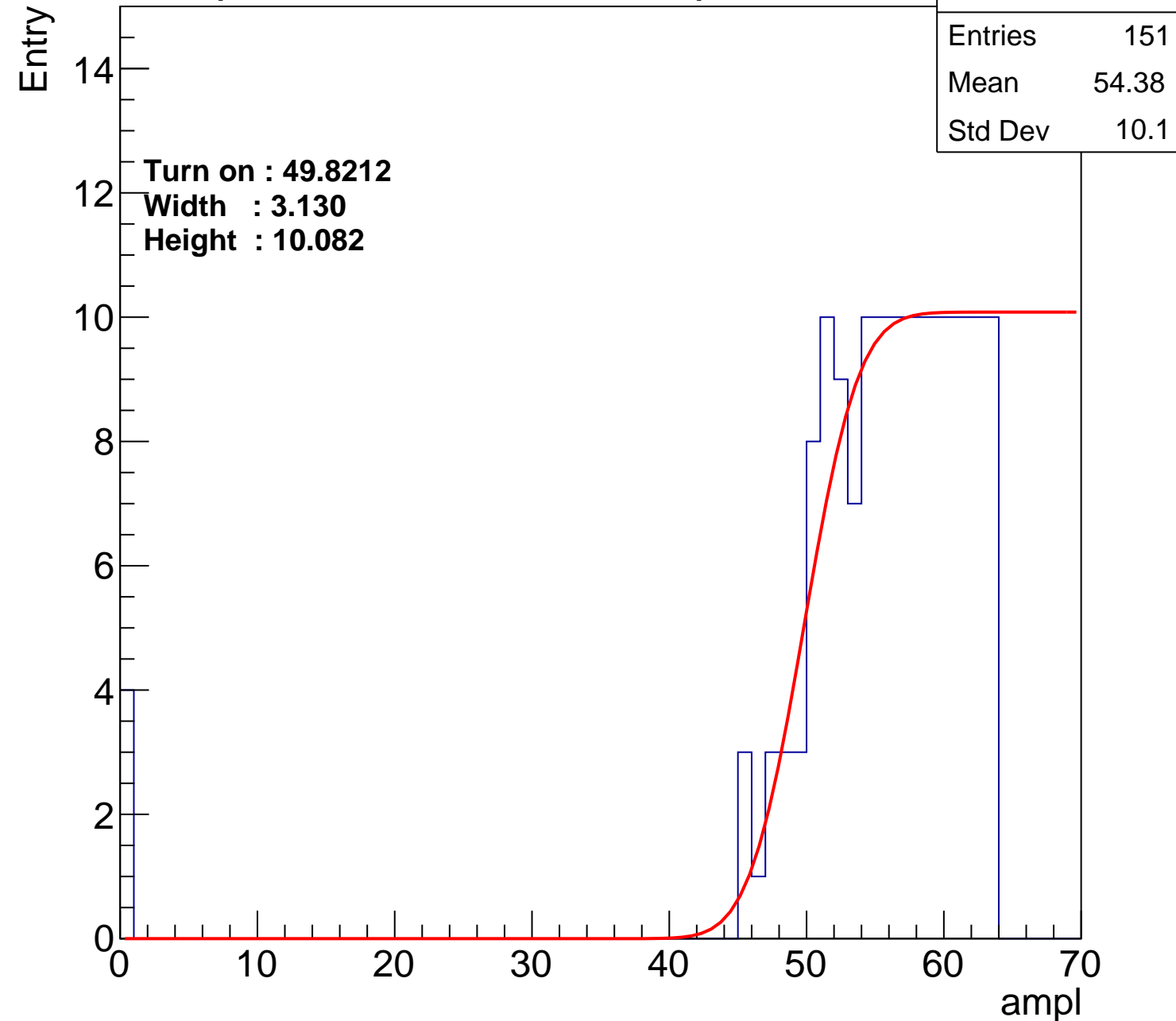
Width : 3.130

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.72
Std Dev	9.669

Turn on : 52.5128

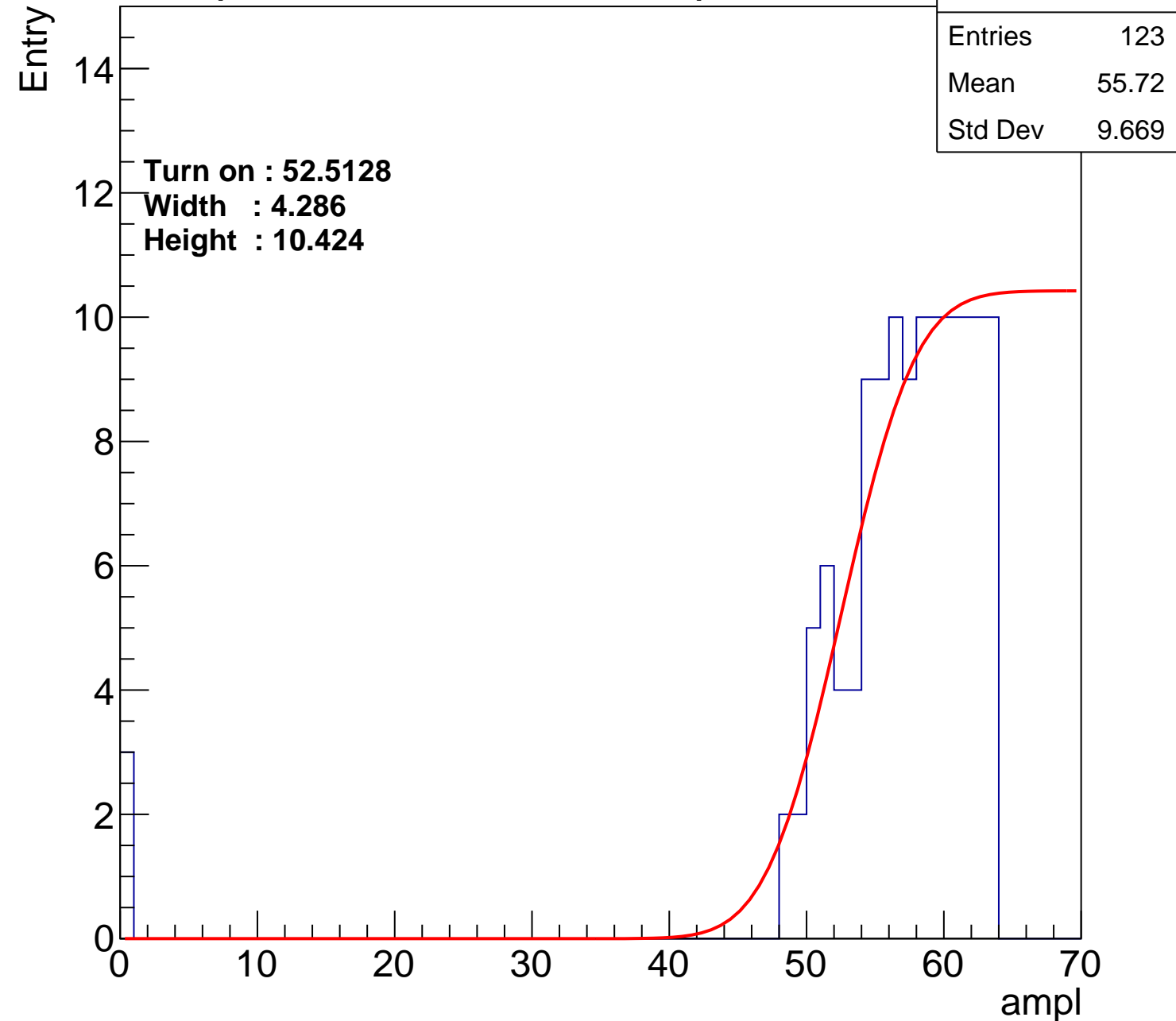
Width : 4.286

Height : 10.424

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch120

calib_packv5_040323_1717.root, FC#2, port C3

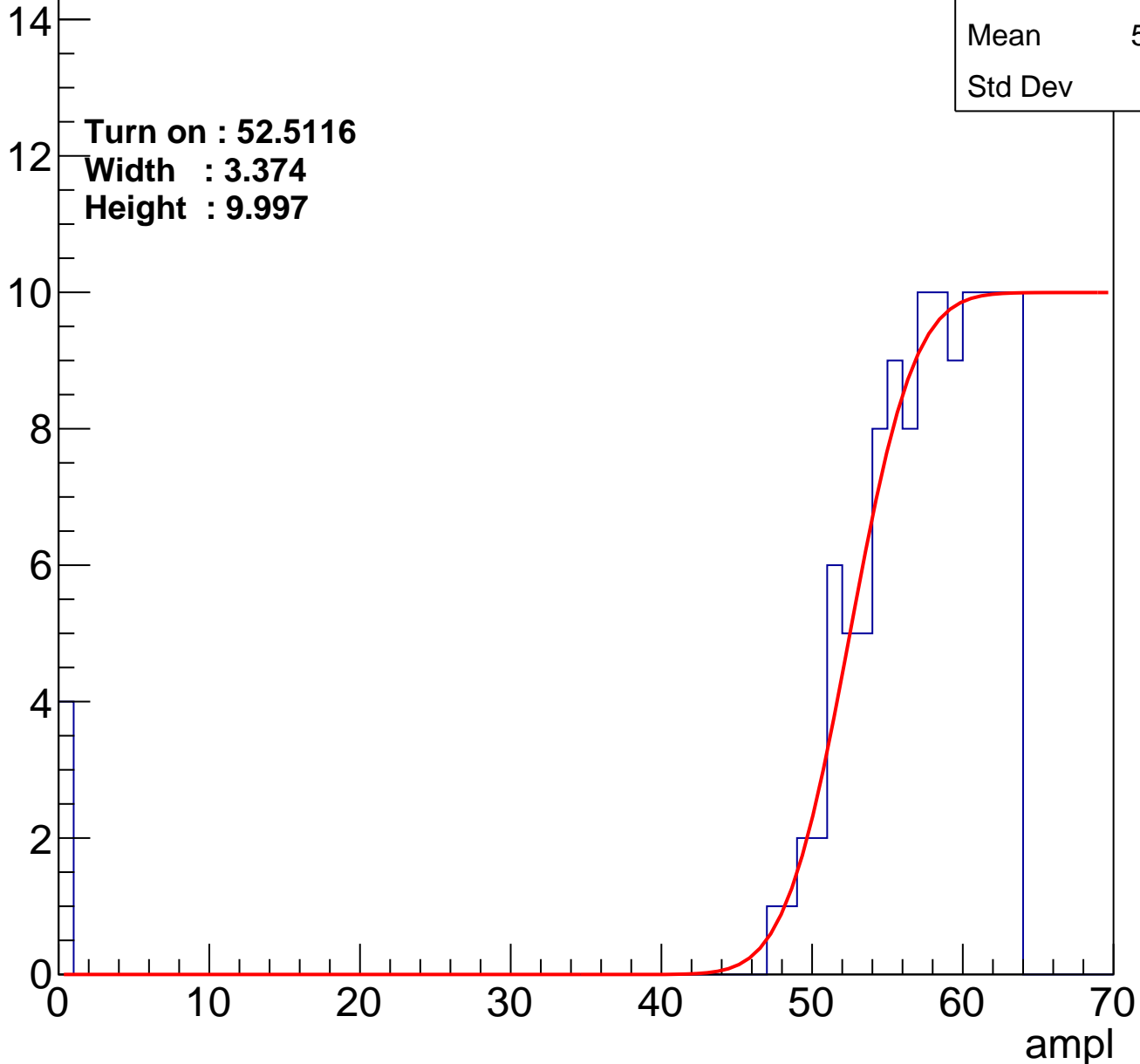
Entries	120
Mean	55.33
Std Dev	11

Turn on : 52.5116

Width : 3.374

Height : 9.997

Entry



B0L103S, U10-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	55.16
Std Dev	12.38

Turn on : 53.2909

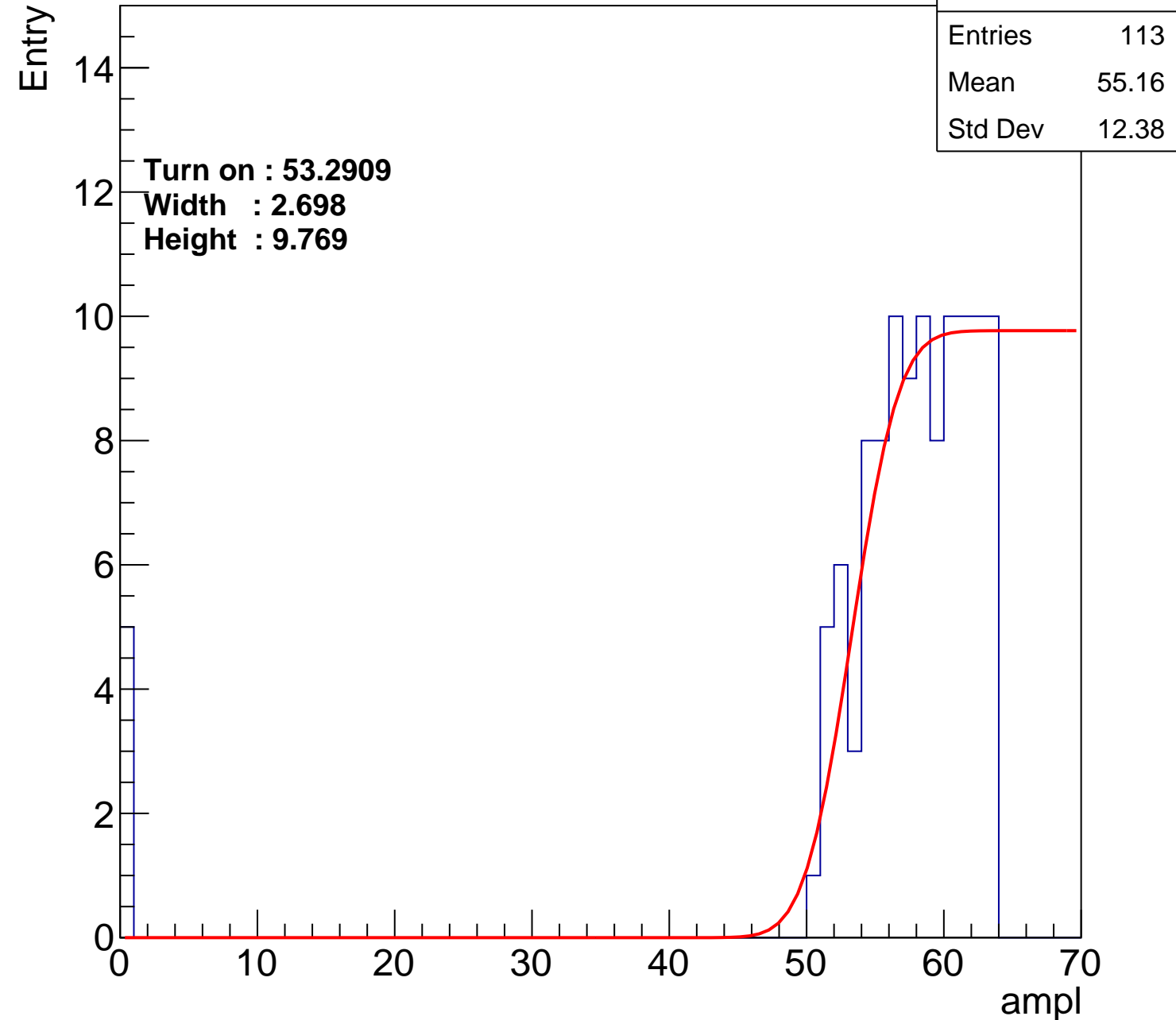
Width : 2.698

Height : 9.769

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.98
Std Dev	10.6

Turn on : 51.0143

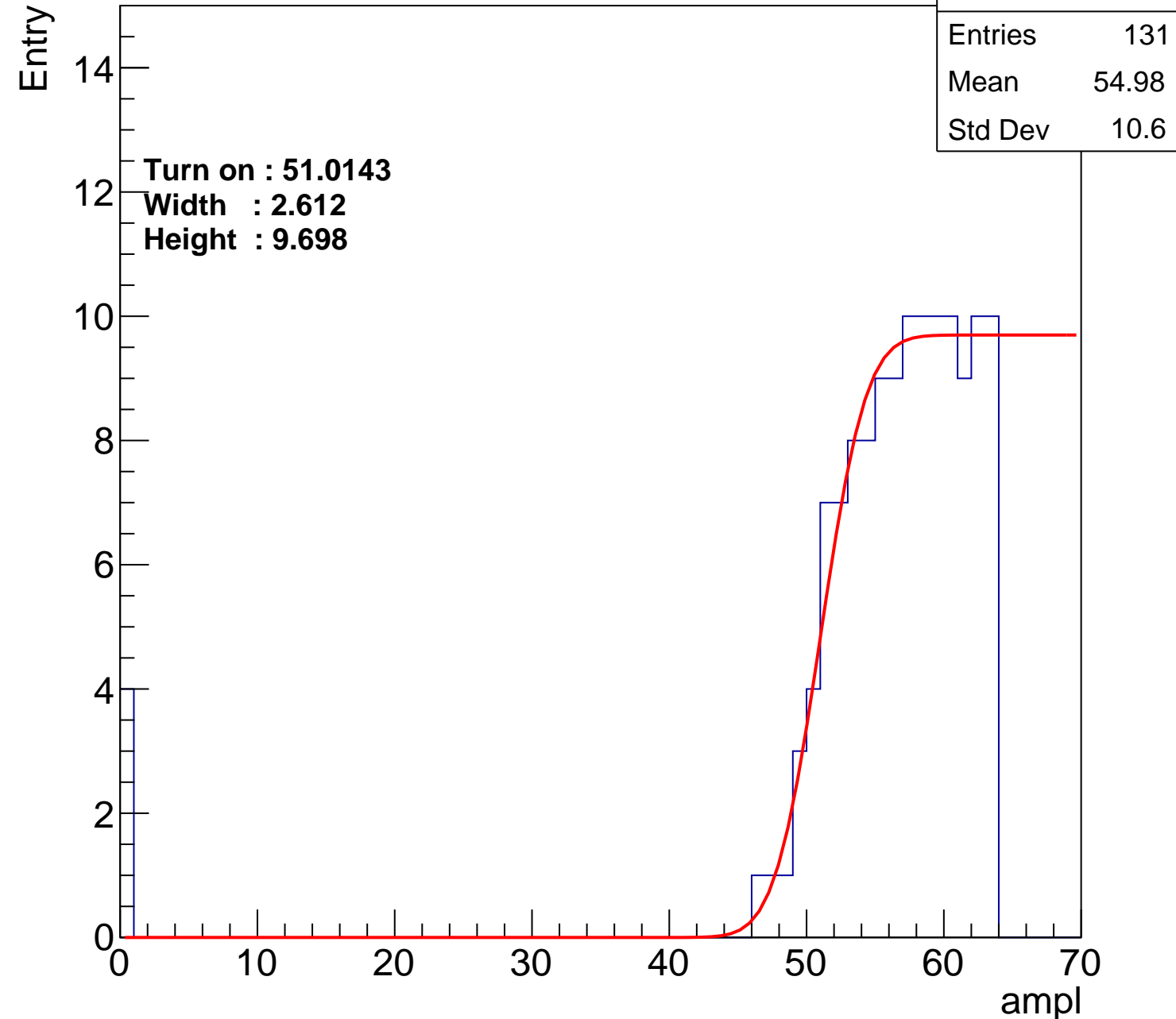
Width : 2.612

Height : 9.698

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	97
Mean	57.28
Std Dev	6.894

Turn on : 54.1471

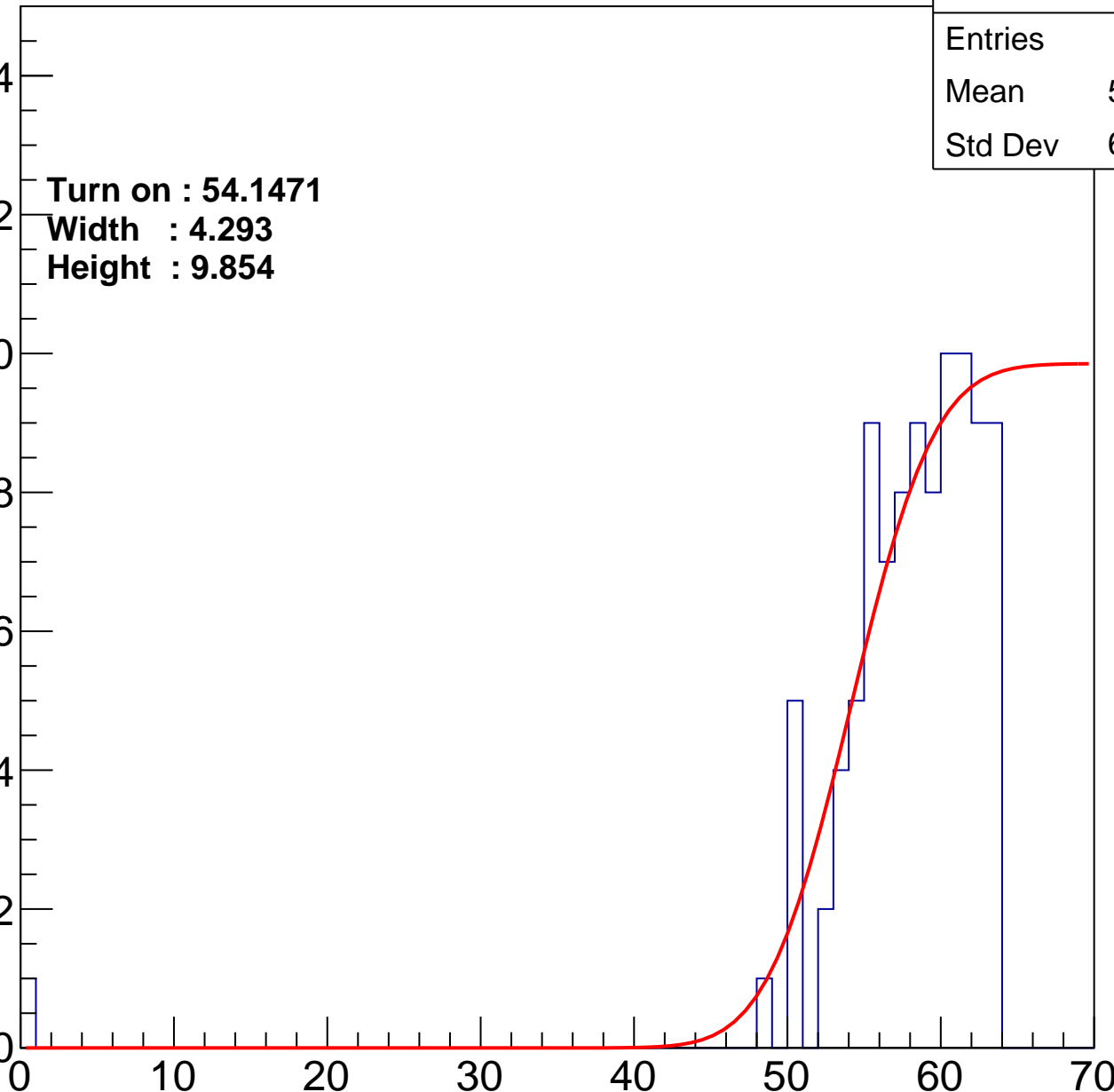
Width : 4.293

Height : 9.854

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch124

calib_packv5_040323_1717.root, FC#2, port C3

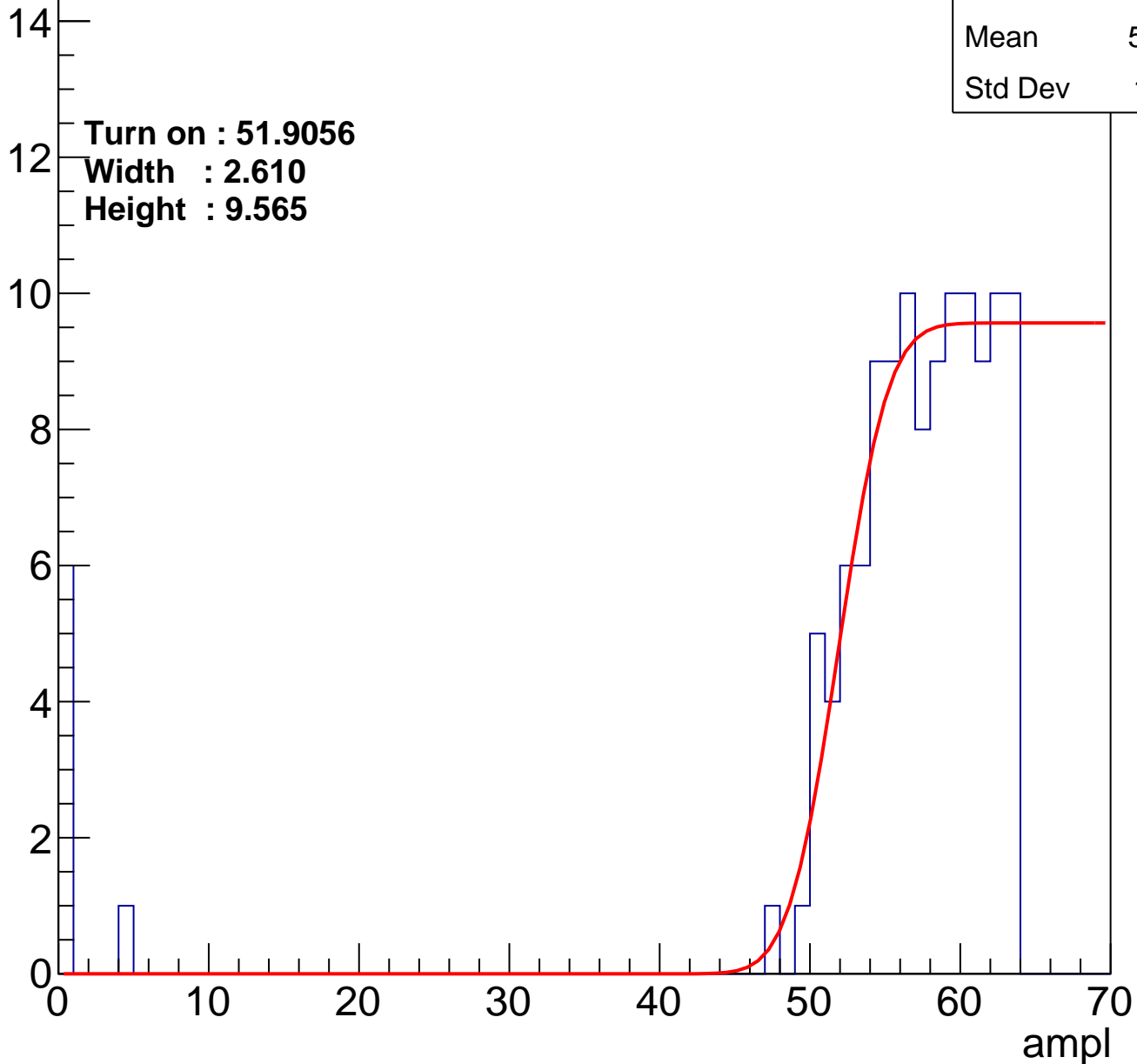
Entries	124
Mean	53.97
Std Dev	13.61

Turn on : 51.9056

Width : 2.610

Height : 9.565

Entry



B0L103S, U10-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	54.28
Std Dev	12.72

Turn on : 51.4874

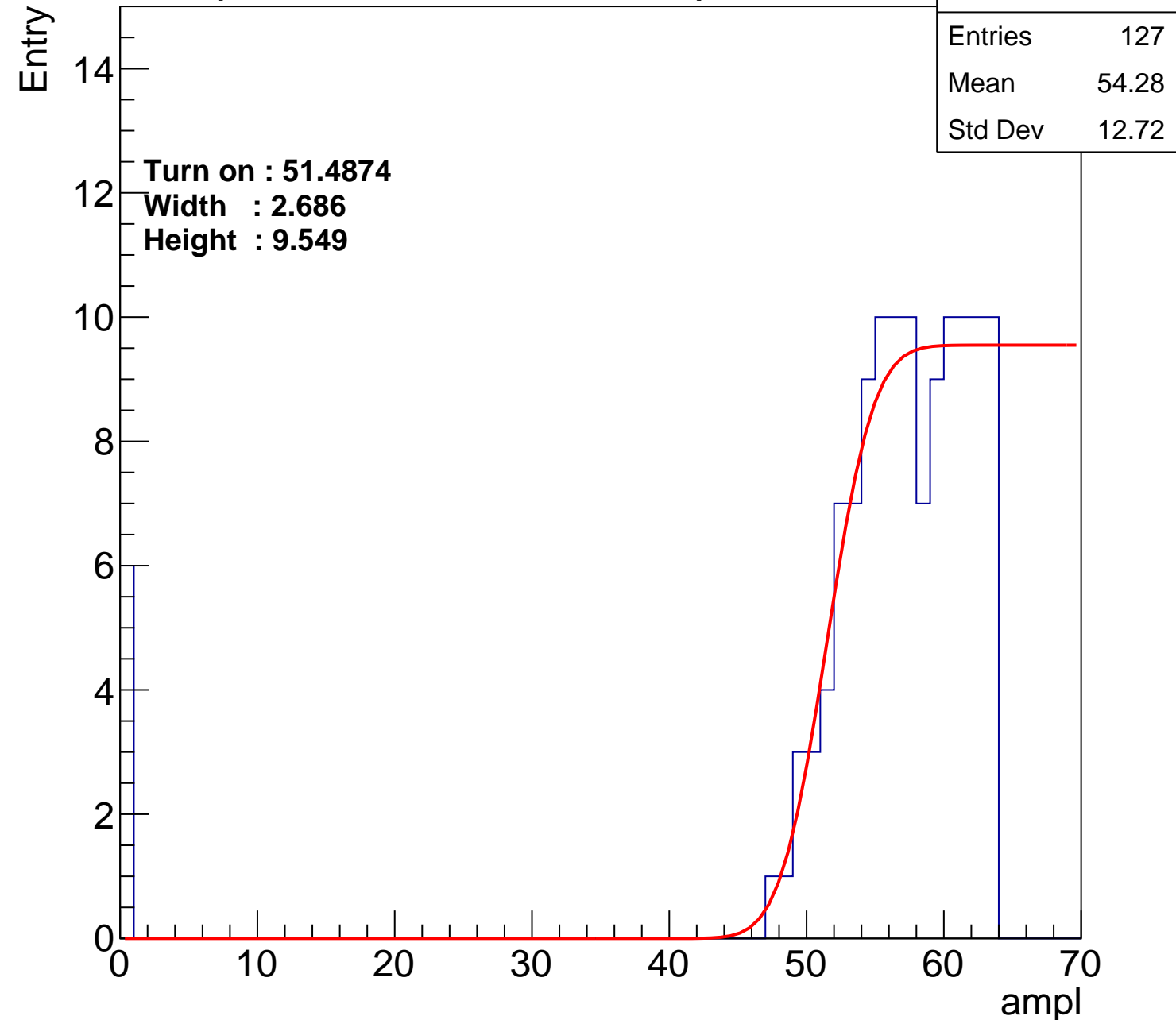
Width : 2.686

Height : 9.549

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.9
Std Dev	10.53

Turn on : 51.1197

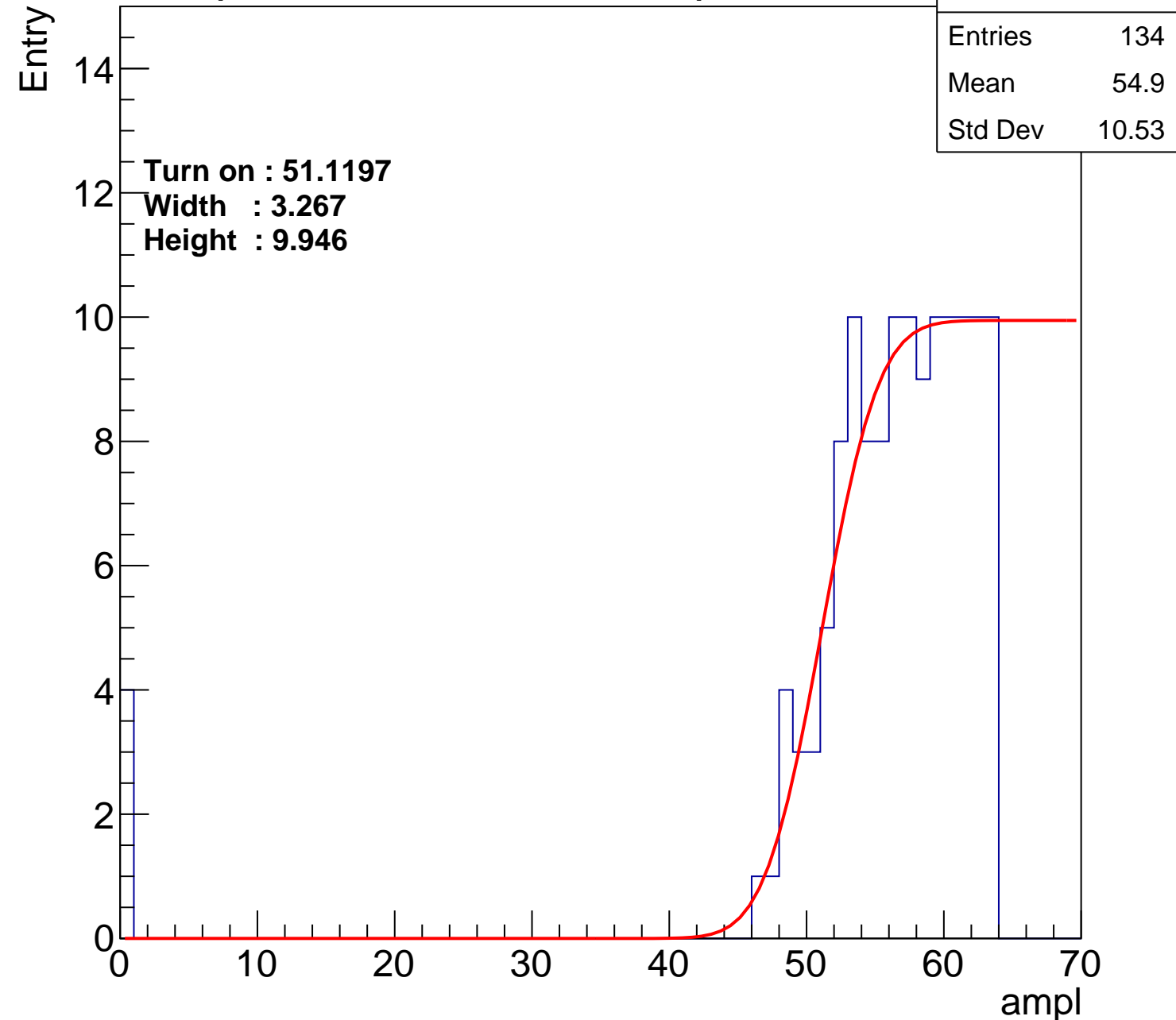
Width : 3.267

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U10-ch127

calib_packv5_040323_1717.root, FC#2, port C3

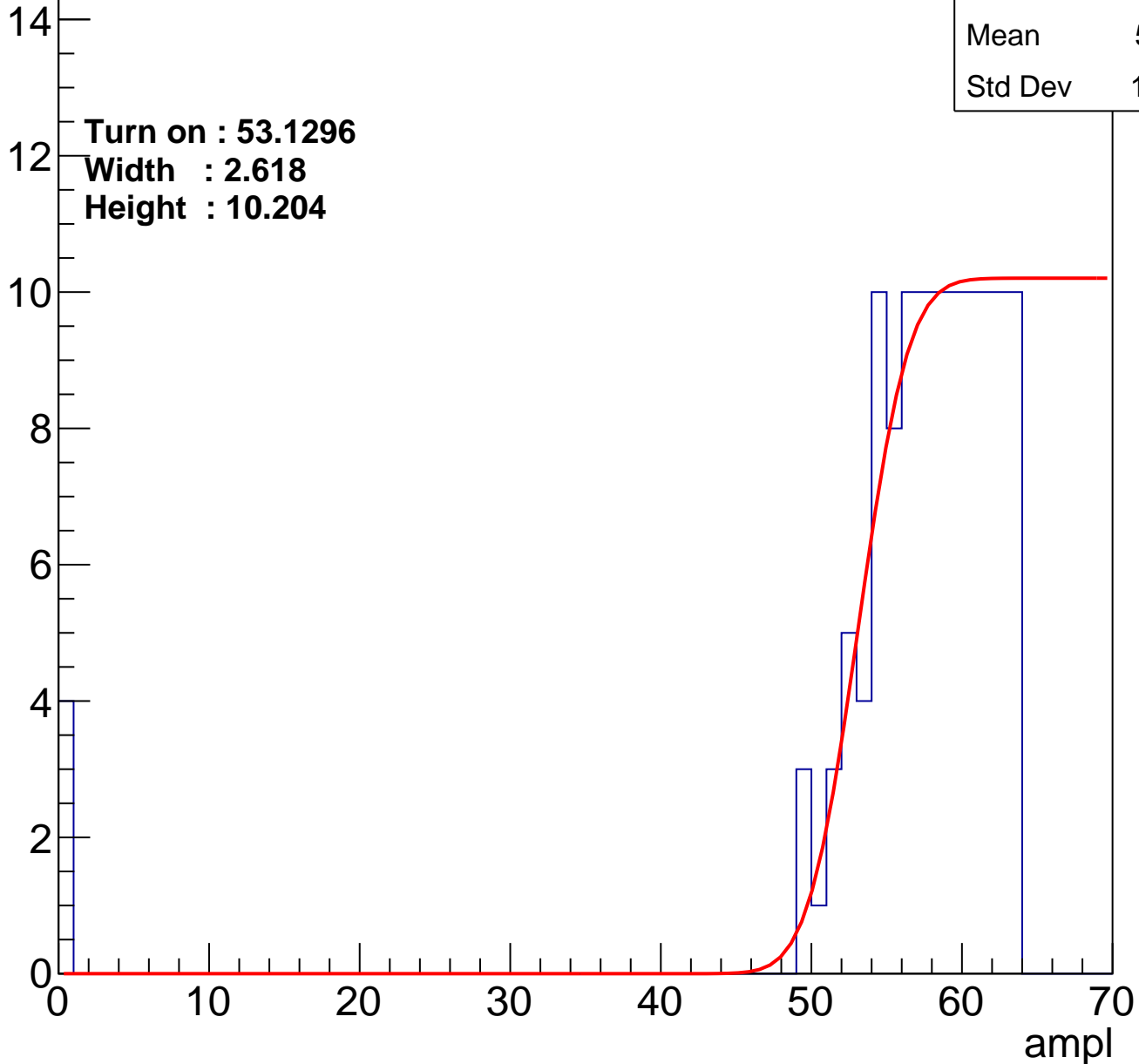
Entries	118
Mean	55.61
Std Dev	11.03

Turn on : 53.1296

Width : 2.618

Height : 10.204

Entry



B0L103S, U10-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	55.61
Std Dev	11.03

Turn on : 53.1296

Width : 2.618

Height : 10.204

Entry

14
12
10
8
6
4
2
0

ampl

