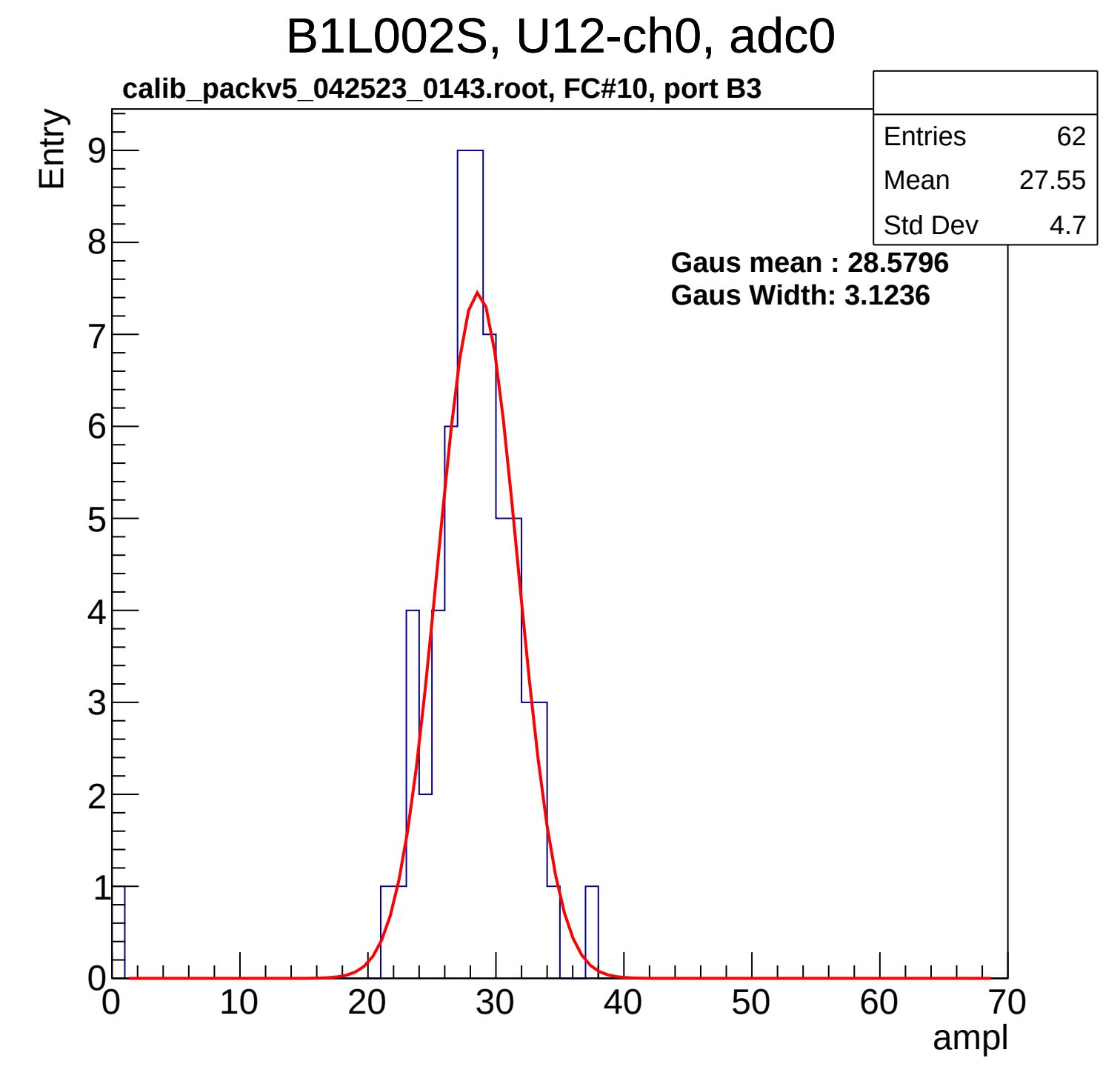
B1L002S, U12-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.55
Std Dev 4.7
Gaus mean : 28.5796
Gaus Width: 3.1236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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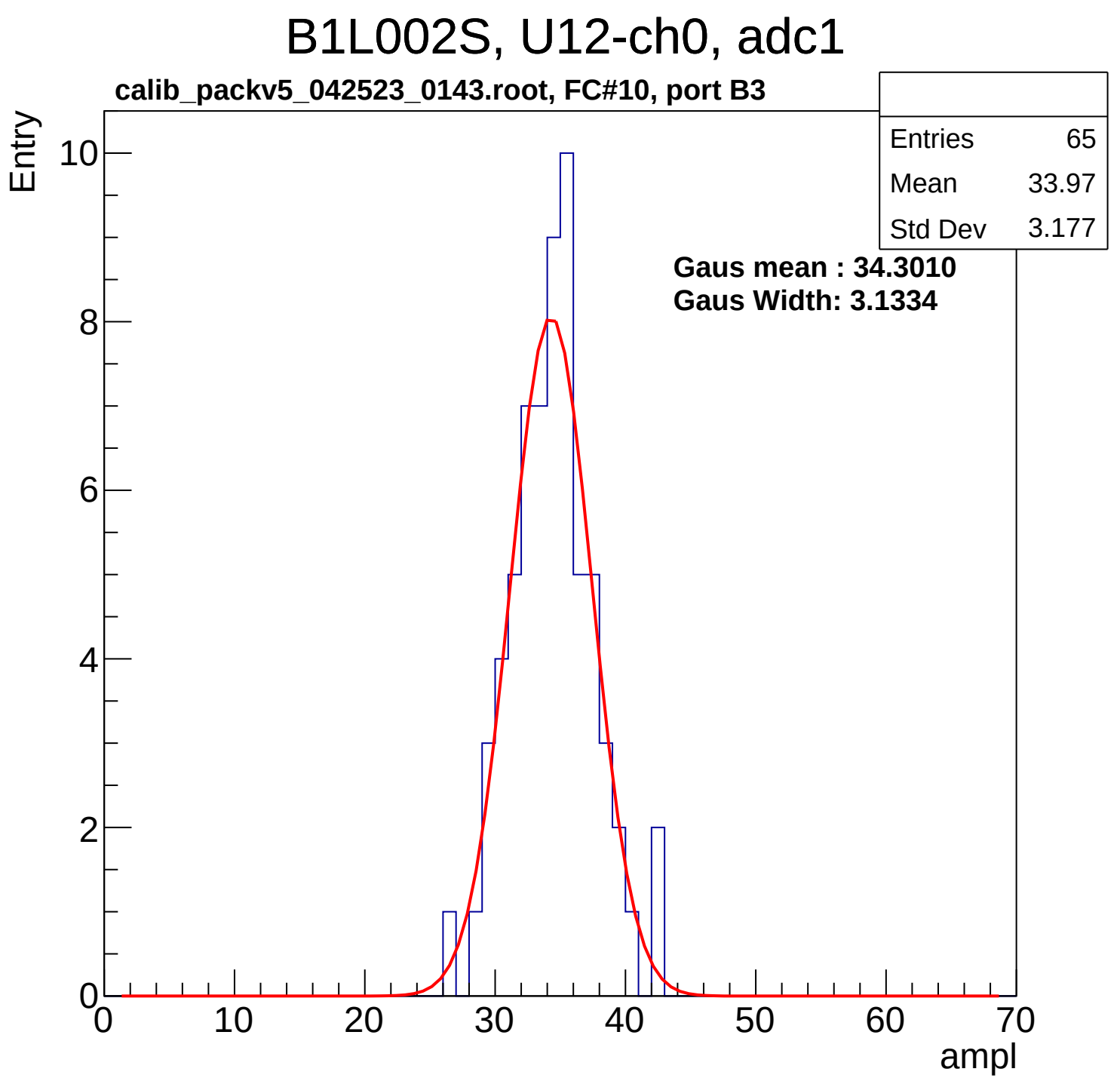
B1L002S, U12-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.97
Std Dev 3.177
Gaus mean : 34.3010
Gaus Width: 3.1334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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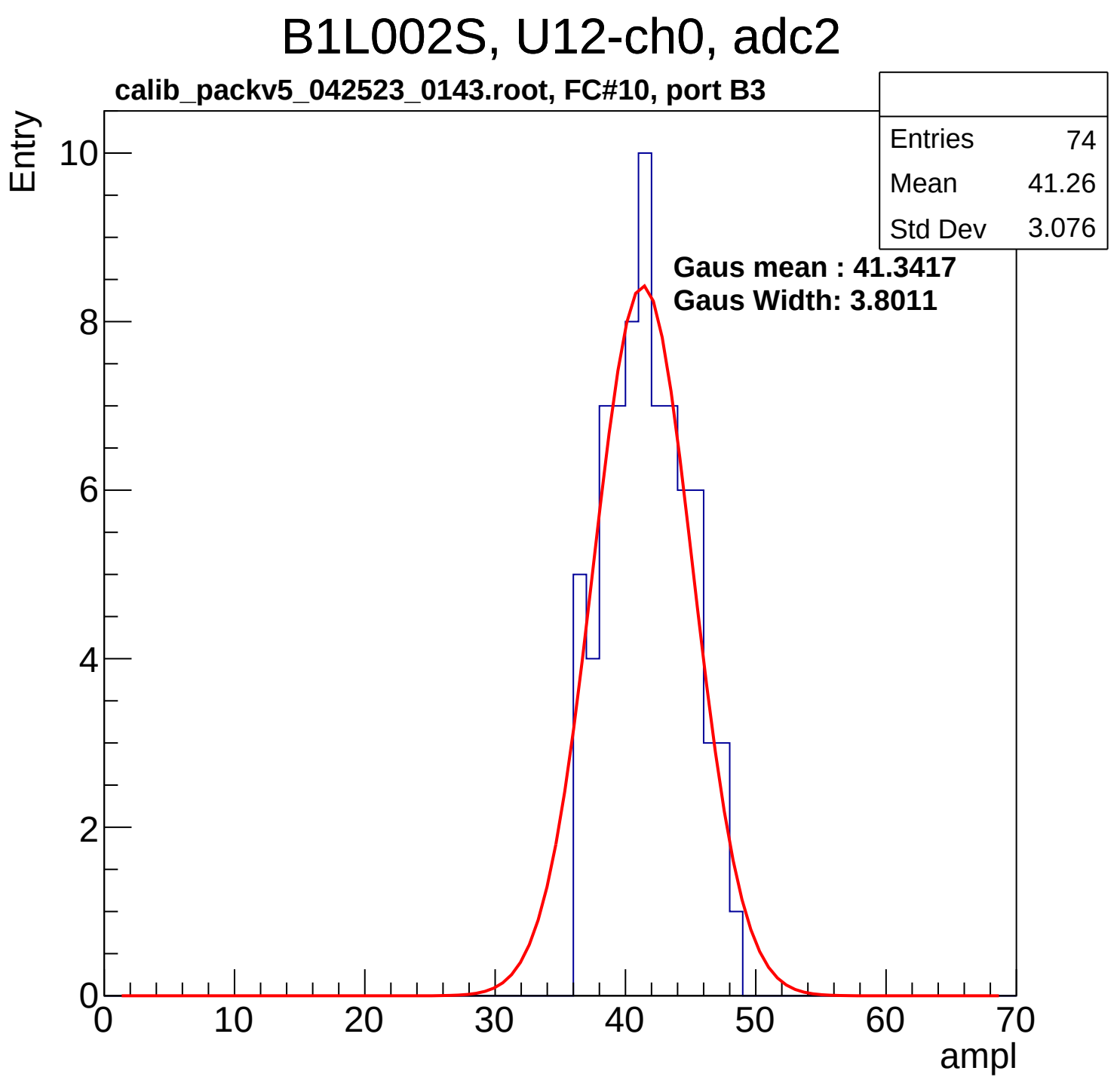
B1L002S, U12-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.26
Std Dev 3.076
Gaus mean : 41.3417
Gaus Width: 3.8011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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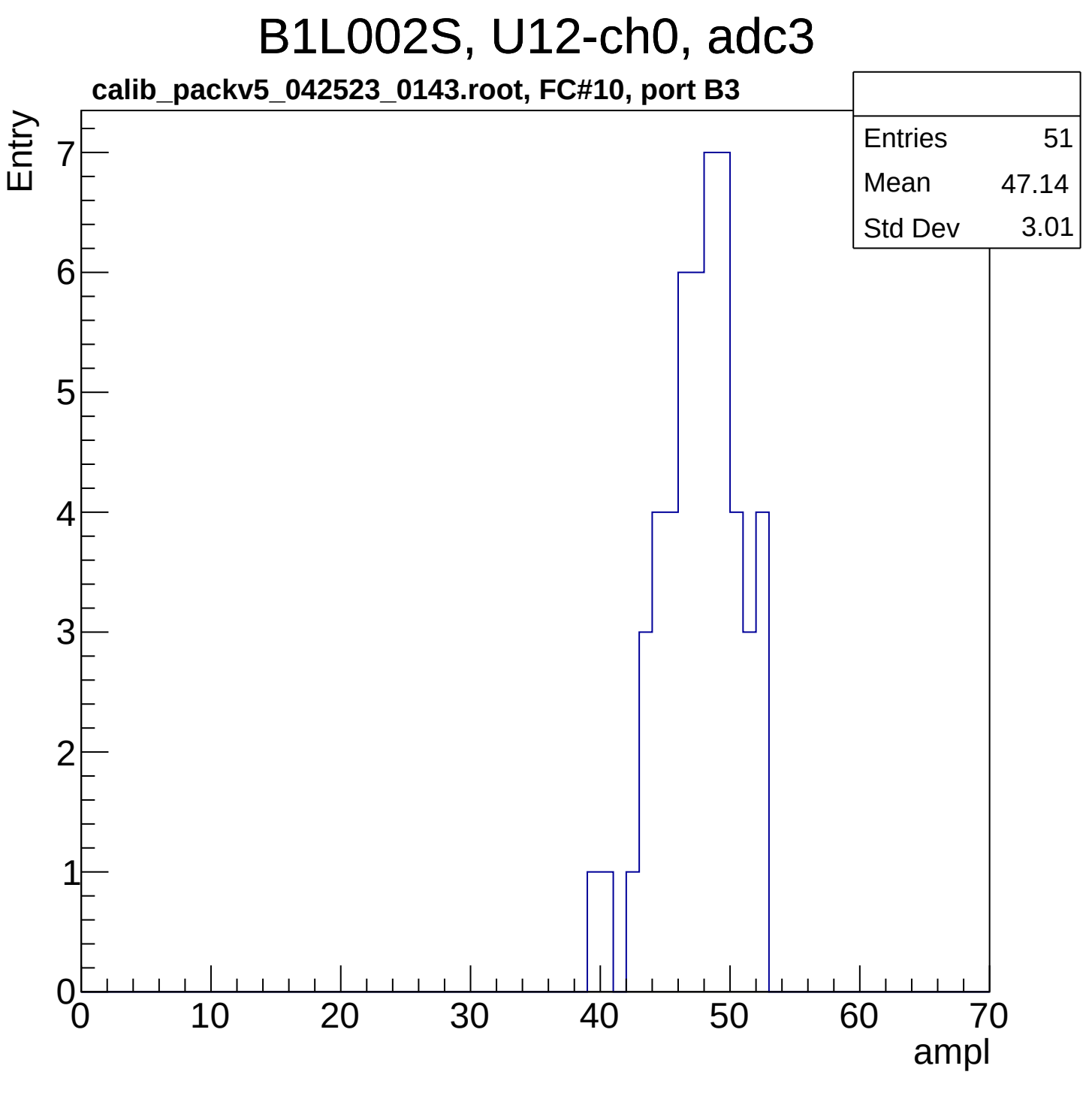
B1L002S, U12-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.14
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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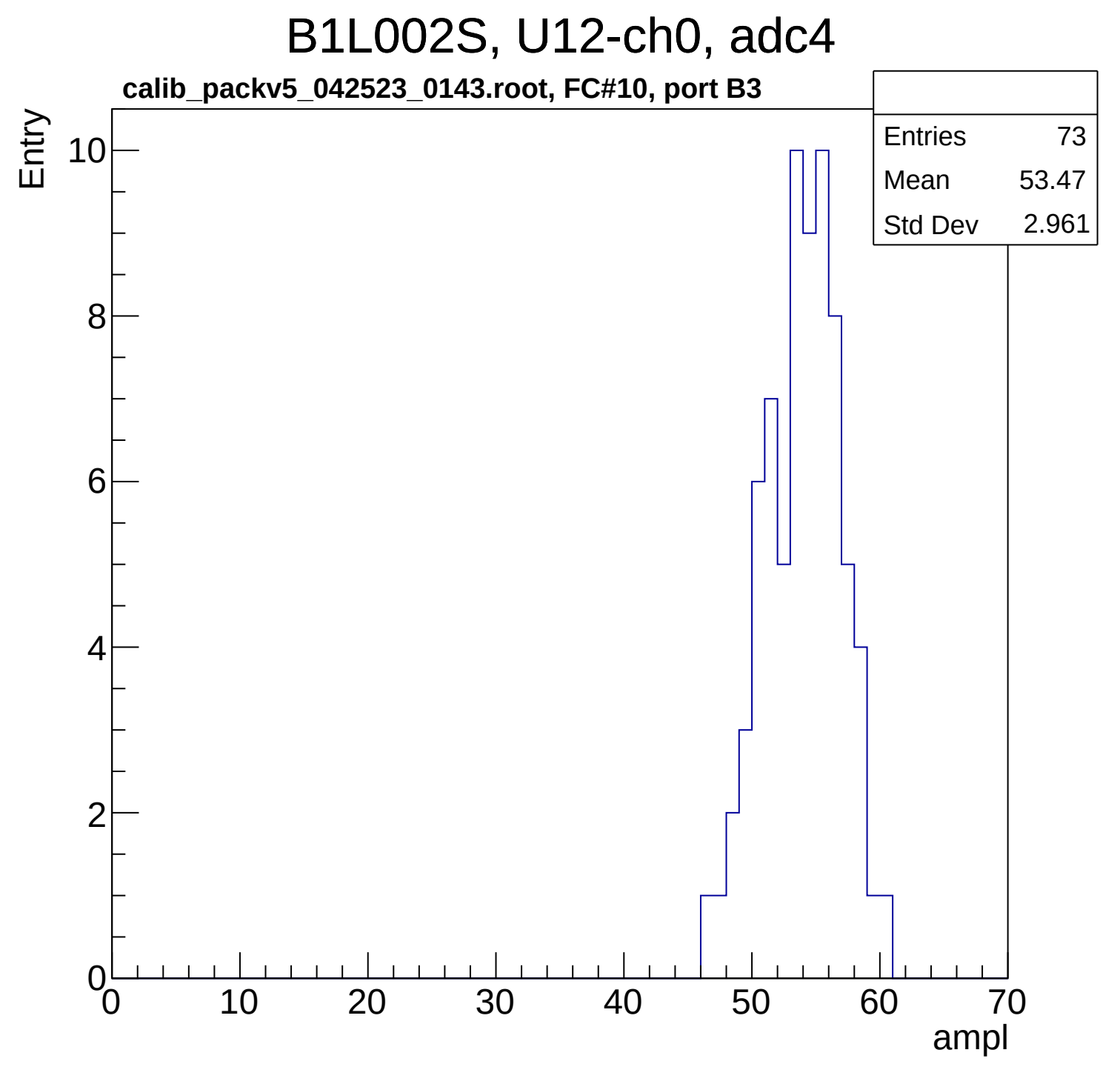
B1L002S, U12-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 53.47
Std Dev 2.961
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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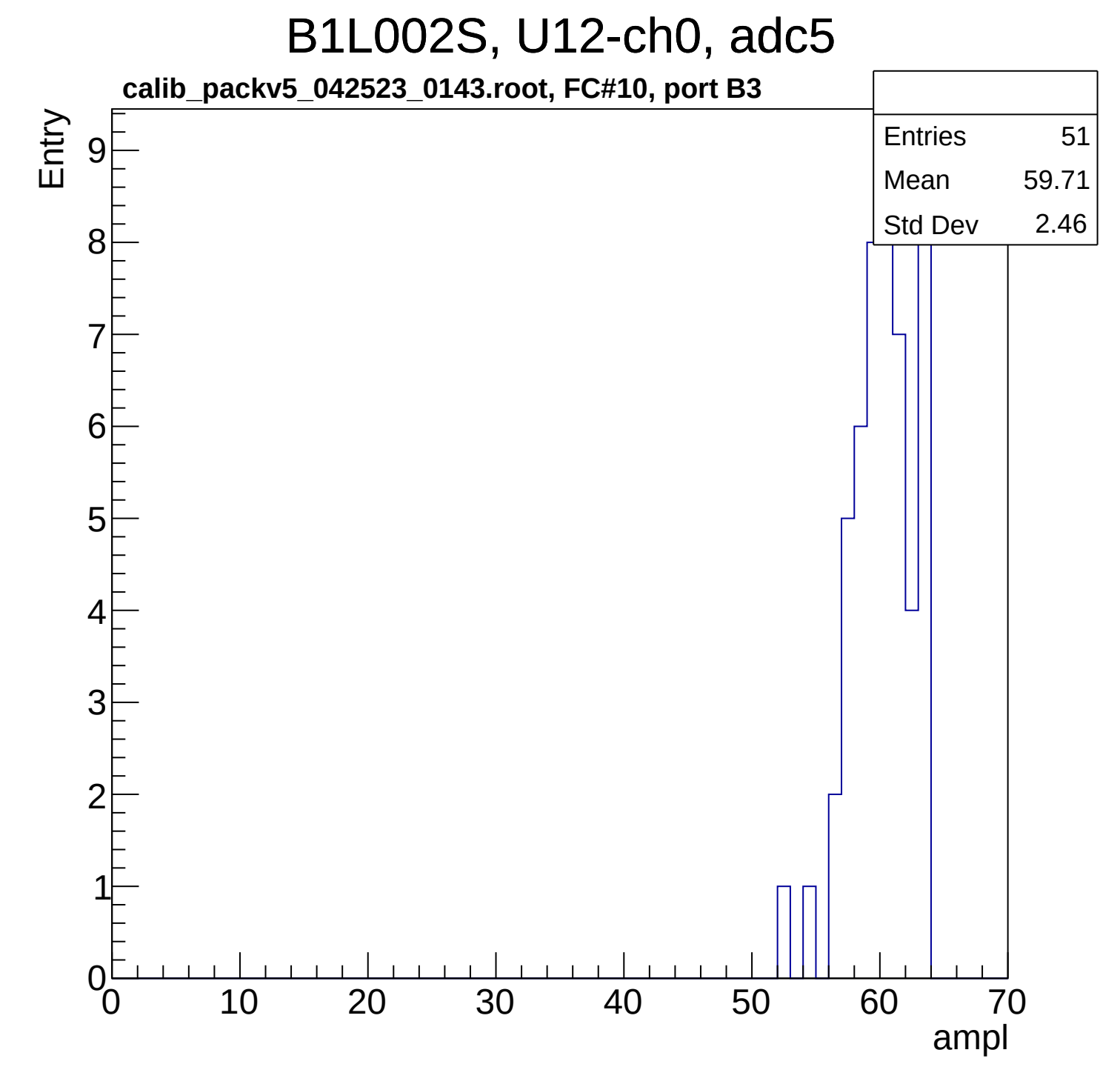
B1L002S, U12-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.71
Std Dev 2.46
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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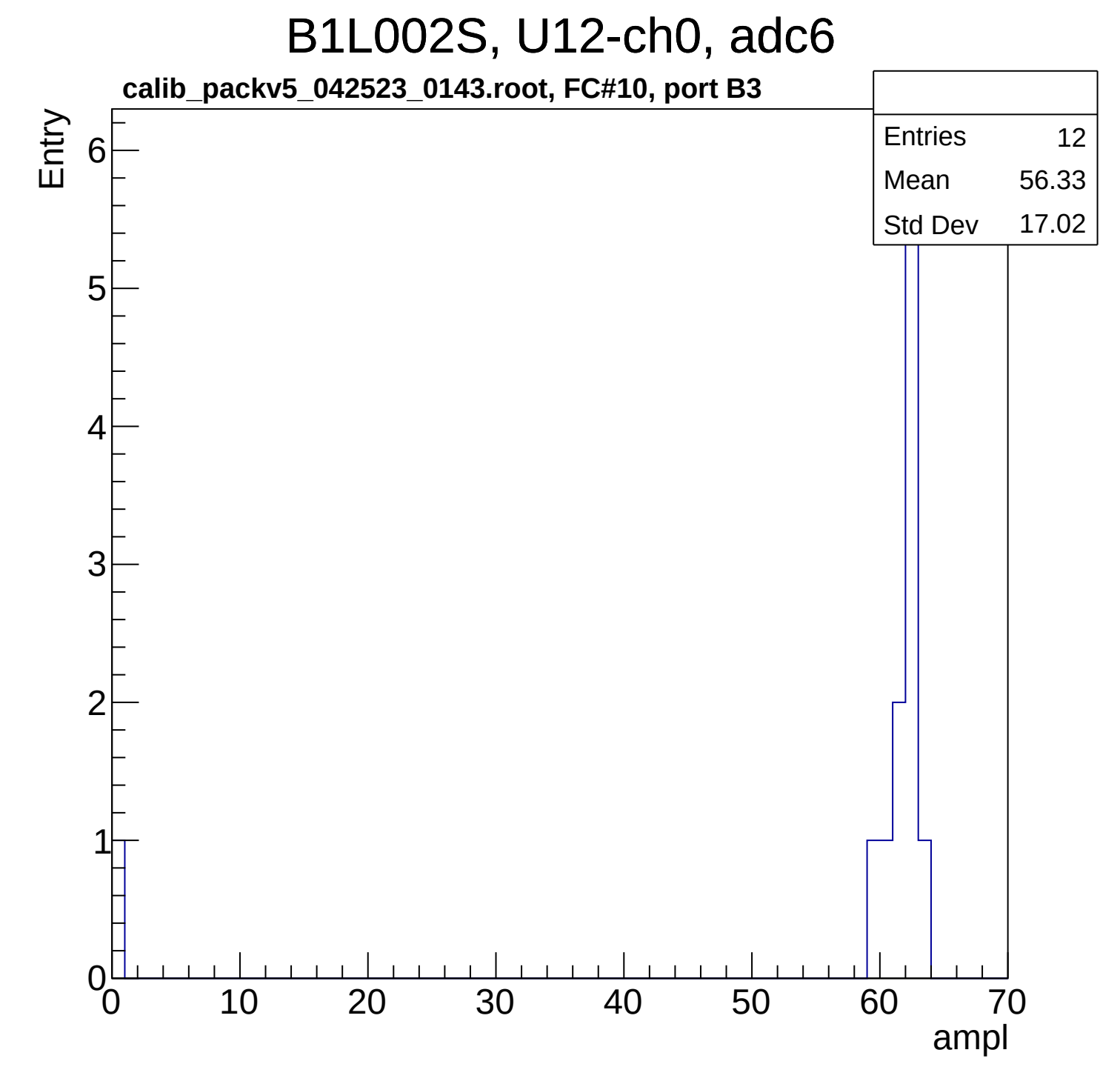
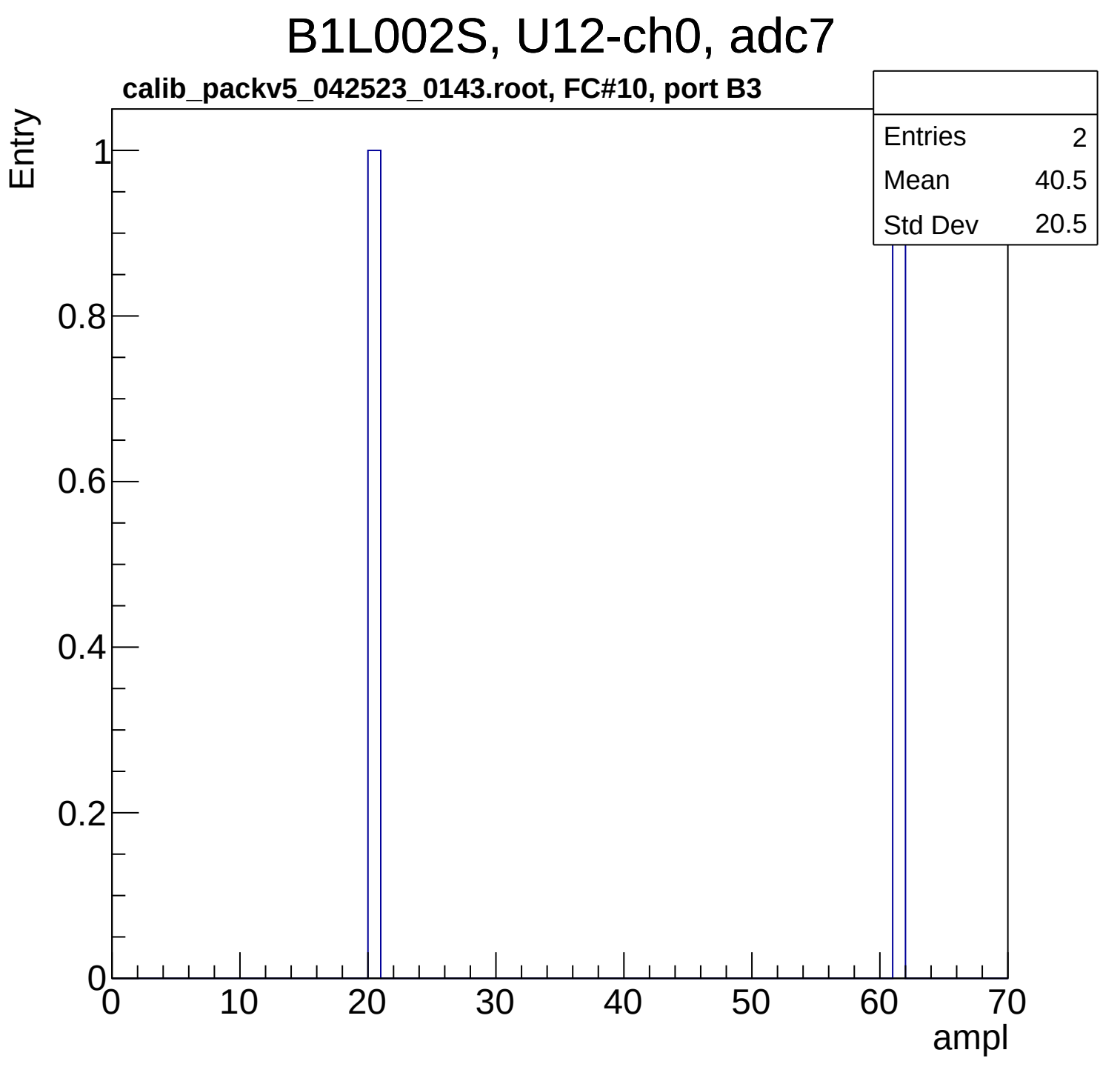
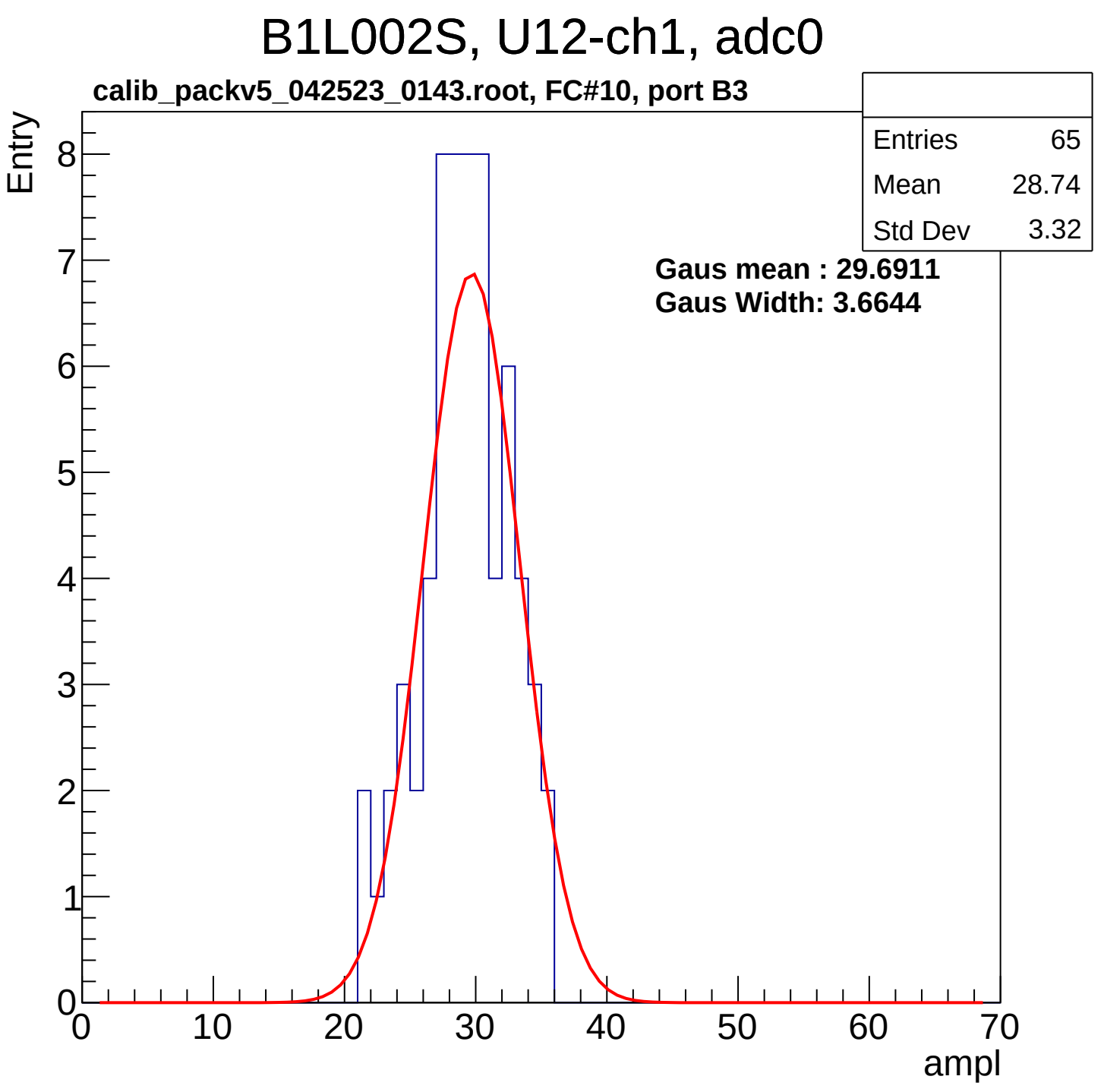

B1L002S, U12-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.33
Std Dev 17.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 40.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U12-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.74
Std Dev 3.32
Gaus mean : 29.6911
Gaus Width: 3.6644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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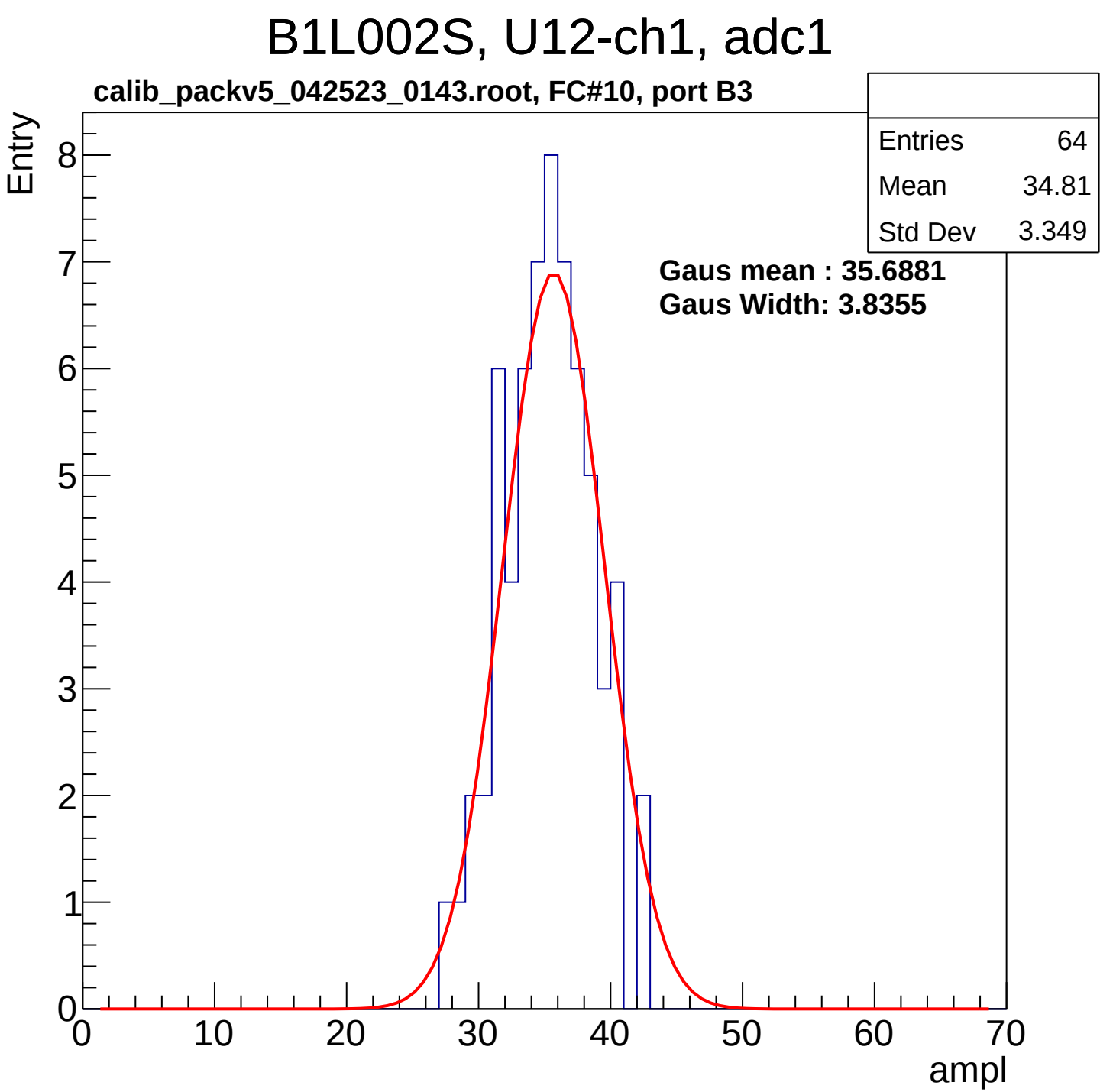
B1L002S, U12-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.81
Std Dev 3.349
Gaus mean : 35.6881
Gaus Width: 3.8355
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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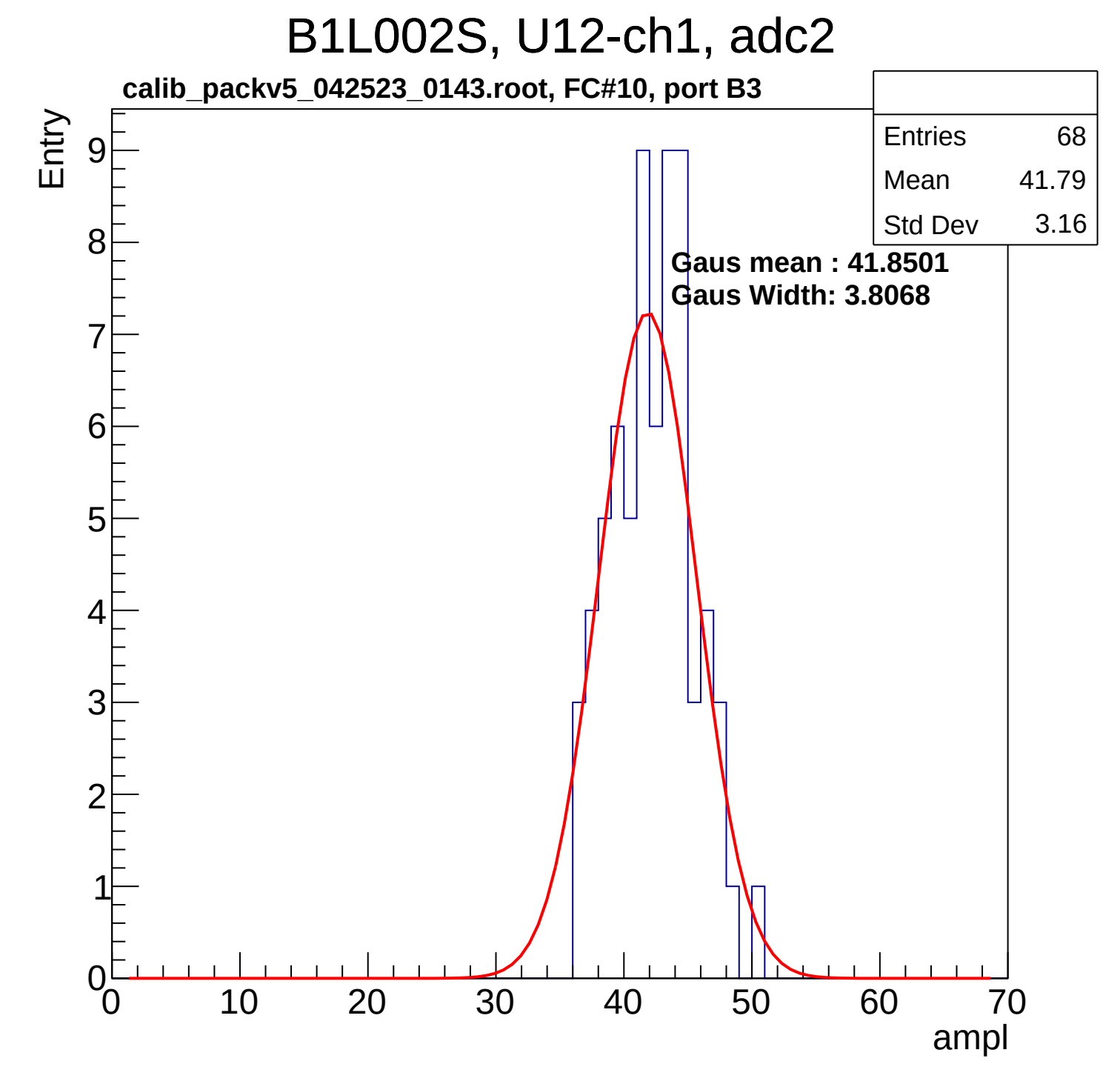
B1L002S, U12-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.79
Std Dev 3.16
Gaus mean : 41.8501
Gaus Width: 3.8068
Entry
ampl
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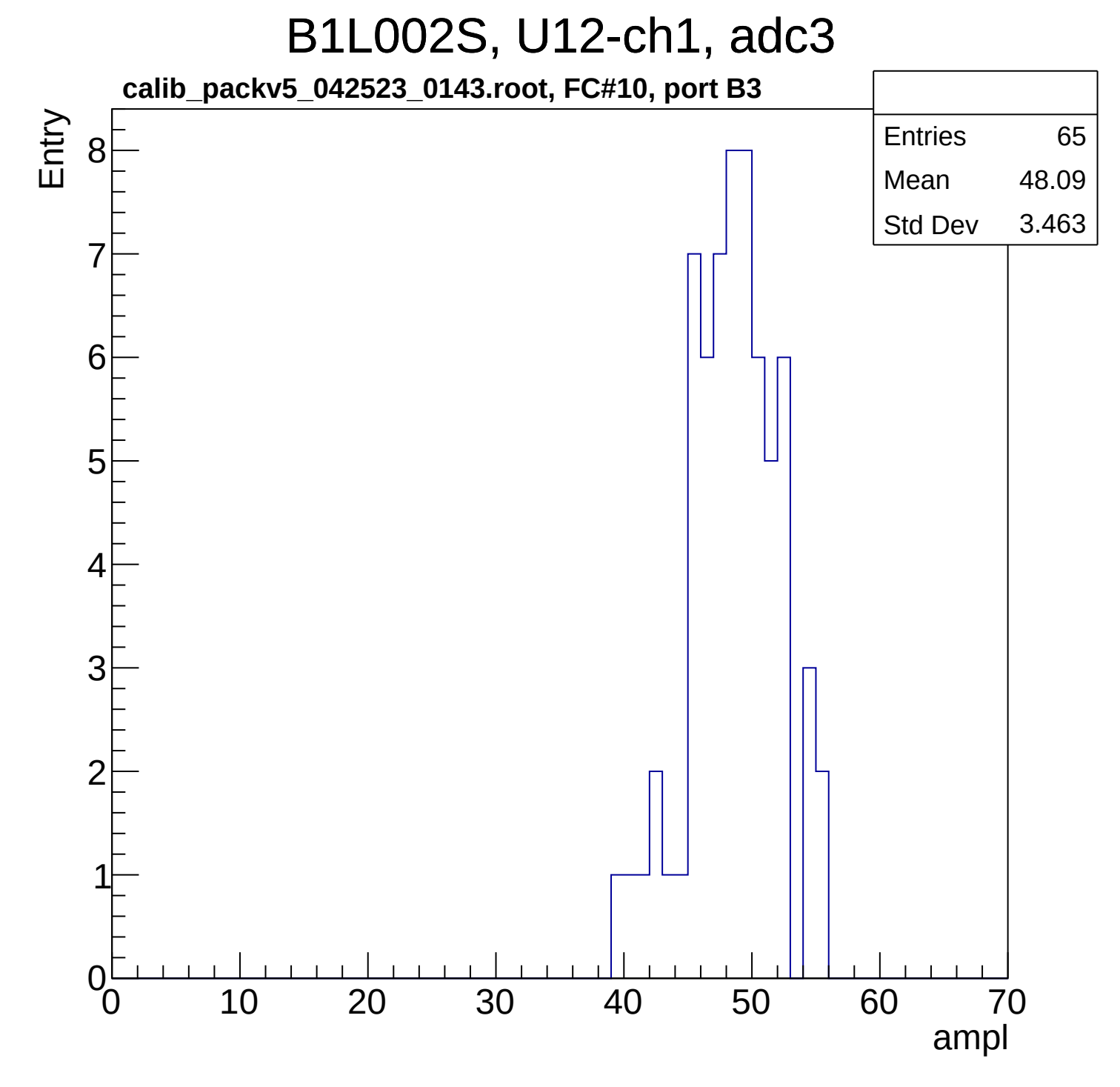
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.09
Std Dev 3.463
Entry
ampl
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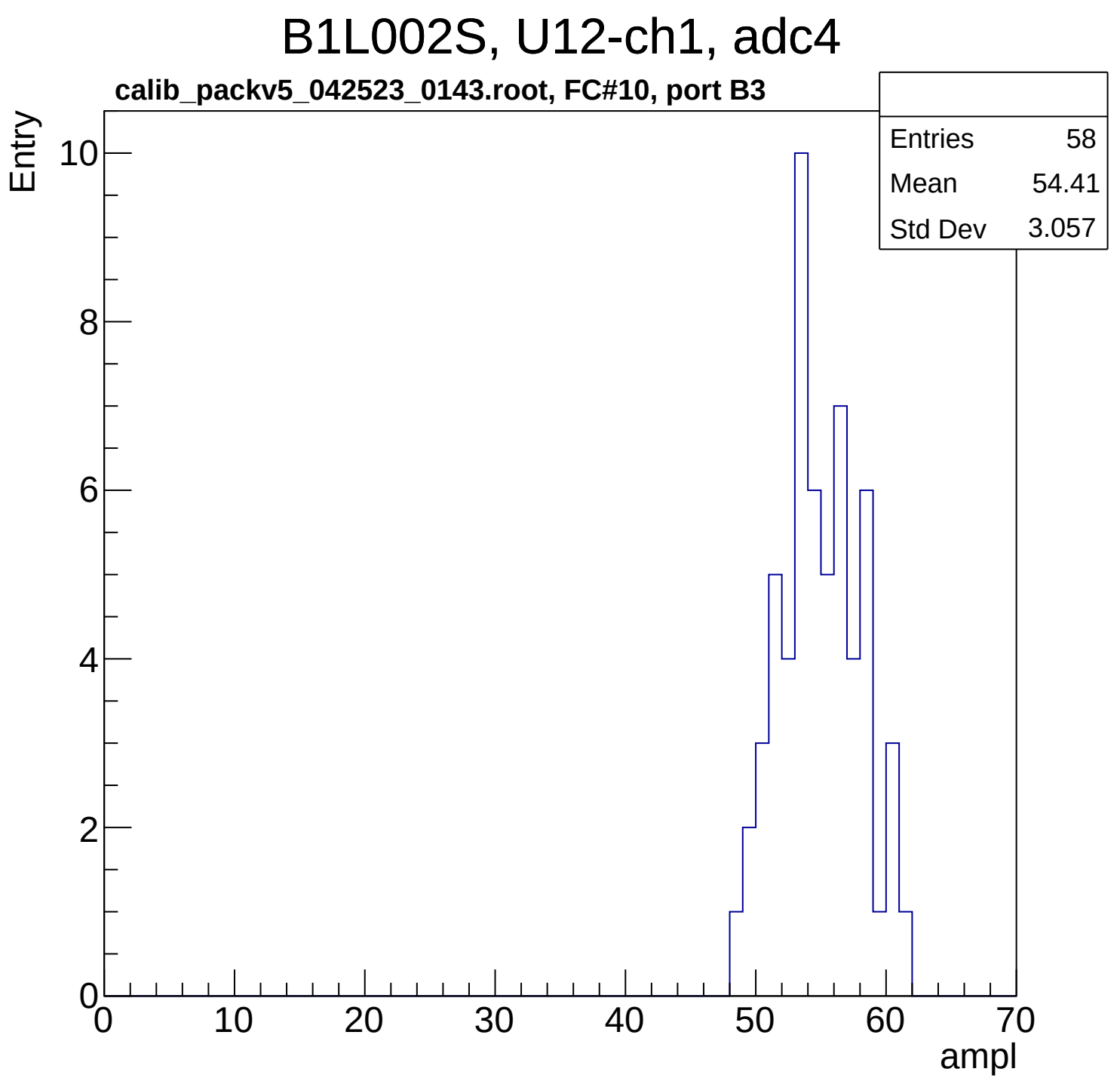
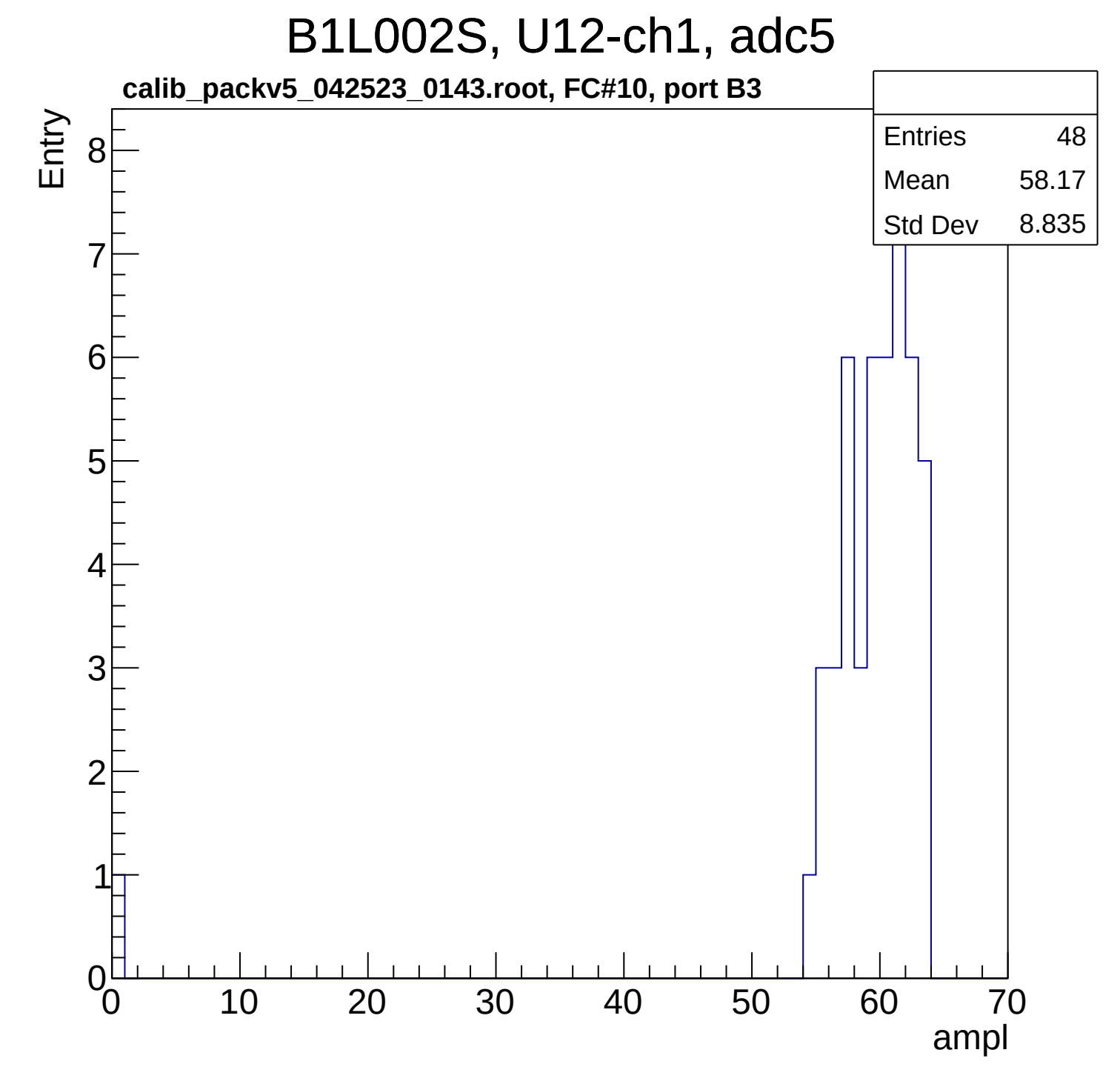
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.41
Std Dev 3.057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.17
Std Dev 8.835
Entry
ampl
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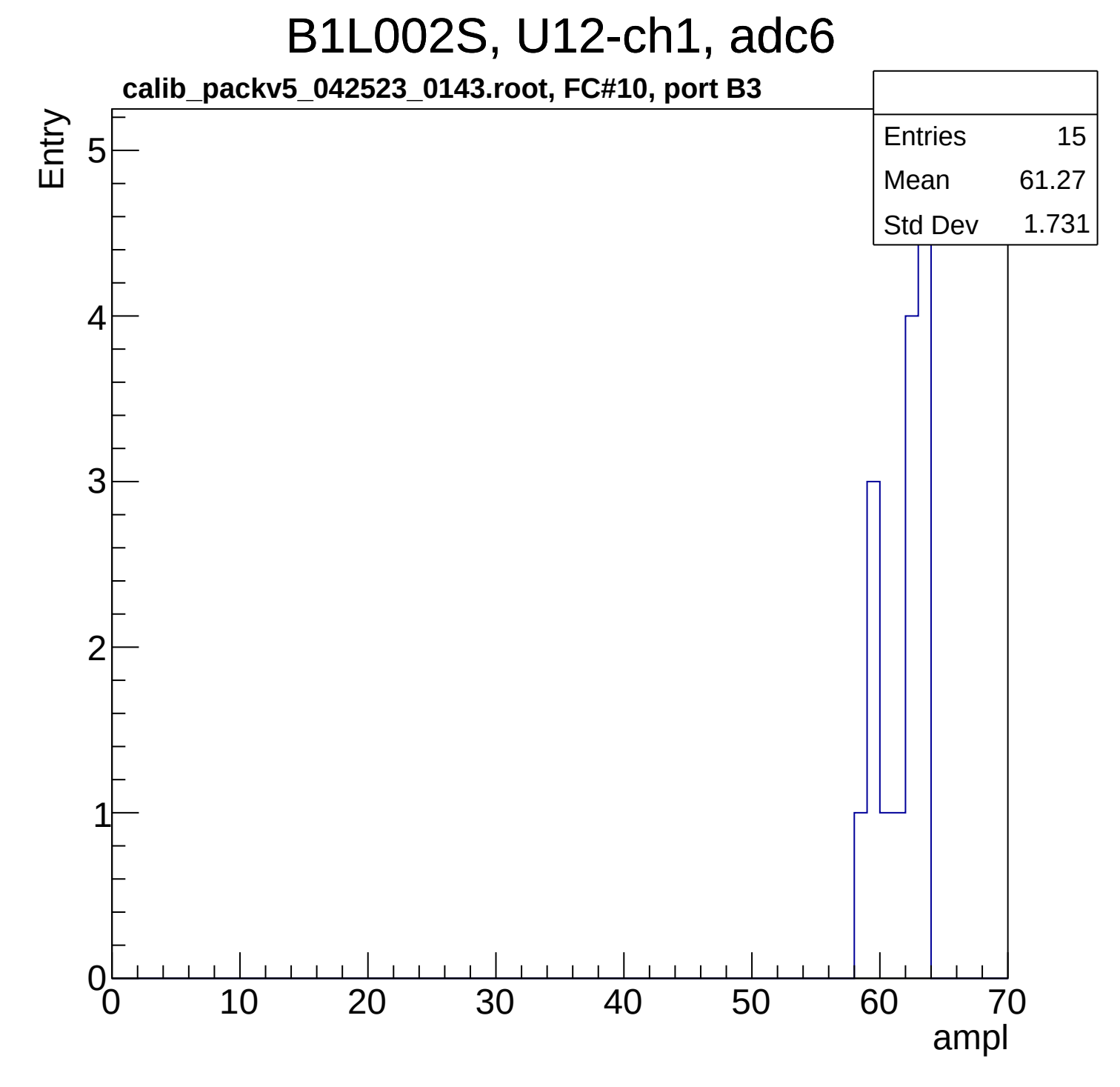
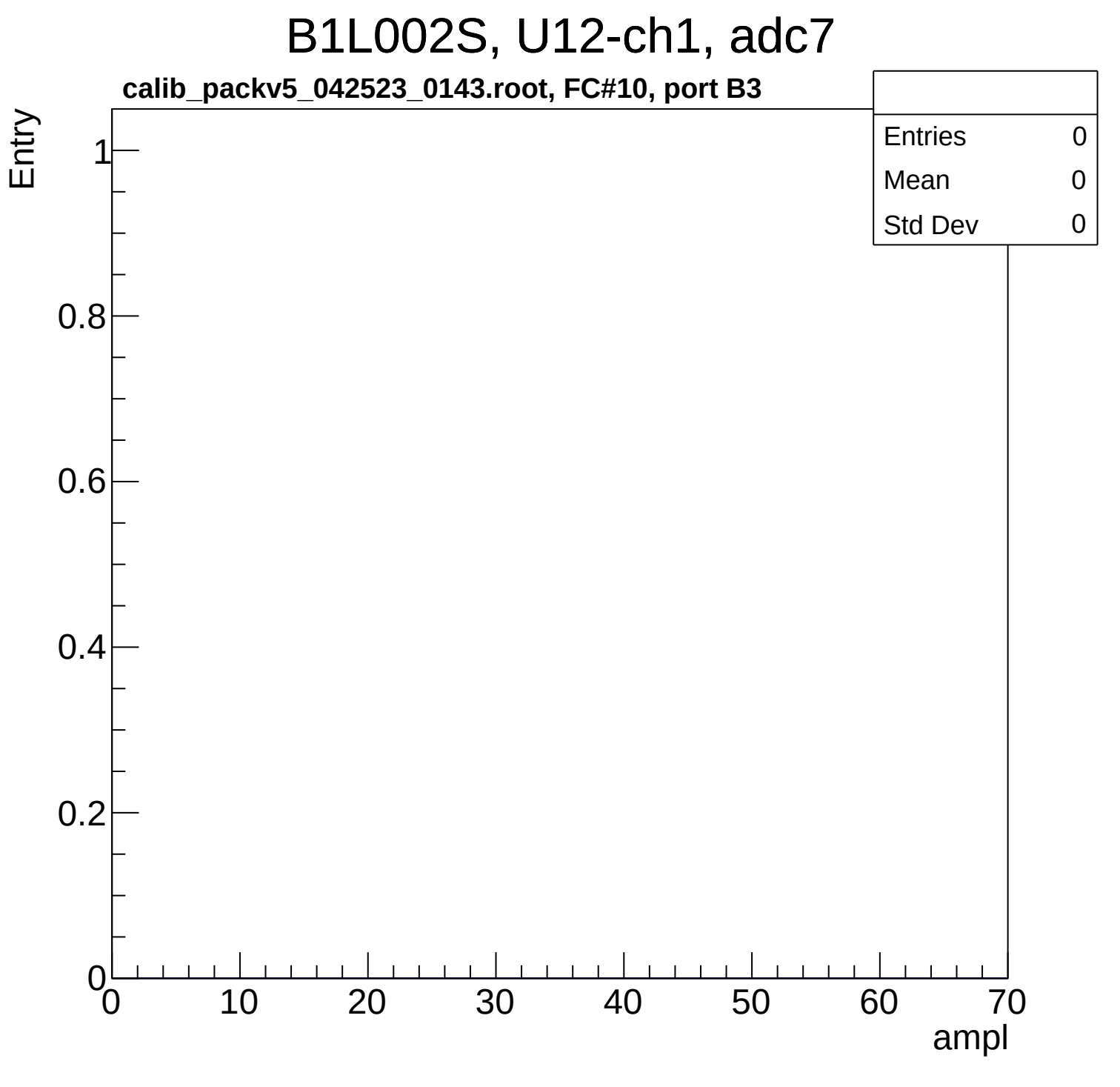
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.27
Std Dev 1.731
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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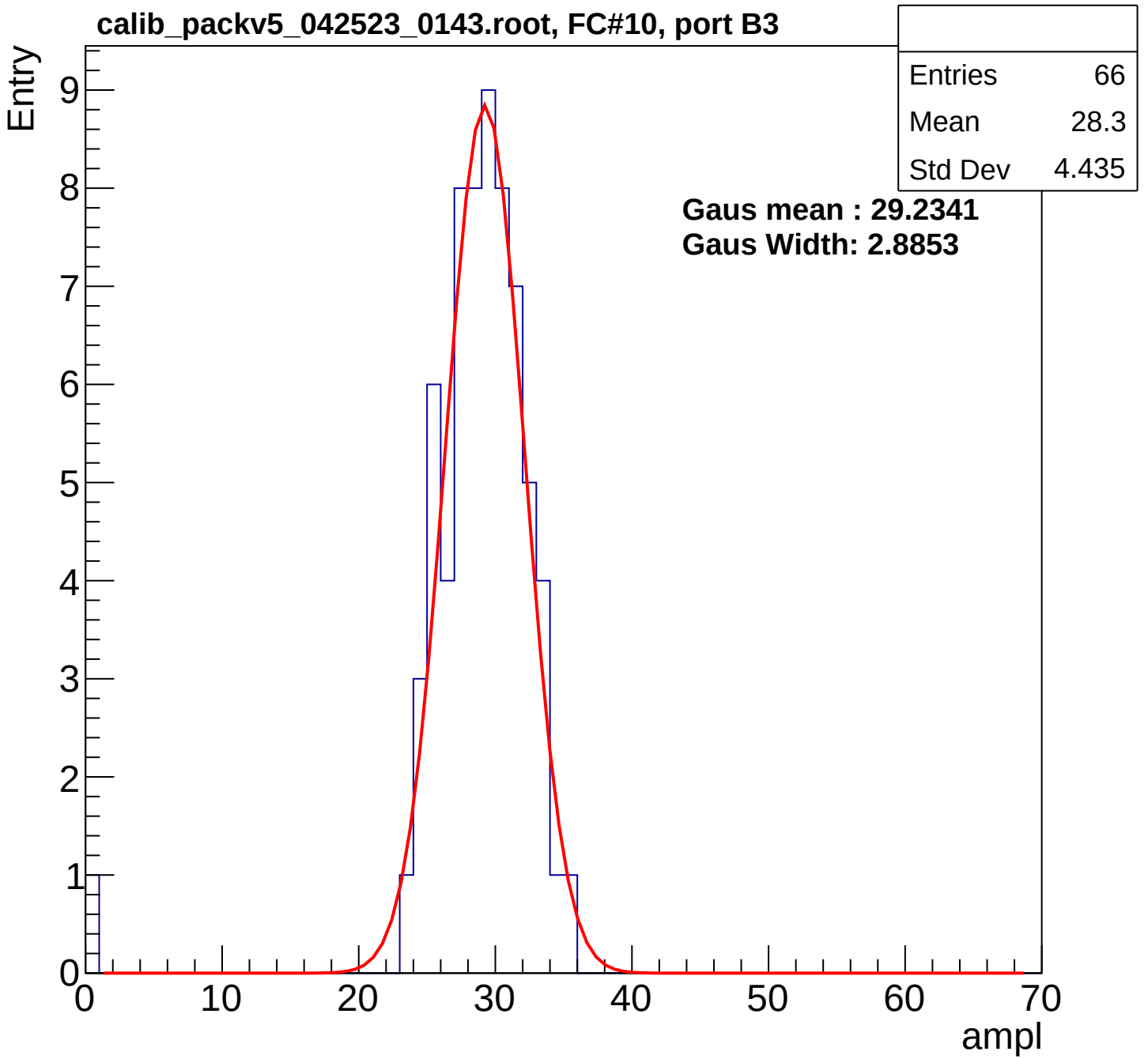
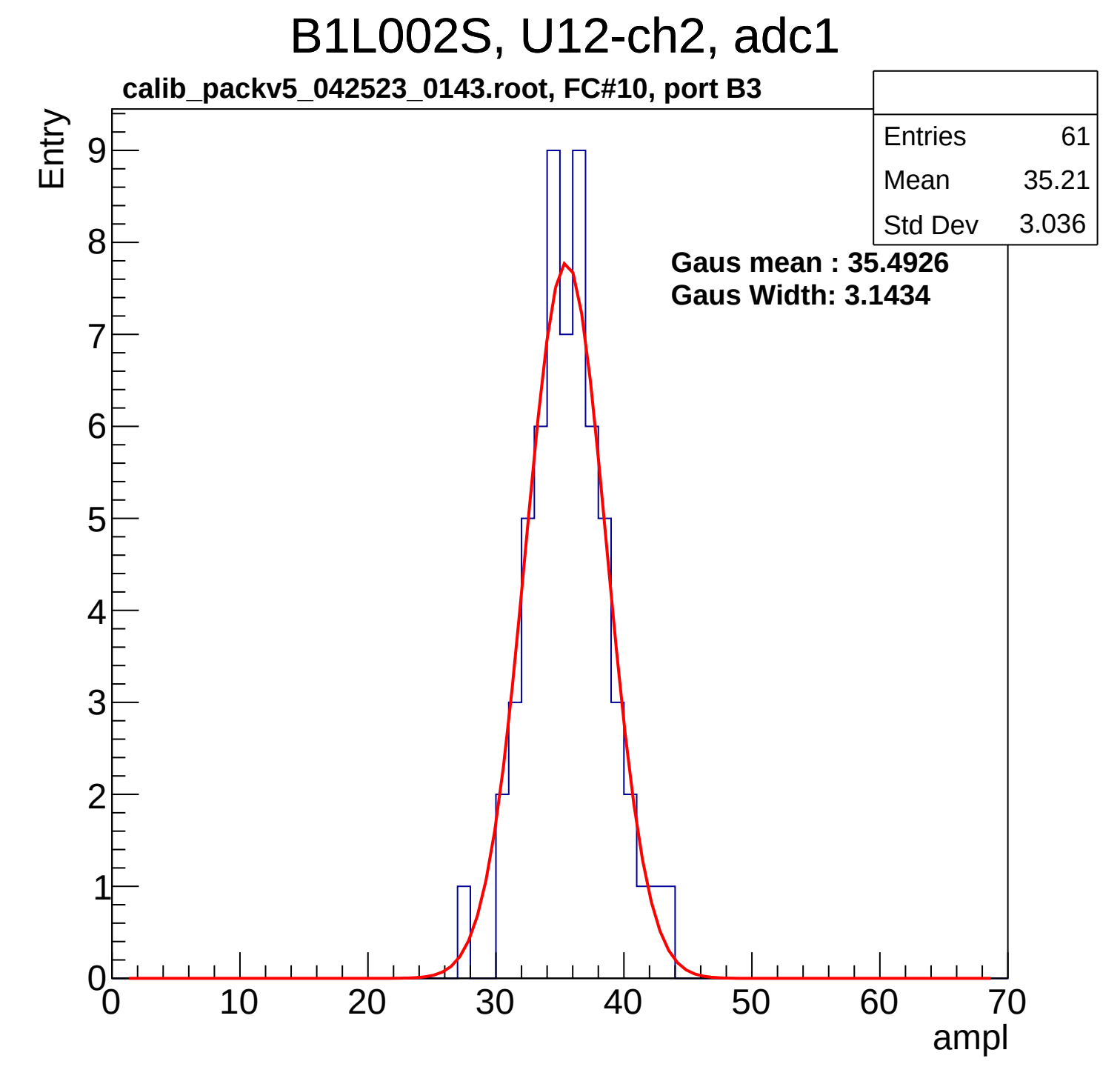
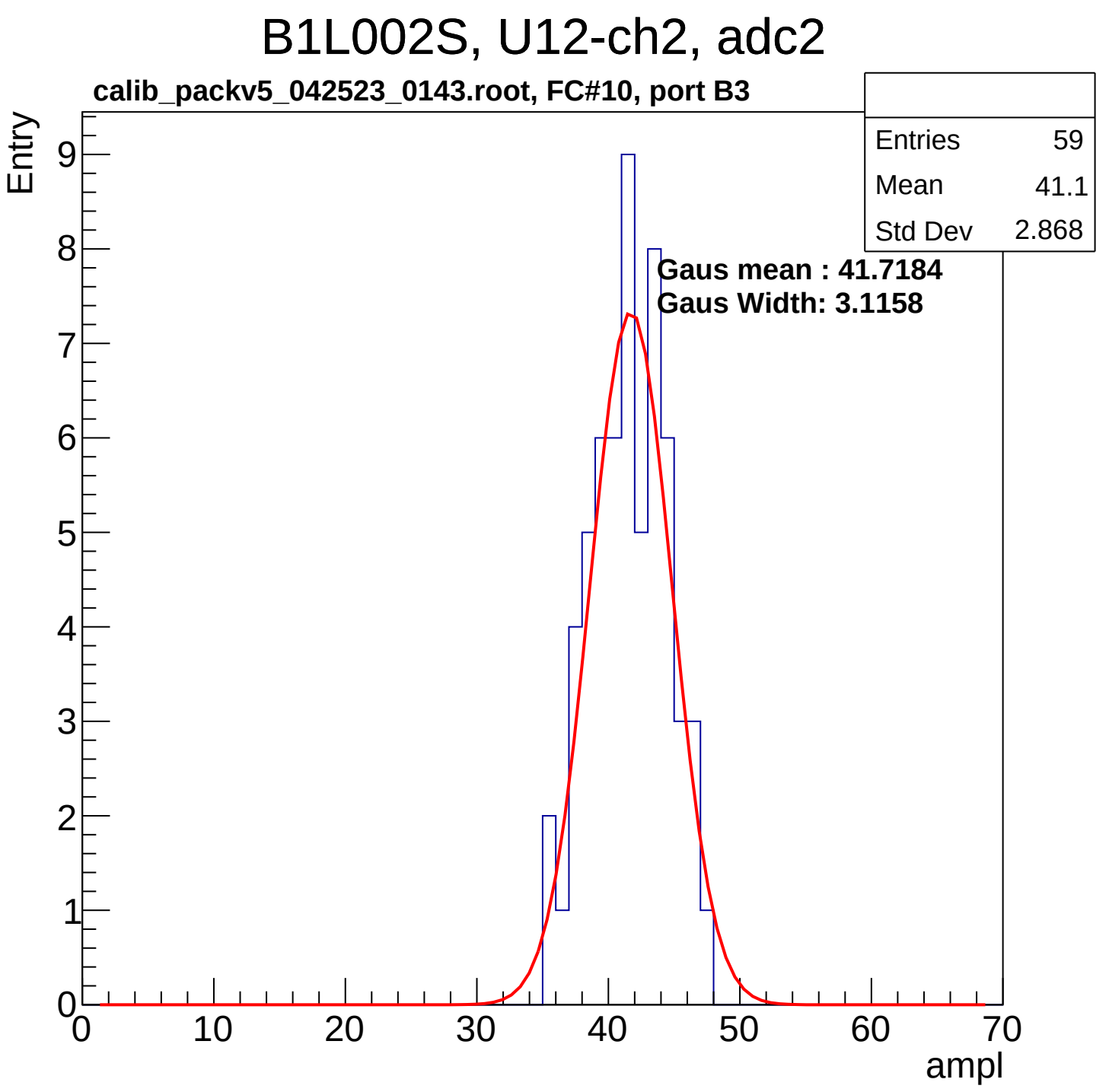
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.3
Std Dev 4.435
Gaus mean : 29.2341
Gaus Width: 2.8853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.21
Std Dev 3.036
Gaus mean : 35.4926
Gaus Width: 3.1434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.1
Std Dev 2.868
Gaus mean : 41.7184
Gaus Width: 3.1158
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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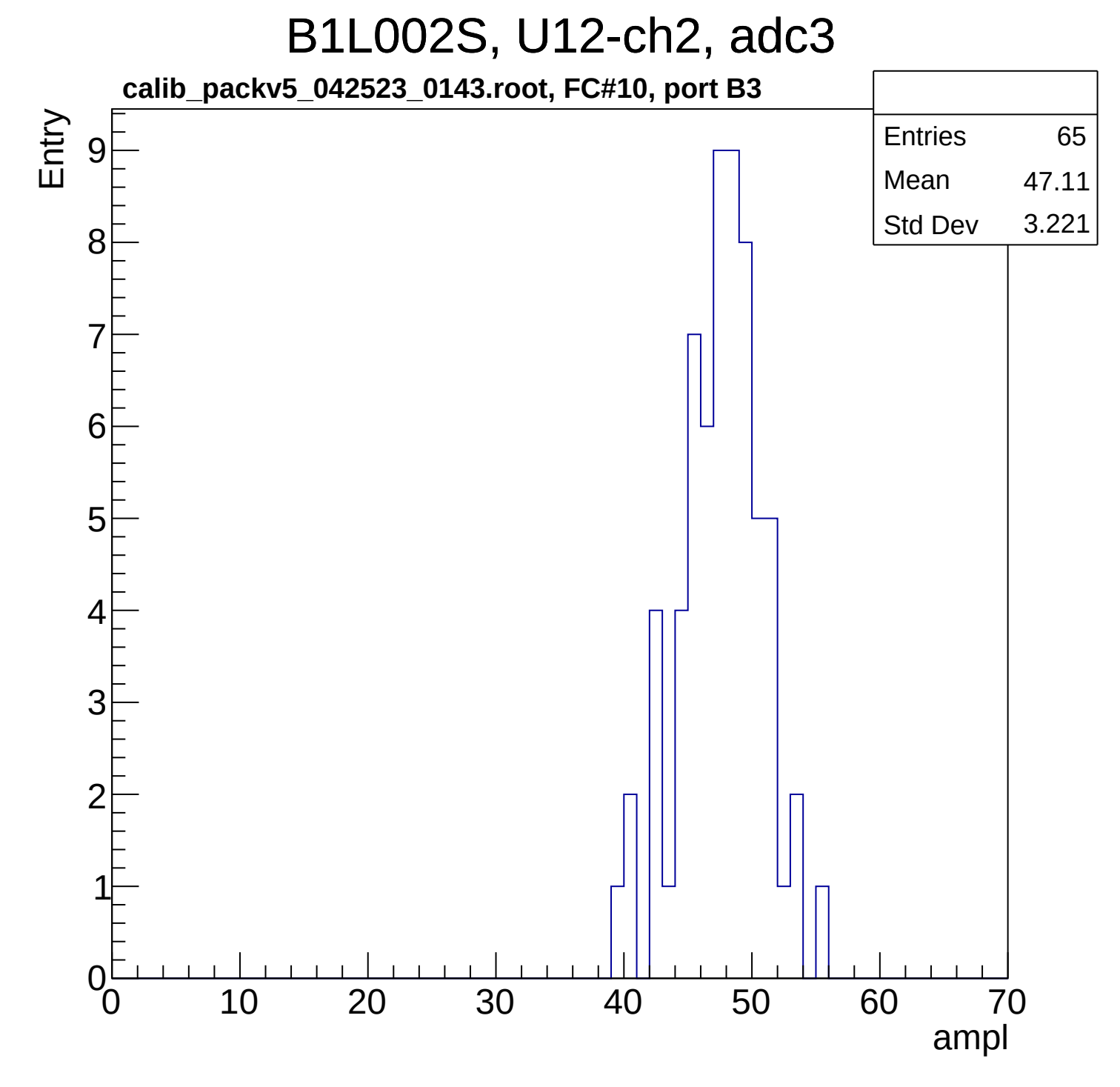
B1L002S, U12-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.11
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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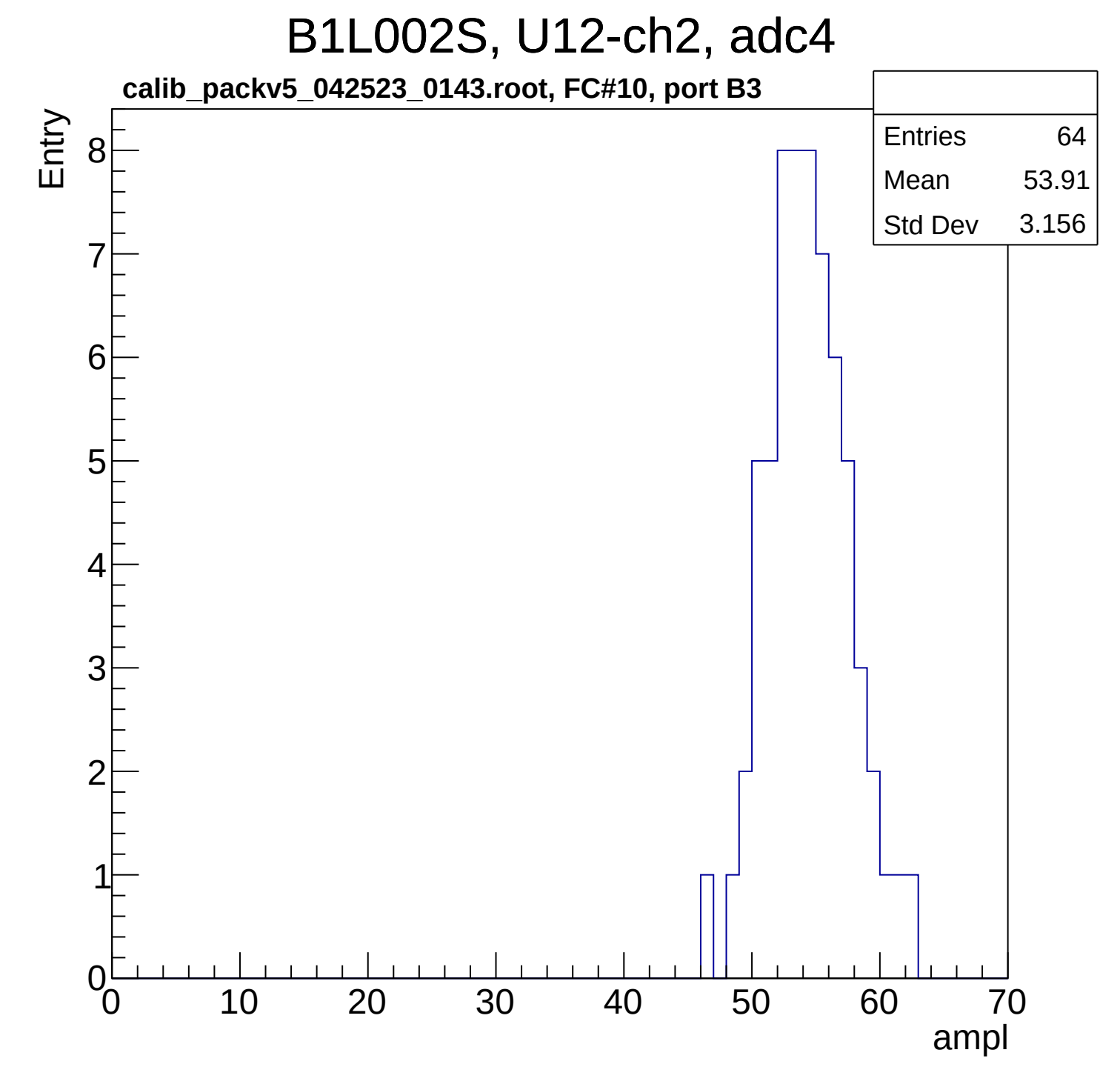
B1L002S, U12-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.91
Std Dev 3.156
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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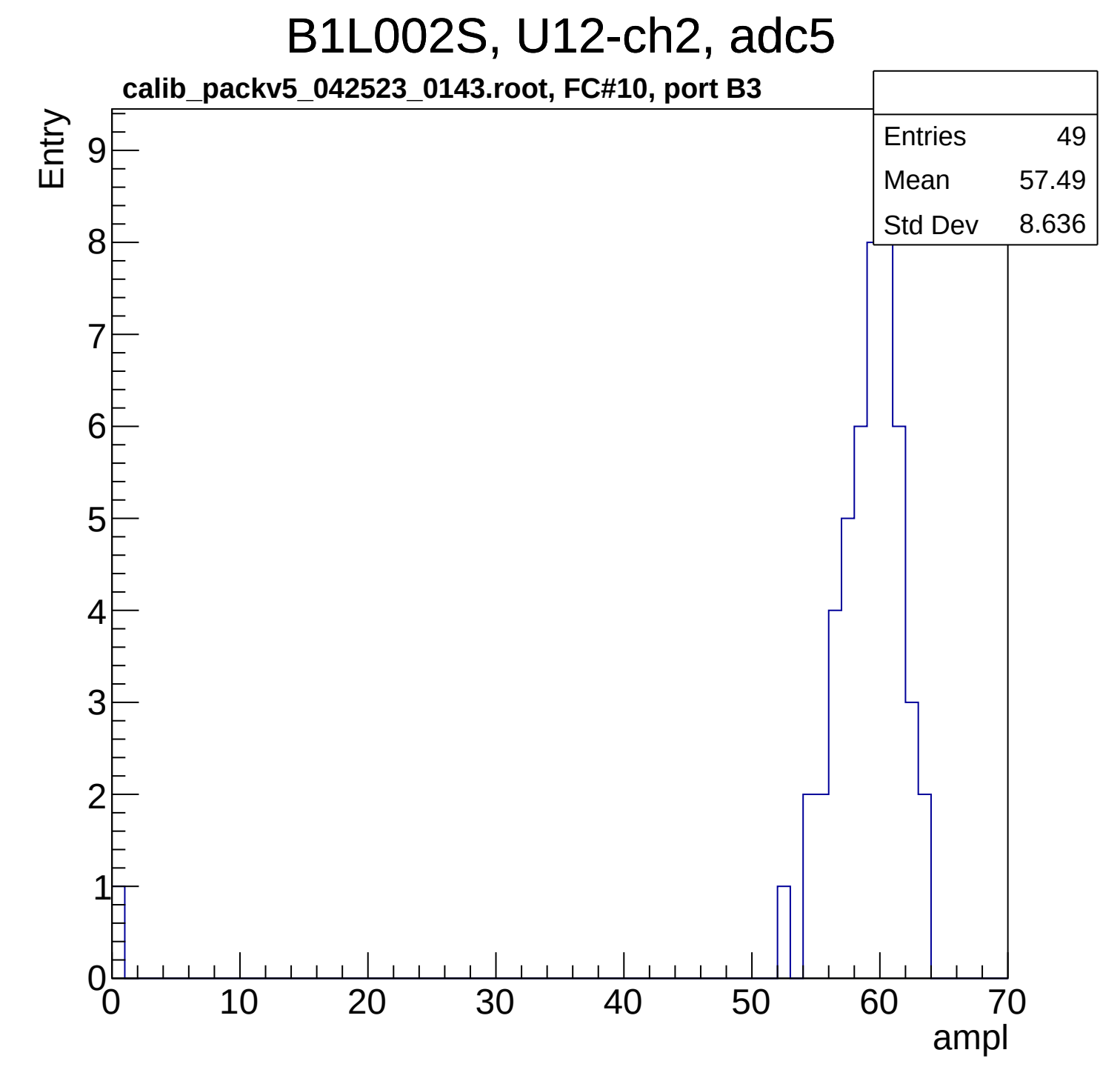
B1L002S, U12-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.49
Std Dev 8.636
Entry
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3
4
5
6
7
8
9
0
10
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30
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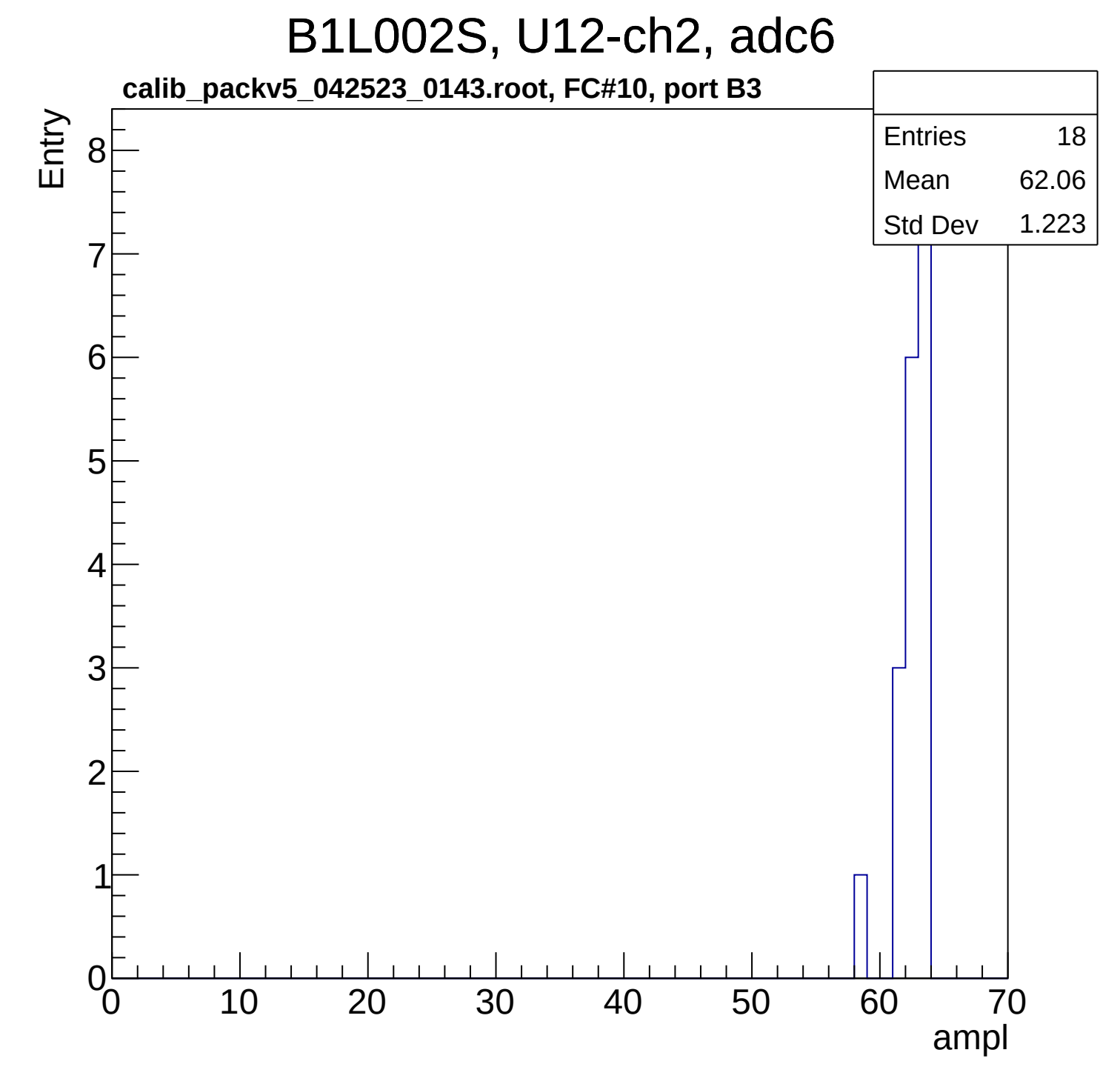
50
60
70
ampl


B1L002S, U12-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 62.06
Std Dev 1.223
Entry
ampl
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2
3
4
5
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7
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0
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20
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40
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60
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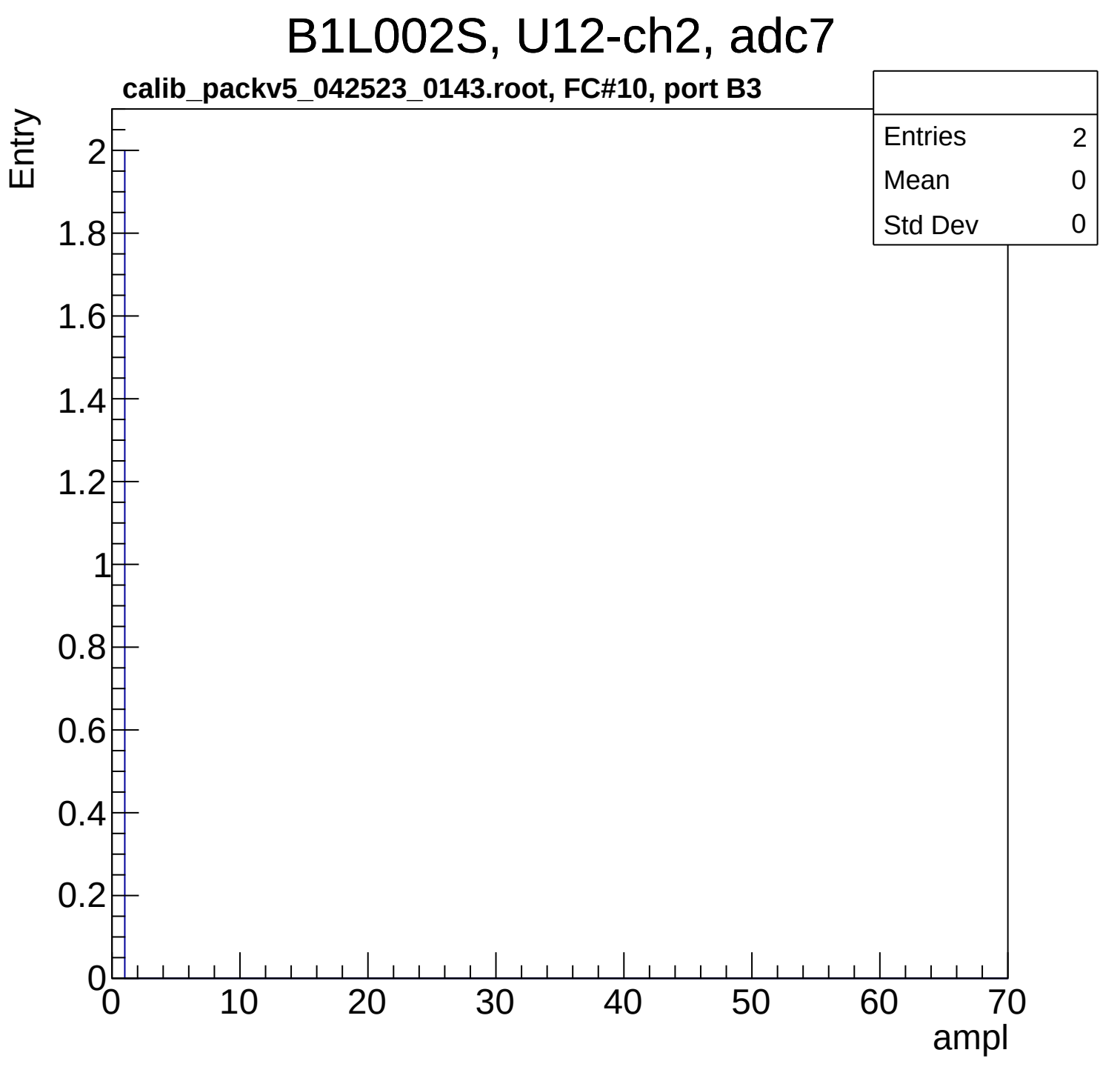
B1L002S, U12-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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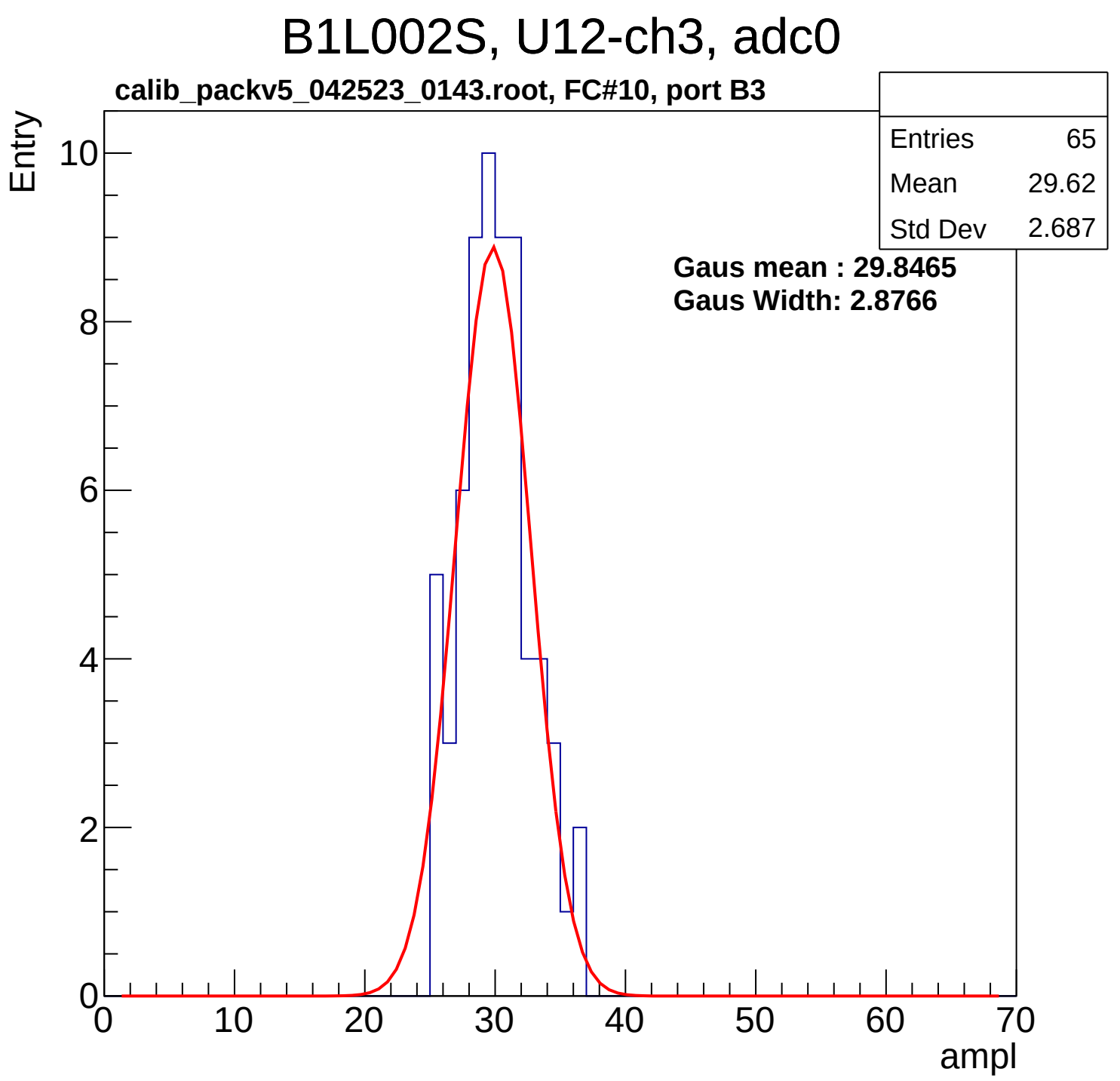
B1L002S, U12-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.62
Std Dev 2.687
Gaus mean : 29.8465
Gaus Width: 2.8766
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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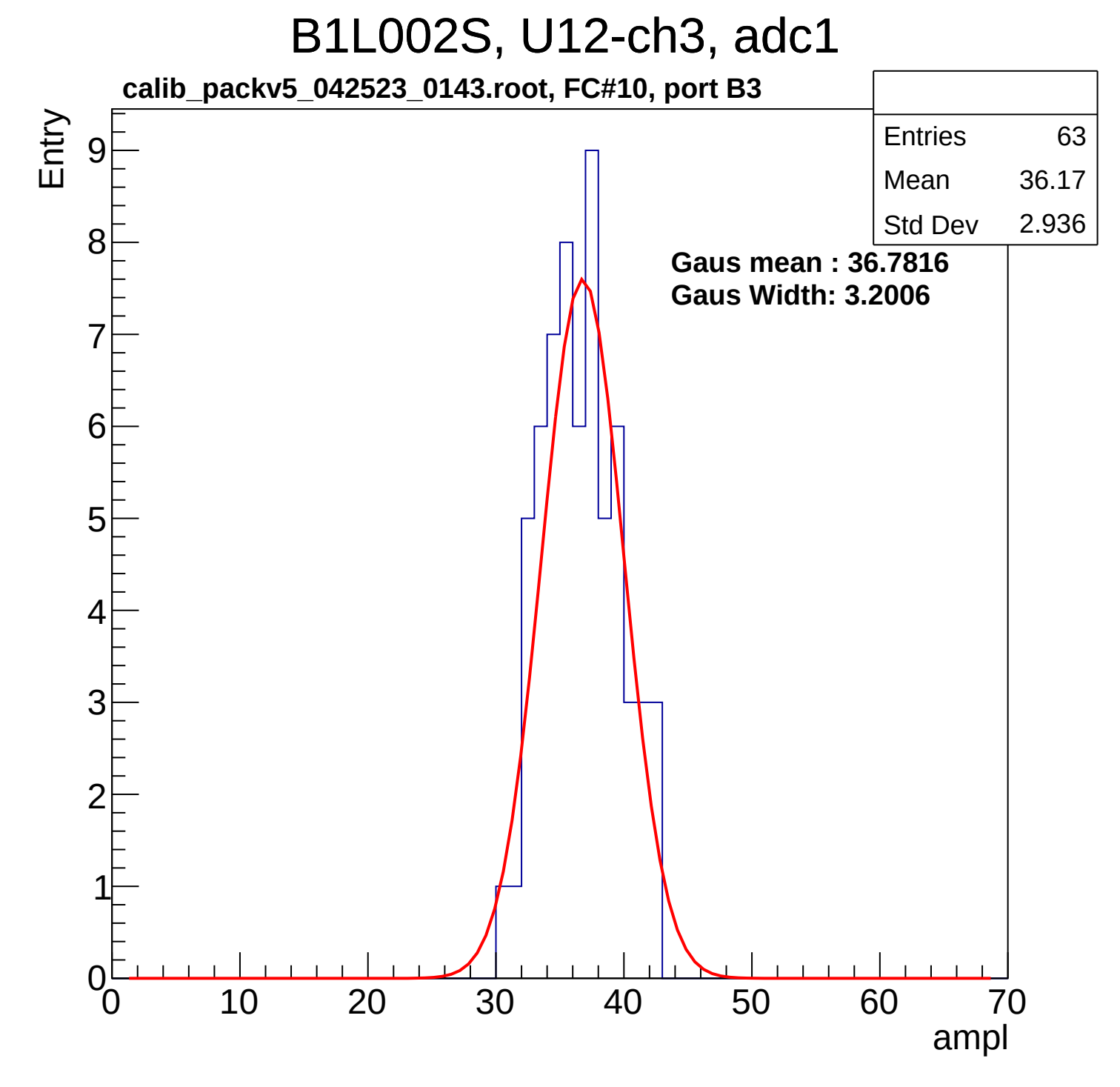
B1L002S, U12-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.17
Std Dev 2.936
Gaus mean : 36.7816
Gaus Width: 3.2006
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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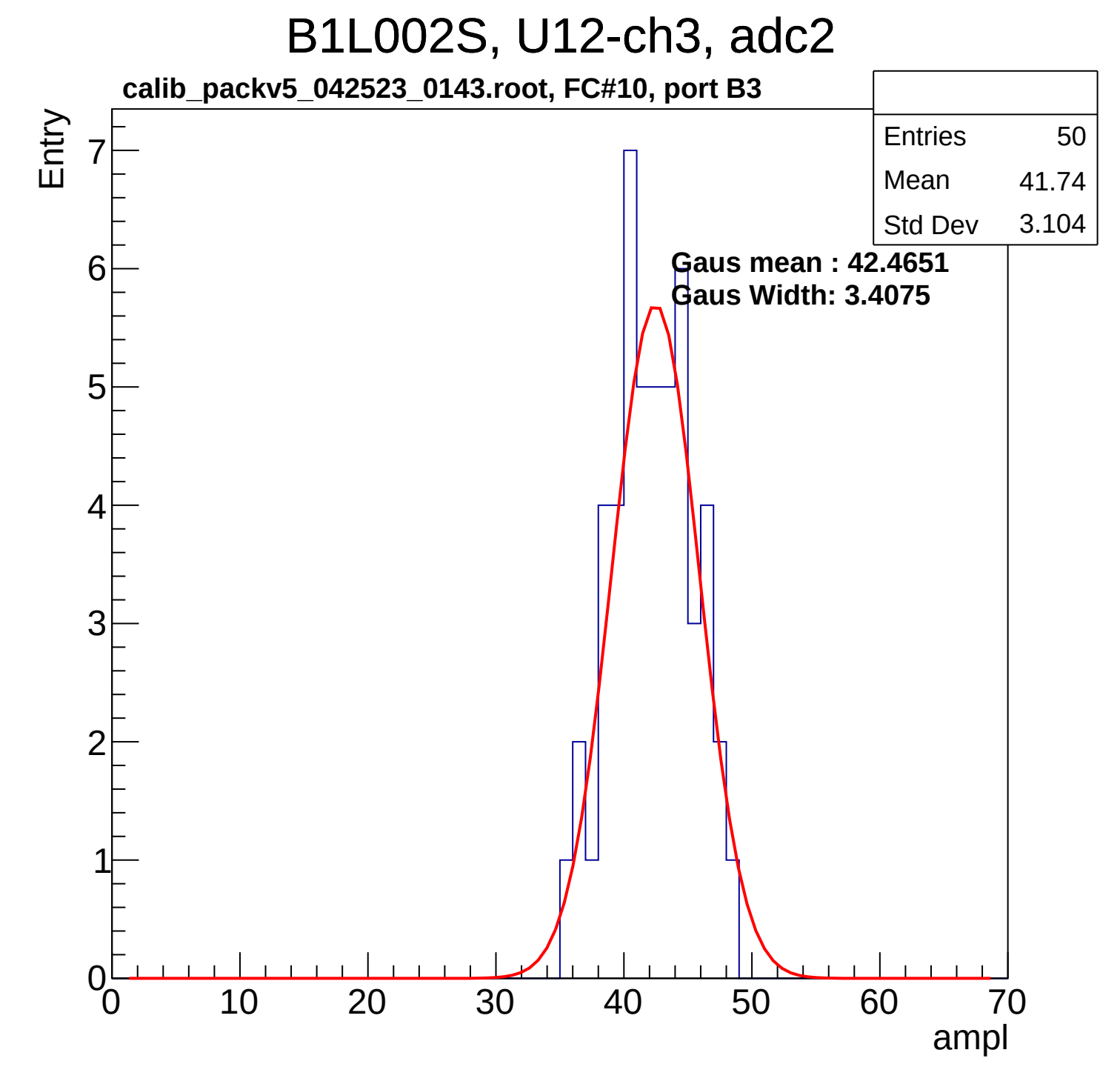
B1L002S, U12-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.74
Std Dev 3.104
Gaus mean : 42.4651
Gaus Width: 3.4075
Entry
ampl
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2
3
4
5
6
7
0
10
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60
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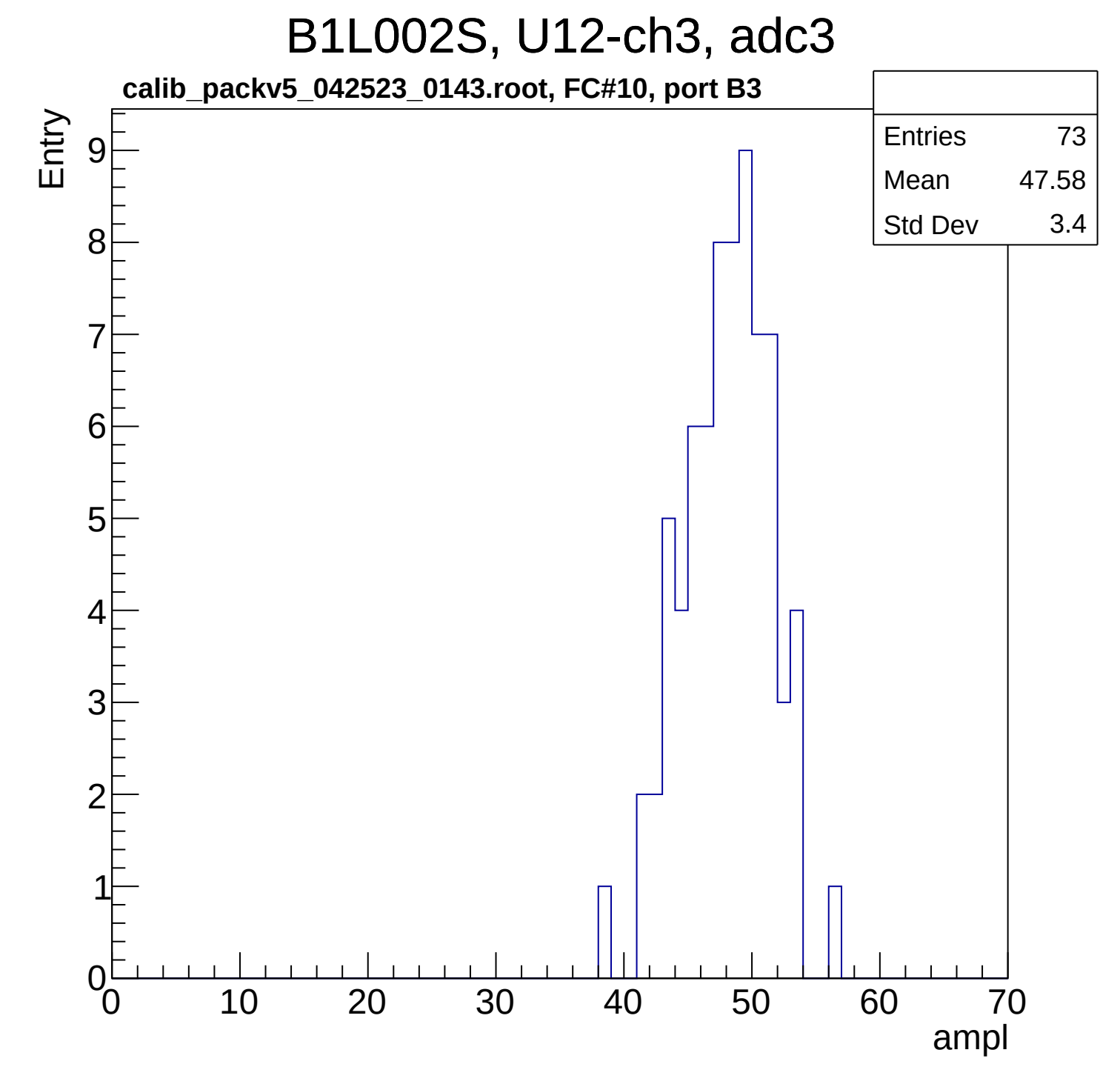
B1L002S, U12-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.58
Std Dev 3.4
Entry
ampl
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4
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6
7
8
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0
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40
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60
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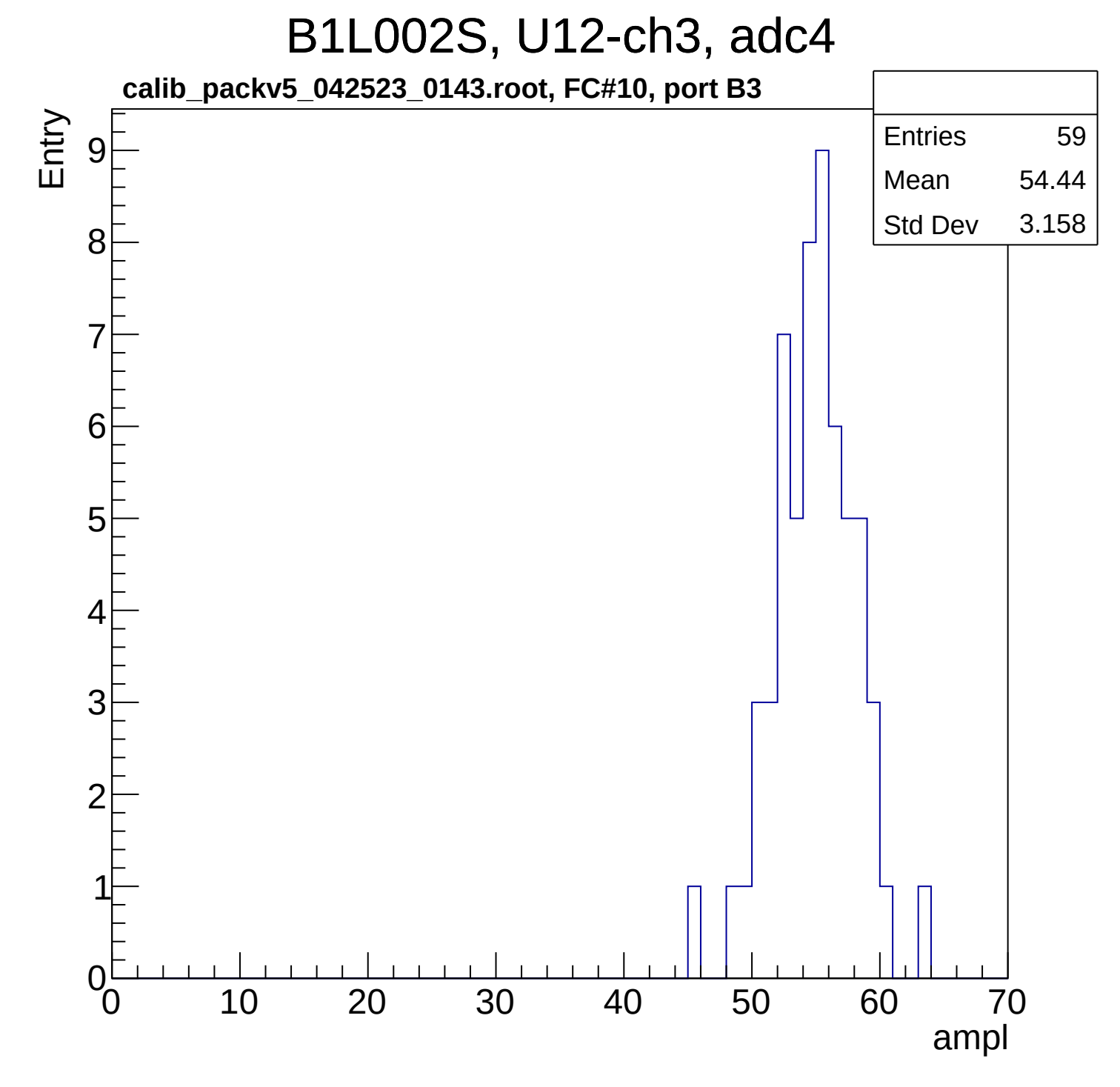
B1L002S, U12-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.44
Std Dev 3.158
Entry
ampl
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2
3
4
5
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7
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0
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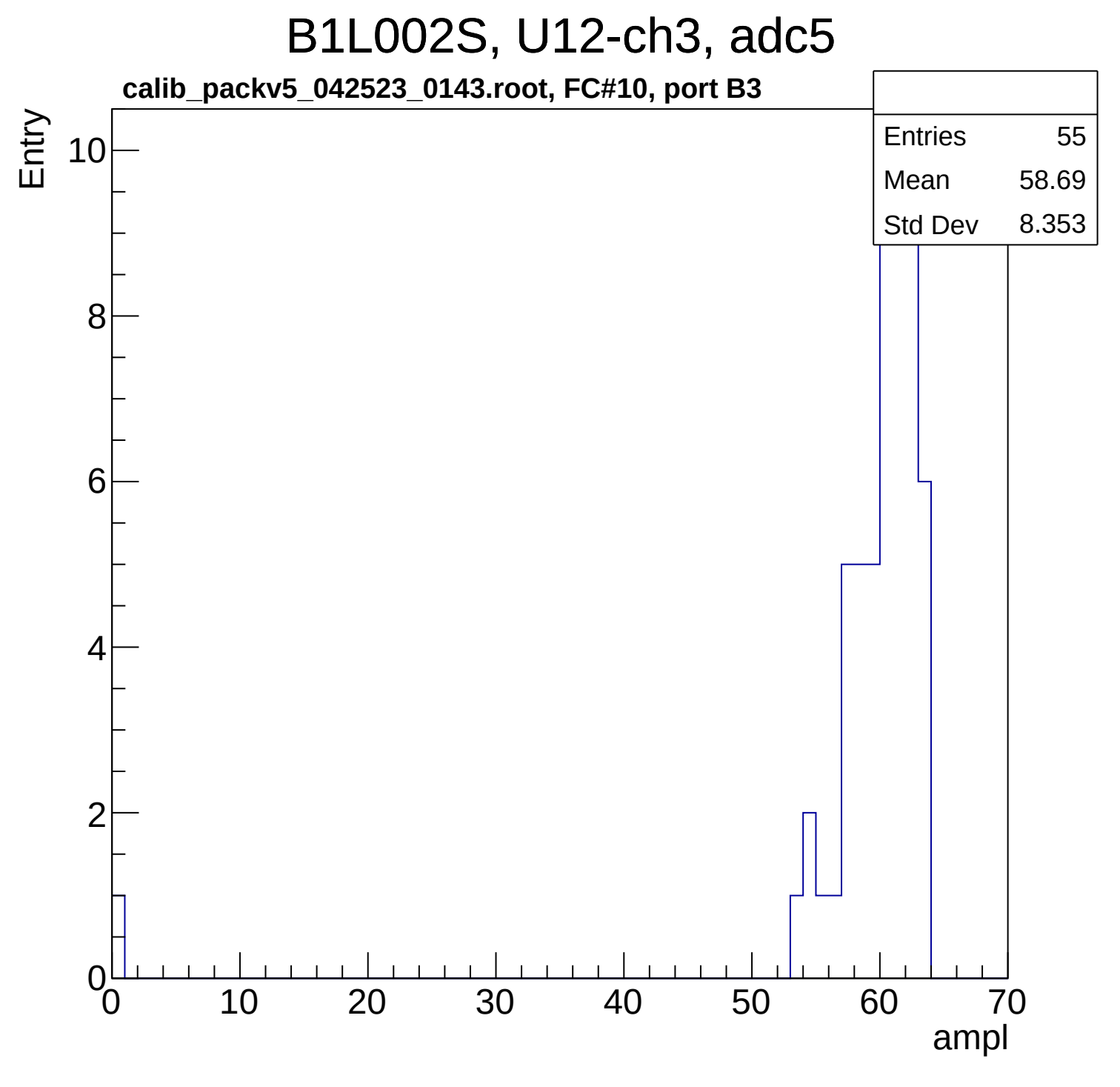
B1L002S, U12-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.69
Std Dev 8.353
Entry
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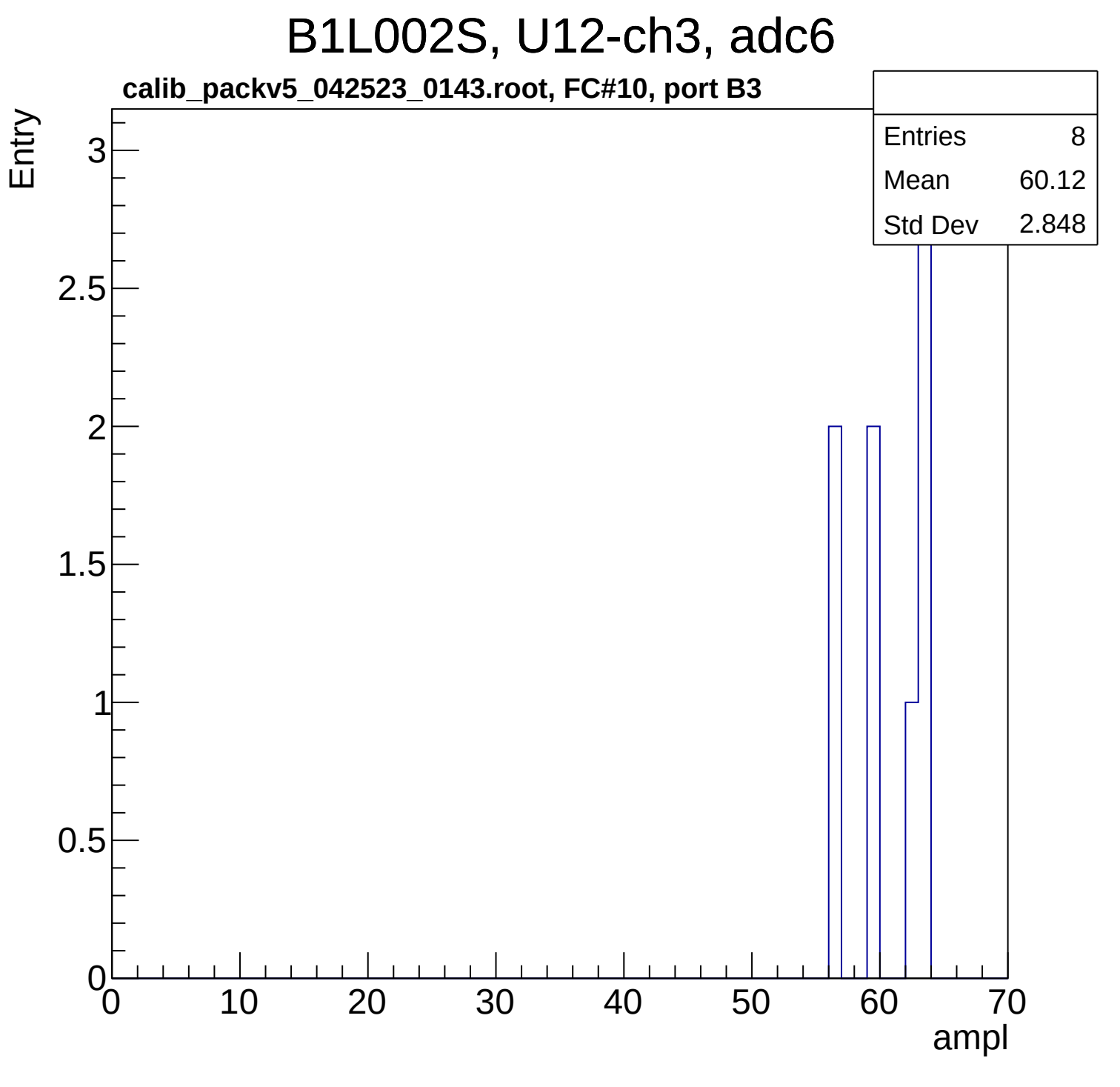
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 60.12
Std Dev 2.848
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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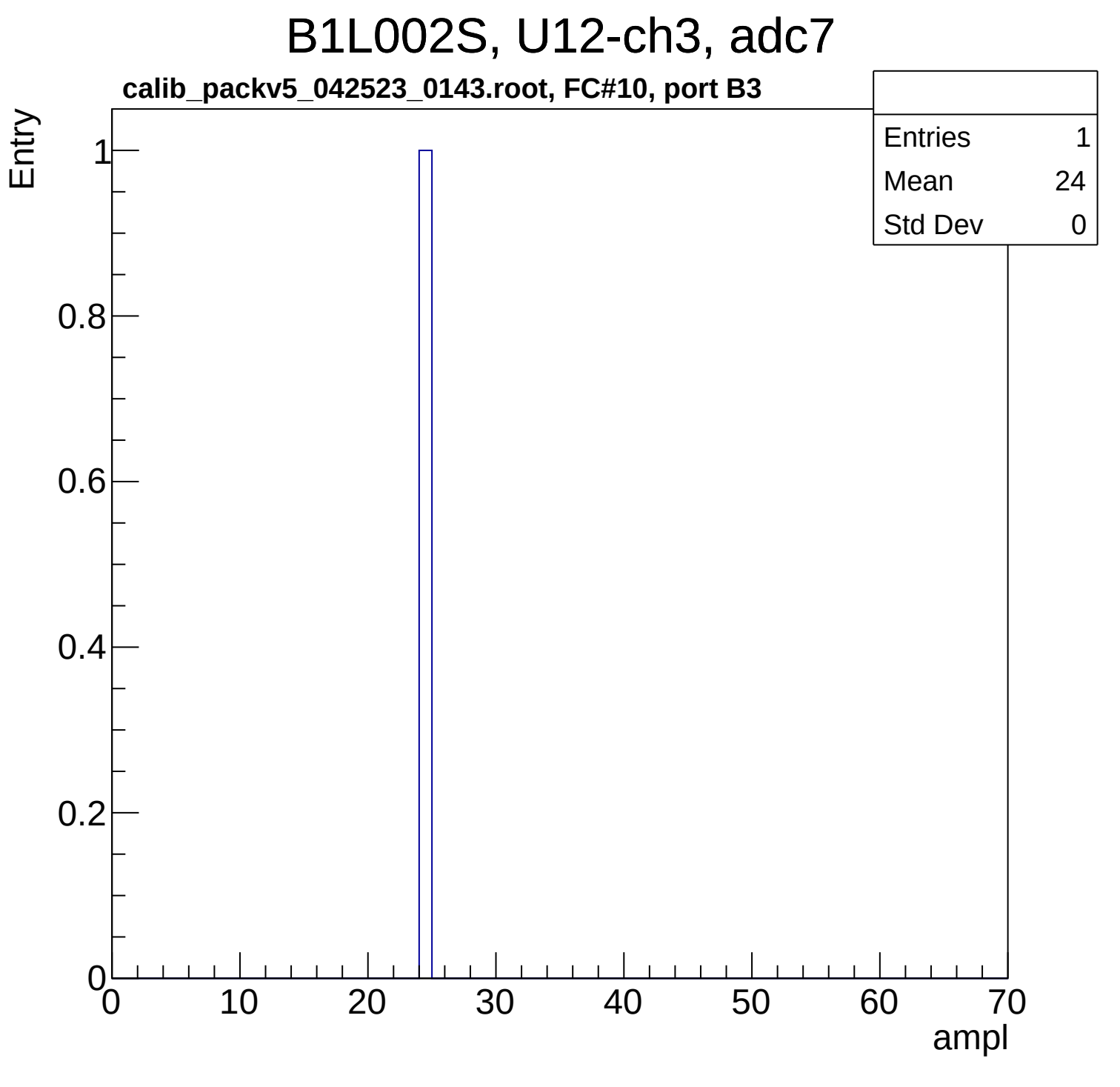
B1L002S, U12-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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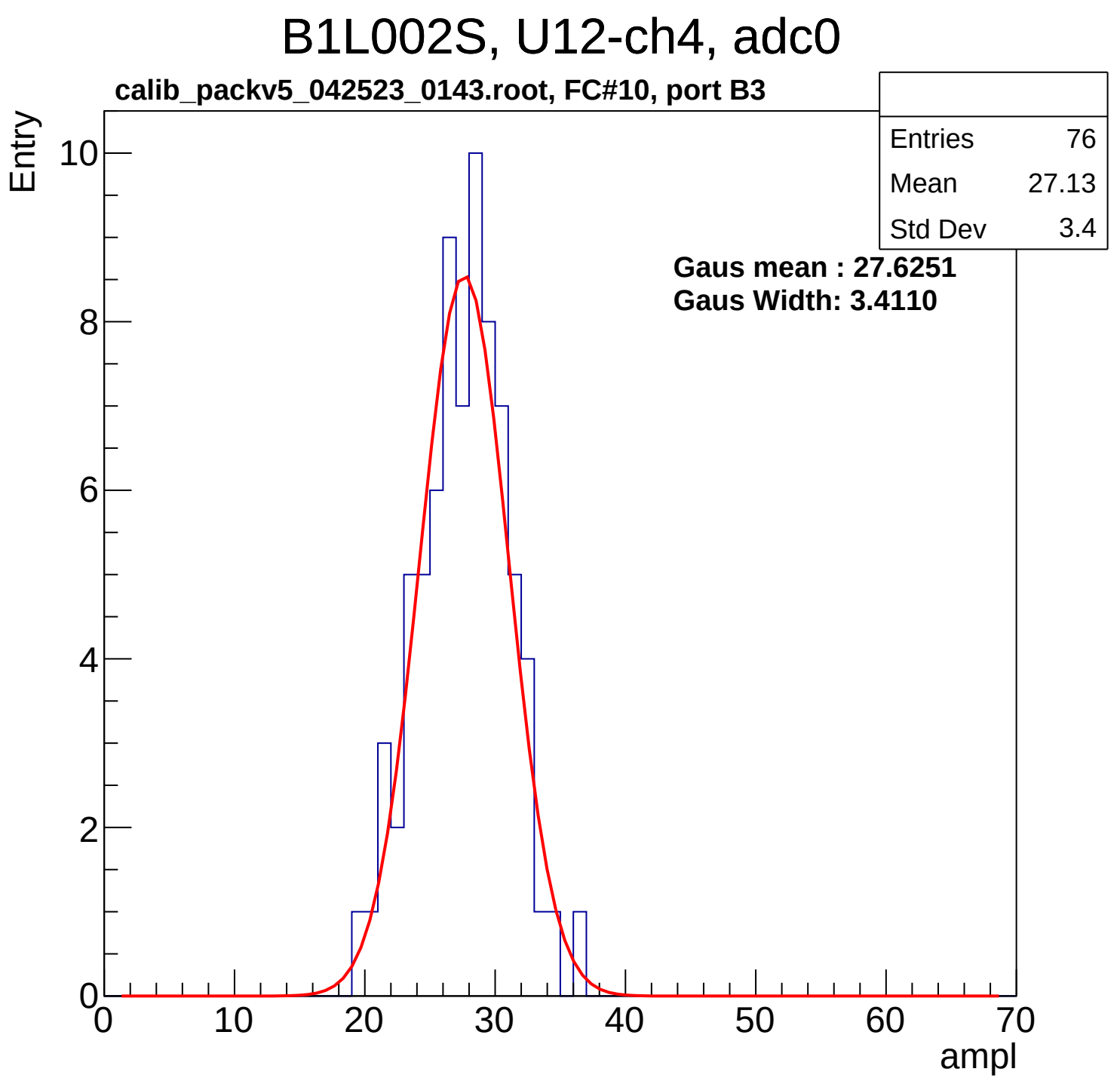
ampl


B1L002S, U12-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.13
Std Dev 3.4
Gaus mean : 27.6251
Gaus Width: 3.4110
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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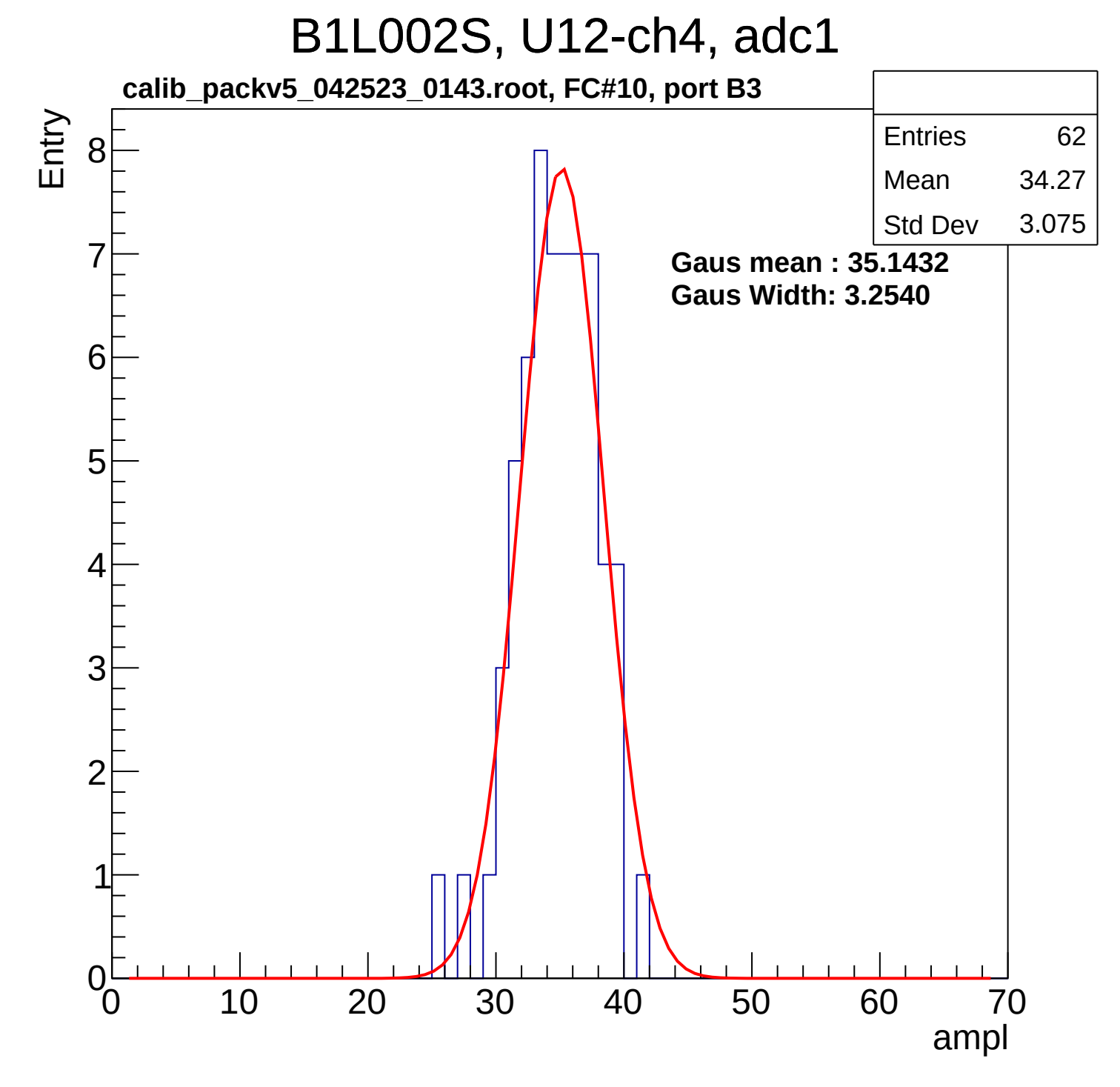
B1L002S, U12-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.27
Std Dev 3.075
Gaus mean : 35.1432
Gaus Width: 3.2540
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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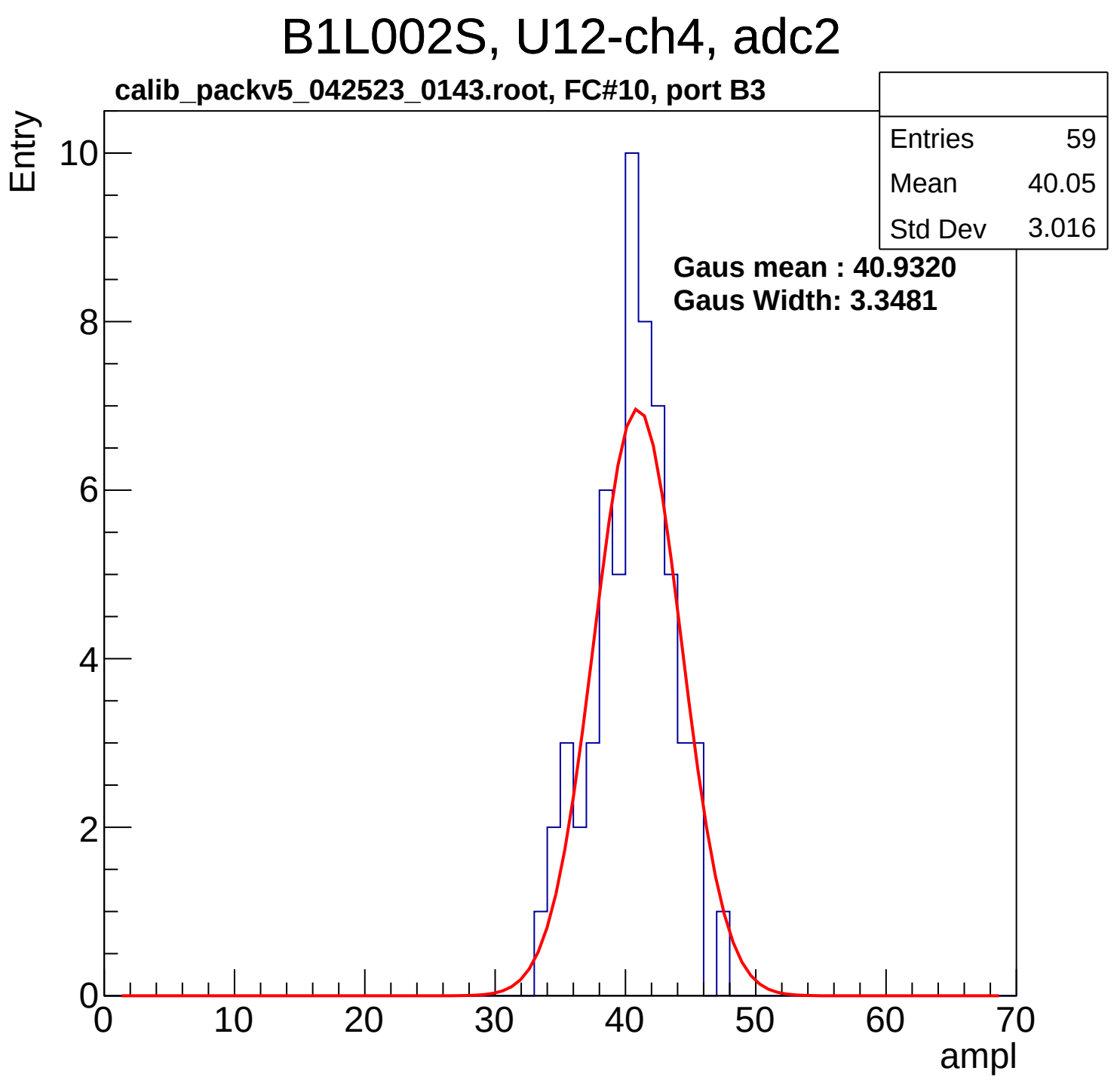
B1L002S, U12-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.05
Std Dev 3.016
Gaus mean : 40.9320
Gaus Width: 3.3481
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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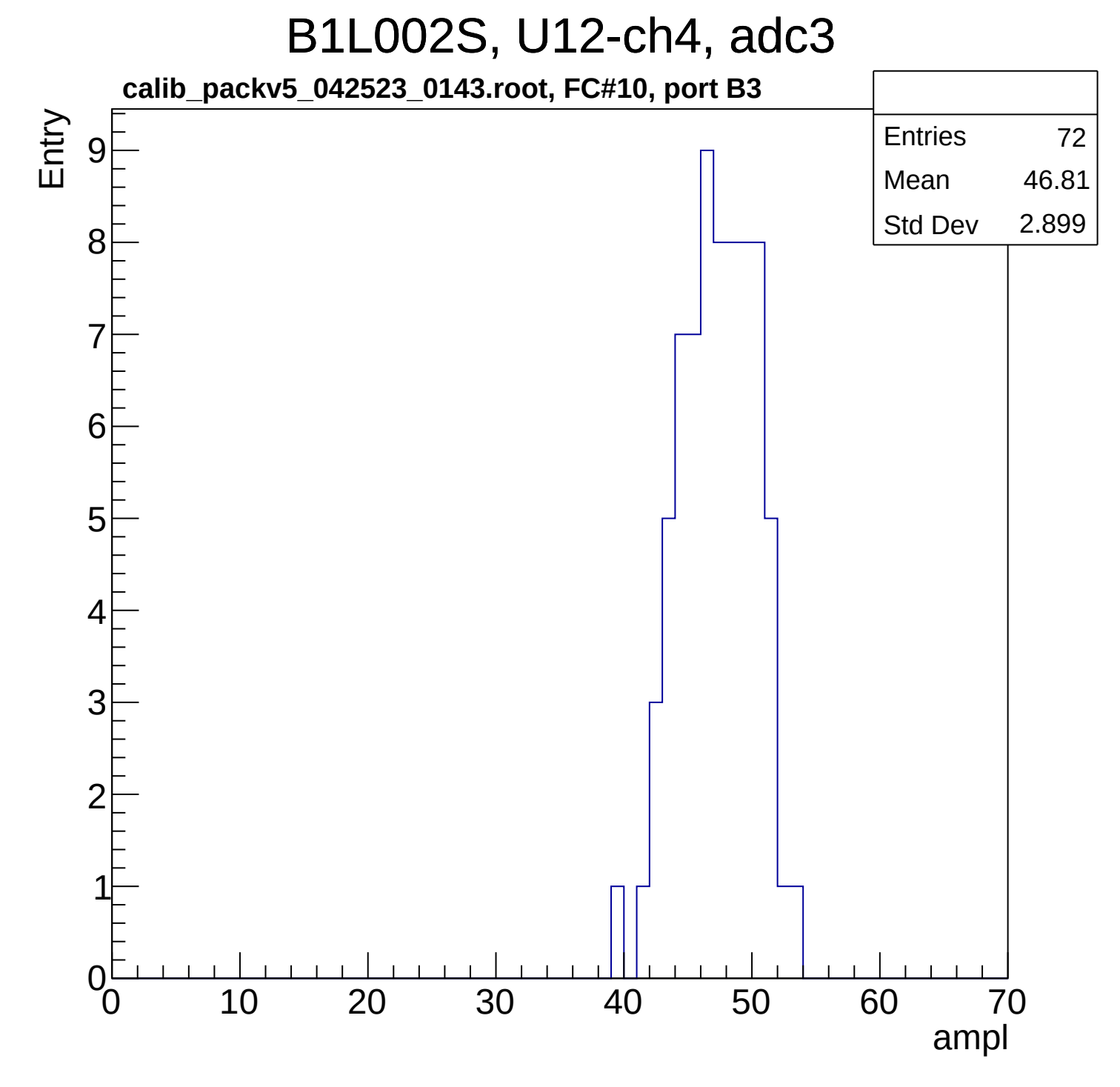
B1L002S, U12-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.81
Std Dev 2.899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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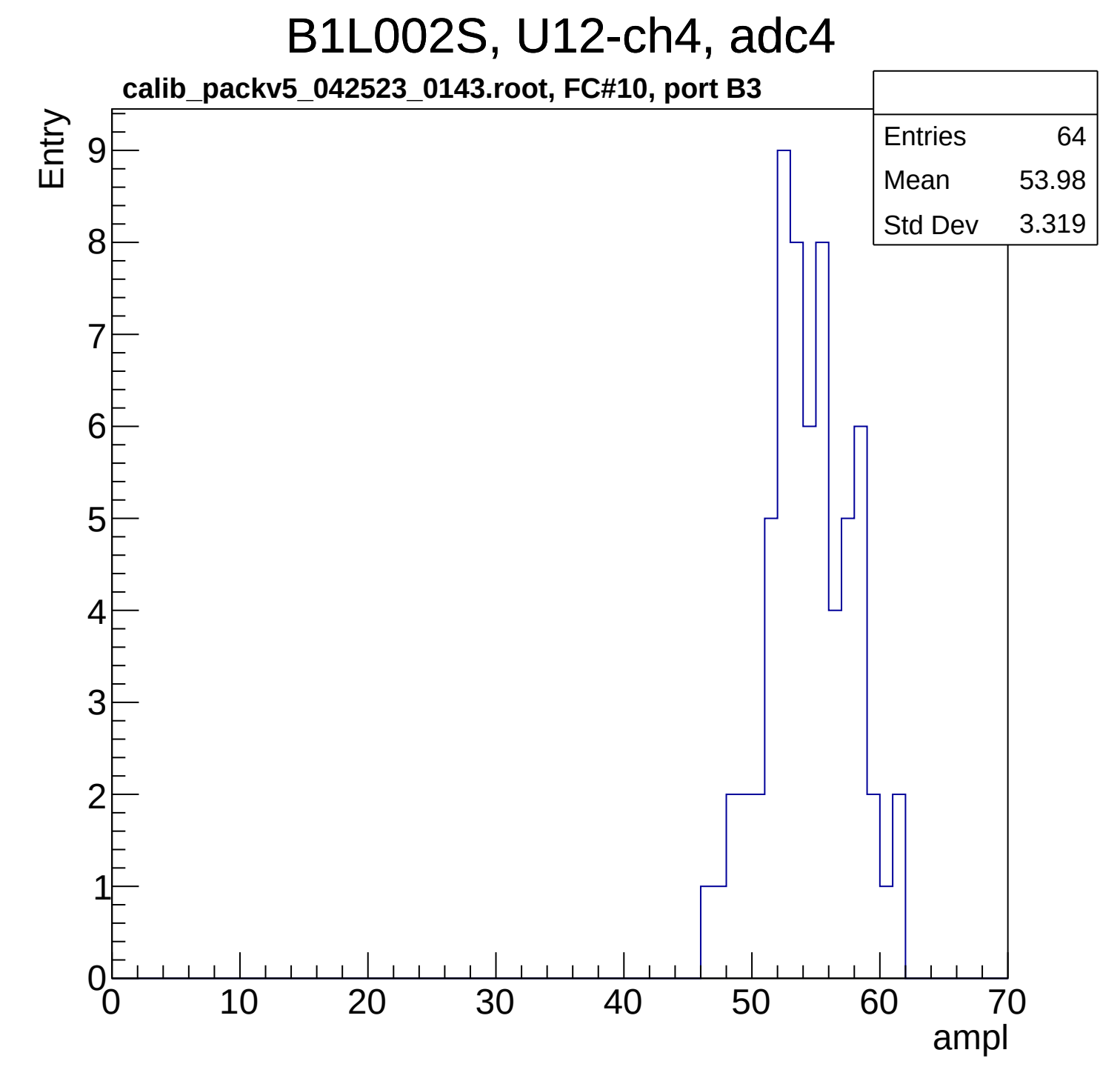
B1L002S, U12-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.98
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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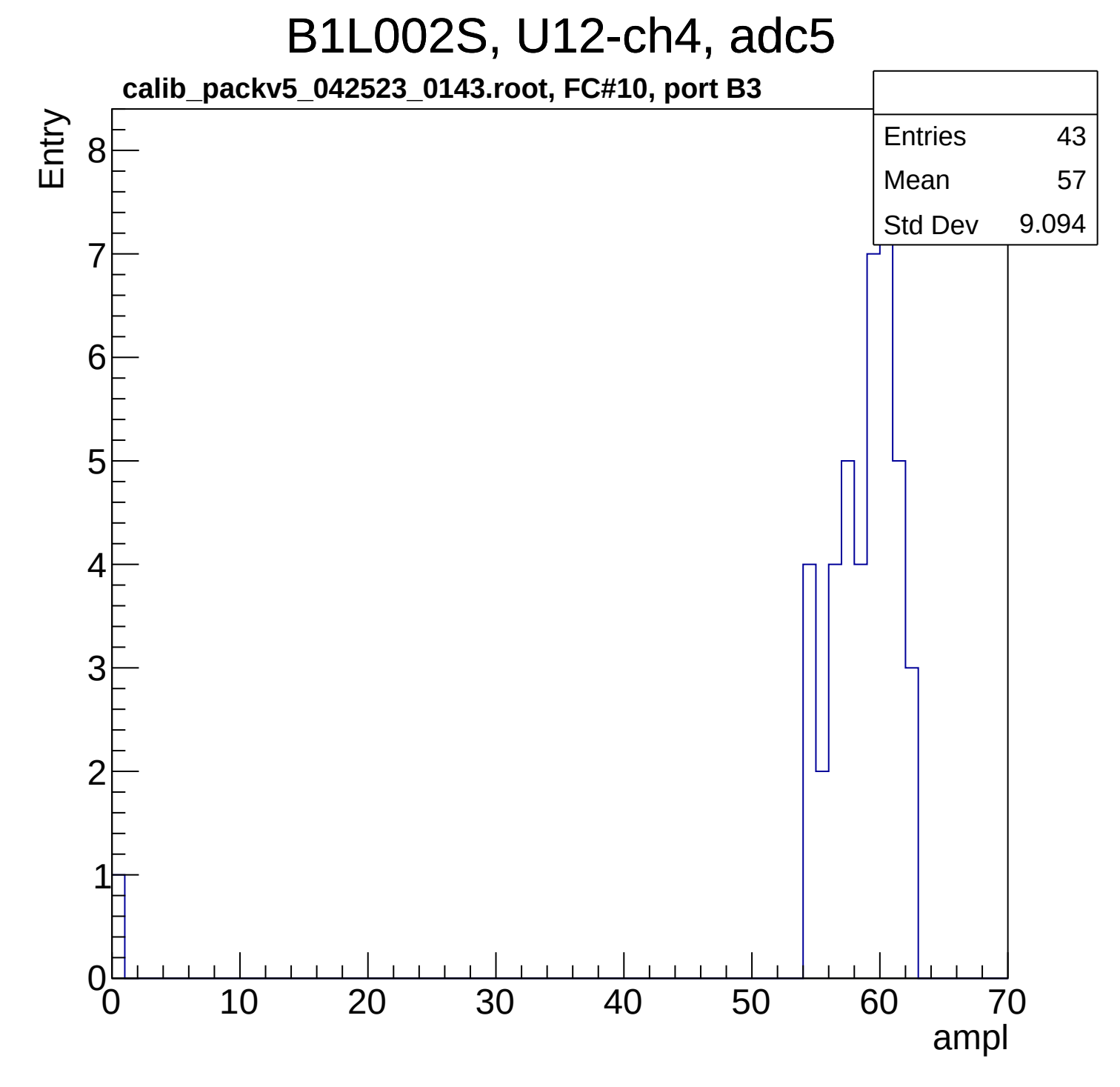
B1L002S, U12-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 57
Std Dev 9.094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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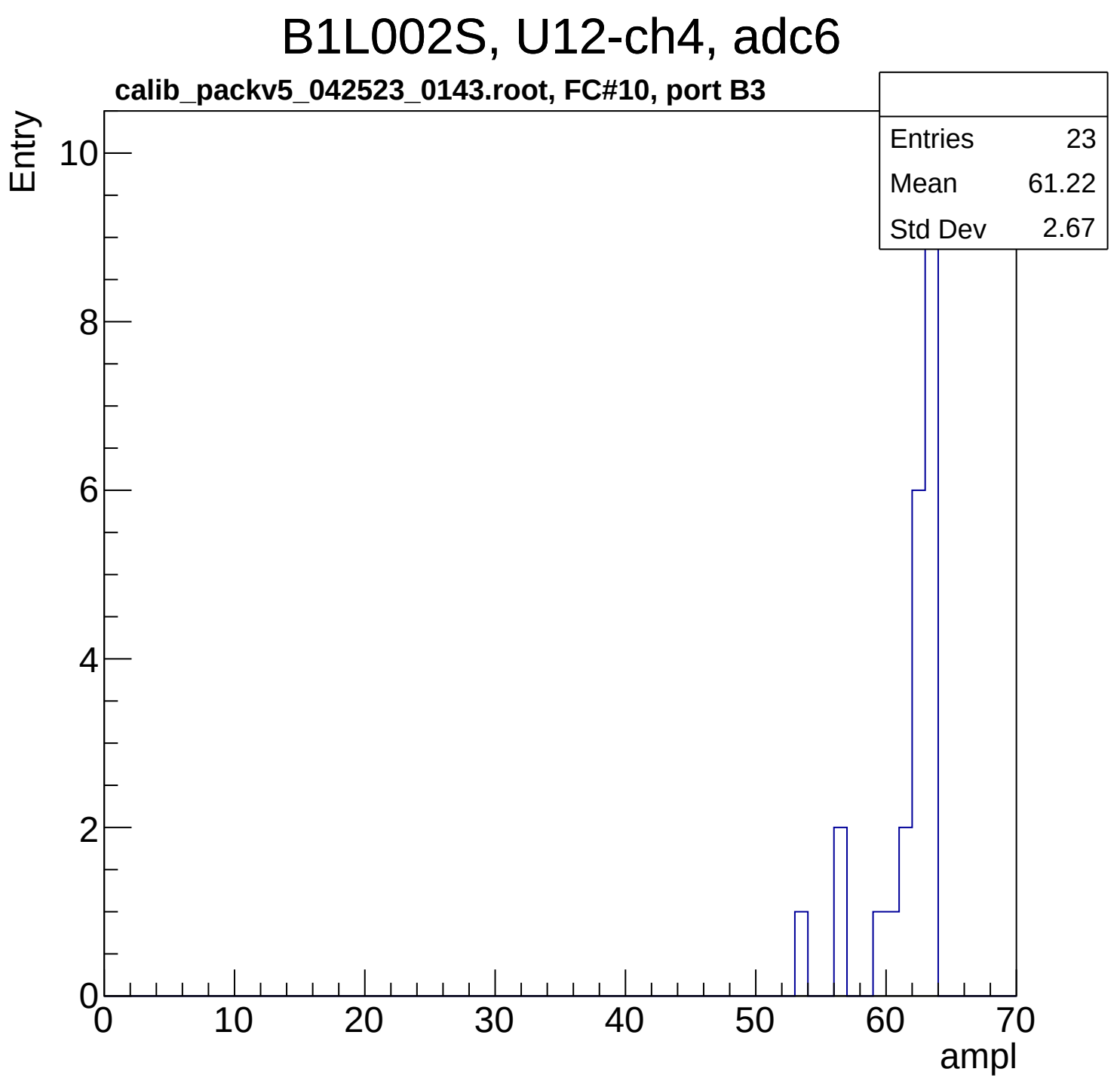
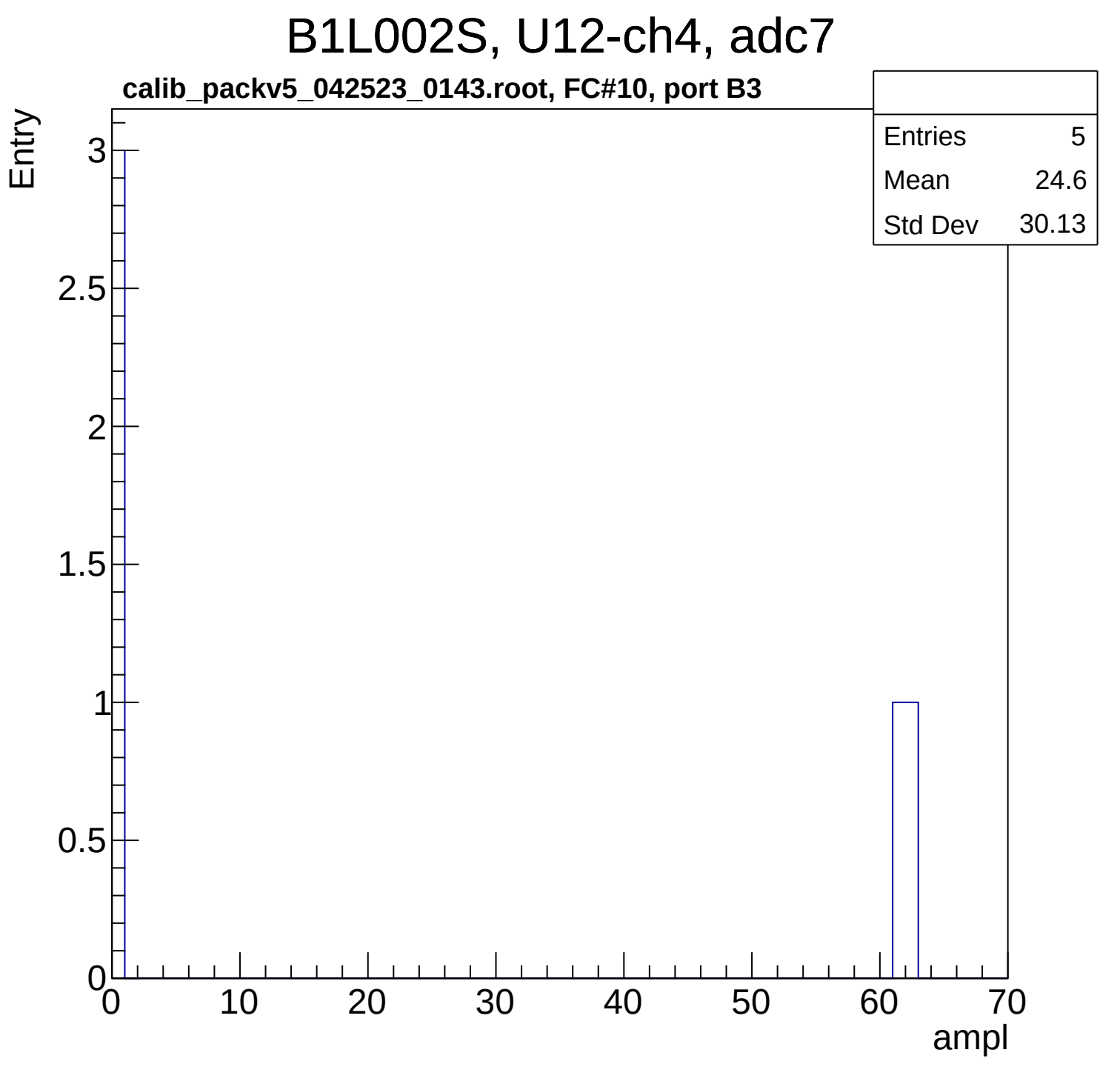
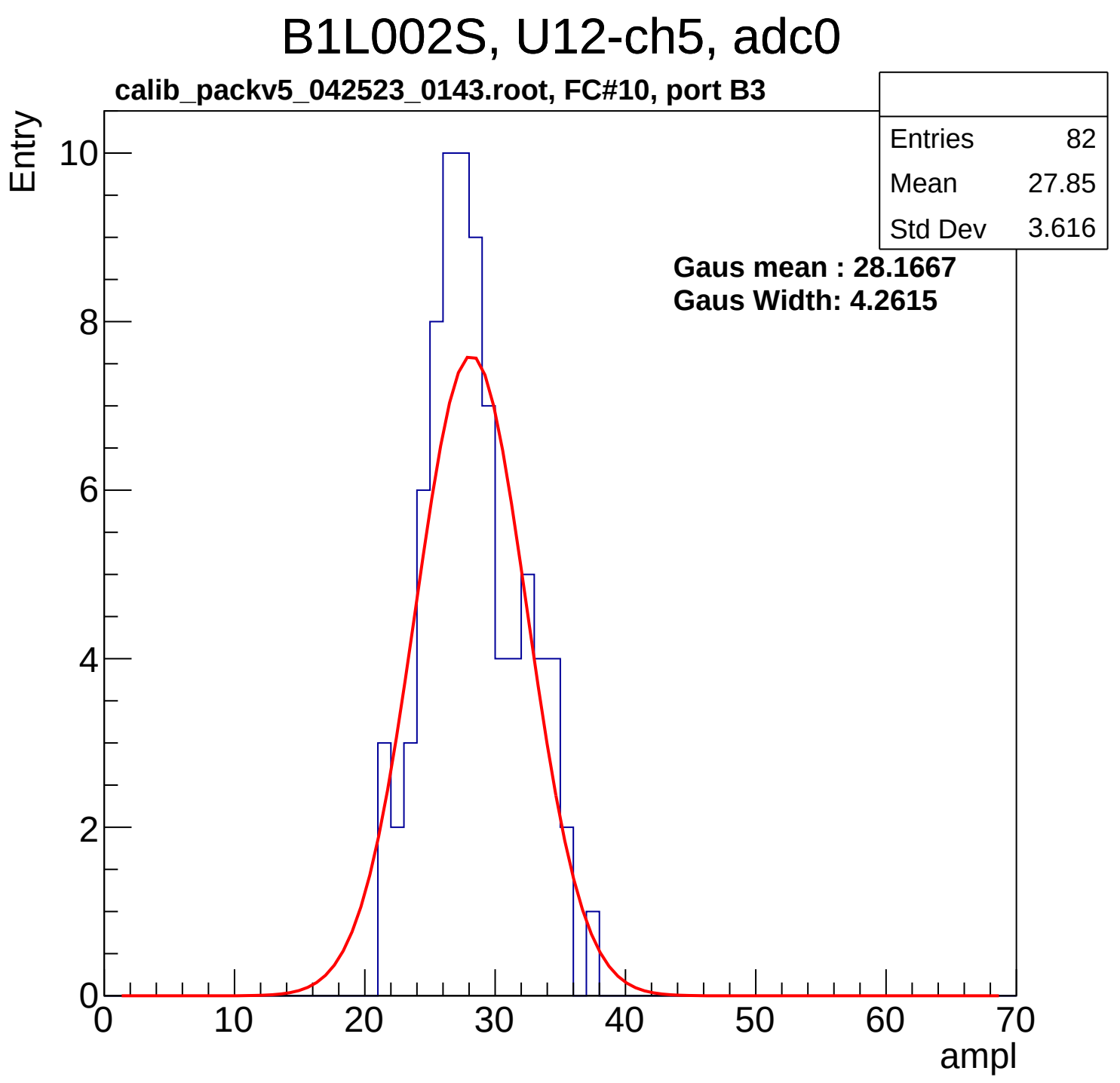
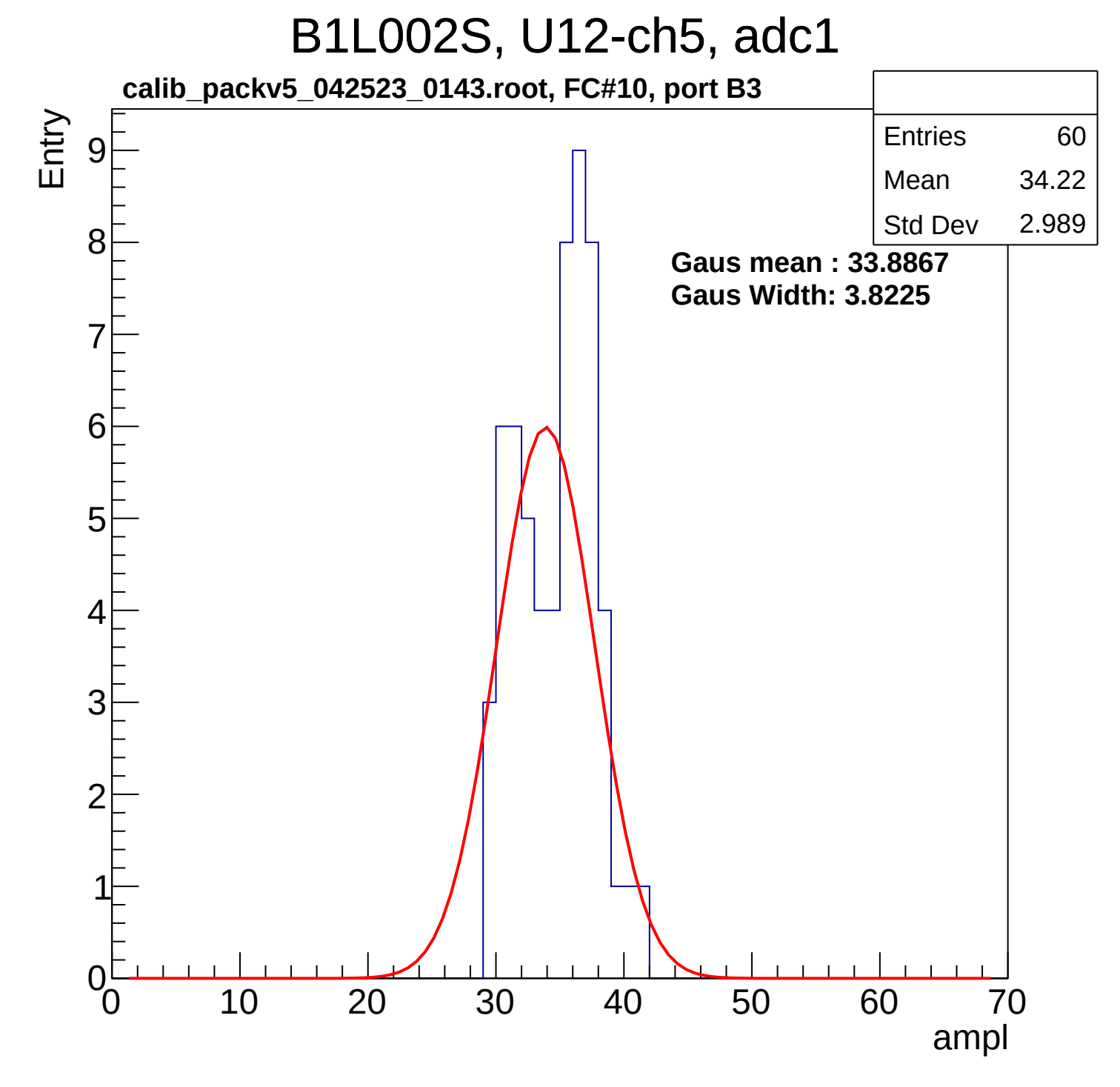

B1L002S, U12-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.22
Std Dev 2.67
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 24.6
Std Dev 30.13
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 27.85
Std Dev 3.616
Gaus mean : 28.1667
Gaus Width: 4.2615
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.22
Std Dev 2.989
Gaus mean : 33.8867
Gaus Width: 3.8225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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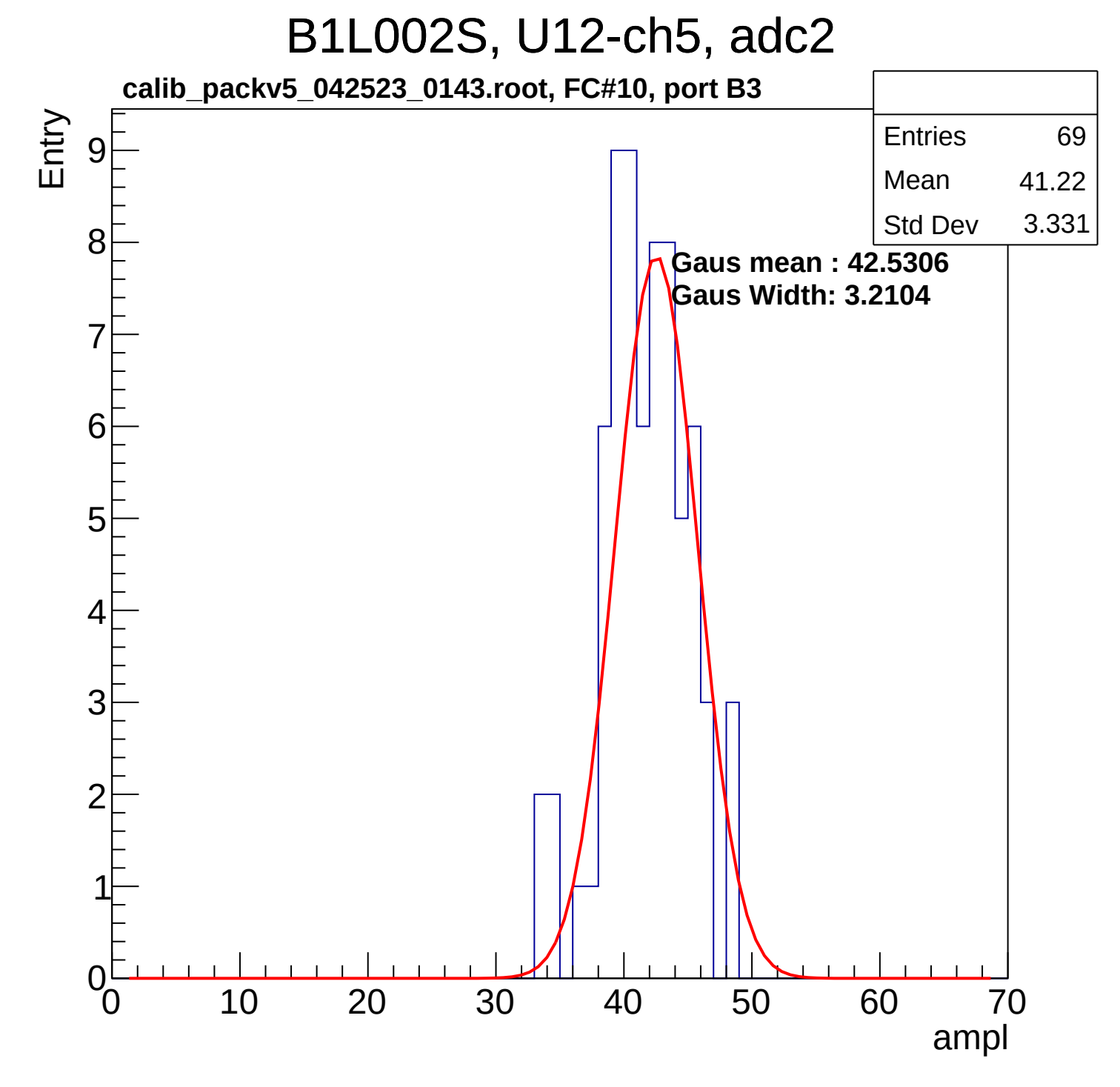
B1L002S, U12-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.22
Std Dev 3.331
Gaus mean : 42.5306
Gaus Width: 3.2104
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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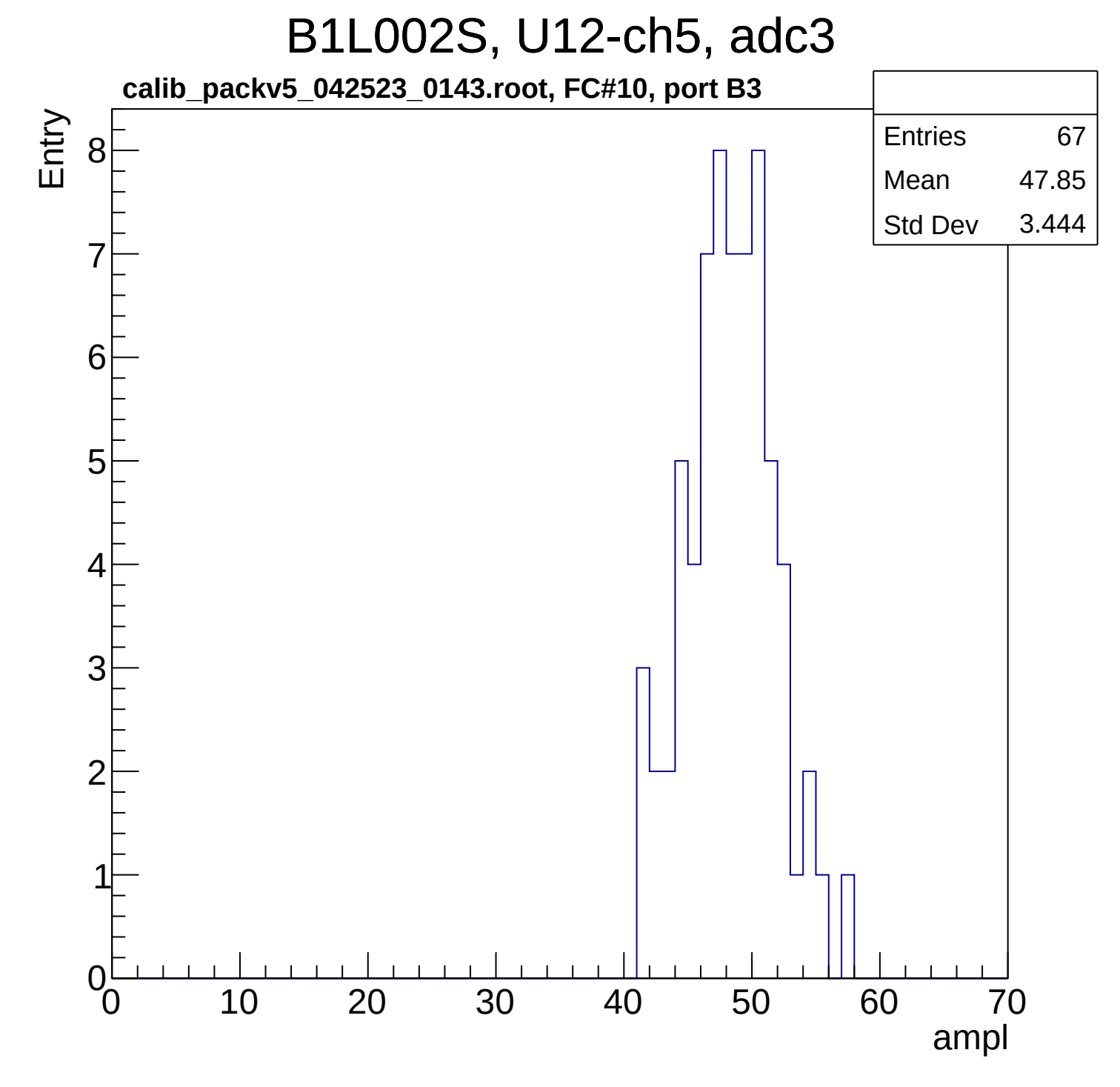
B1L002S, U12-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.85
Std Dev 3.444
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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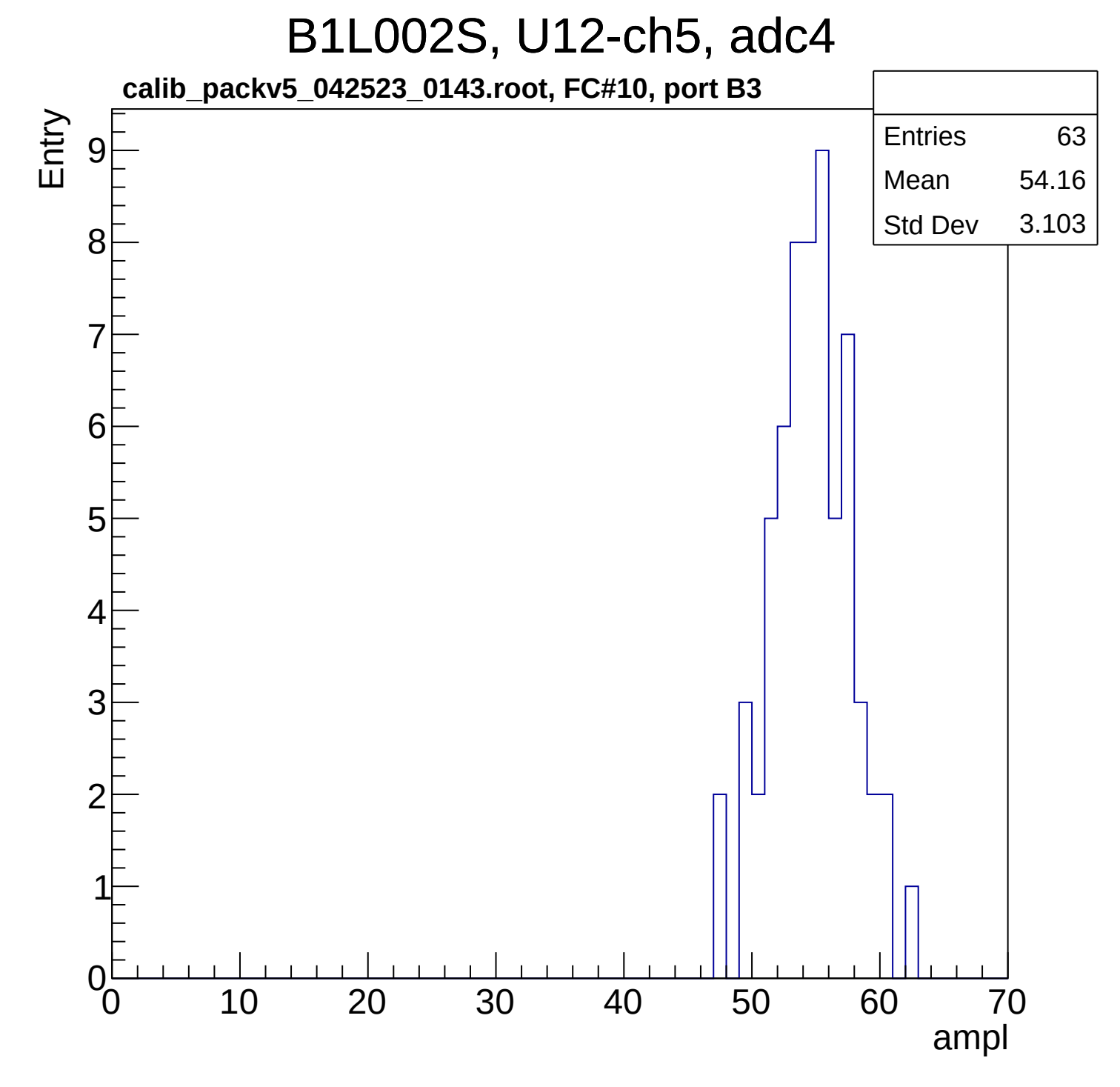
B1L002S, U12-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 54.16
Std Dev 3.103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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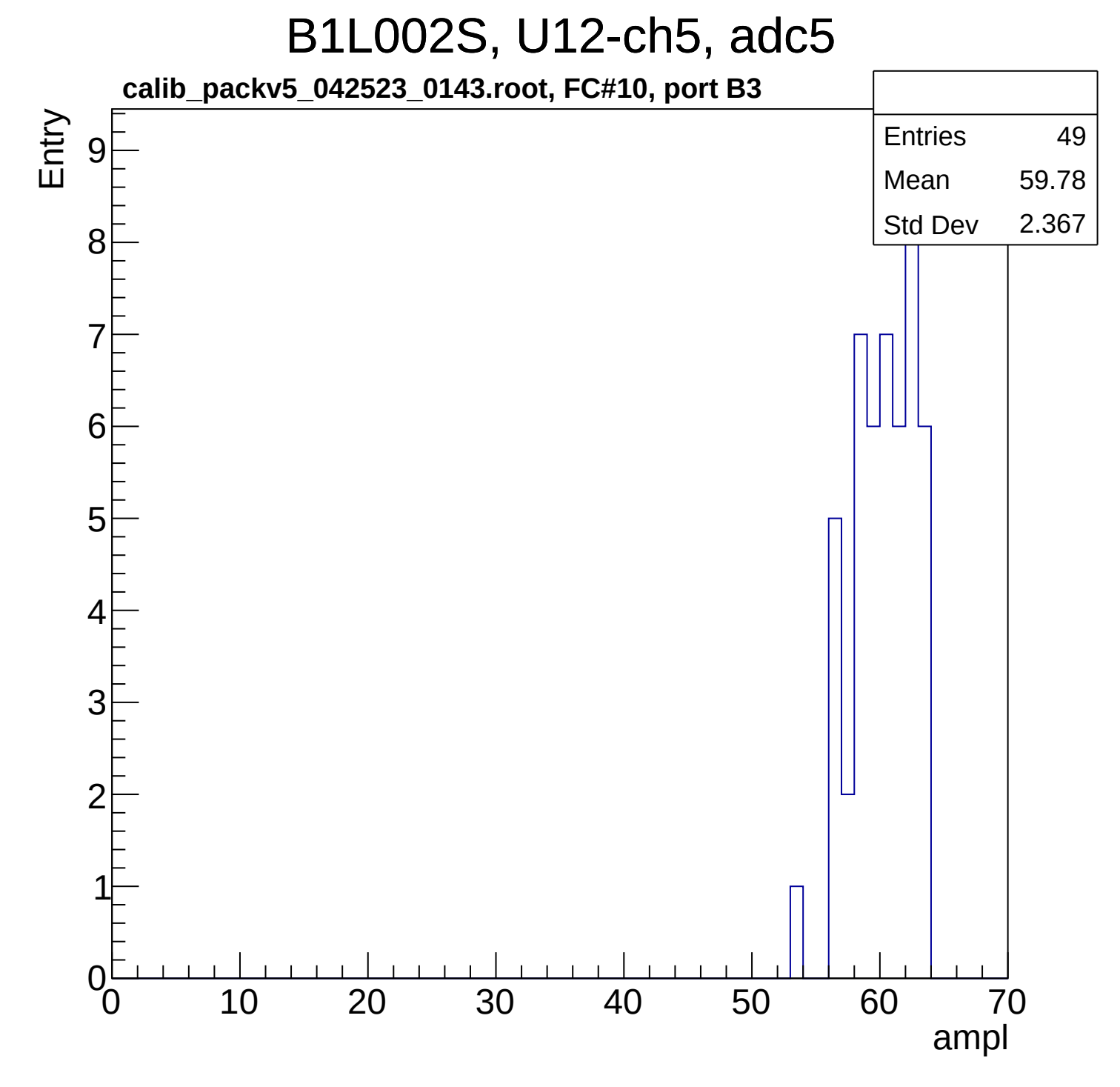
B1L002S, U12-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.78
Std Dev 2.367
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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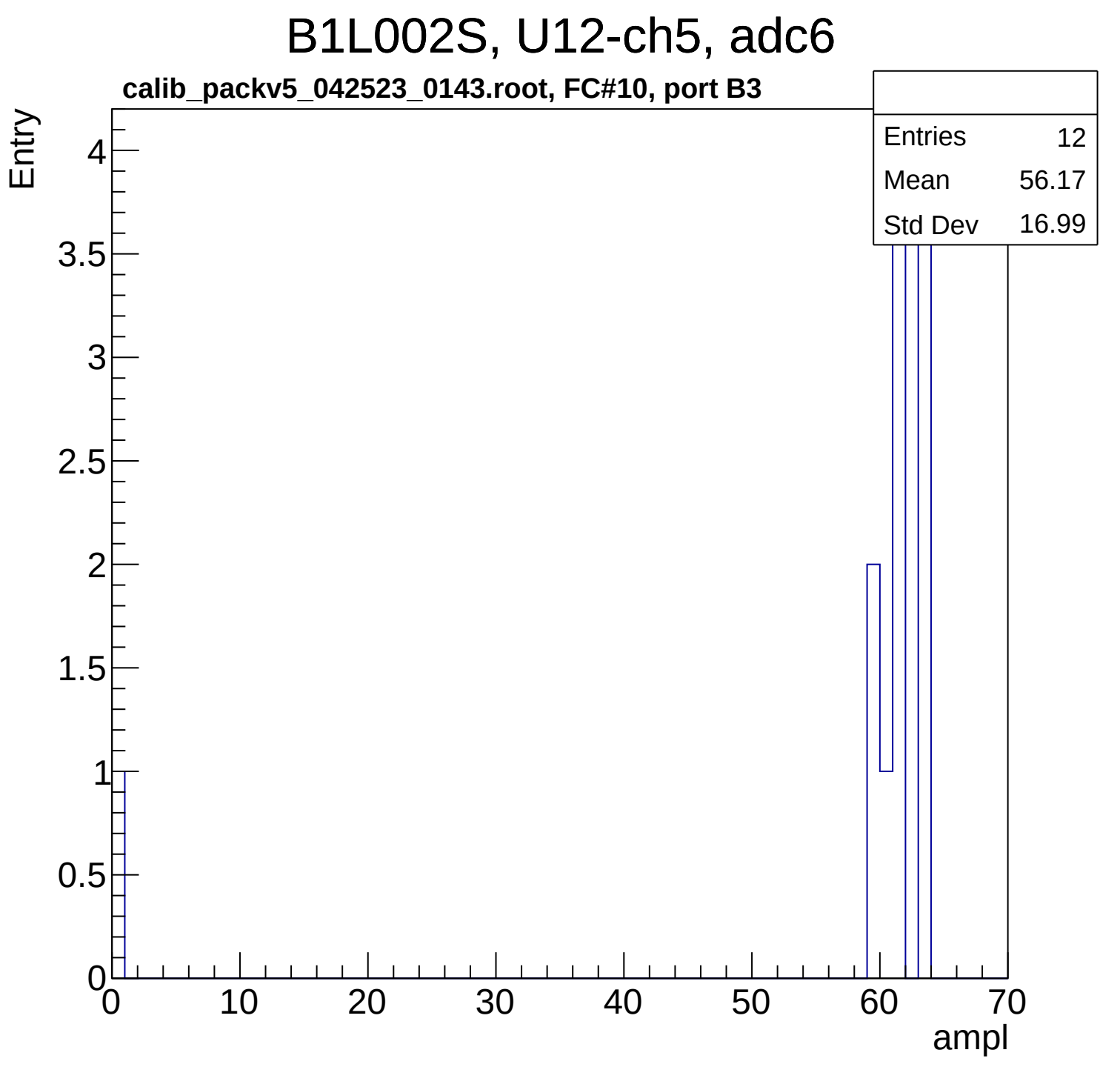
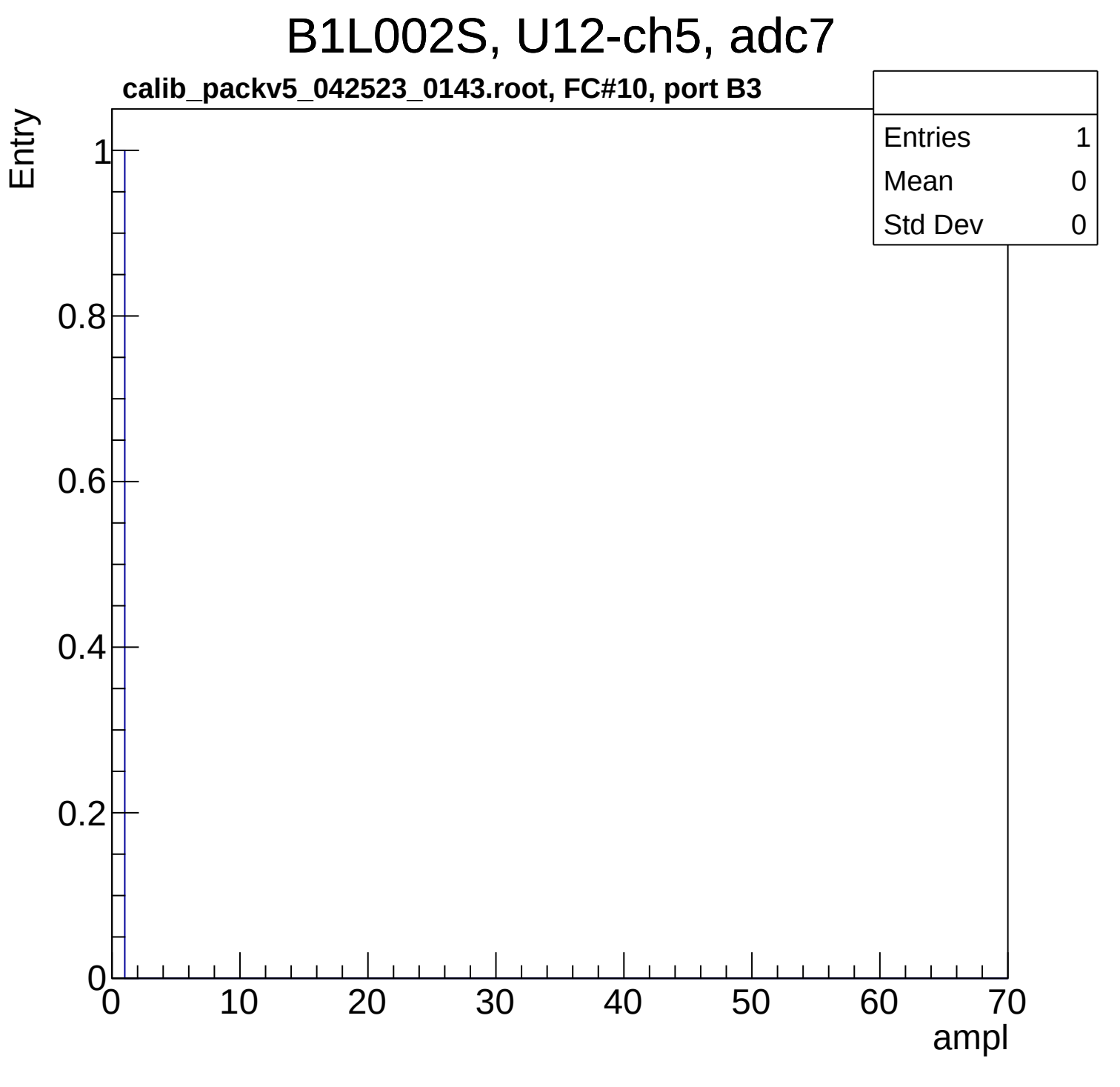

B1L002S, U12-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.17
Std Dev 16.99
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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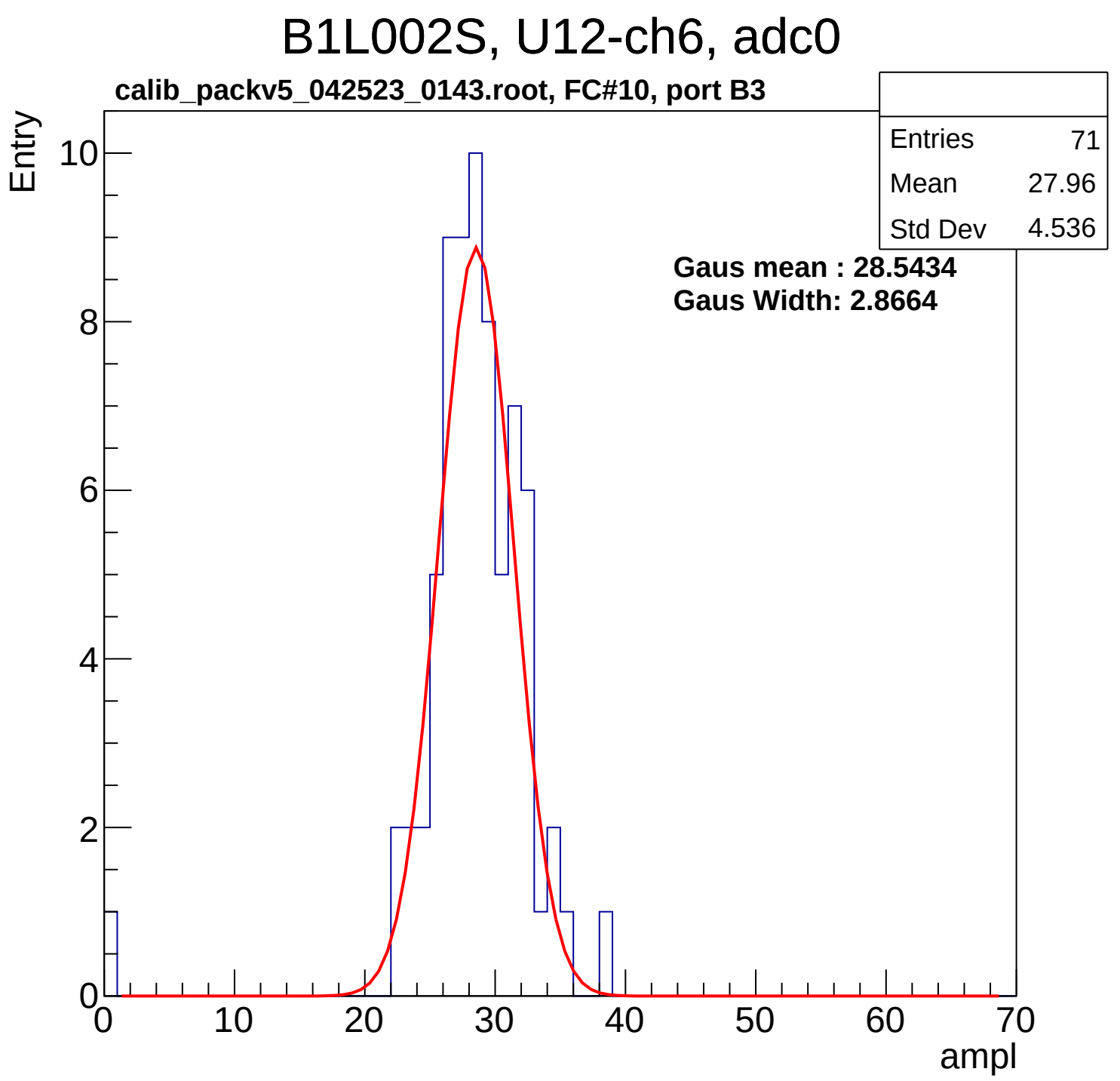
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.96
Std Dev 4.536
Gaus mean : 28.5434
Gaus Width: 2.8664
Entry
ampl
0
2
4
6
8
10
0
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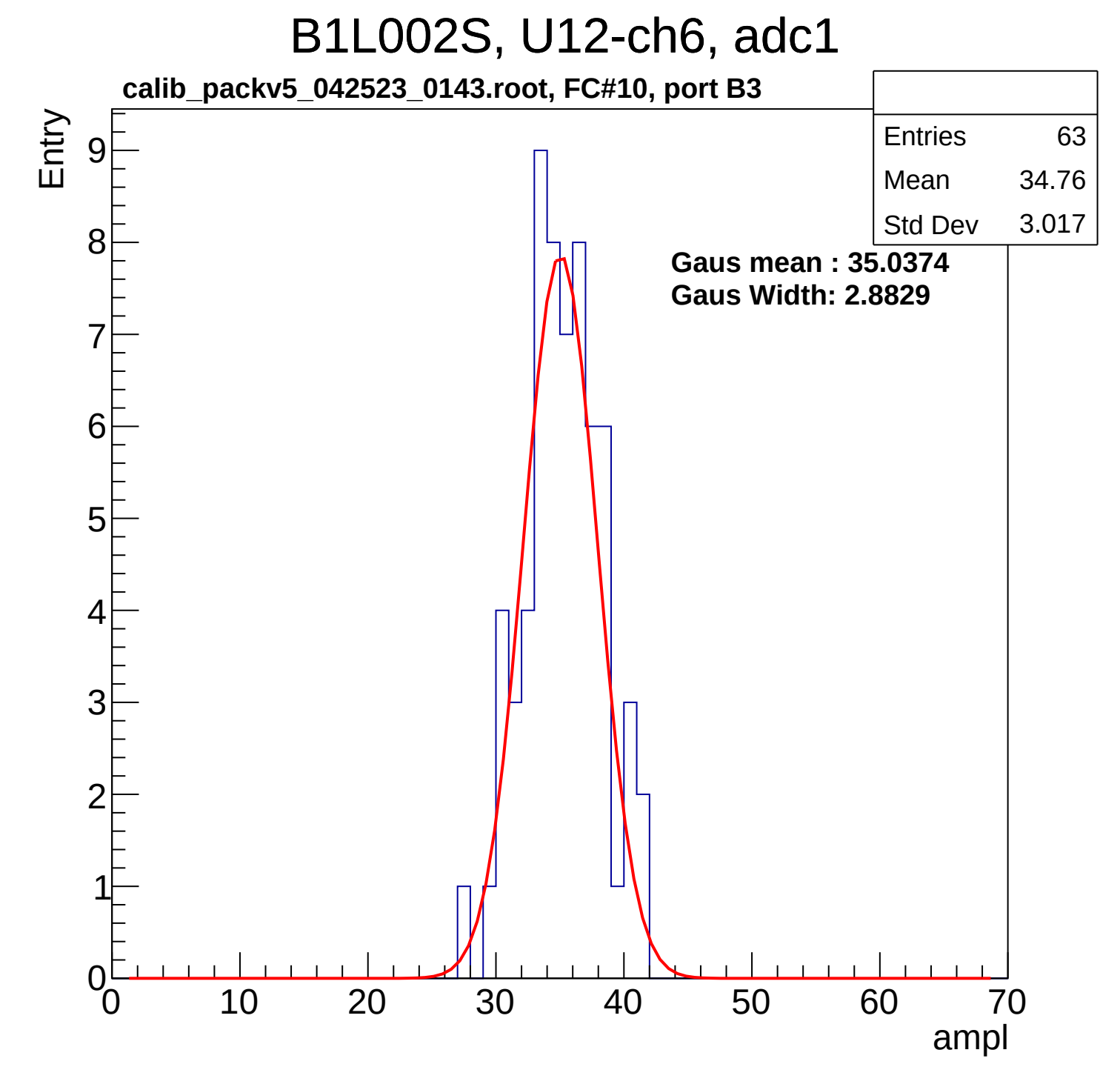
20
30
40
50
60
70


B1L002S, U12-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.76
Std Dev 3.017
Gaus mean : 35.0374
Gaus Width: 2.8829
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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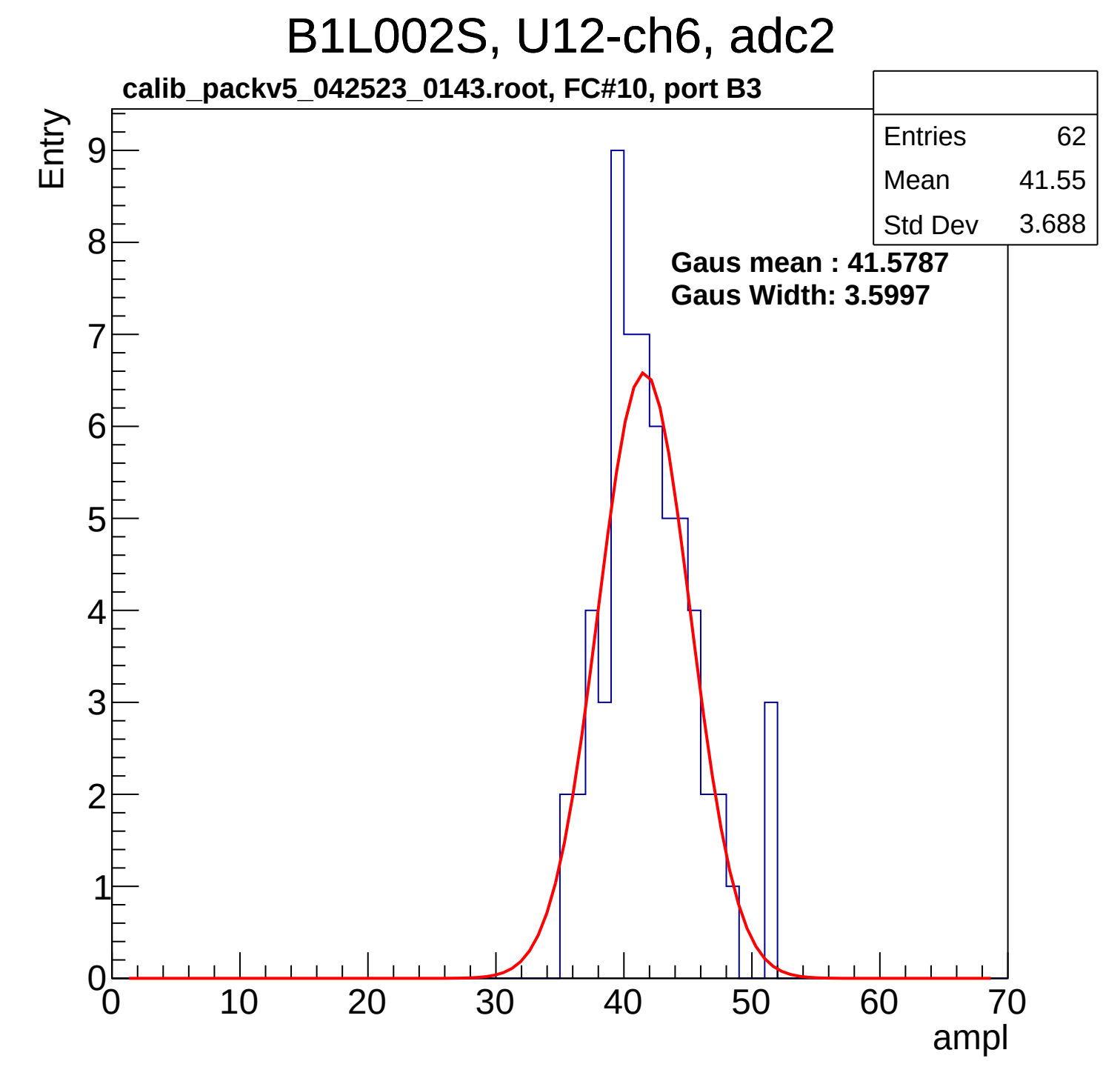
B1L002S, U12-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.55
Std Dev 3.688
Gaus mean : 41.5787
Gaus Width: 3.5997
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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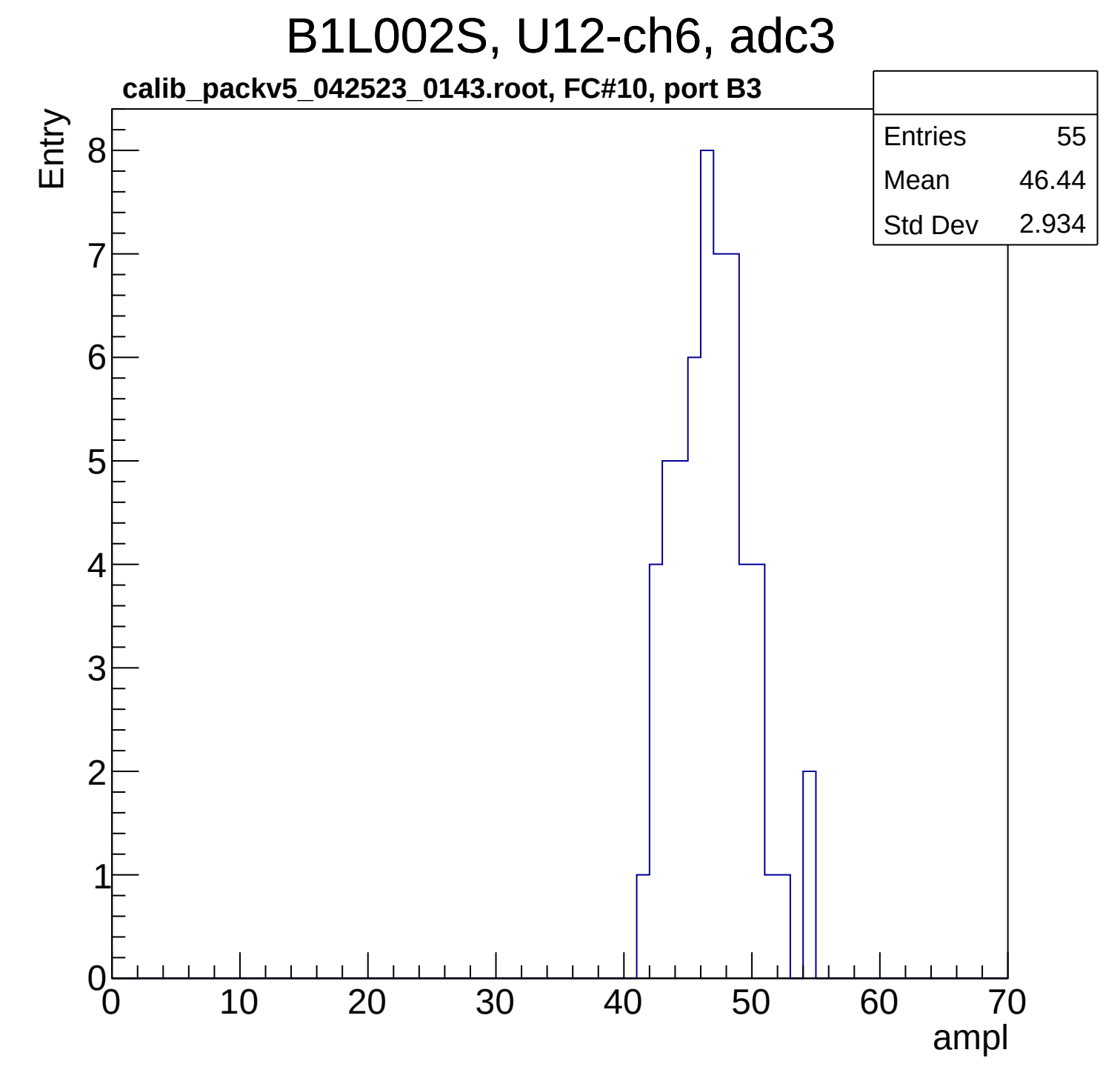
B1L002S, U12-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.44
Std Dev 2.934
Entry
ampl
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4
5
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0
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60
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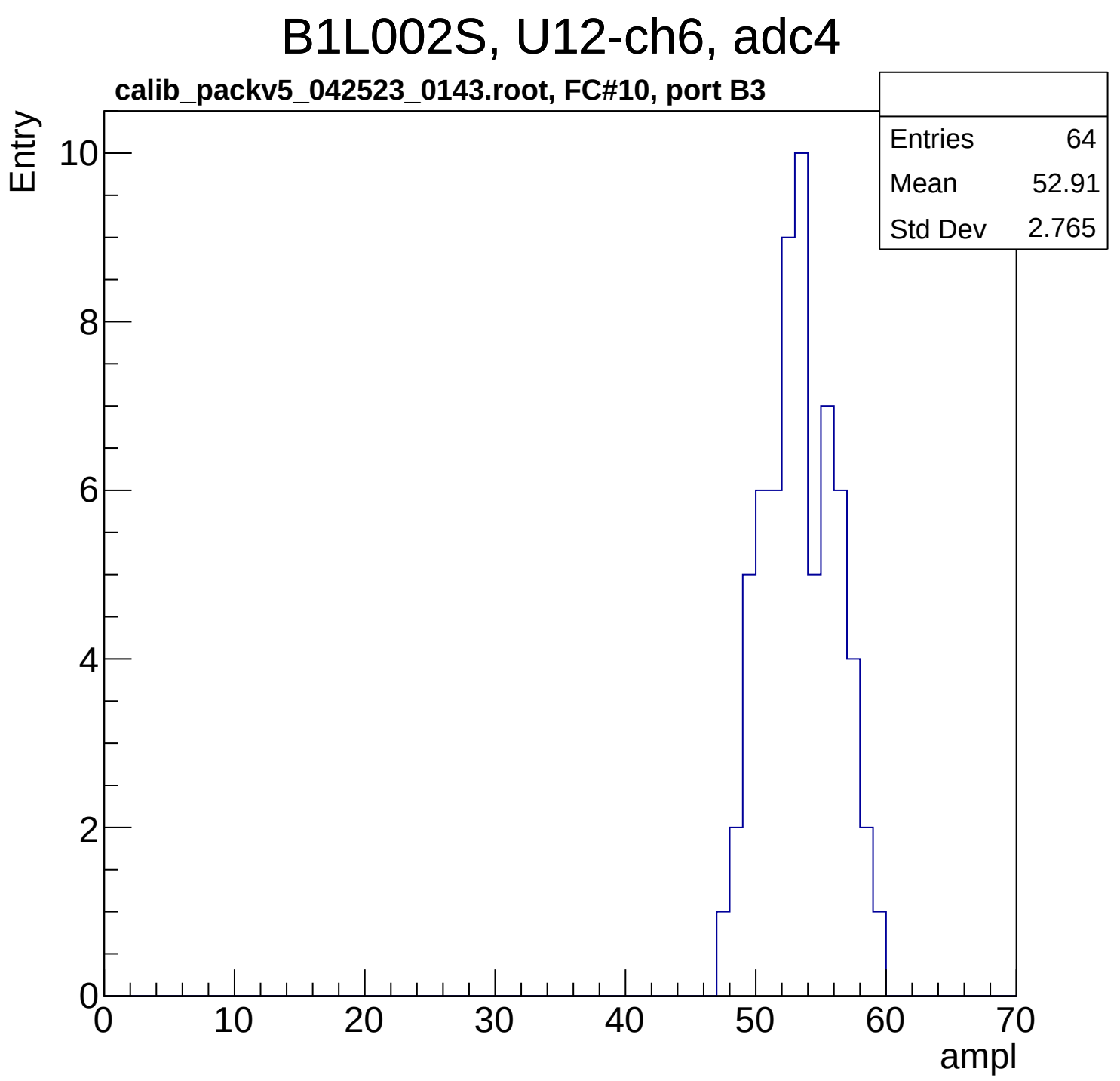
B1L002S, U12-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.91
Std Dev 2.765
Entry
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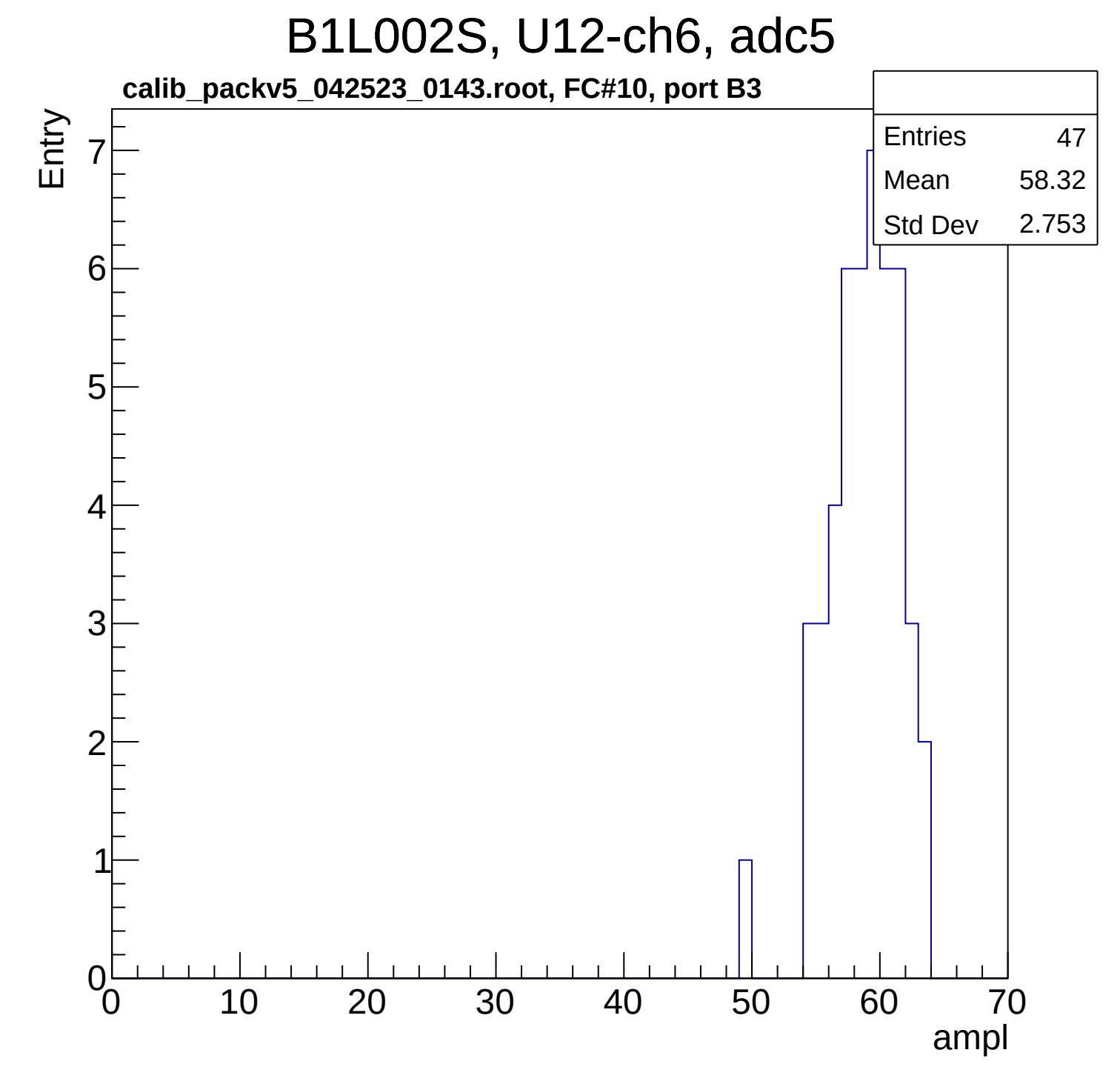
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.32
Std Dev 2.753
Entry
ampl
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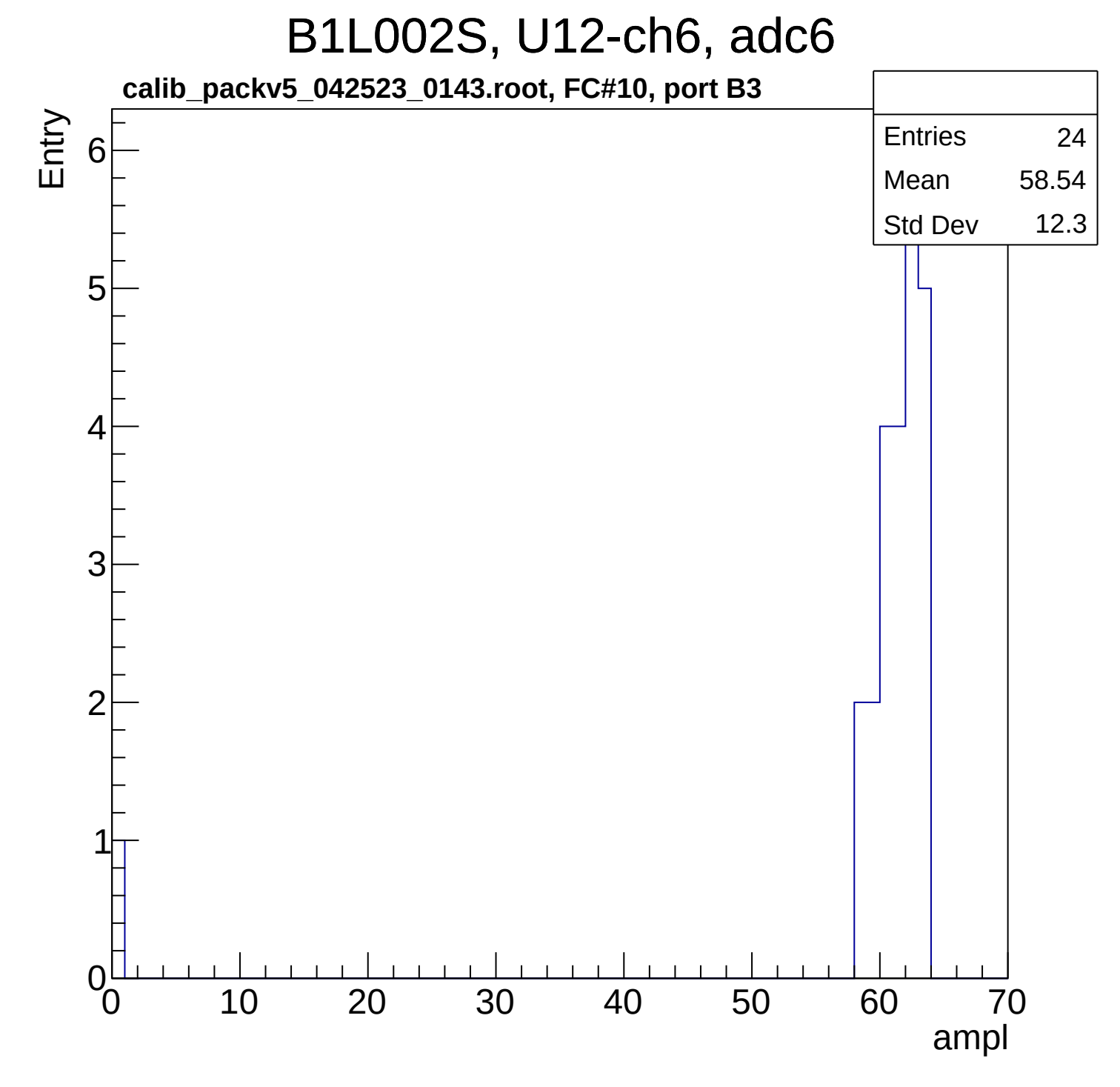
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.54
Std Dev 12.3
Entry
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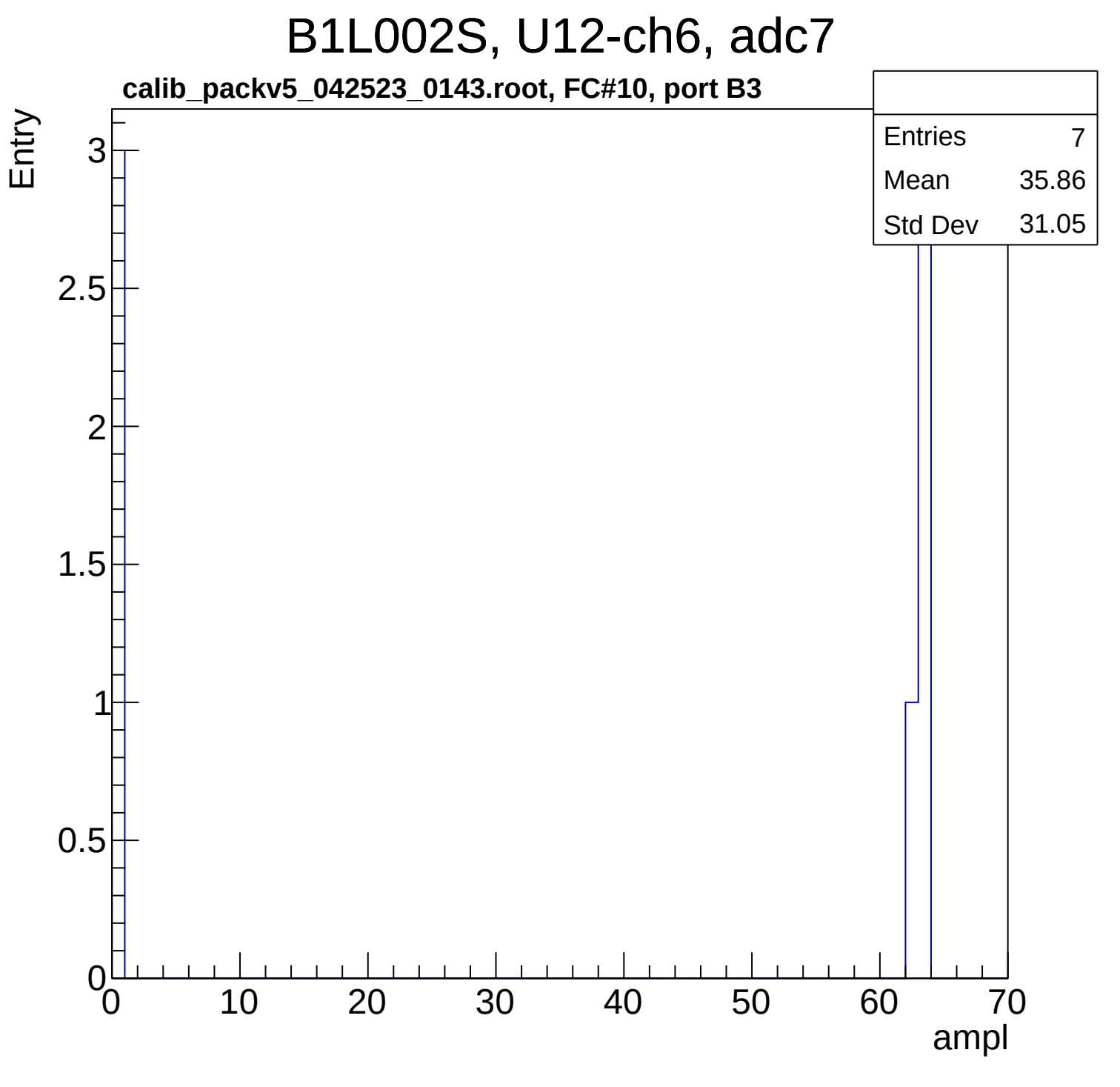
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 35.86
Std Dev 31.05
Entry
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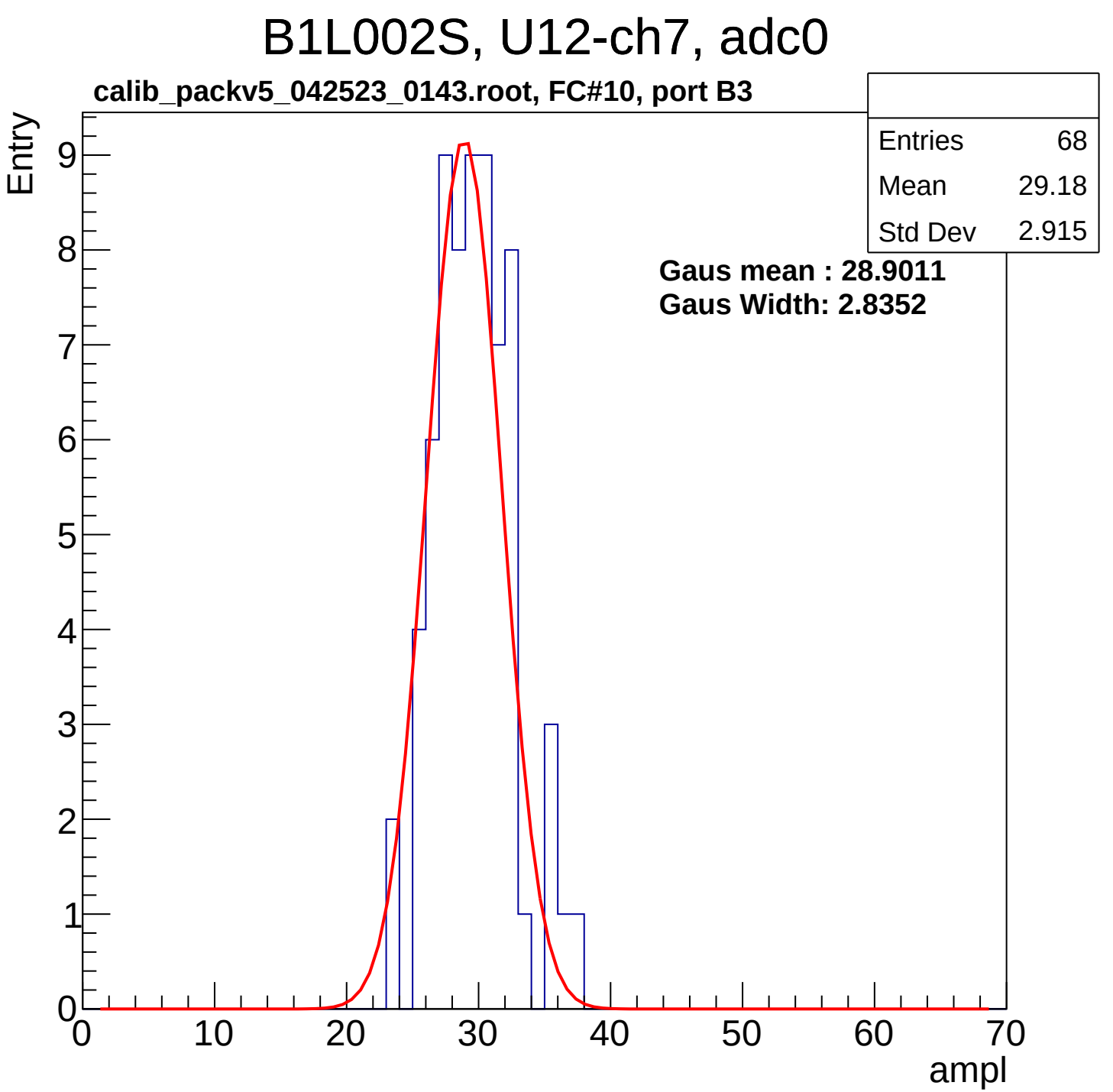
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.18
Std Dev 2.915
Gaus mean : 28.9011
Gaus Width: 2.8352
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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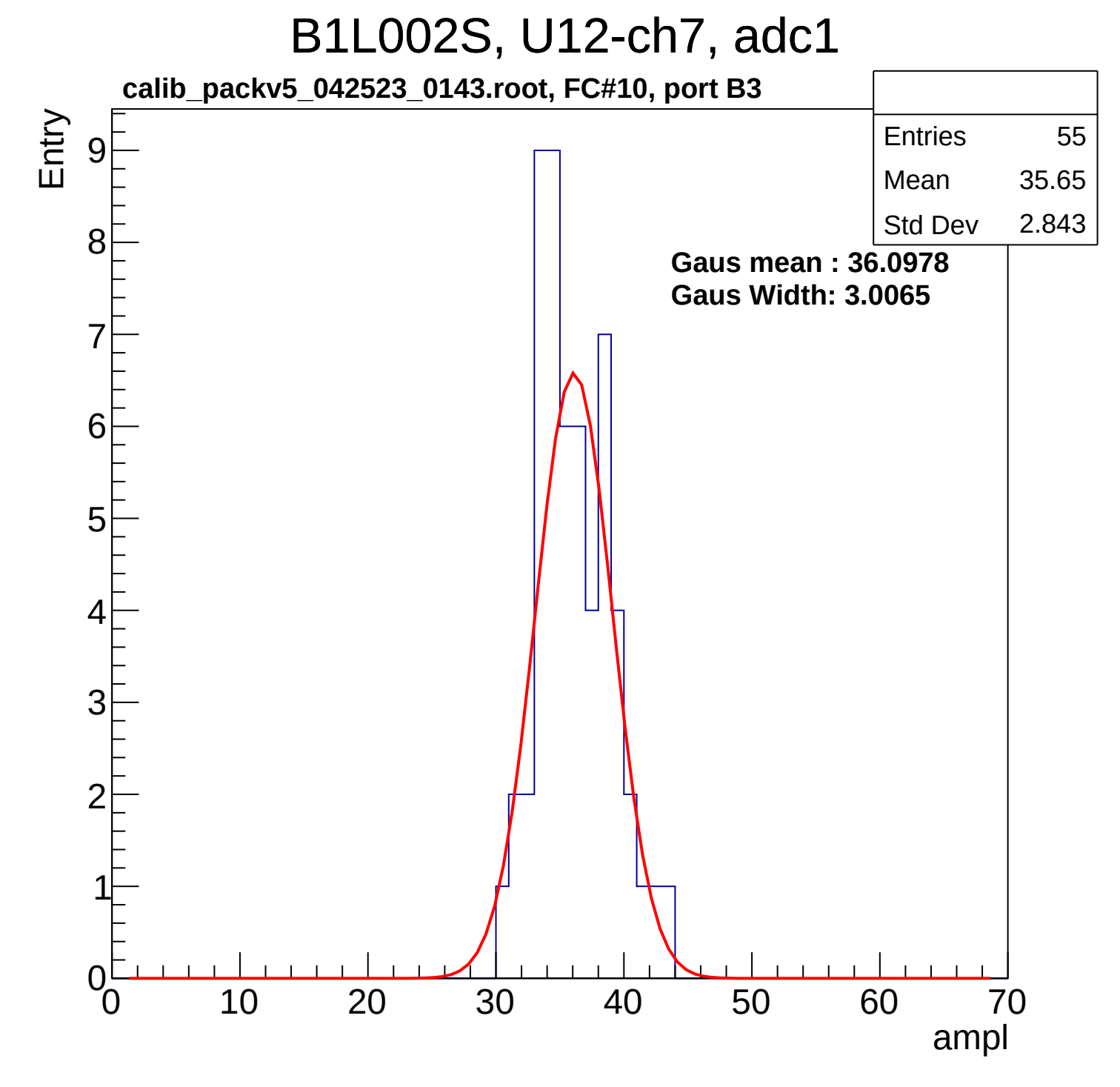
B1L002S, U12-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.65
Std Dev 2.843
Gaus mean : 36.0978
Gaus Width: 3.0065
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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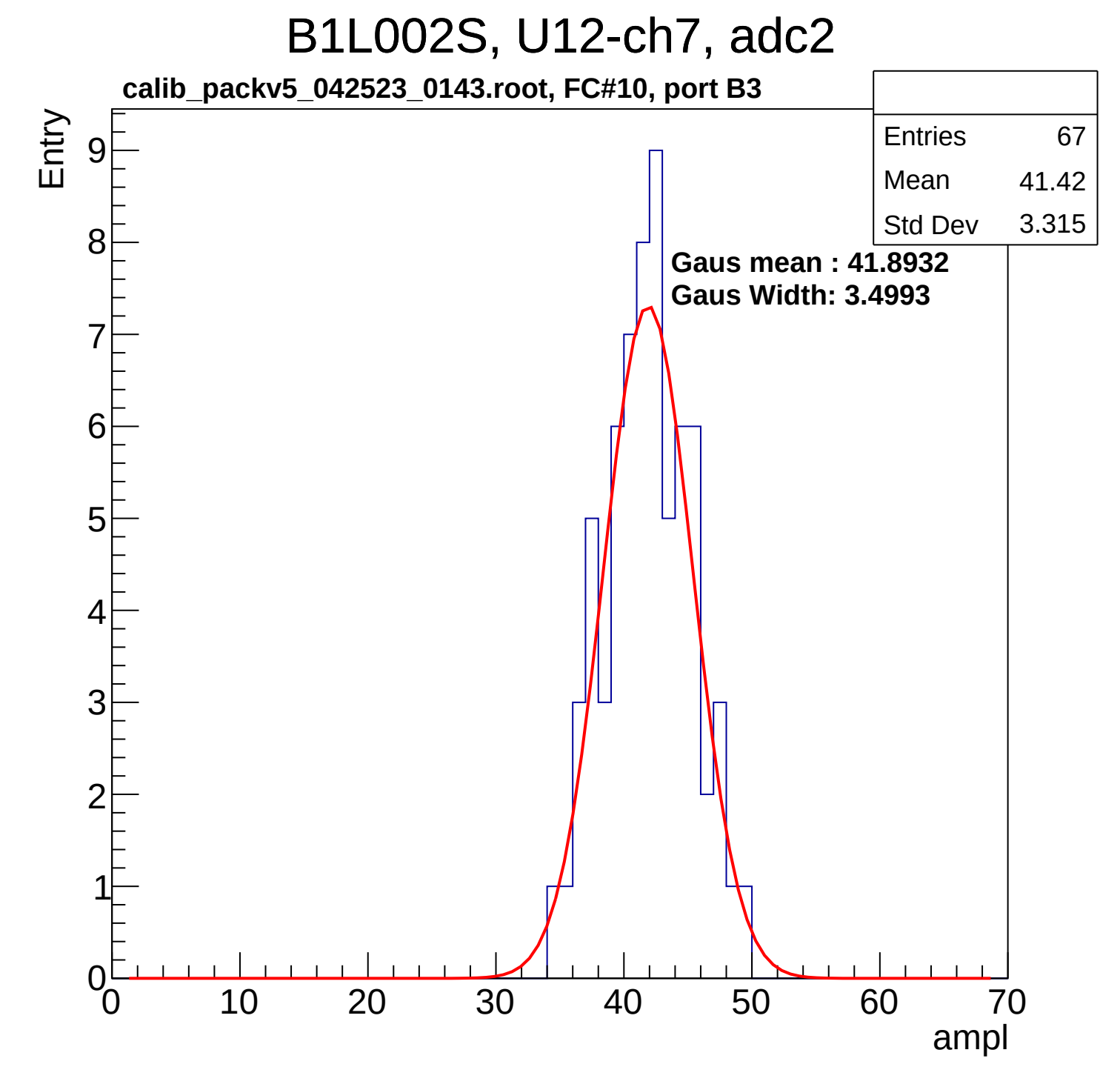
B1L002S, U12-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.42
Std Dev 3.315
Gaus mean : 41.8932
Gaus Width: 3.4993
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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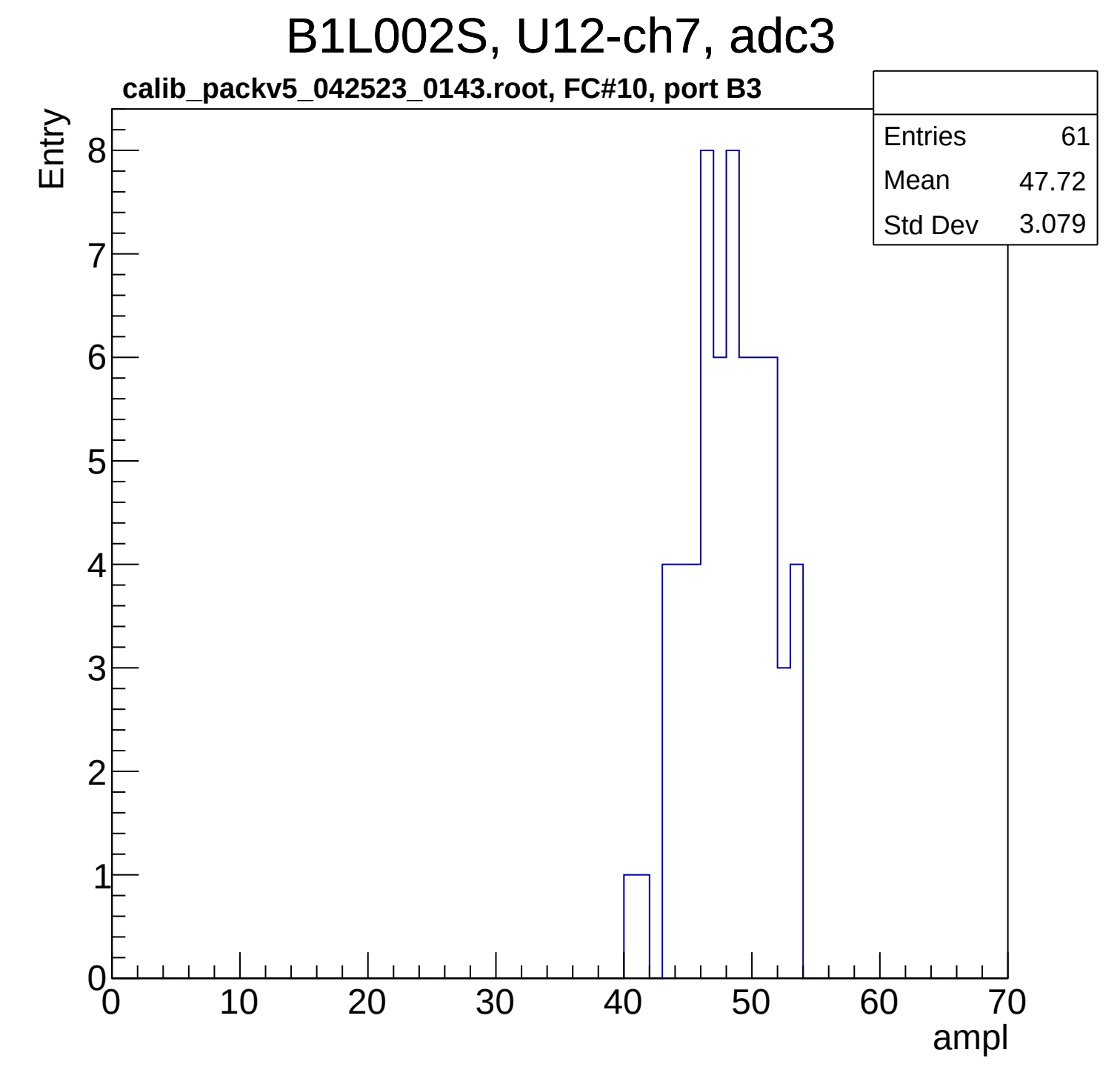
B1L002S, U12-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.72
Std Dev 3.079
Entry
ampl
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1
2
3
4
5
6
7
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20
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40
50
60
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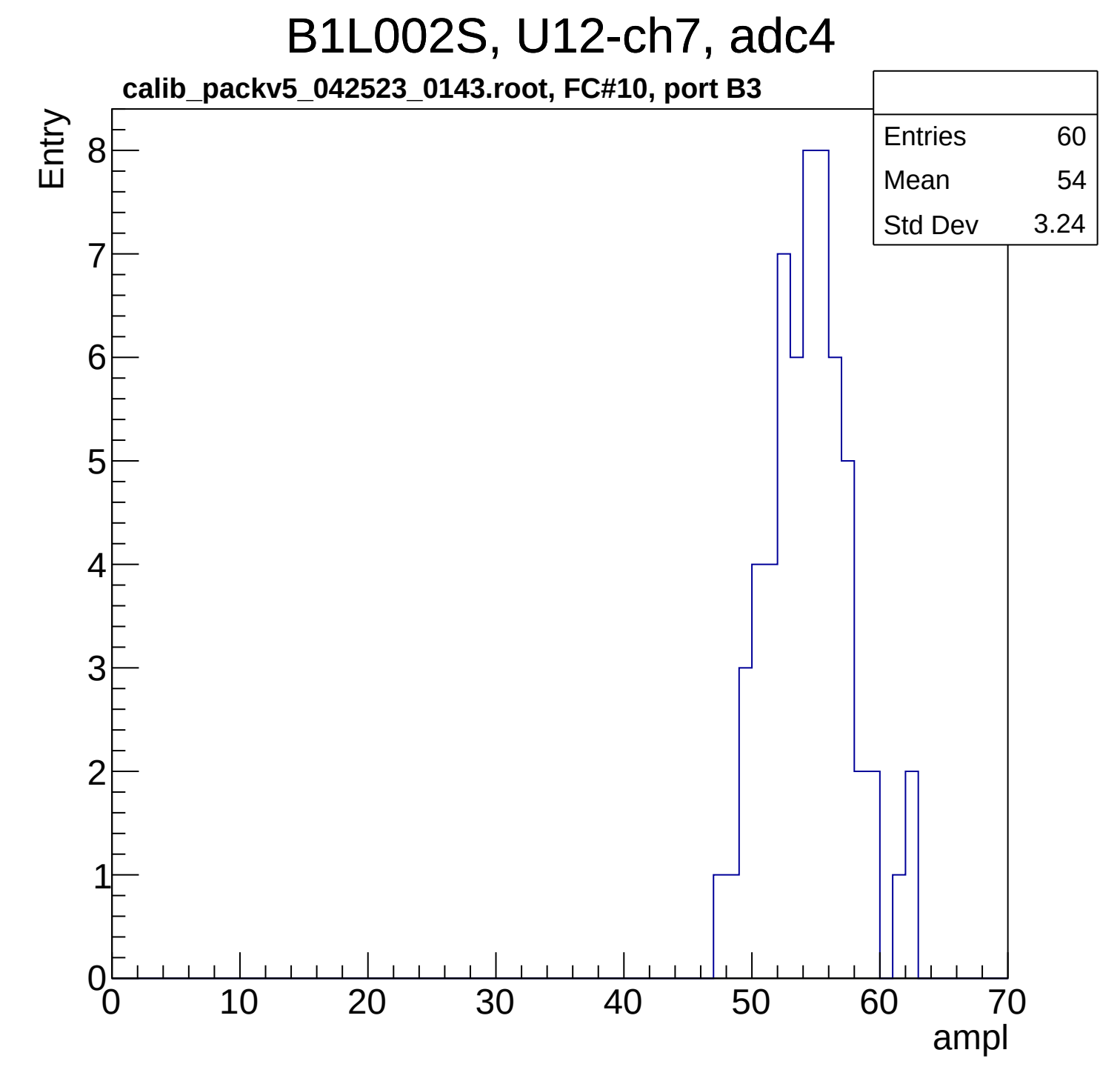
B1L002S, U12-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54
Std Dev 3.24
Entry
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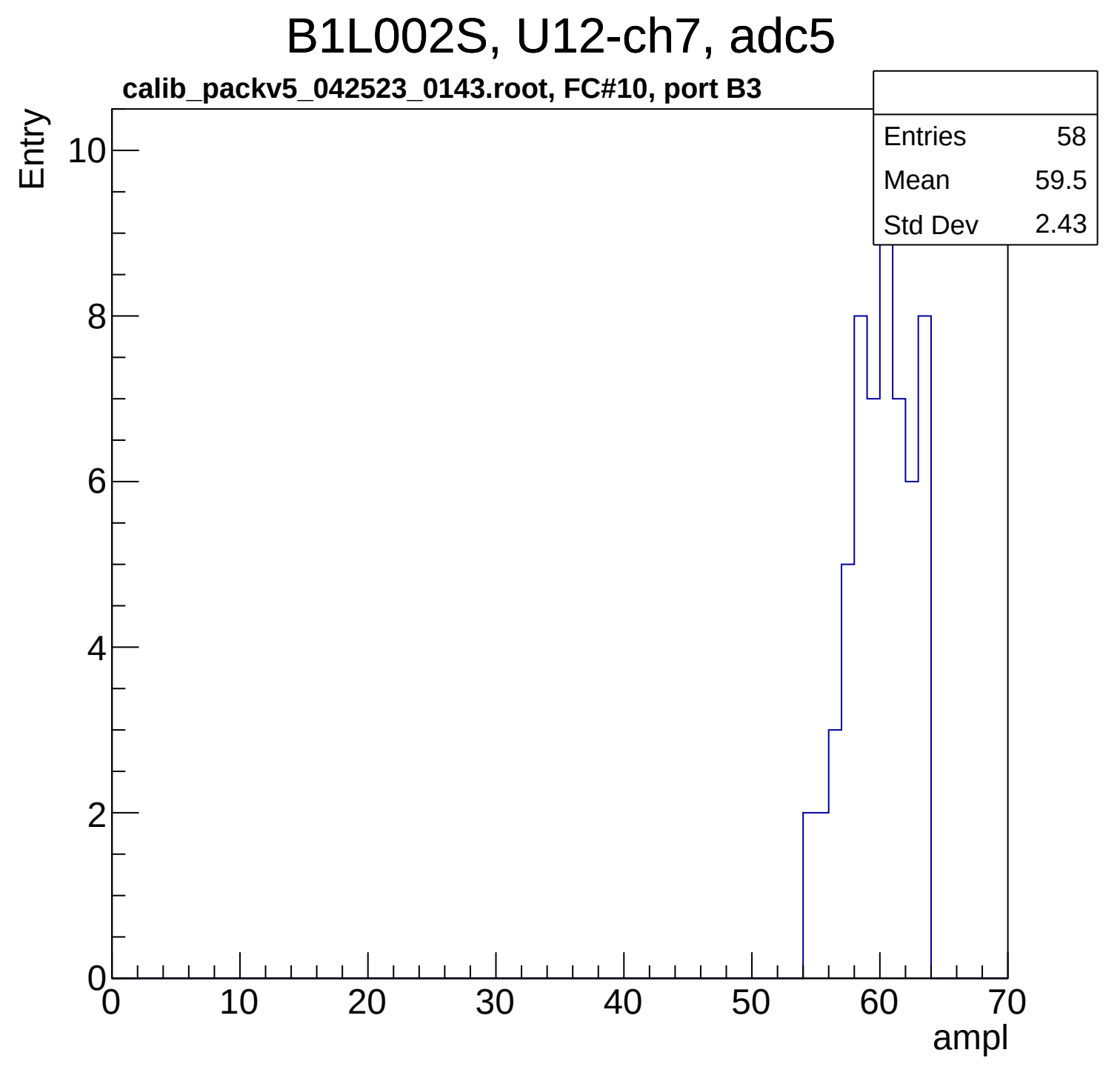
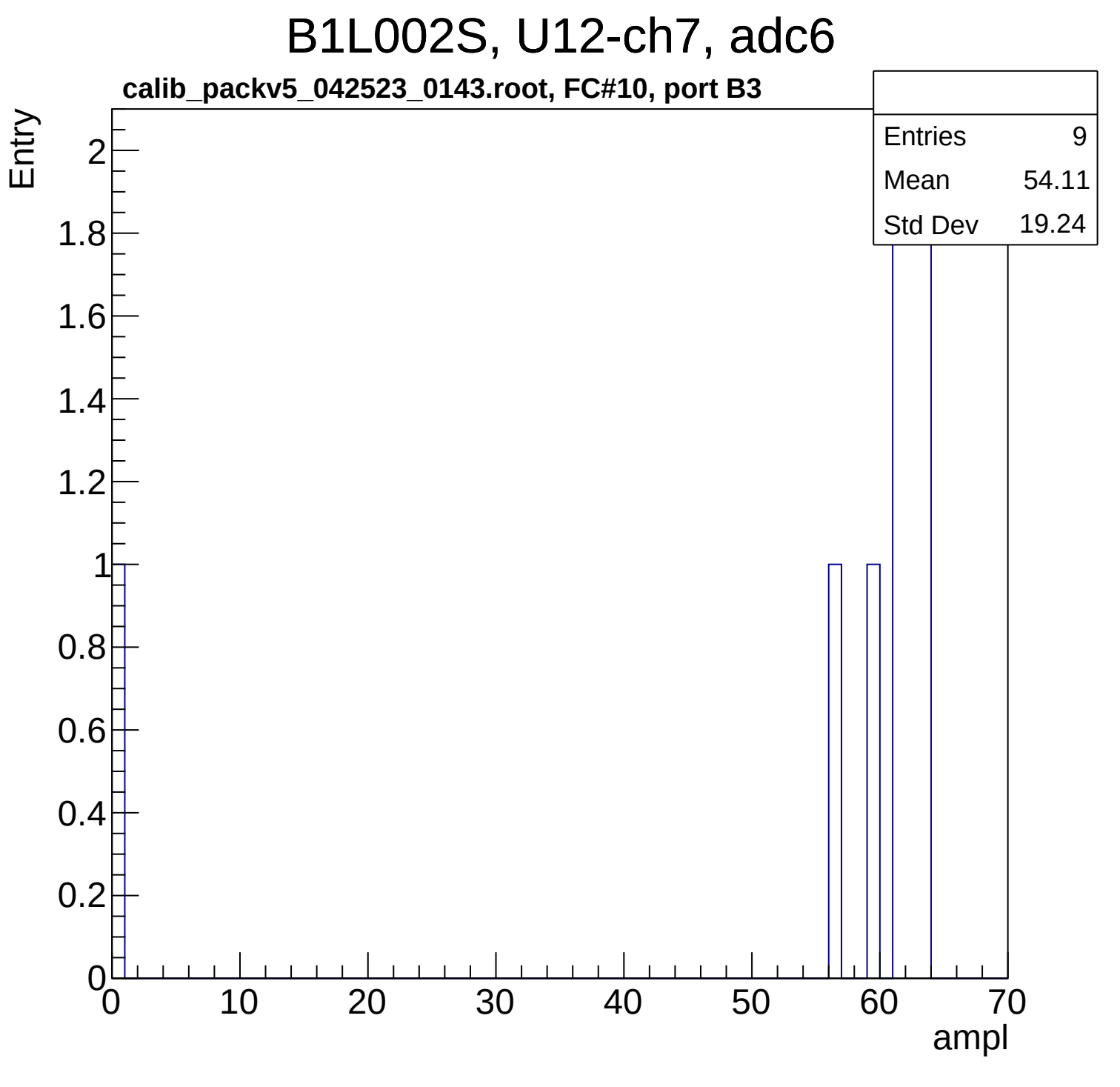
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 59.5
Std Dev 2.43
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 54.11
Std Dev 19.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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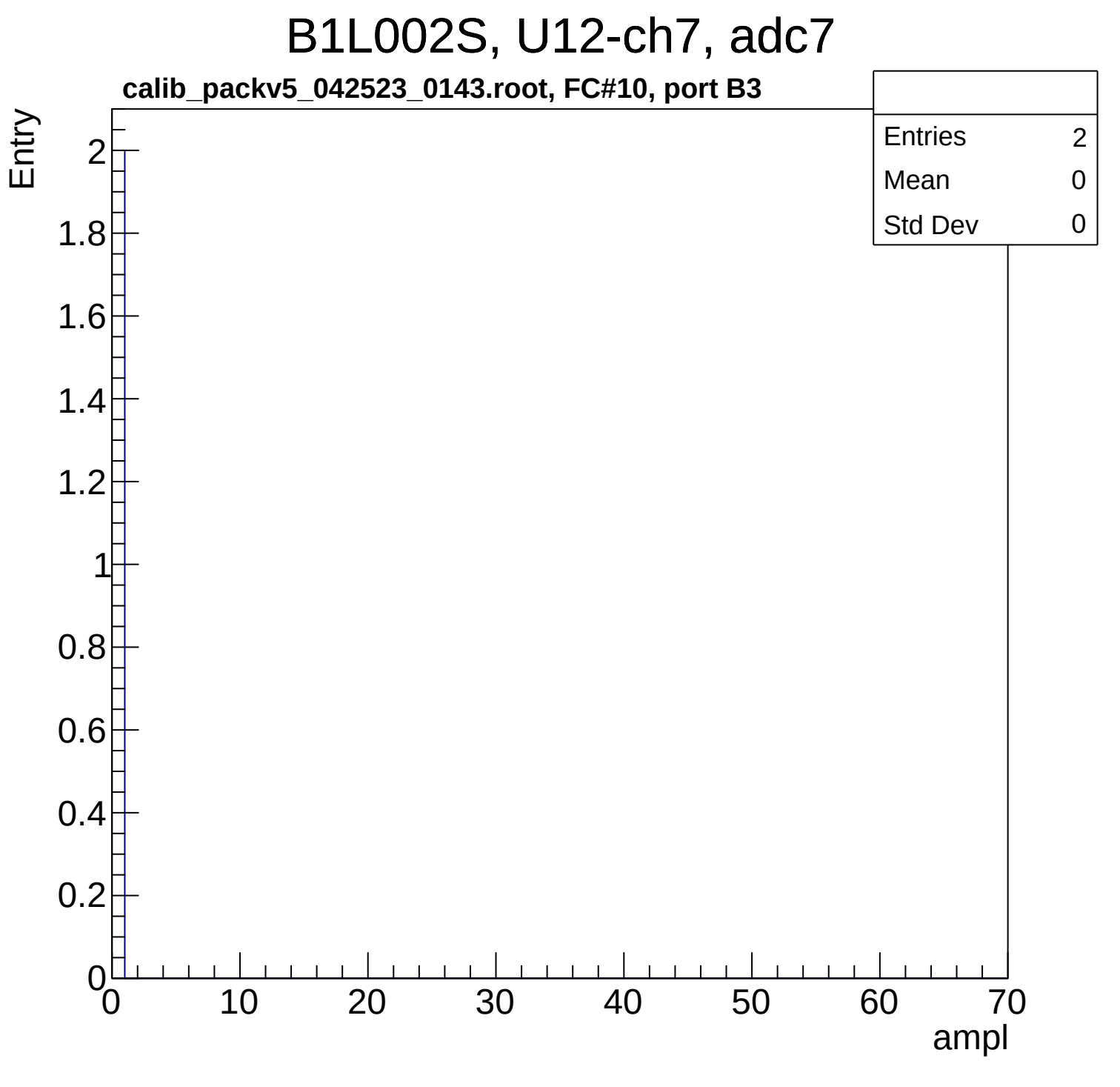
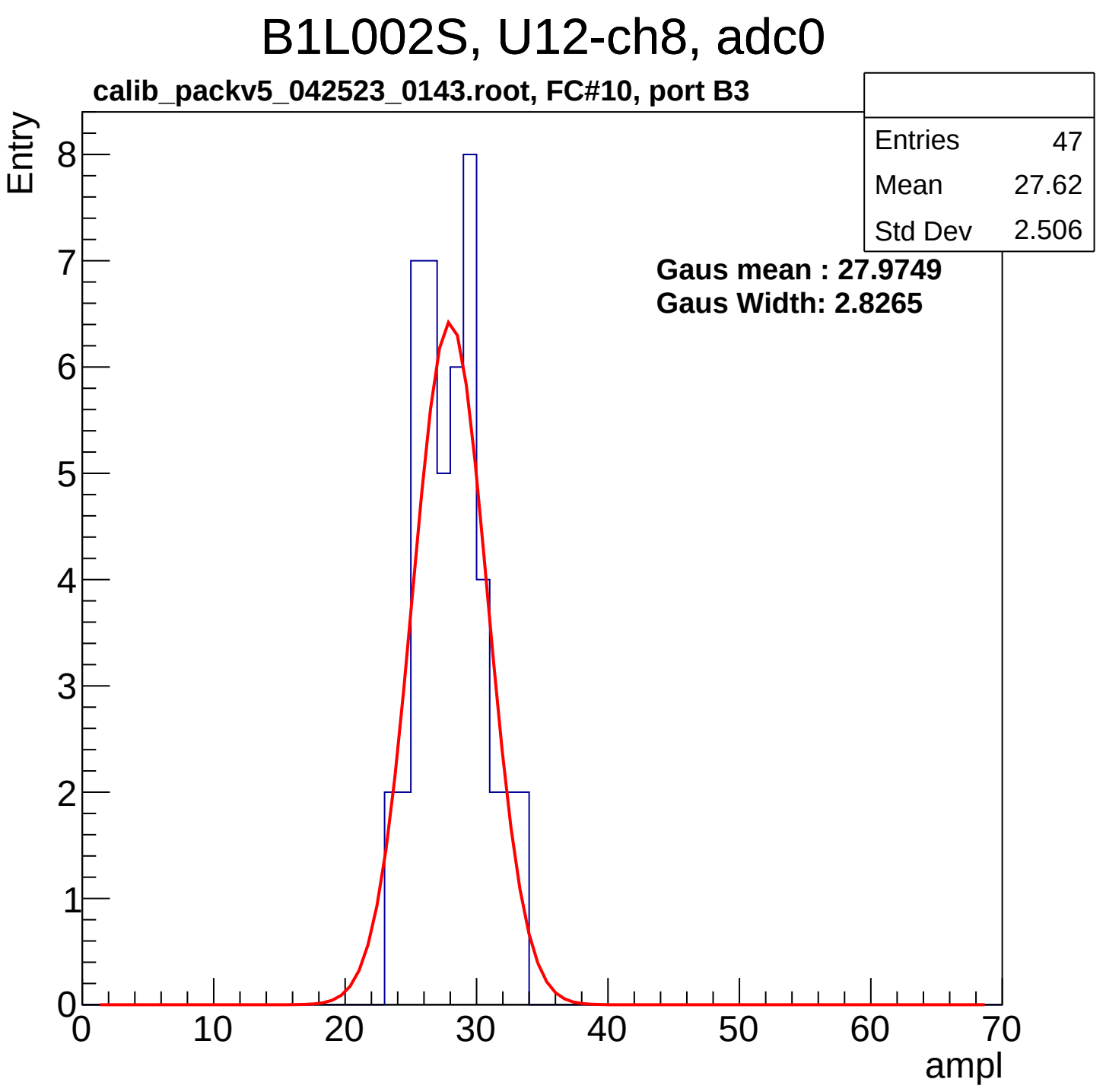
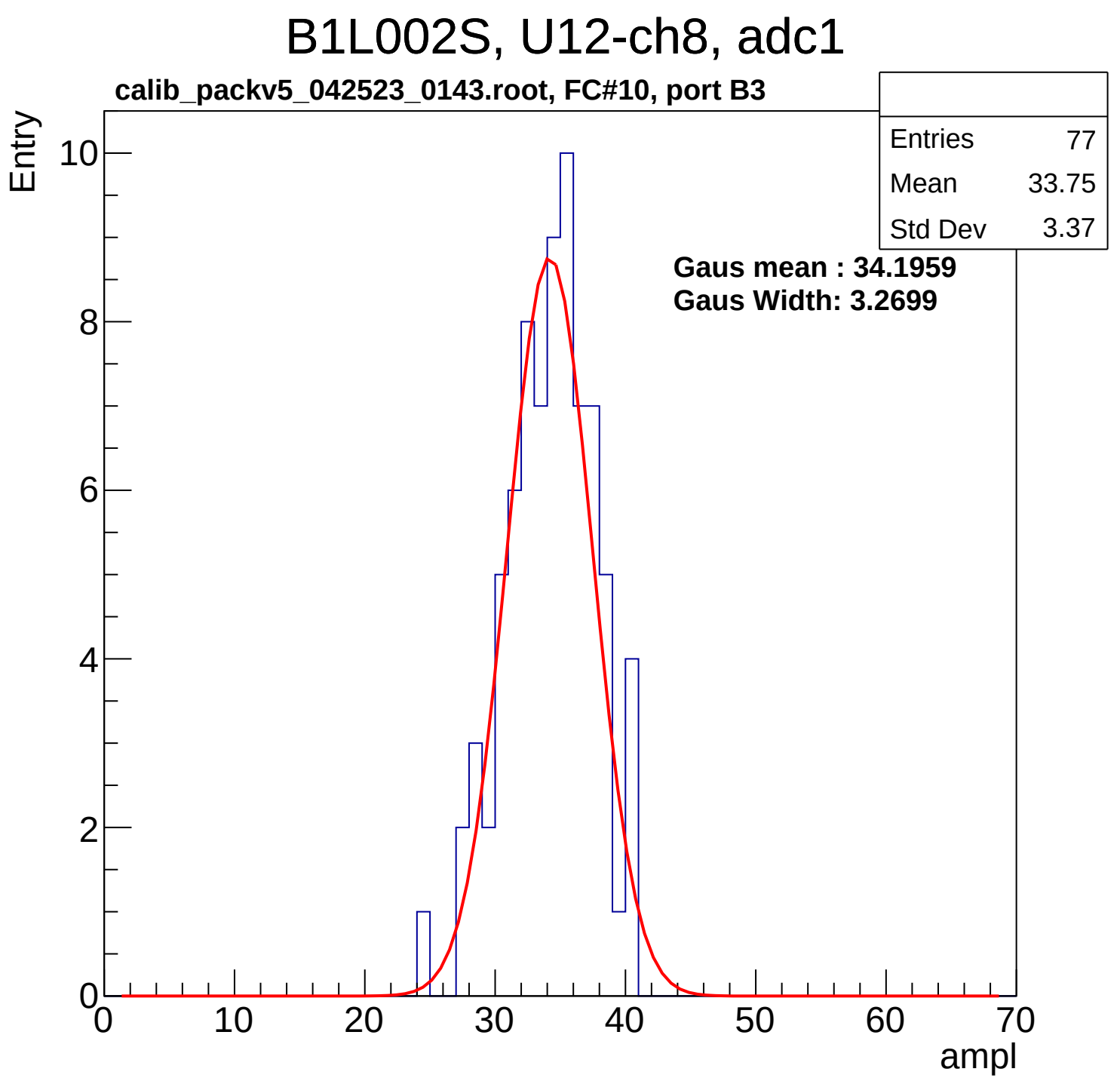
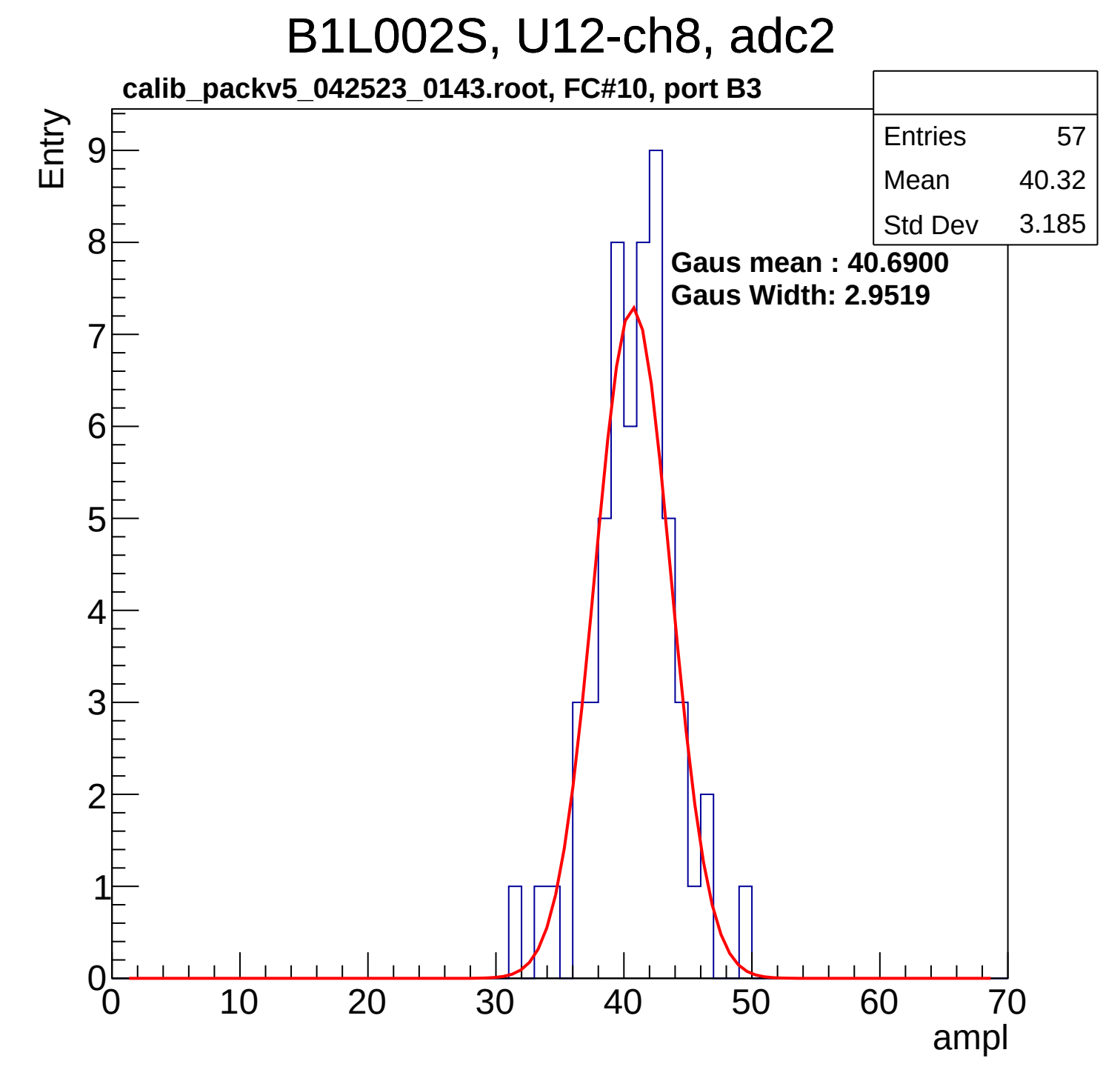

B1L002S, U12-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 27.62
Std Dev 2.506
Gaus mean : 27.9749
Gaus Width: 2.8265
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 33.75
Std Dev 3.37
Gaus mean : 34.1959
Gaus Width: 3.2699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.32
Std Dev 3.185
Gaus mean : 40.6900
Gaus Width: 2.9519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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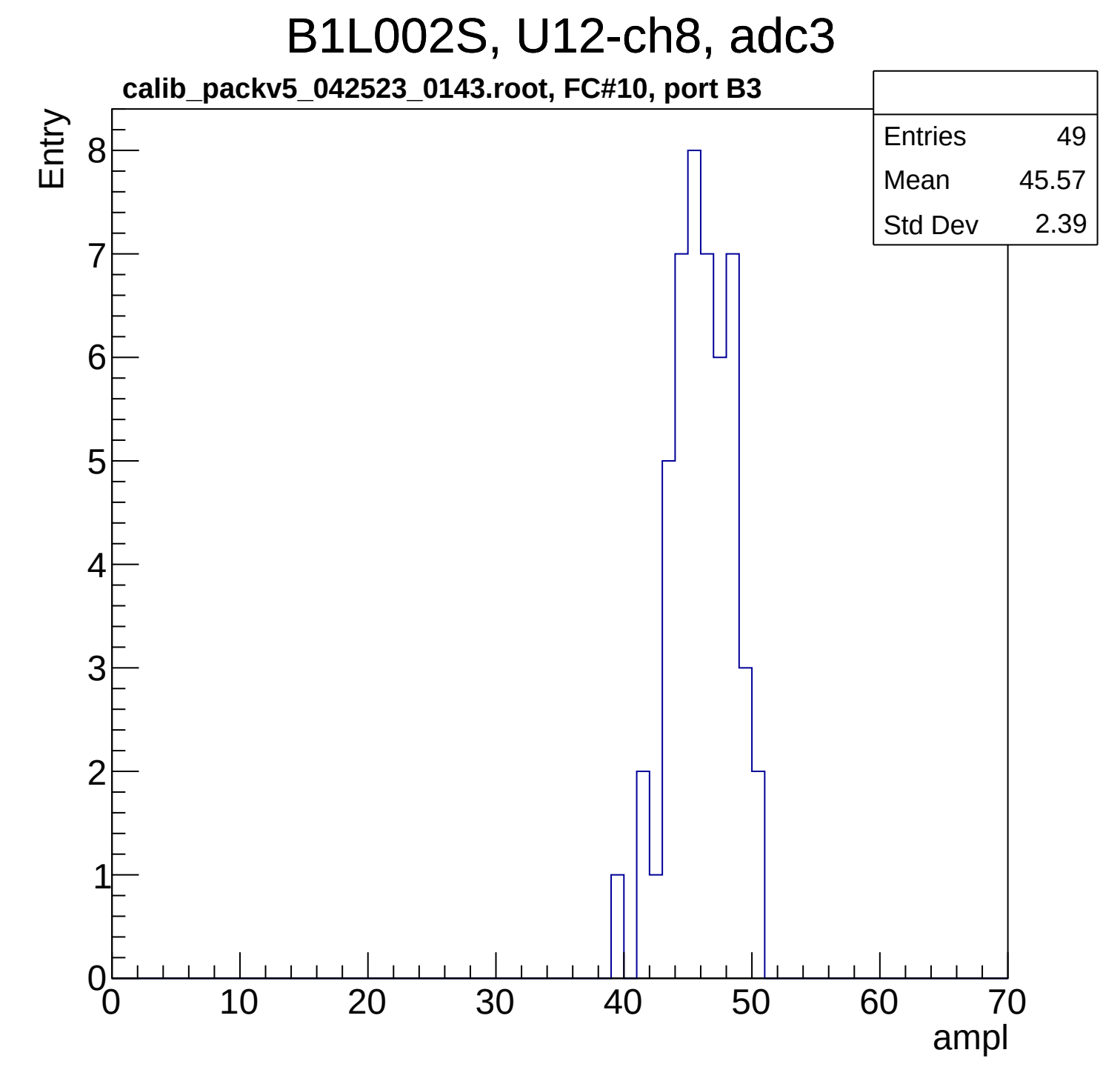
B1L002S, U12-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 45.57
Std Dev 2.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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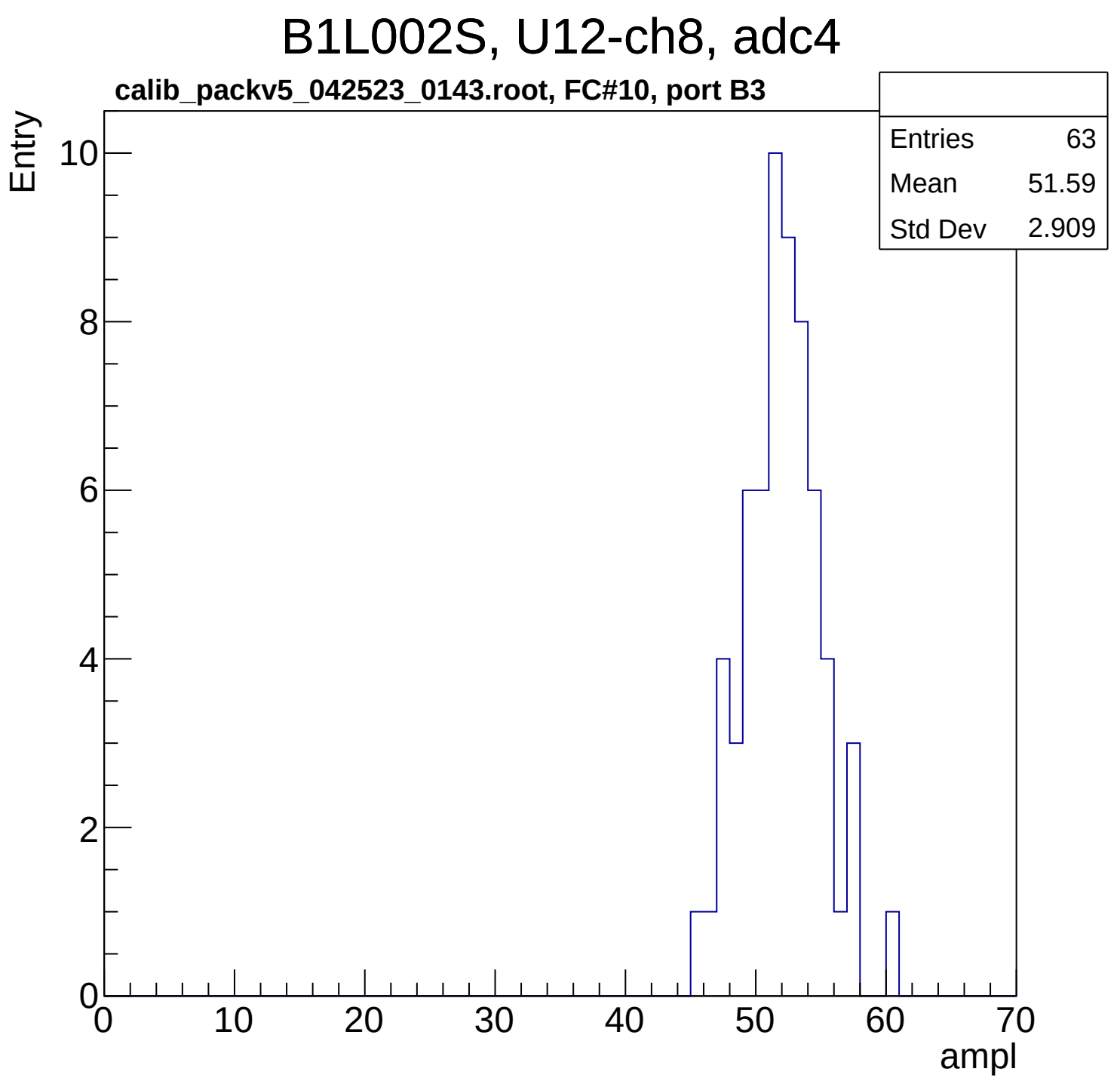
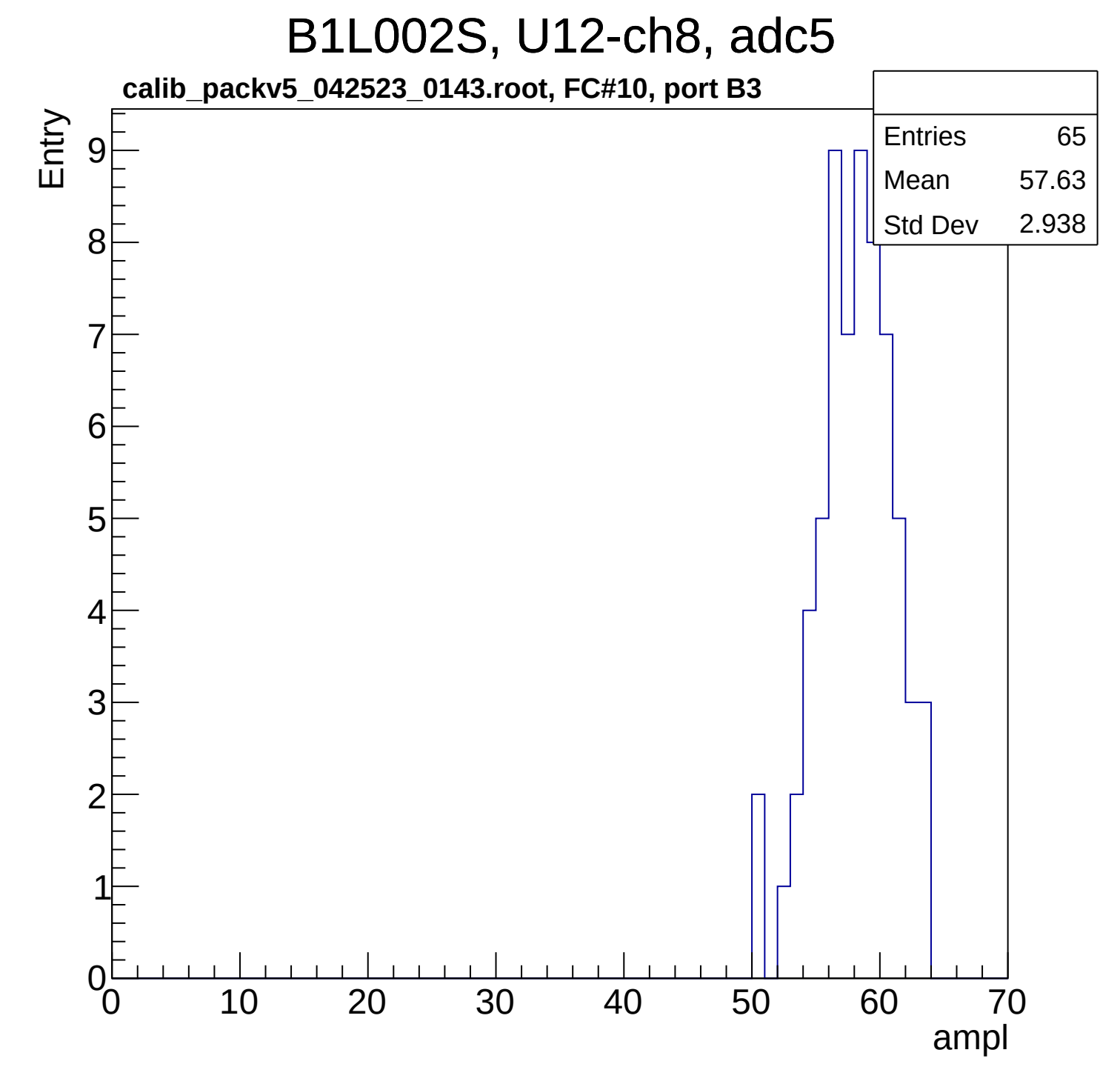
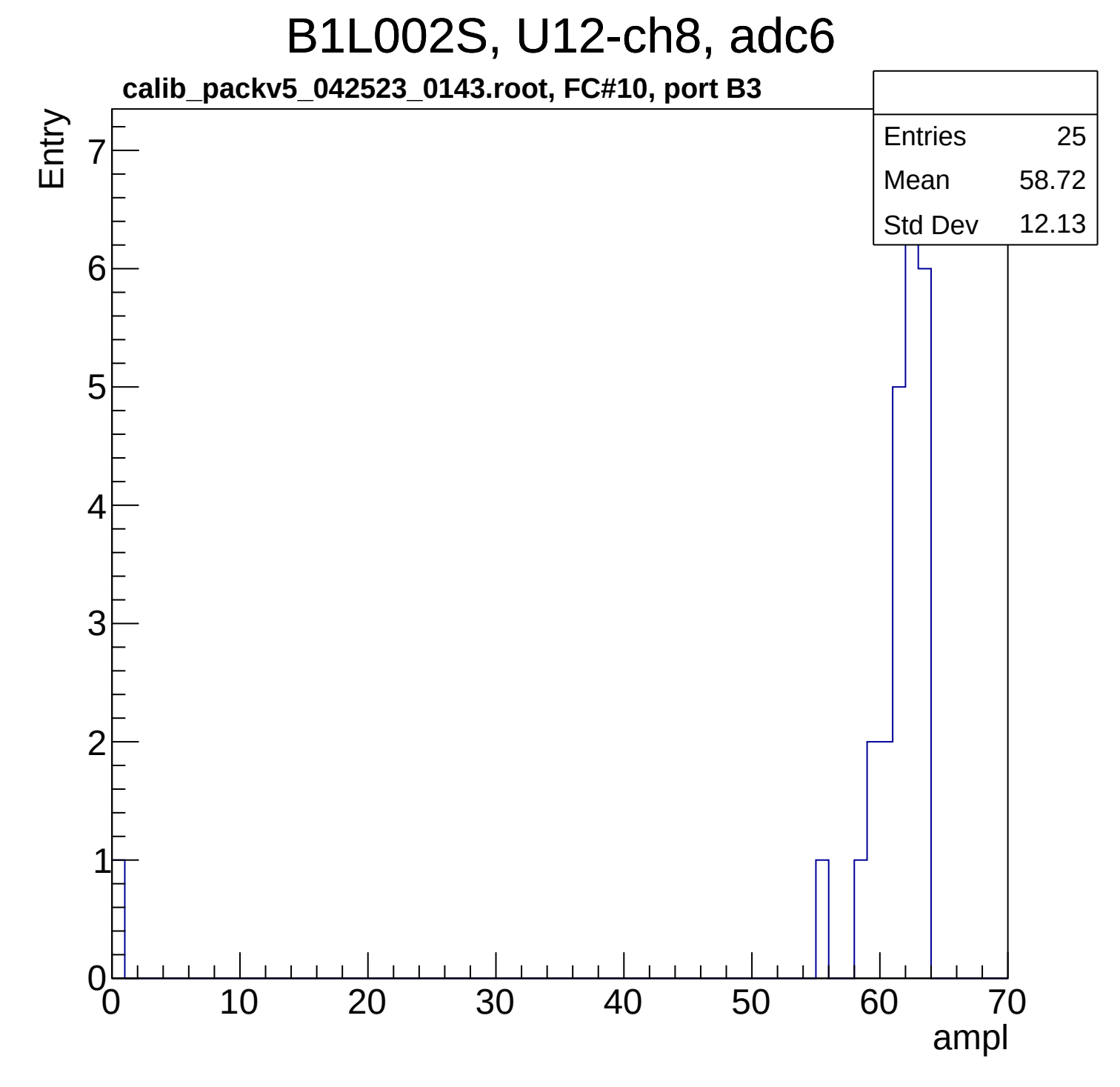
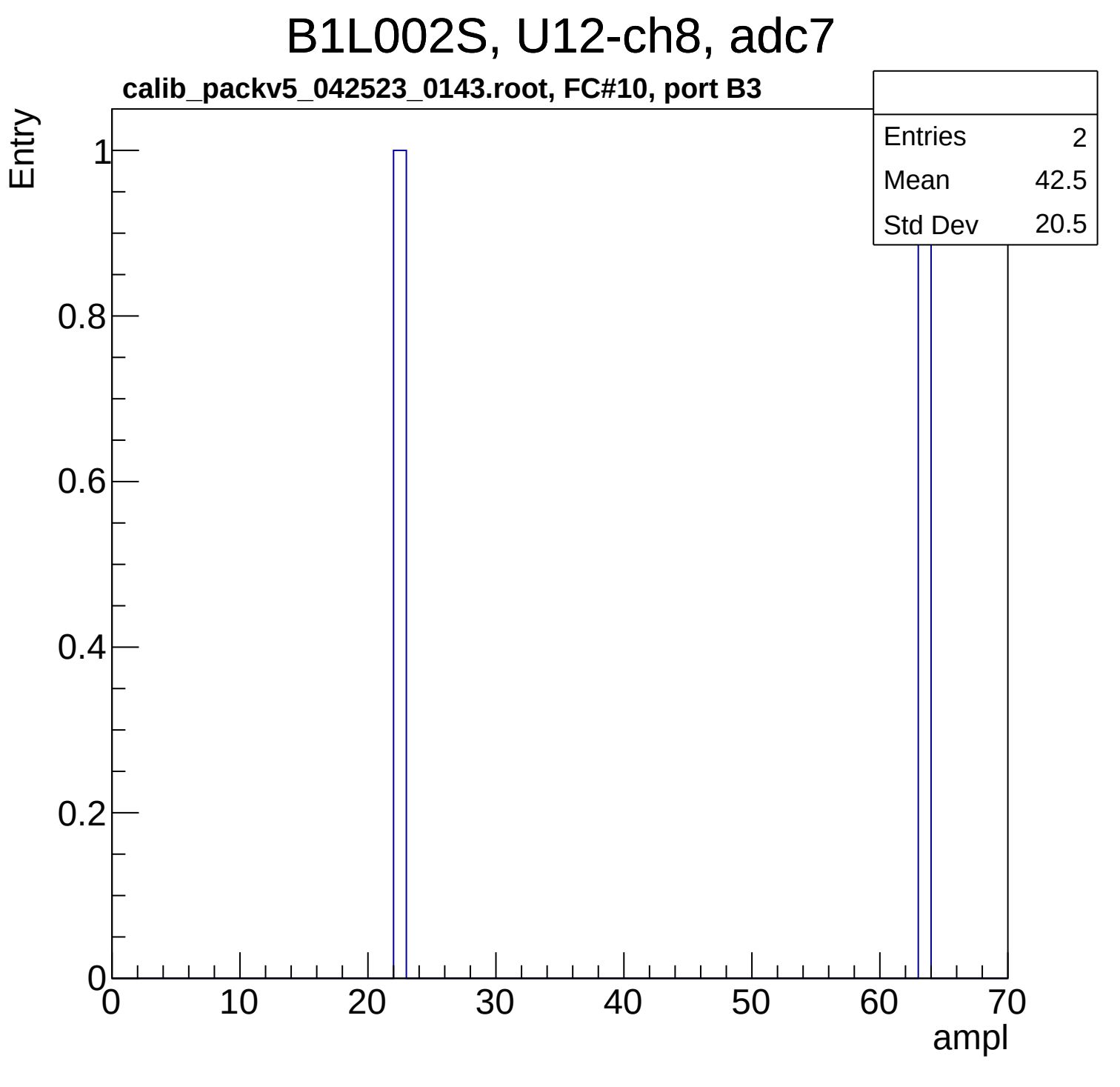
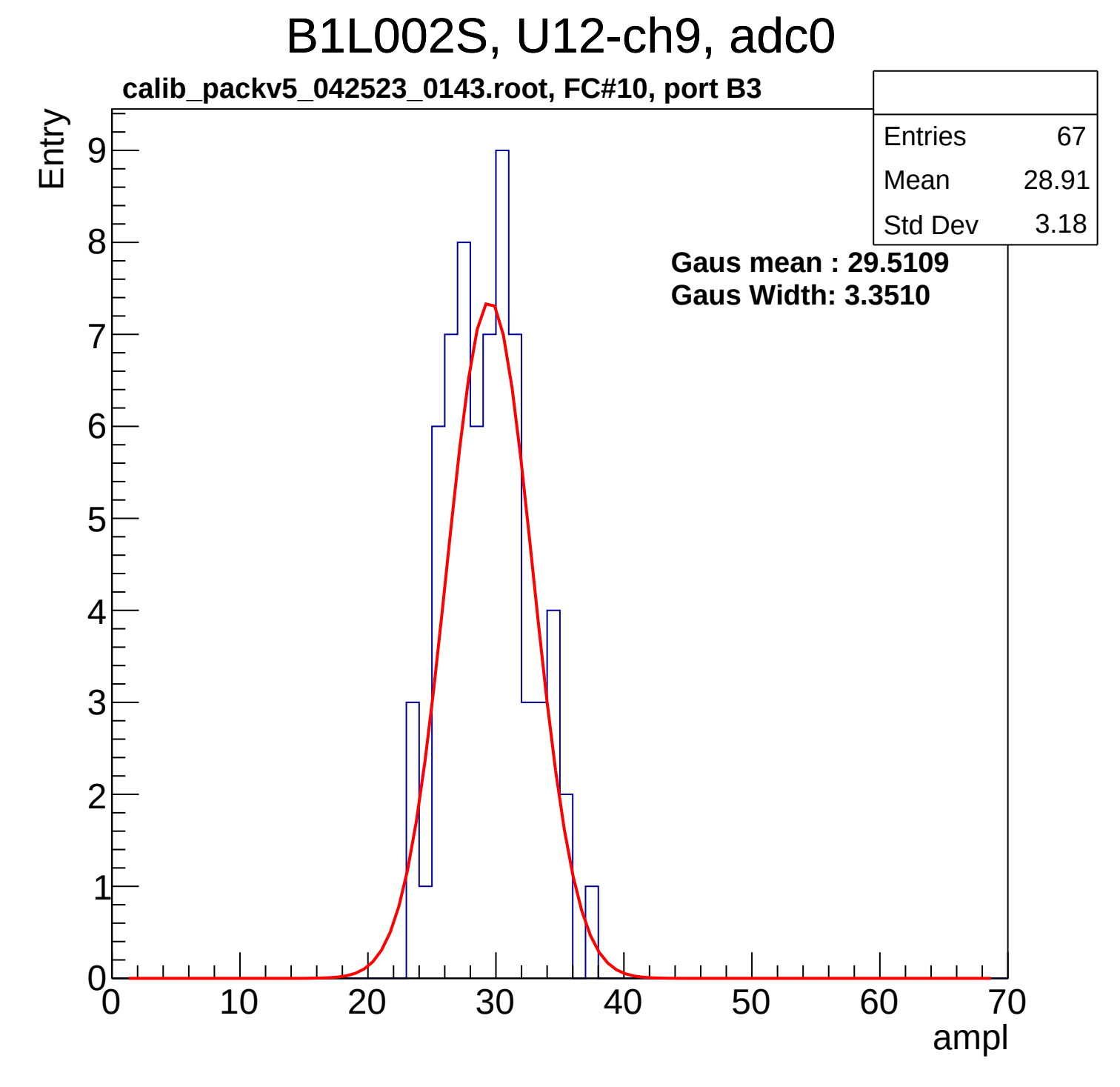
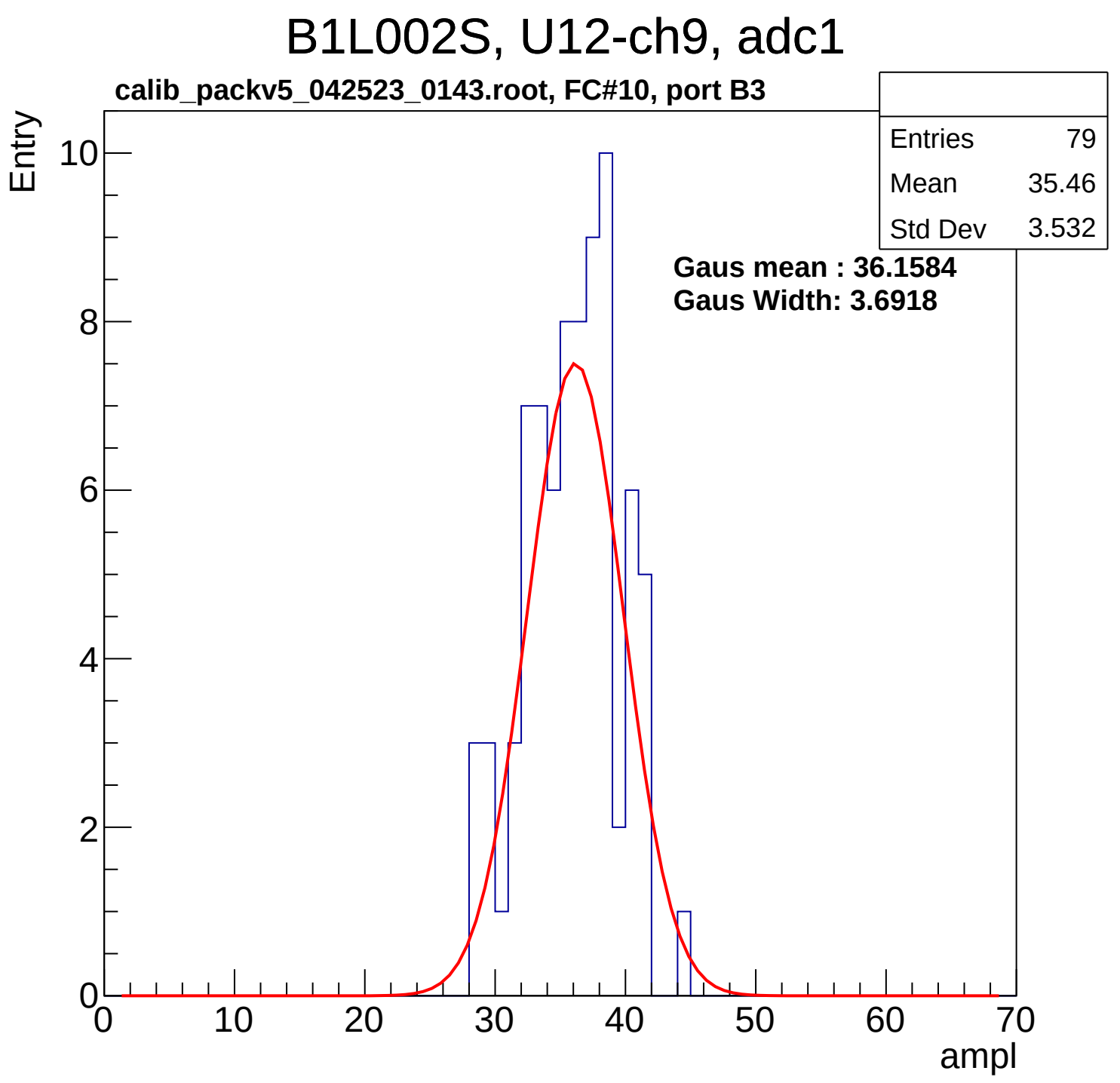

B1L002S, U12-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 51.59
Std Dev 2.909
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

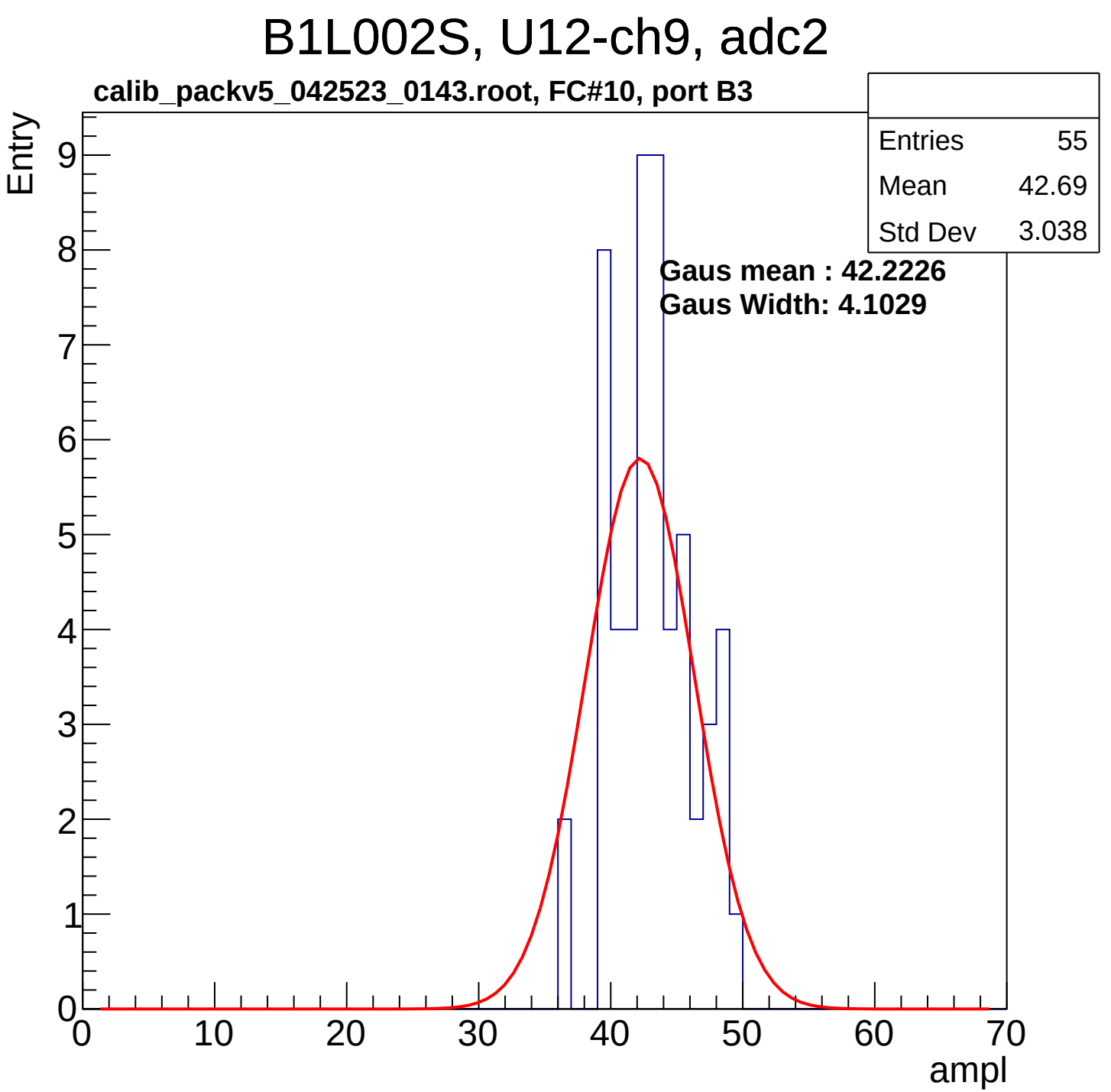

B1L002S, U12-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.63
Std Dev 2.938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

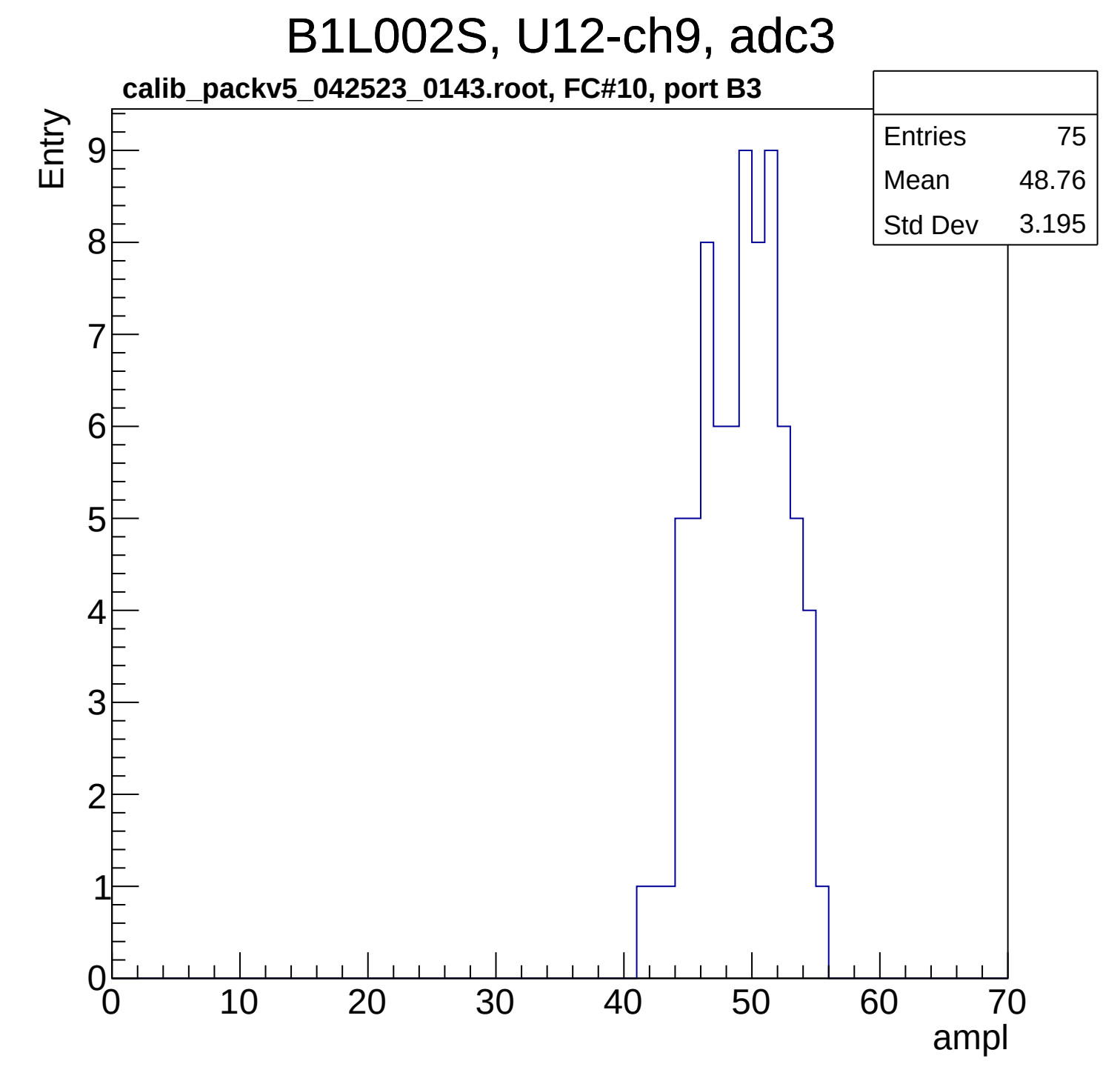

B1L002S, U12-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.72
Std Dev 12.13
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

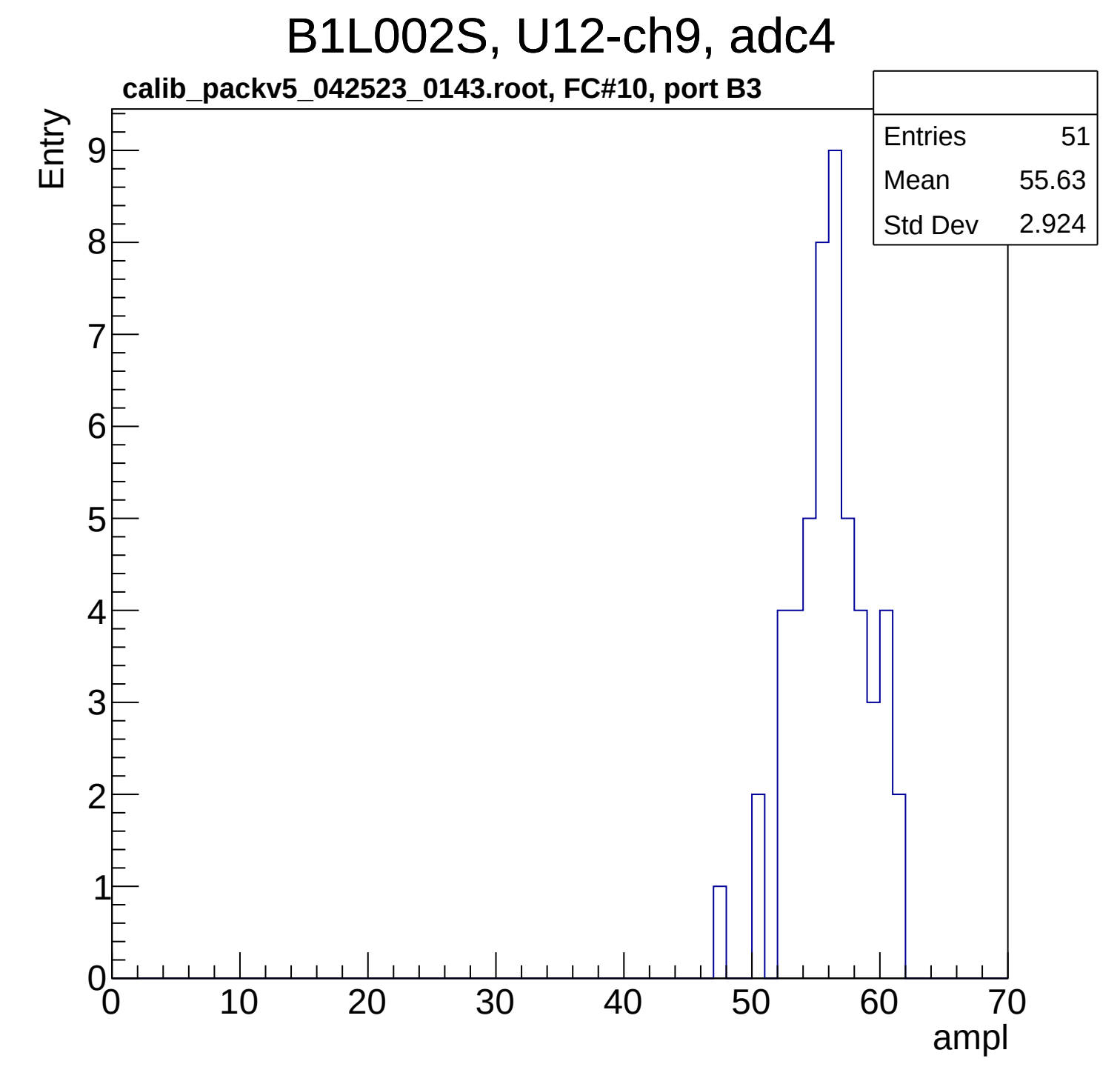

B1L002S, U12-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

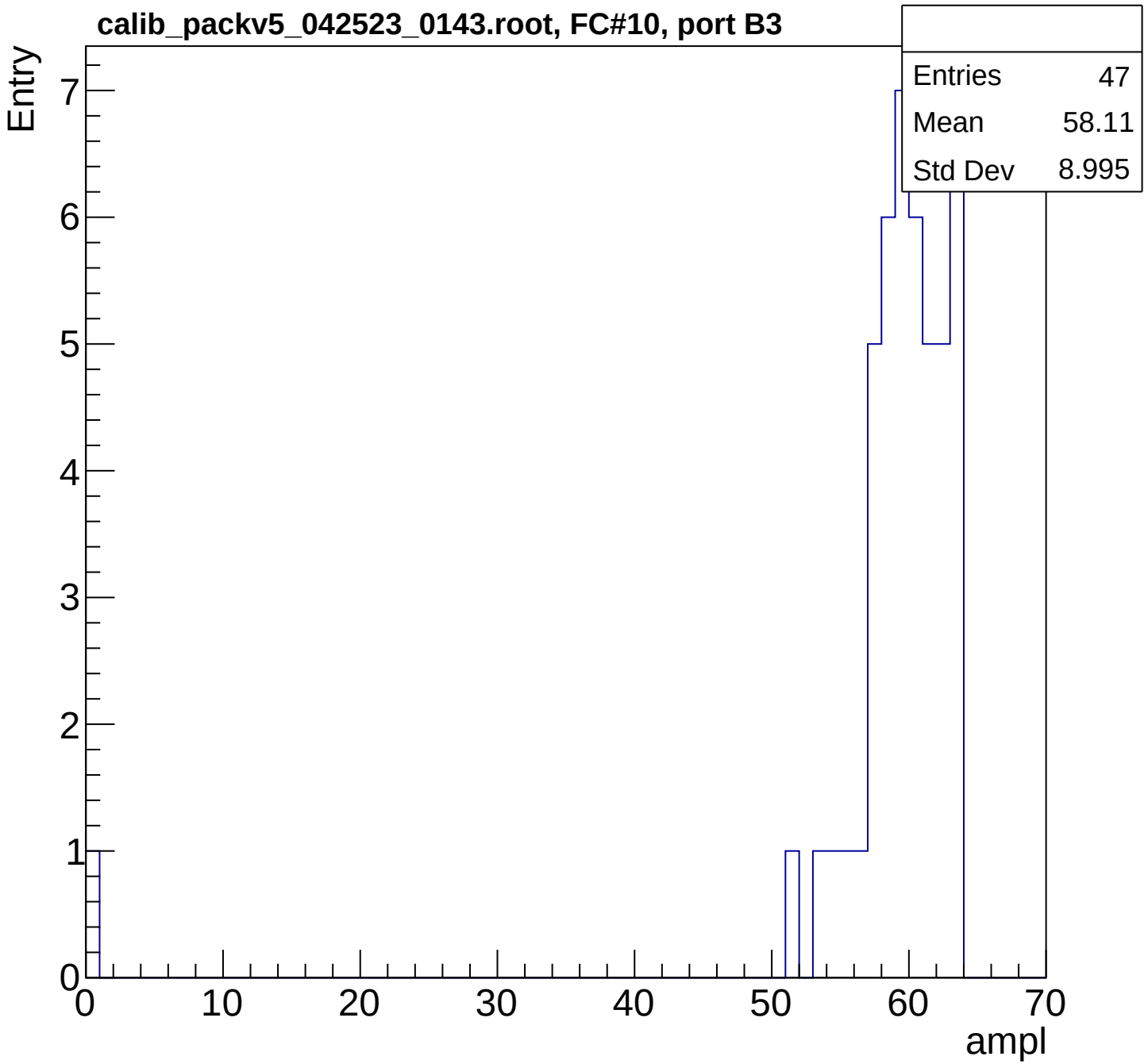

B1L002S, U12-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.91
Std Dev 3.18
Gaus mean : 29.5109
Gaus Width: 3.3510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

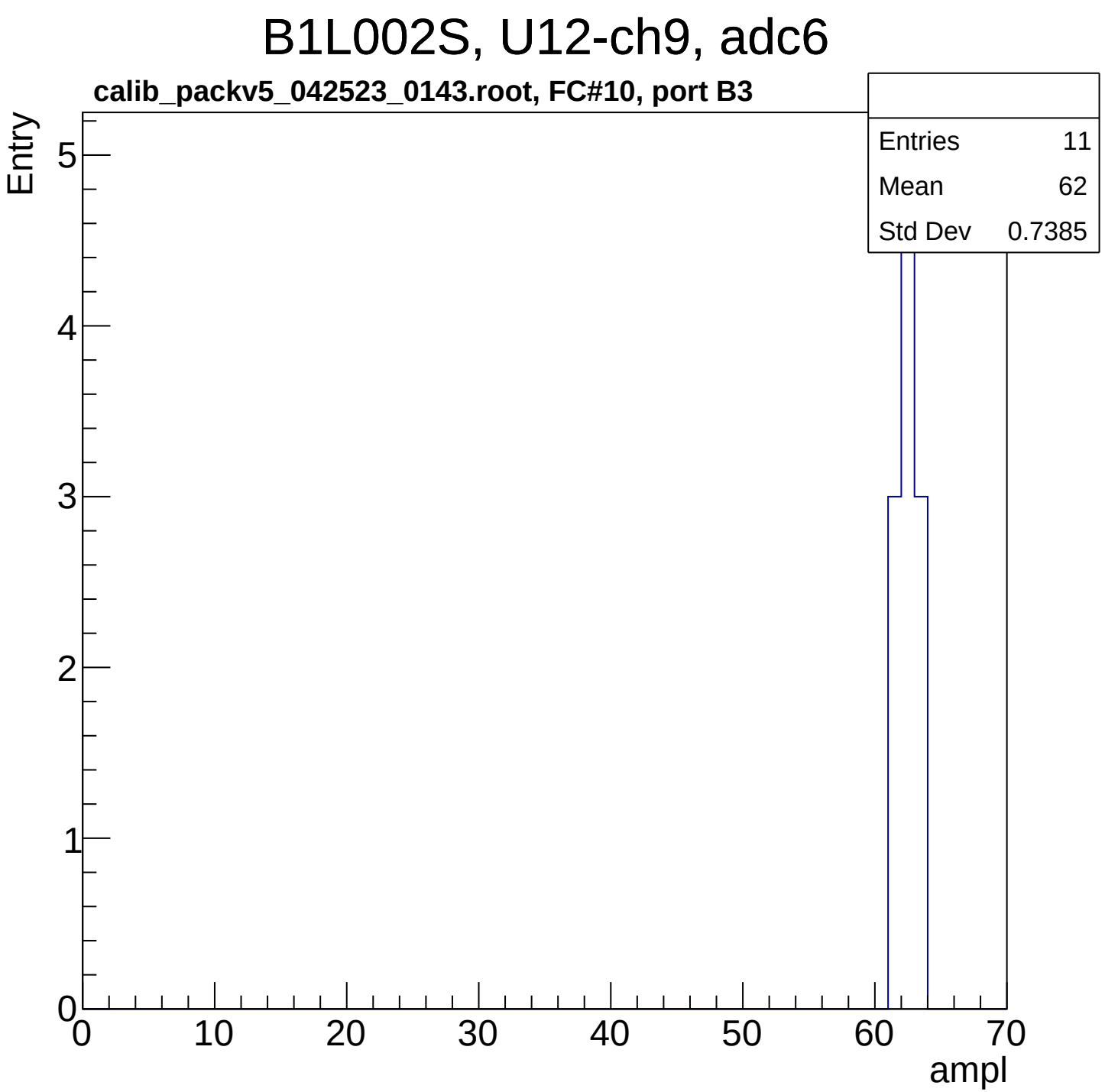

B1L002S, U12-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.46
Std Dev 3.532
Gaus mean : 36.1584
Gaus Width: 3.6918
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

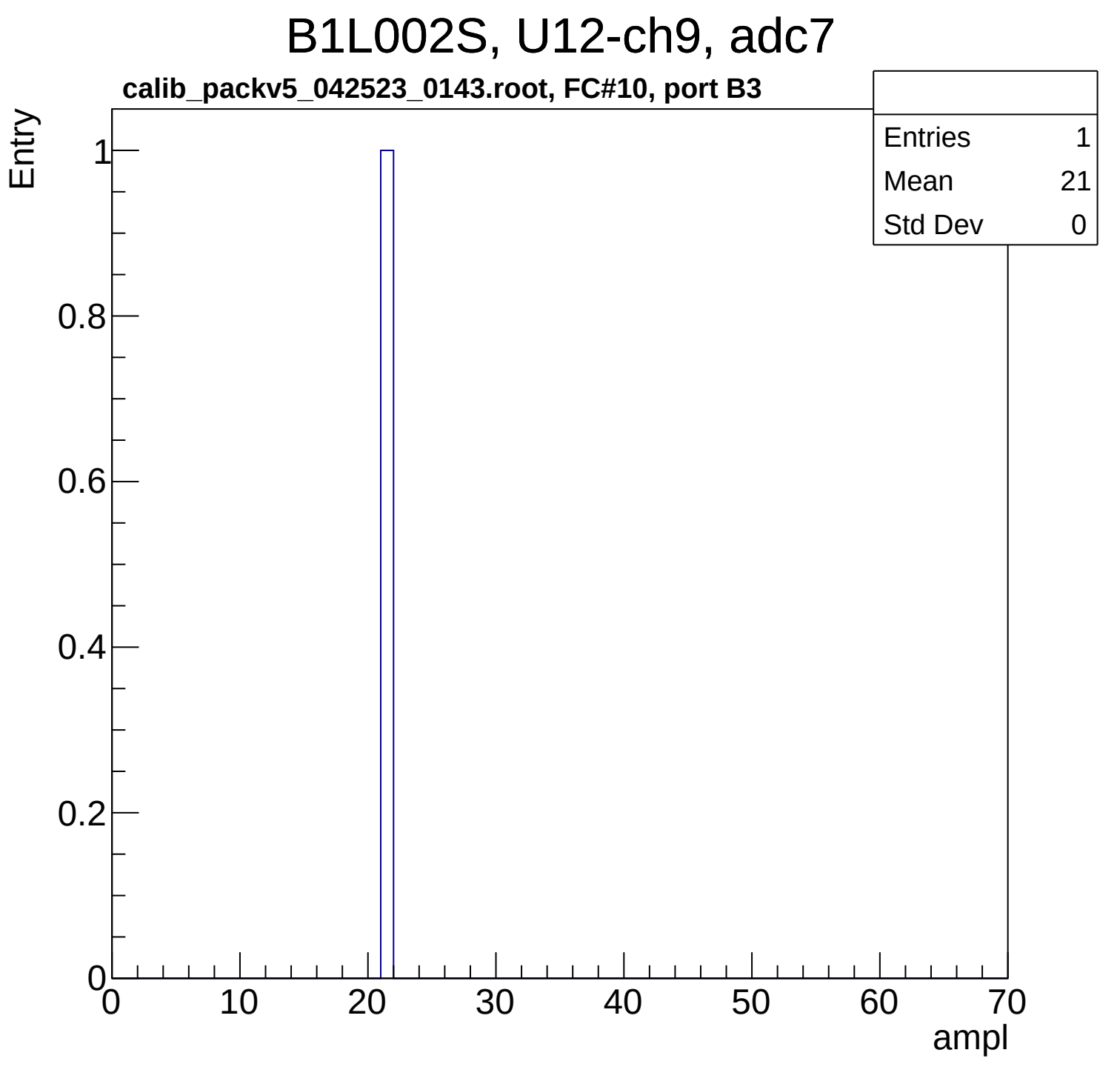

B1L002S, U12-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 42.69
Std Dev 3.038
Gaus mean : 42.2226
Gaus Width: 4.1029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

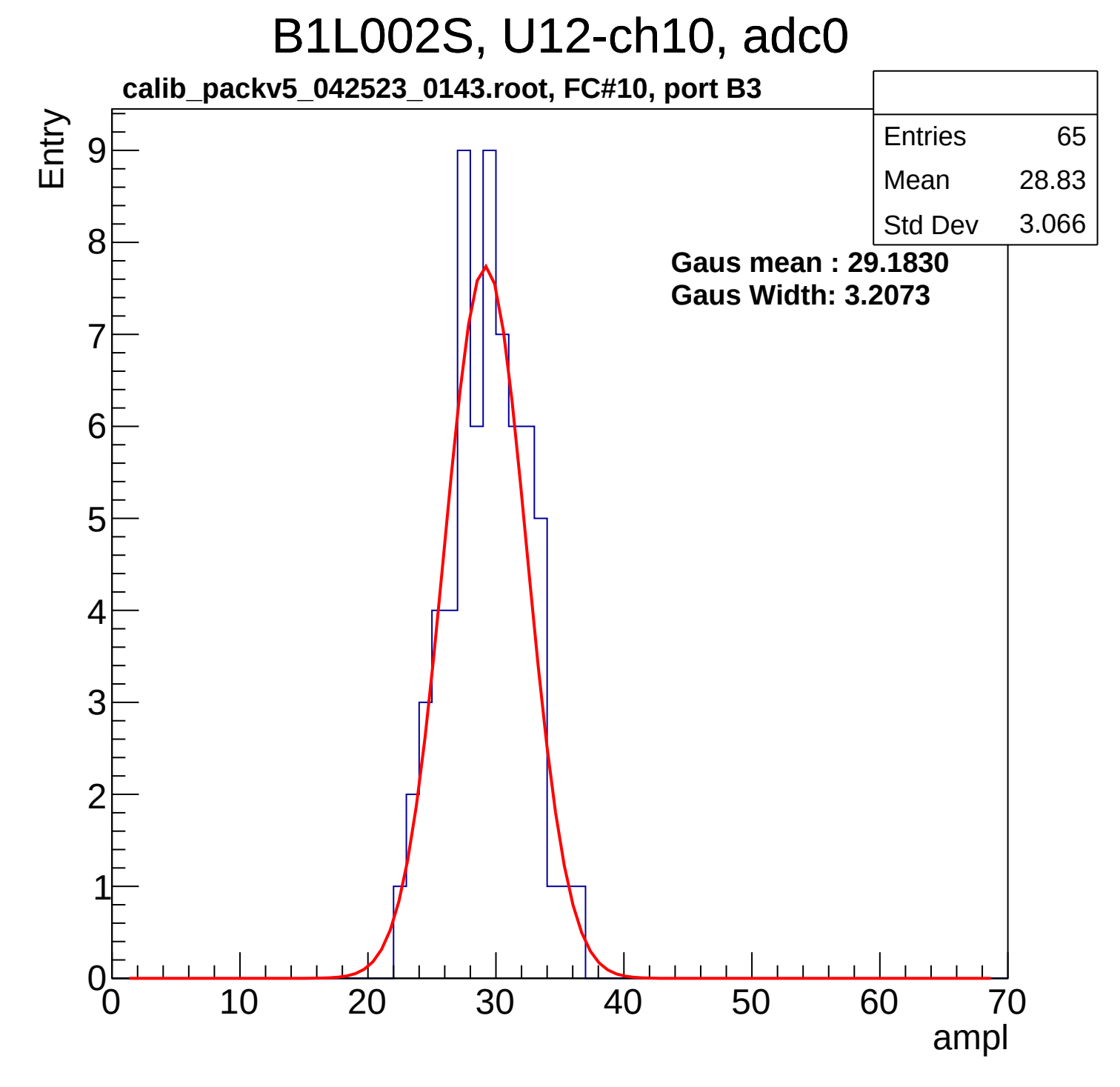

B1L002S, U12-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 48.76
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

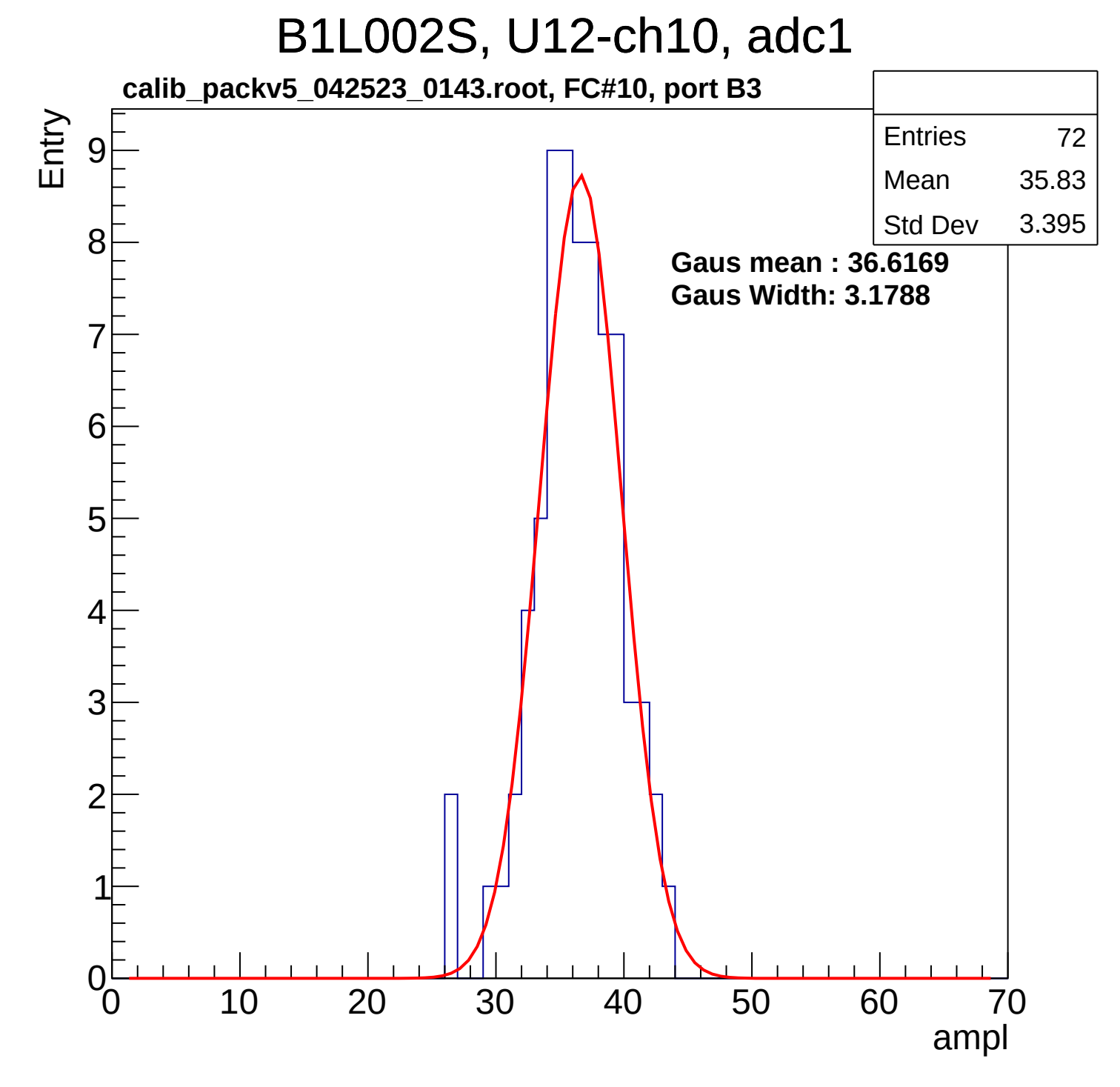

B1L002S, U12-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 55.63
Std Dev 2.924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

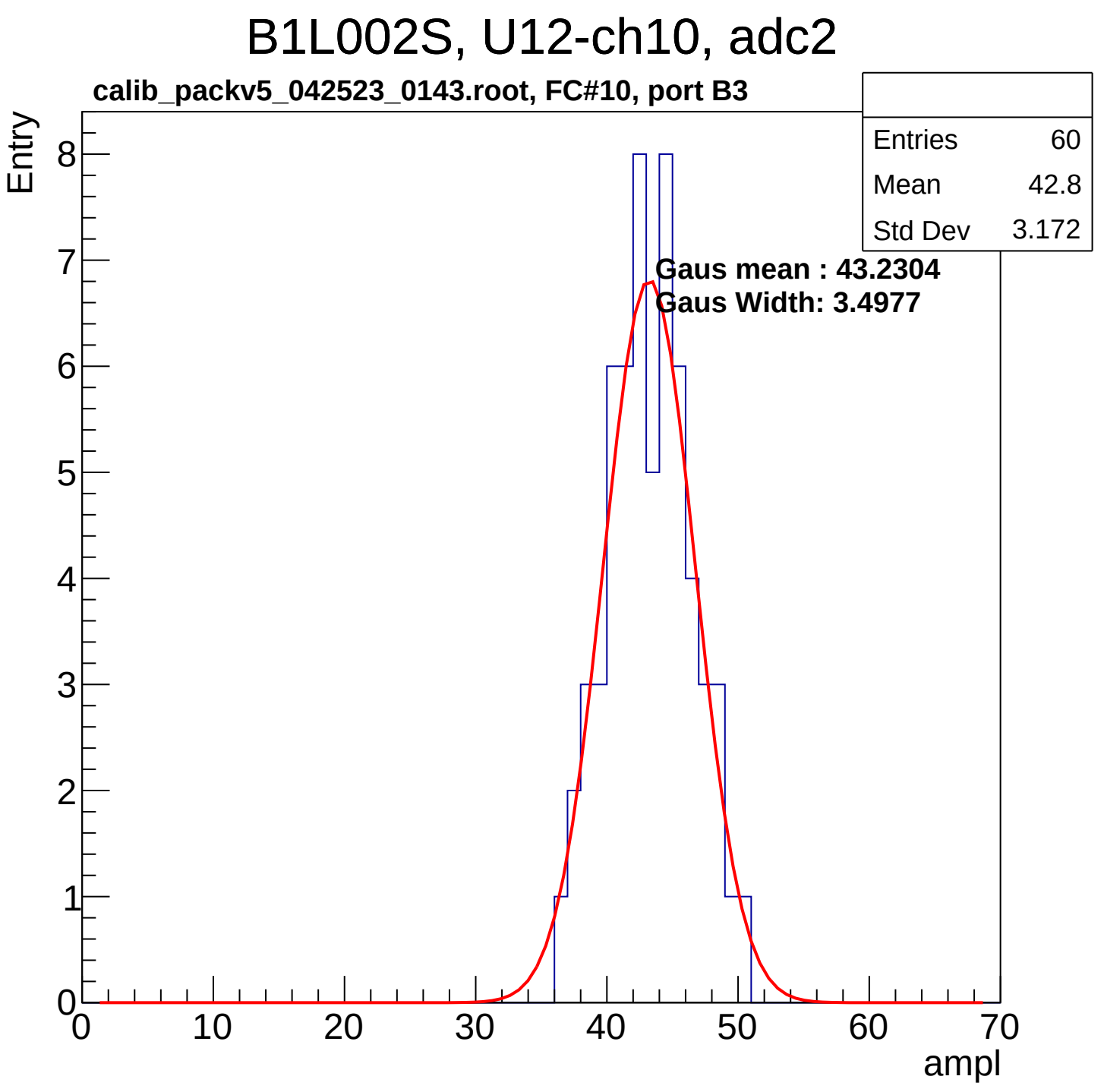

B1L002S, U12-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.11
Std Dev 8.995
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

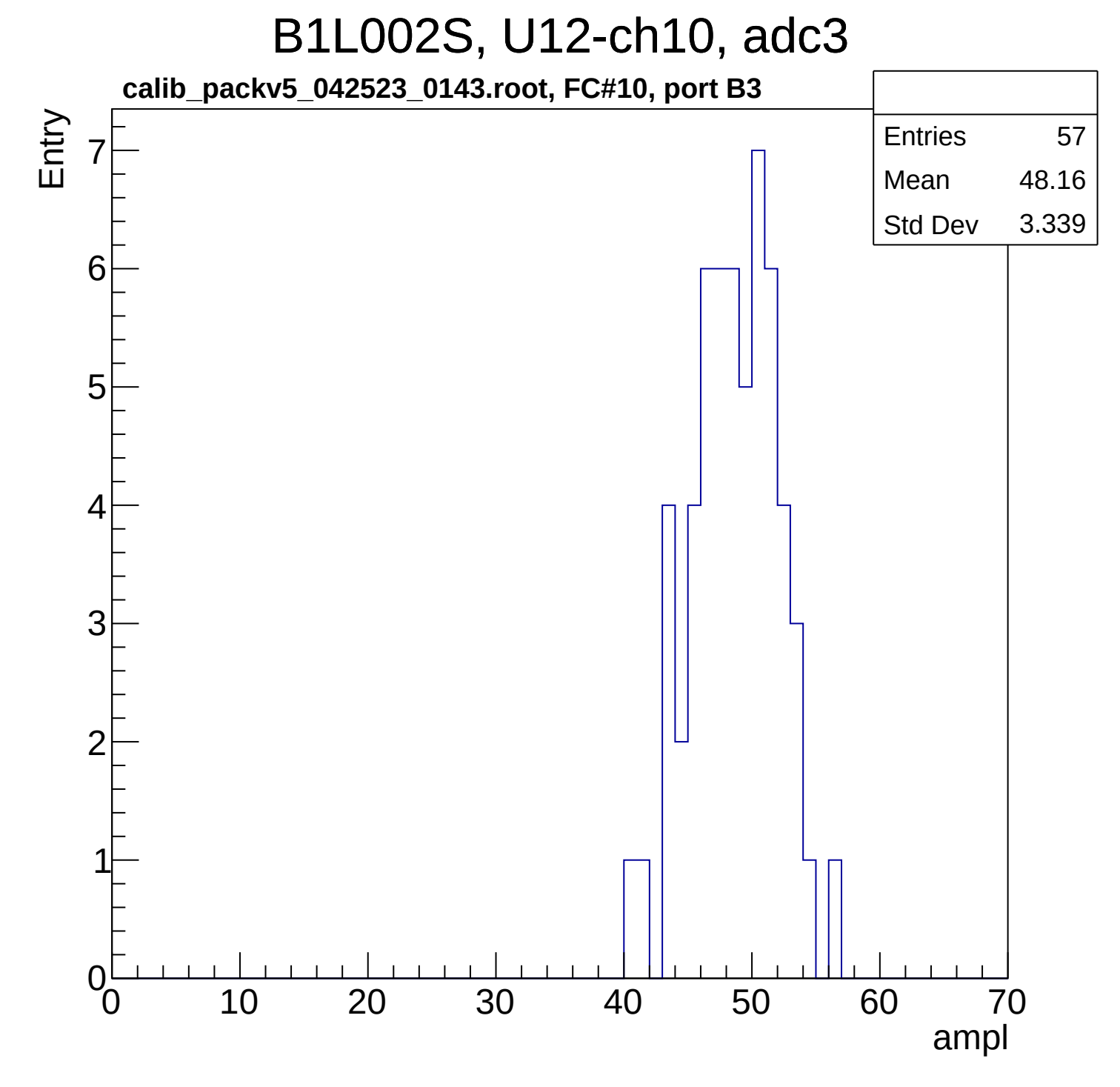

B1L002S, U12-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62
Std Dev 0.7385
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

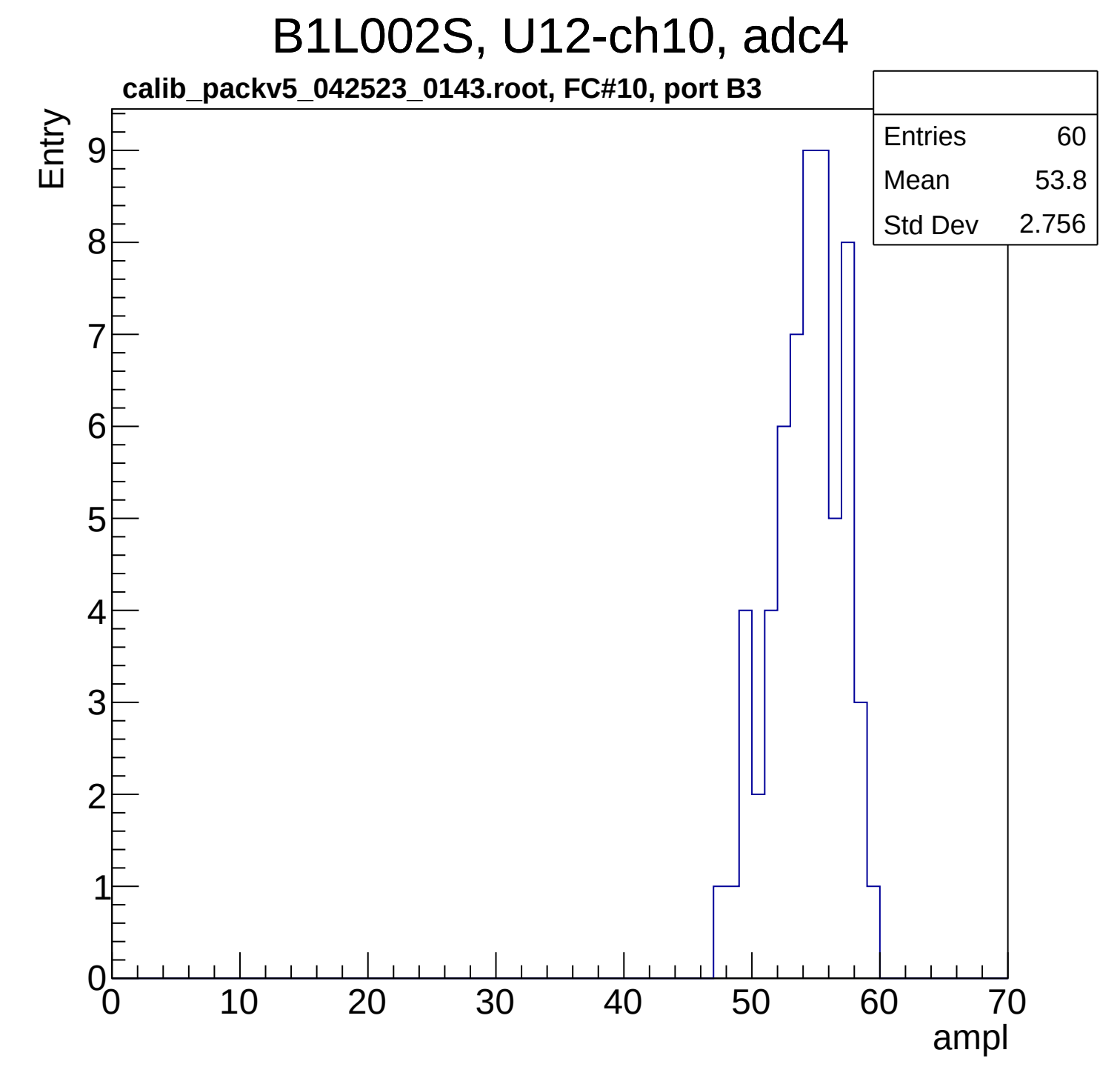

B1L002S, U12-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.83
Std Dev 3.066
Gaus mean : 29.1830
Gaus Width: 3.2073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.83
Std Dev 3.395
Gaus mean : 36.6169
Gaus Width: 3.1788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.8
Std Dev 3.172
Gaus mean : 43.2304
Gaus Width: 3.4977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.16
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.8
Std Dev 2.756
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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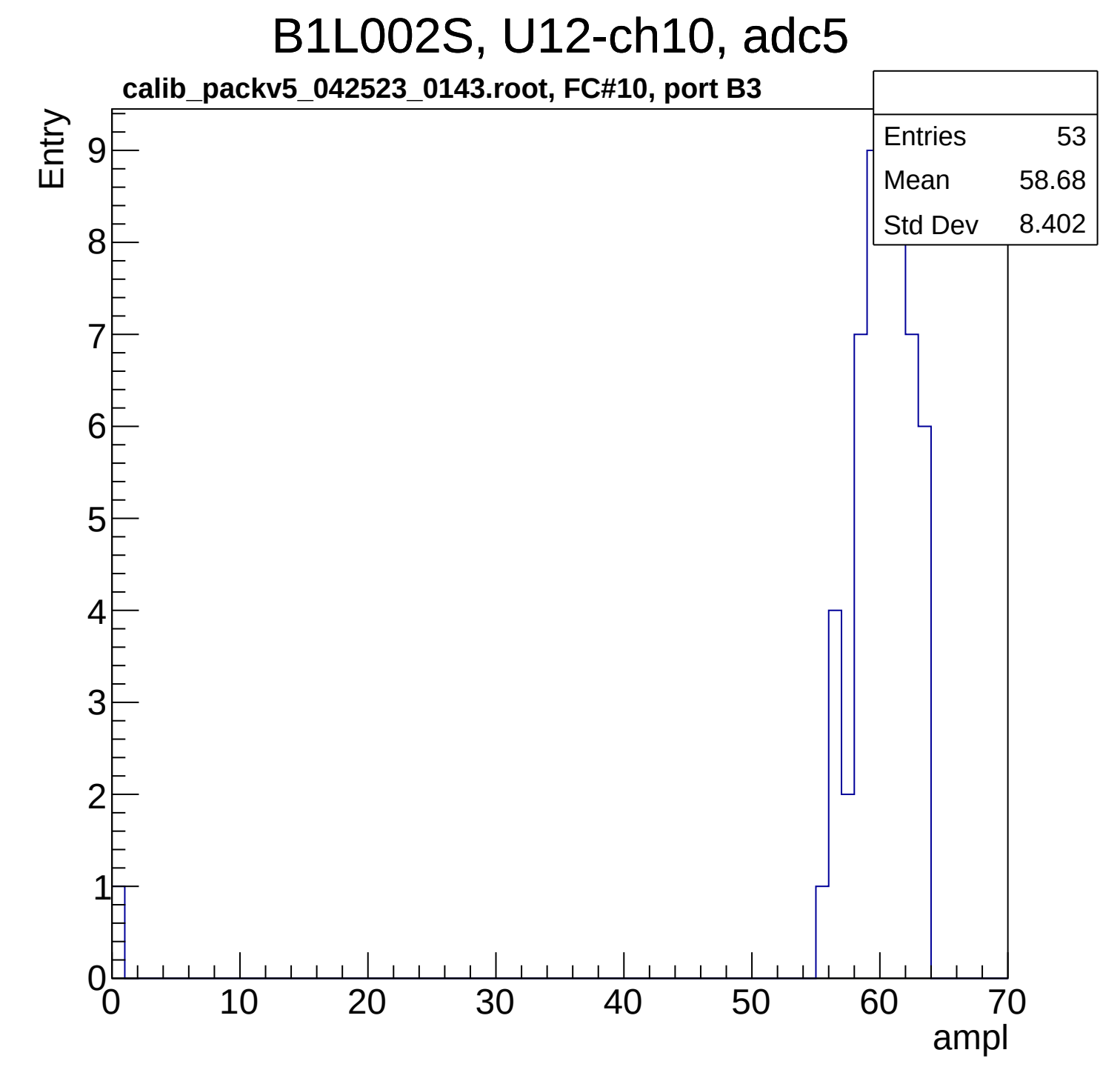
40
50
60
70


B1L002S, U12-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.68
Std Dev 8.402
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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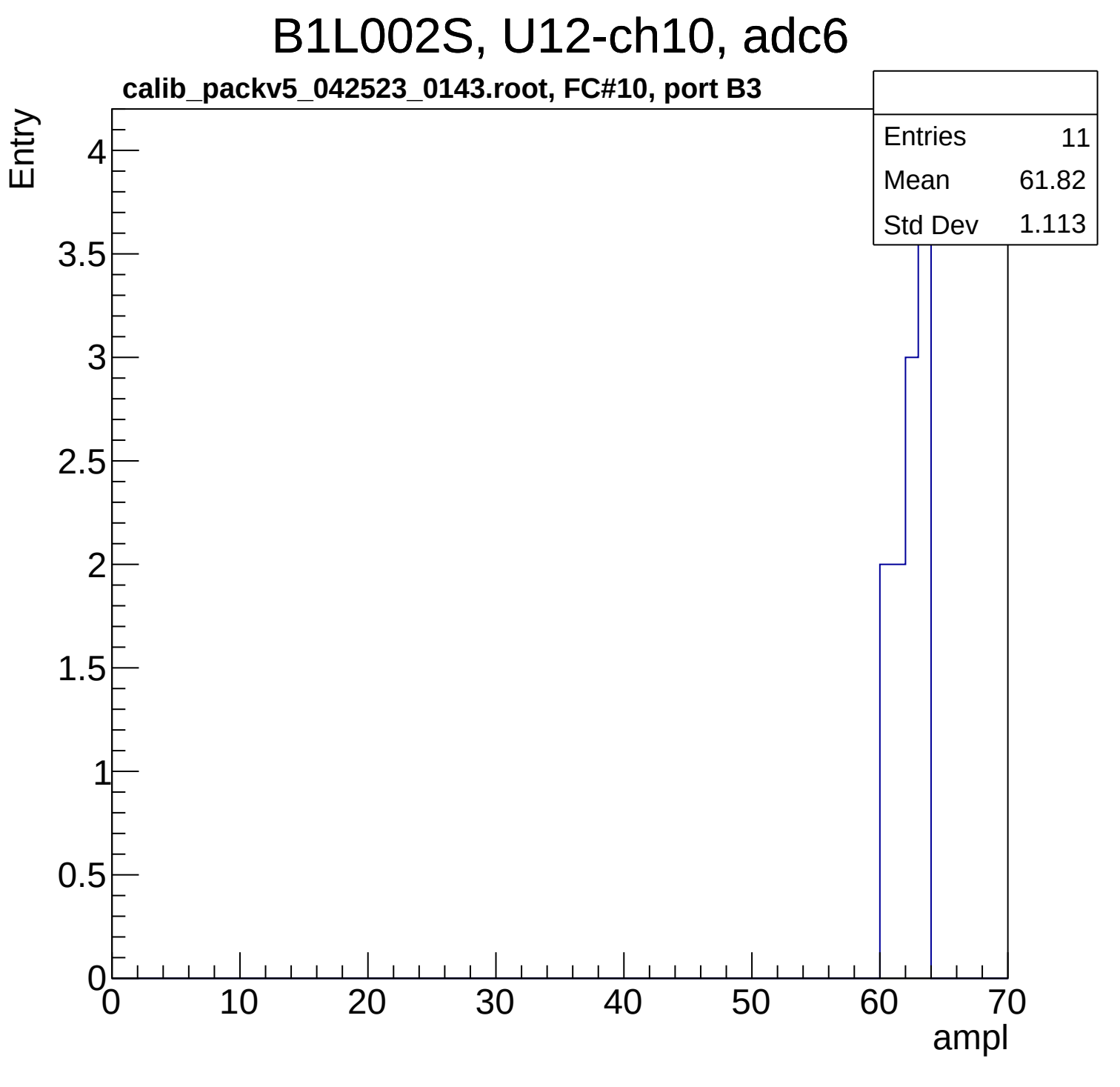
B1L002S, U12-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.82
Std Dev 1.113
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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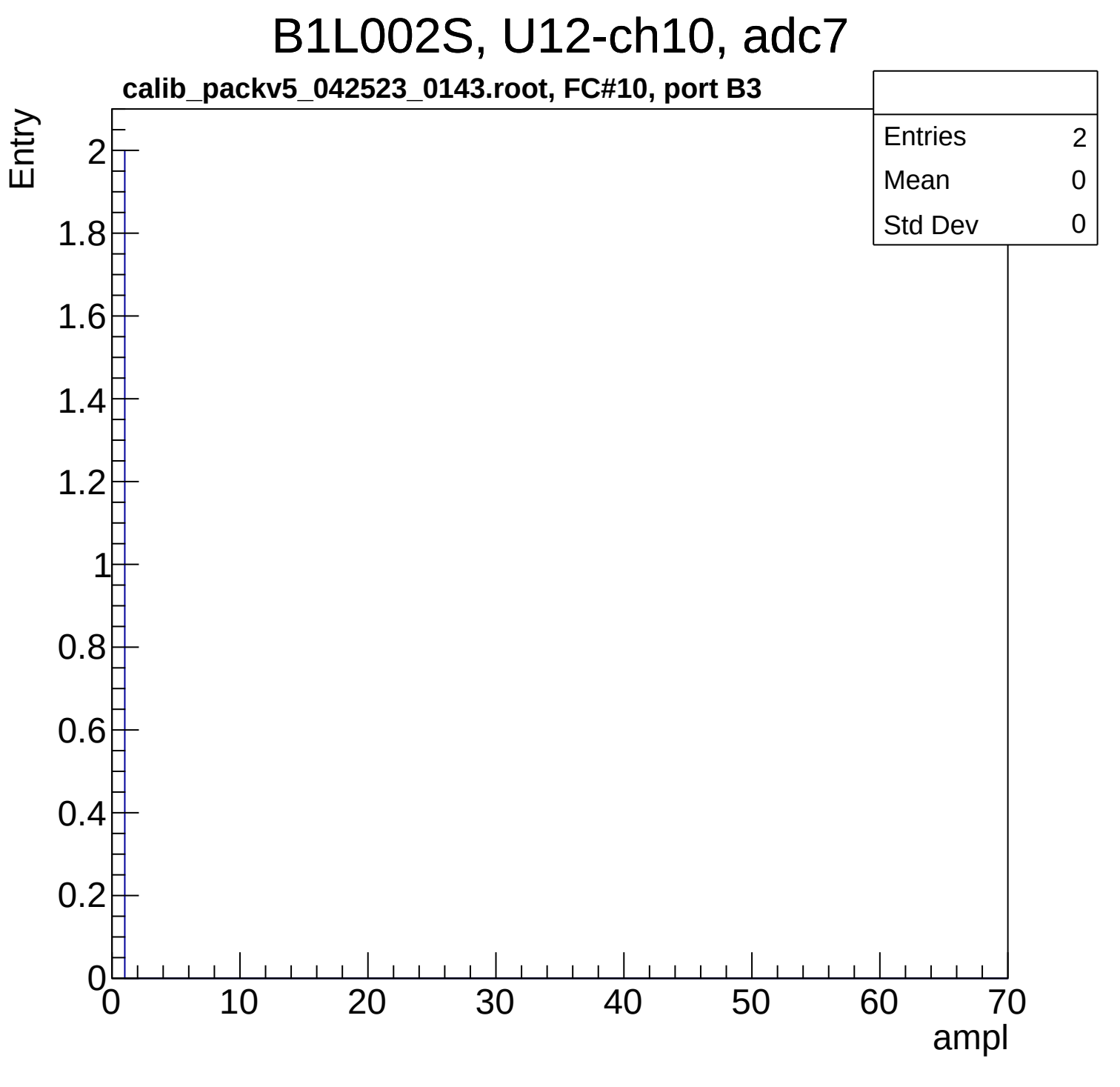
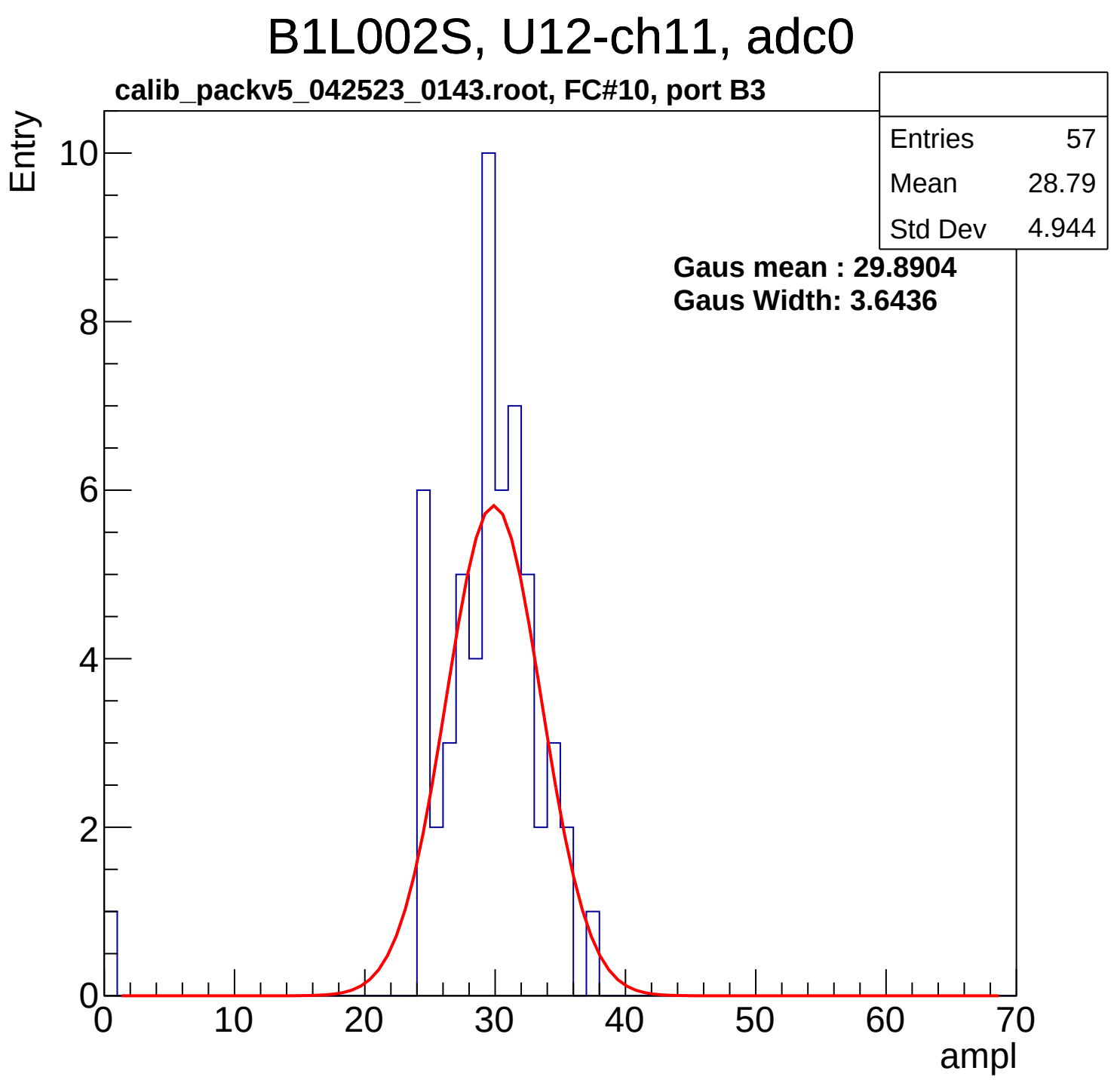
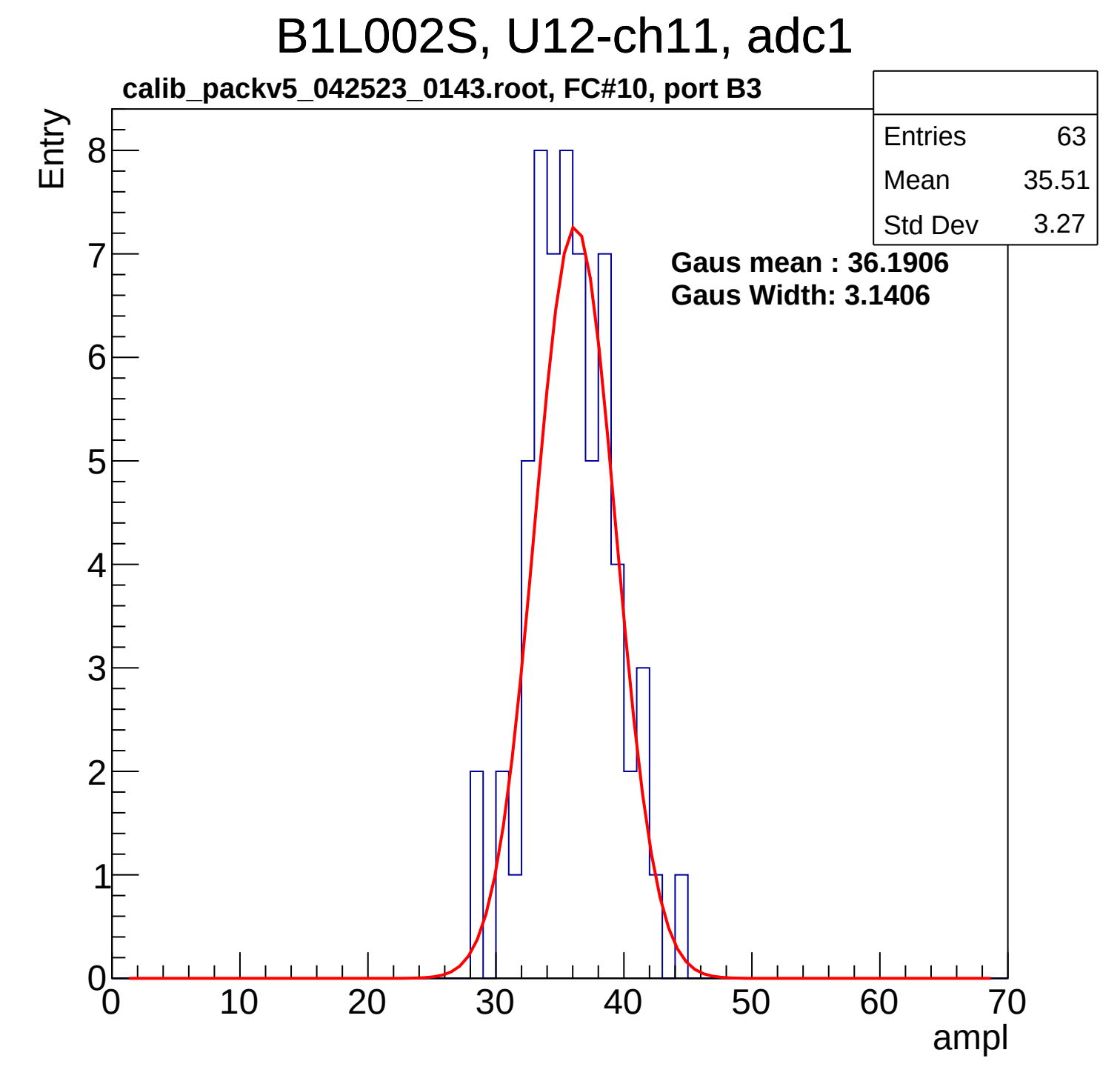

B1L002S, U12-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 28.79
Std Dev 4.944
Gaus mean : 29.8904
Gaus Width: 3.6436
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.51
Std Dev 3.27
Gaus mean : 36.1906
Gaus Width: 3.1406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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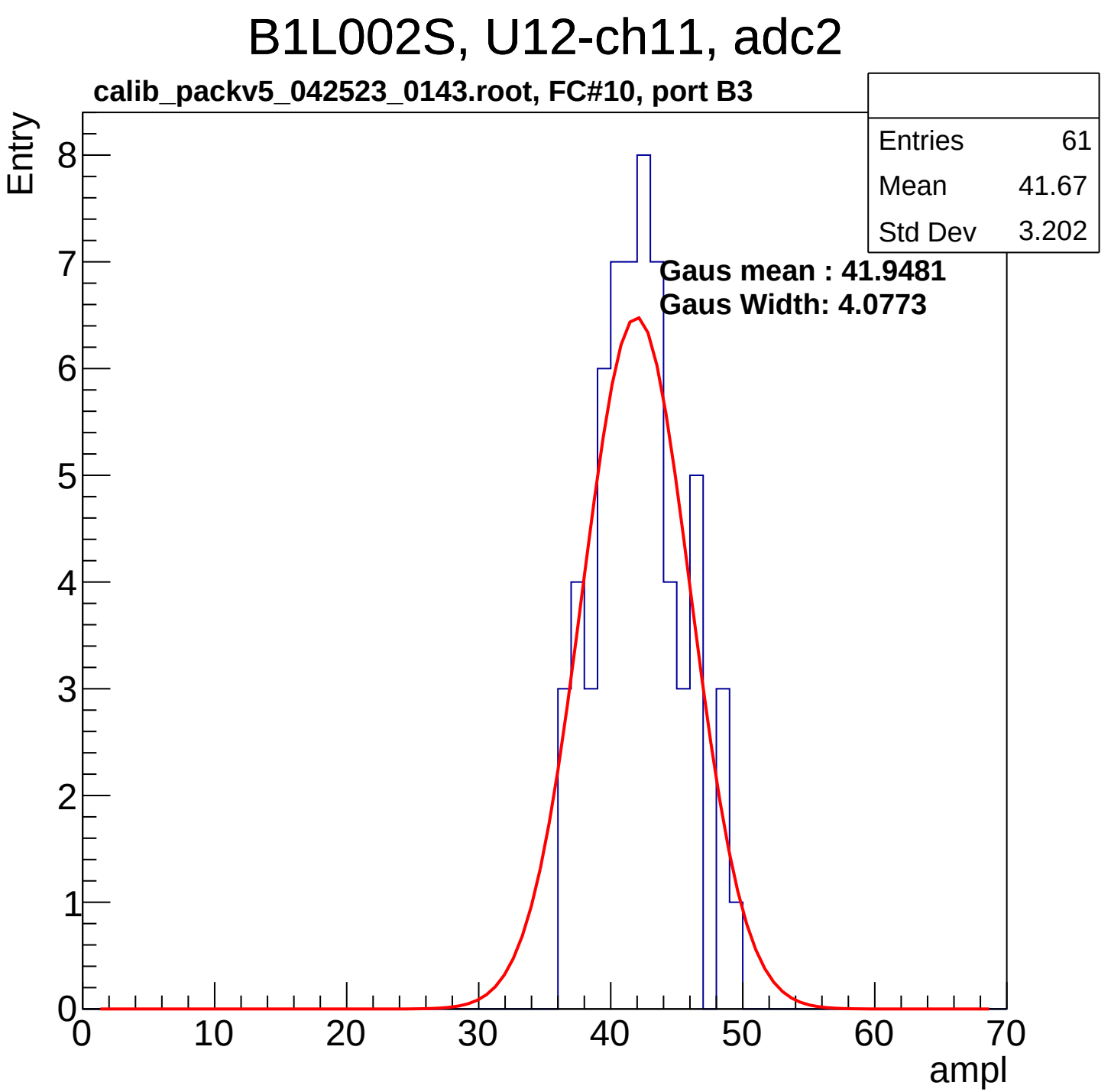
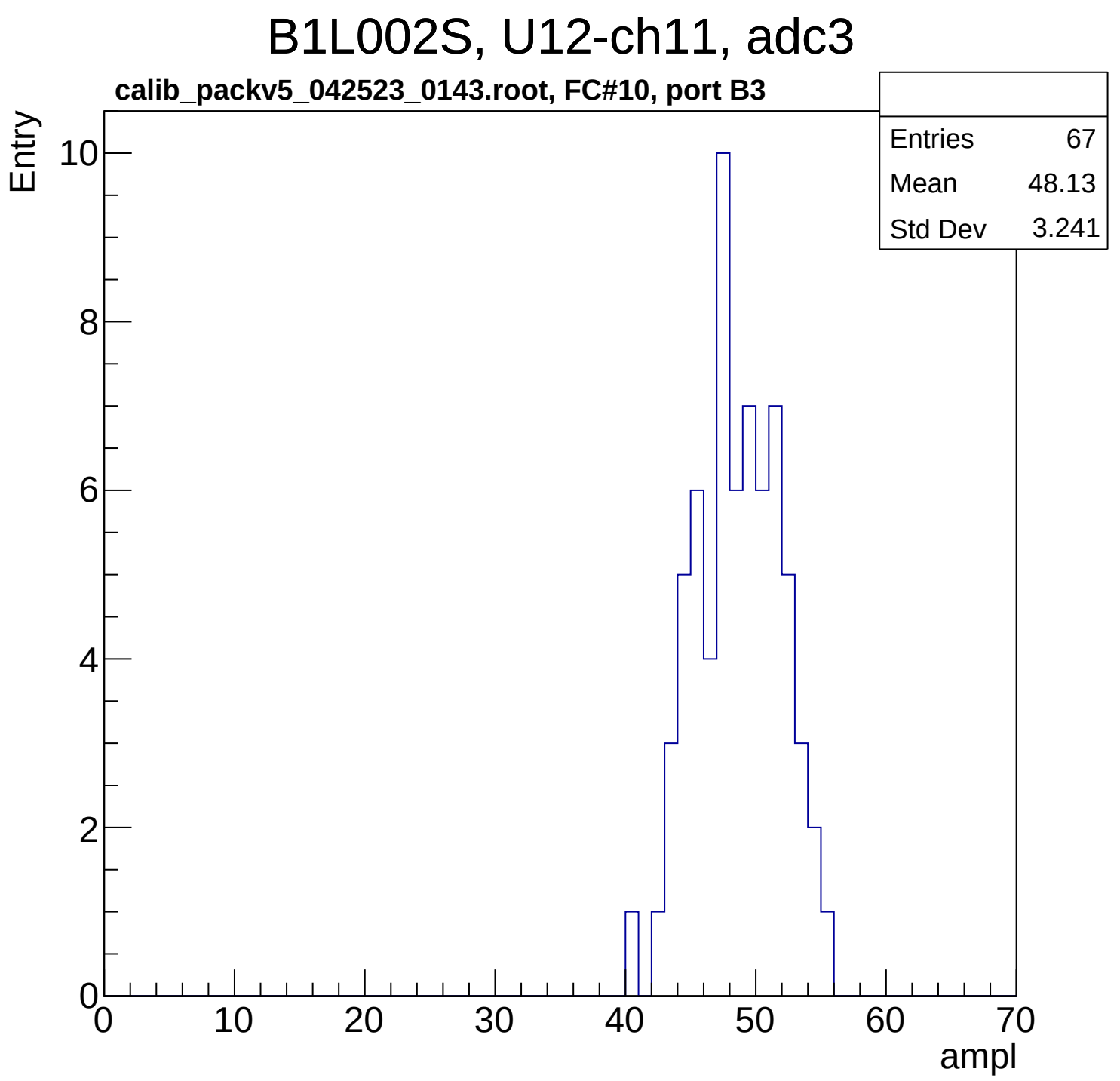

B1L002S, U12-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.67
Std Dev 3.202
Gaus mean : 41.9481
Gaus Width: 4.0773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.13
Std Dev 3.241
Entry
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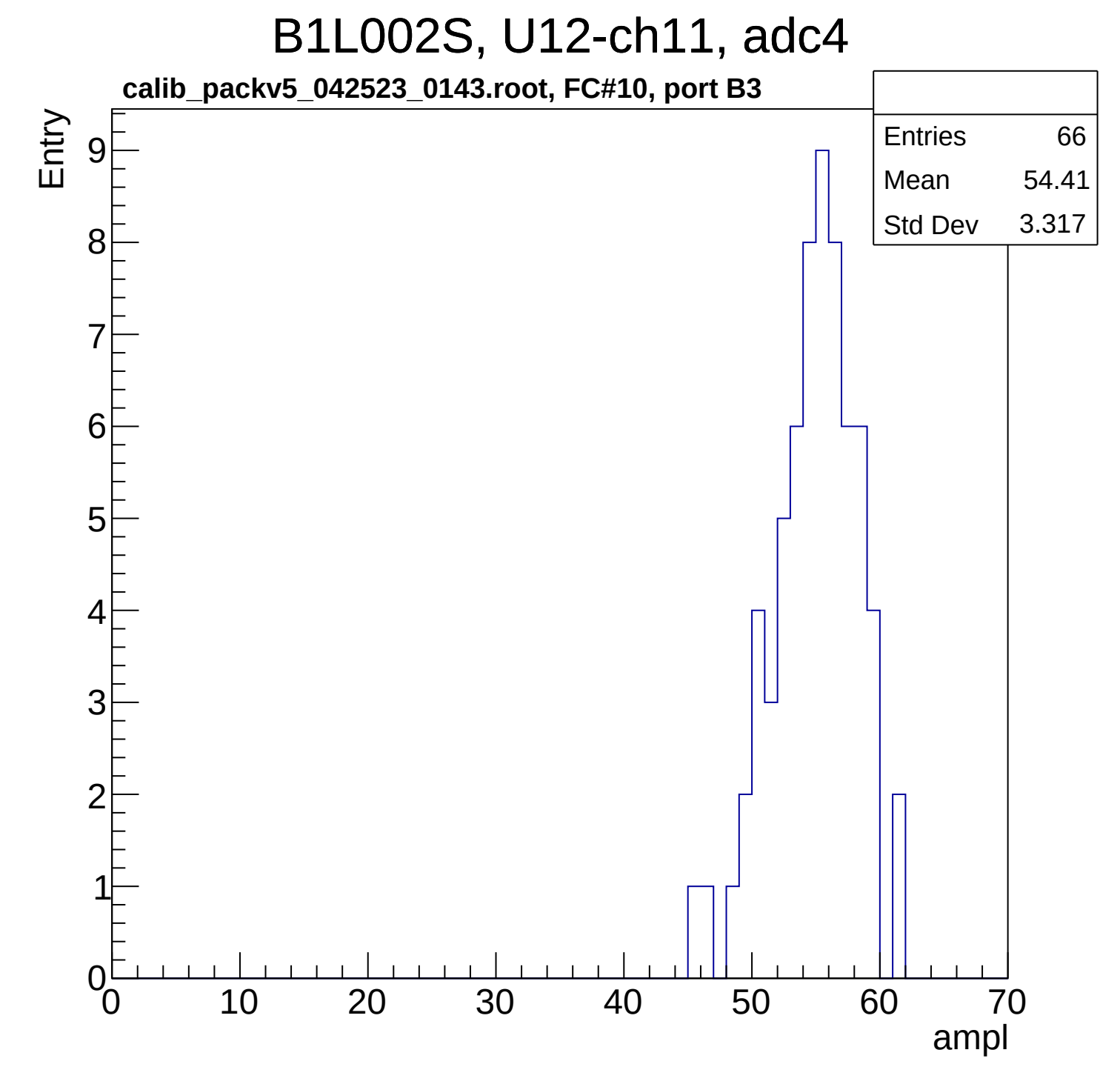
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.41
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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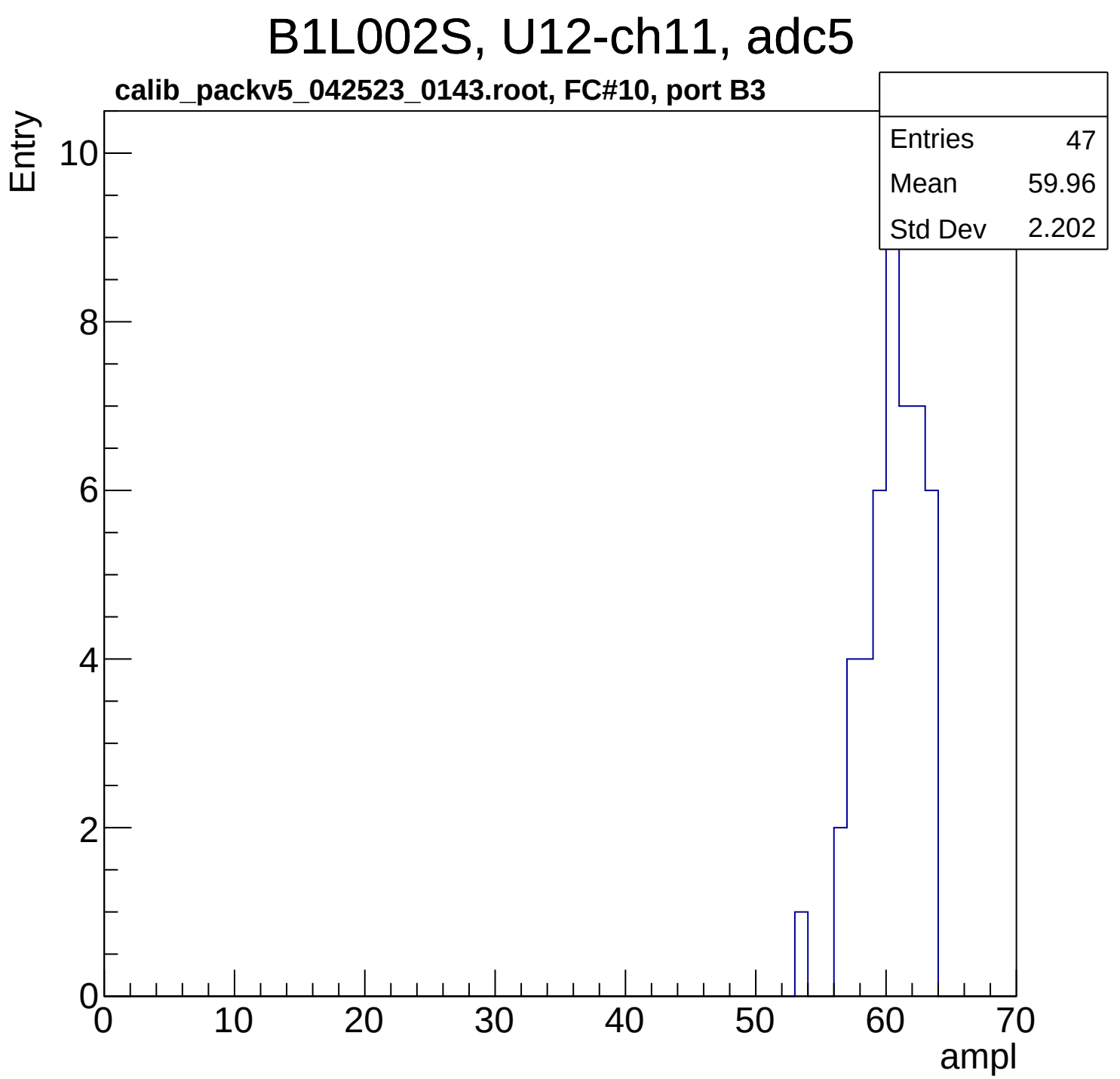
40
50
60
70


B1L002S, U12-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.96
Std Dev 2.202
Entry
ampl
0
2
4
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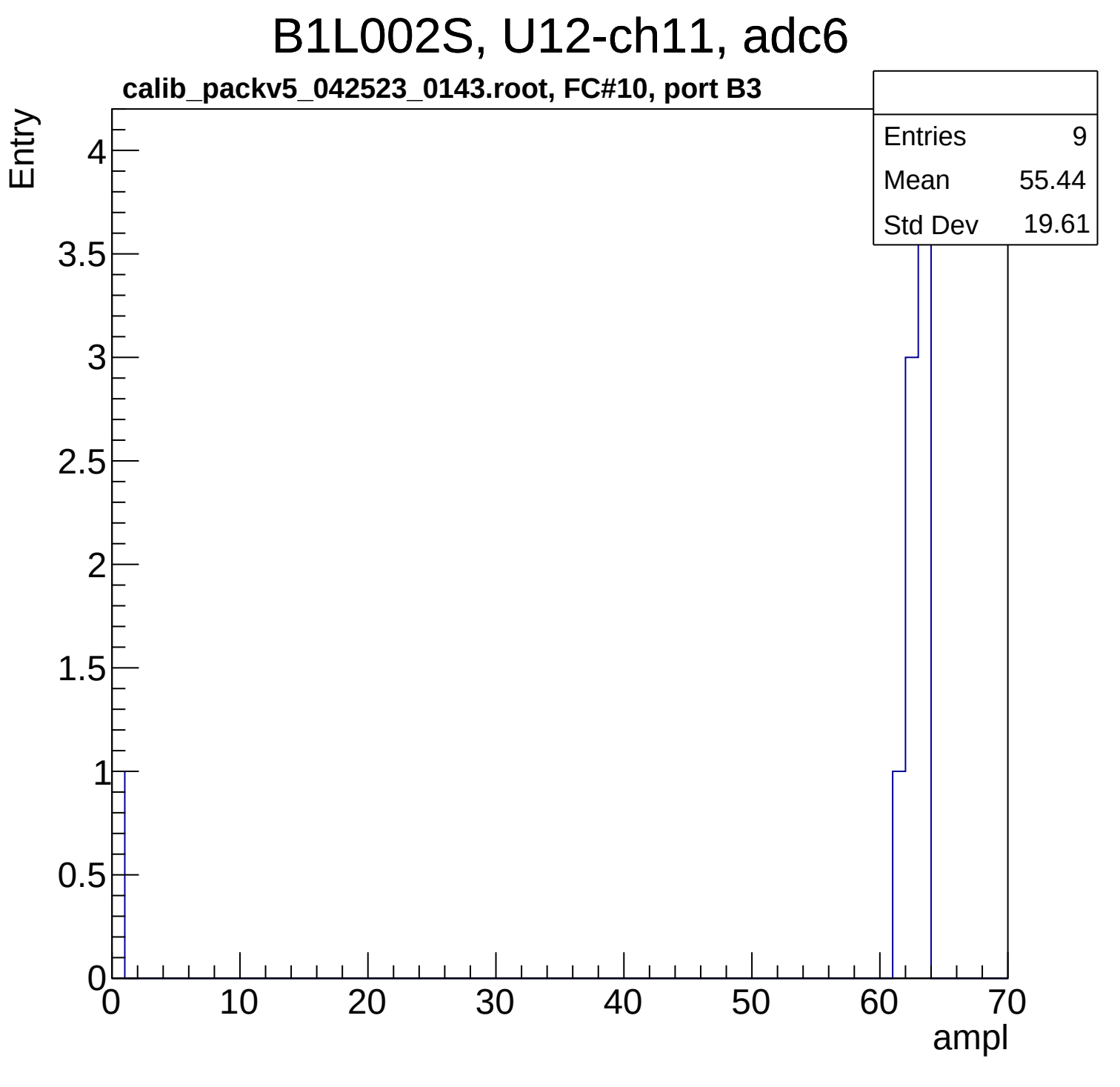
B1L002S, U12-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.44
Std Dev 19.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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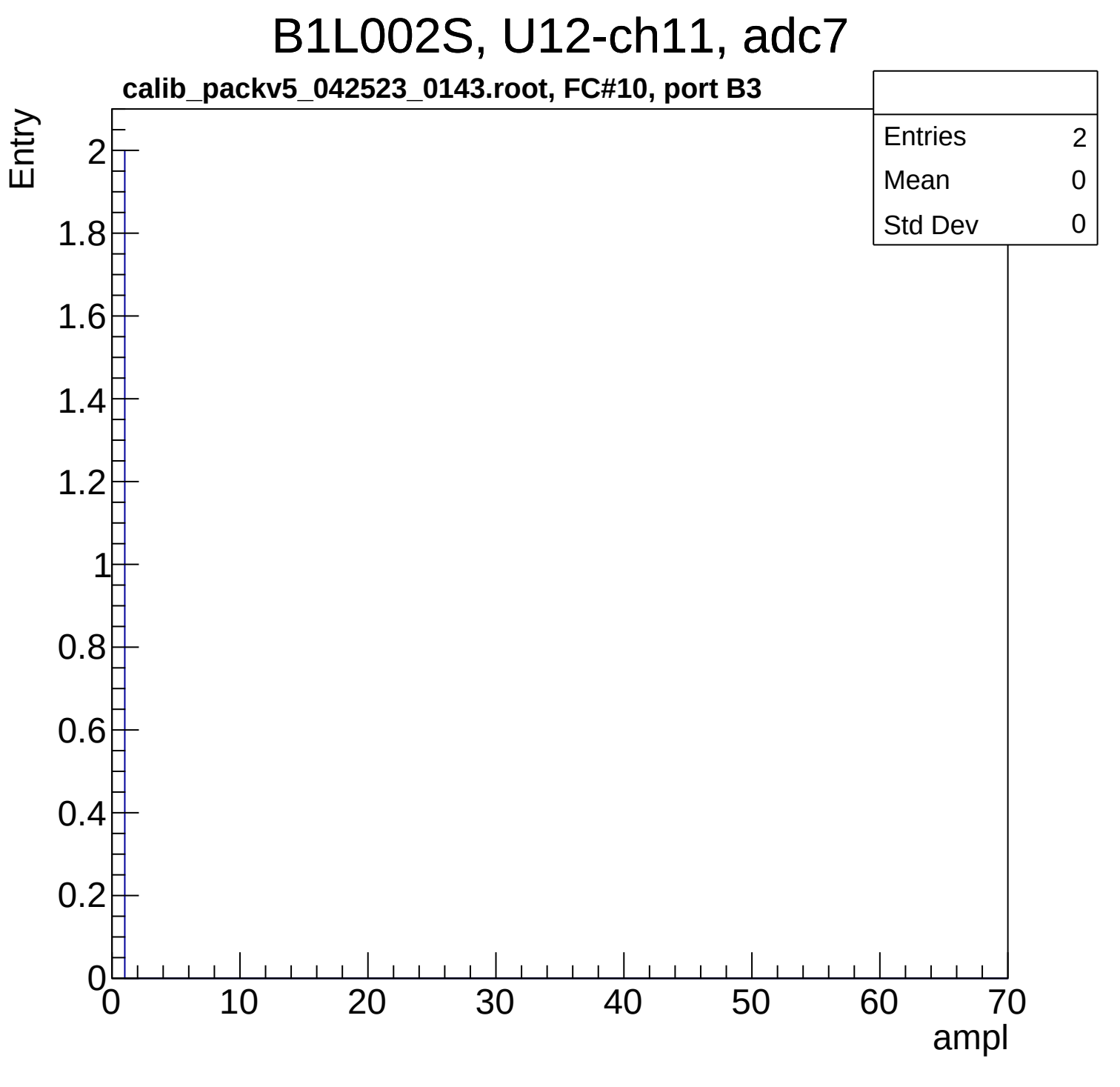
B1L002S, U12-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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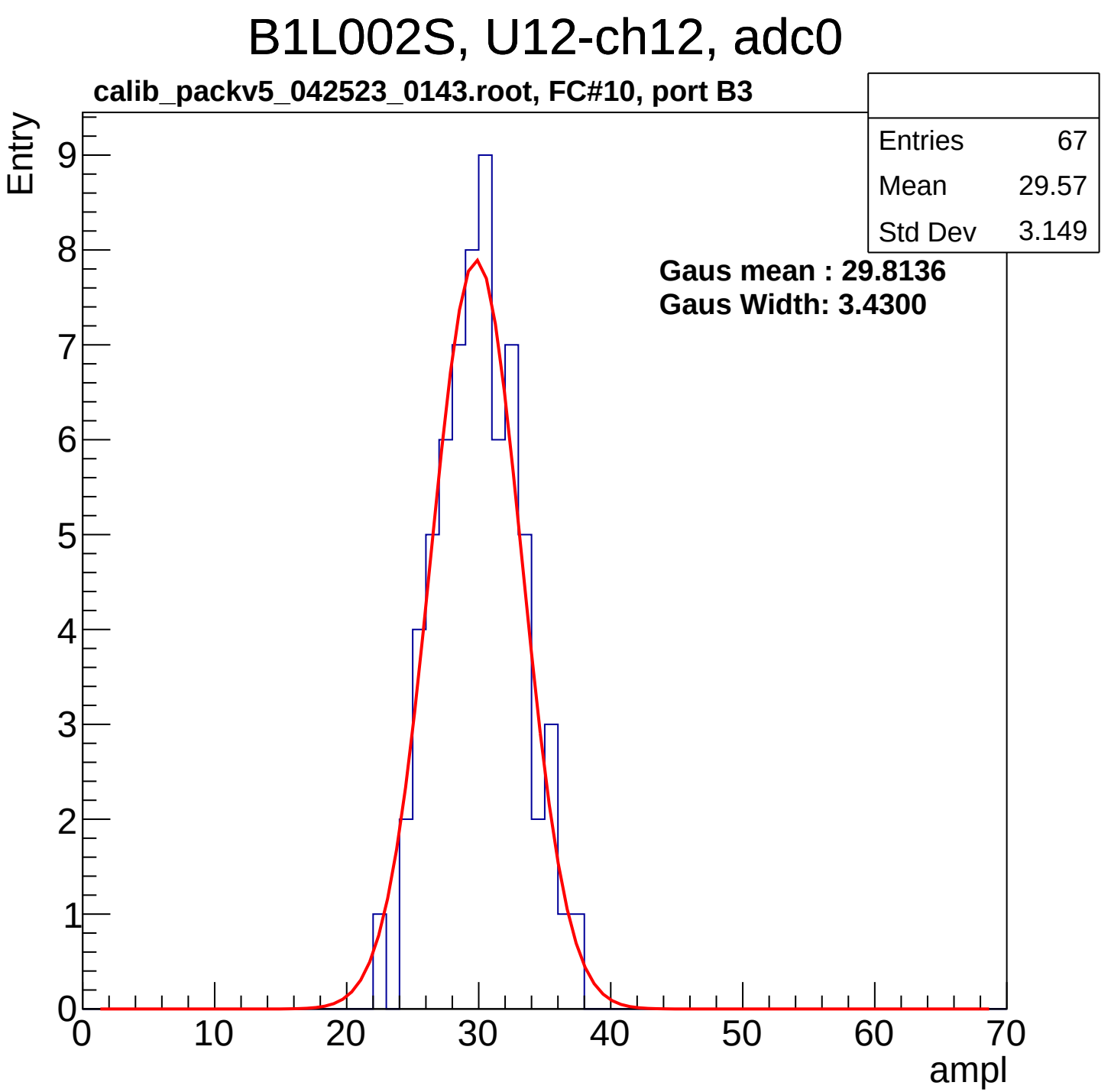
B1L002S, U12-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.57
Std Dev 3.149
Gaus mean : 29.8136
Gaus Width: 3.4300
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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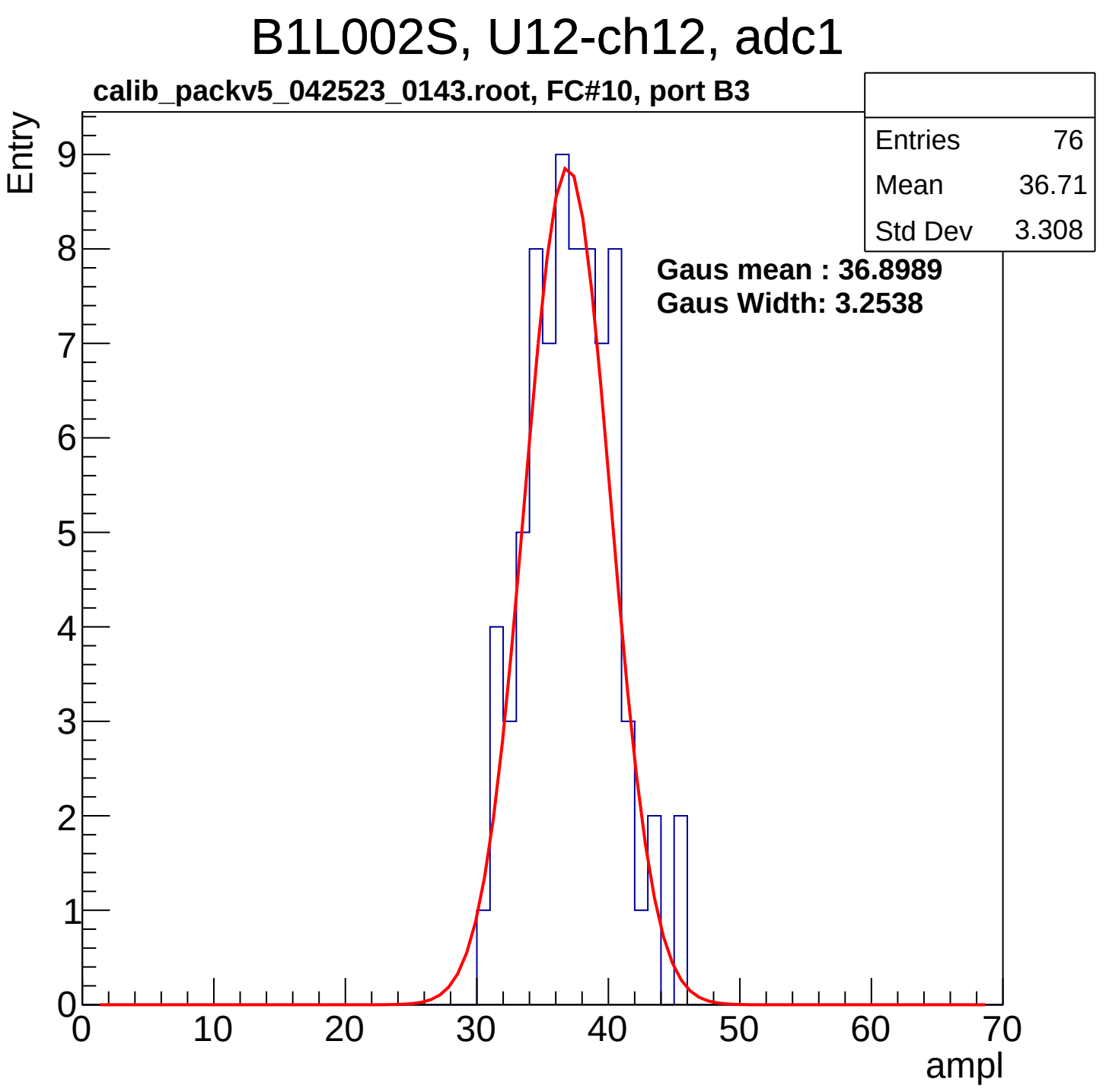
B1L002S, U12-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 36.71
Std Dev 3.308
Gaus mean : 36.8989
Gaus Width: 3.2538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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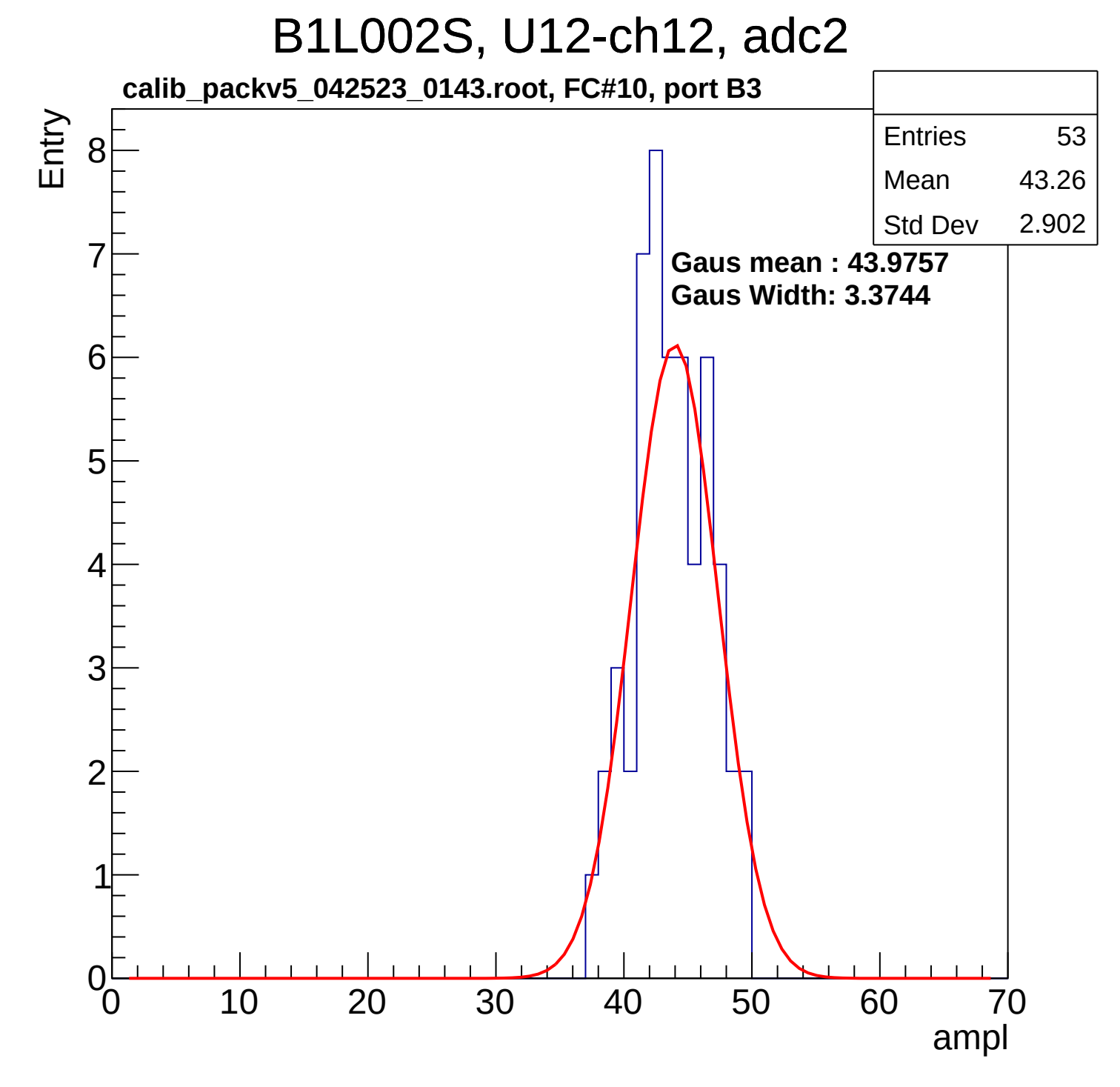
B1L002S, U12-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 43.26
Std Dev 2.902
Gaus mean : 43.9757
Gaus Width: 3.3744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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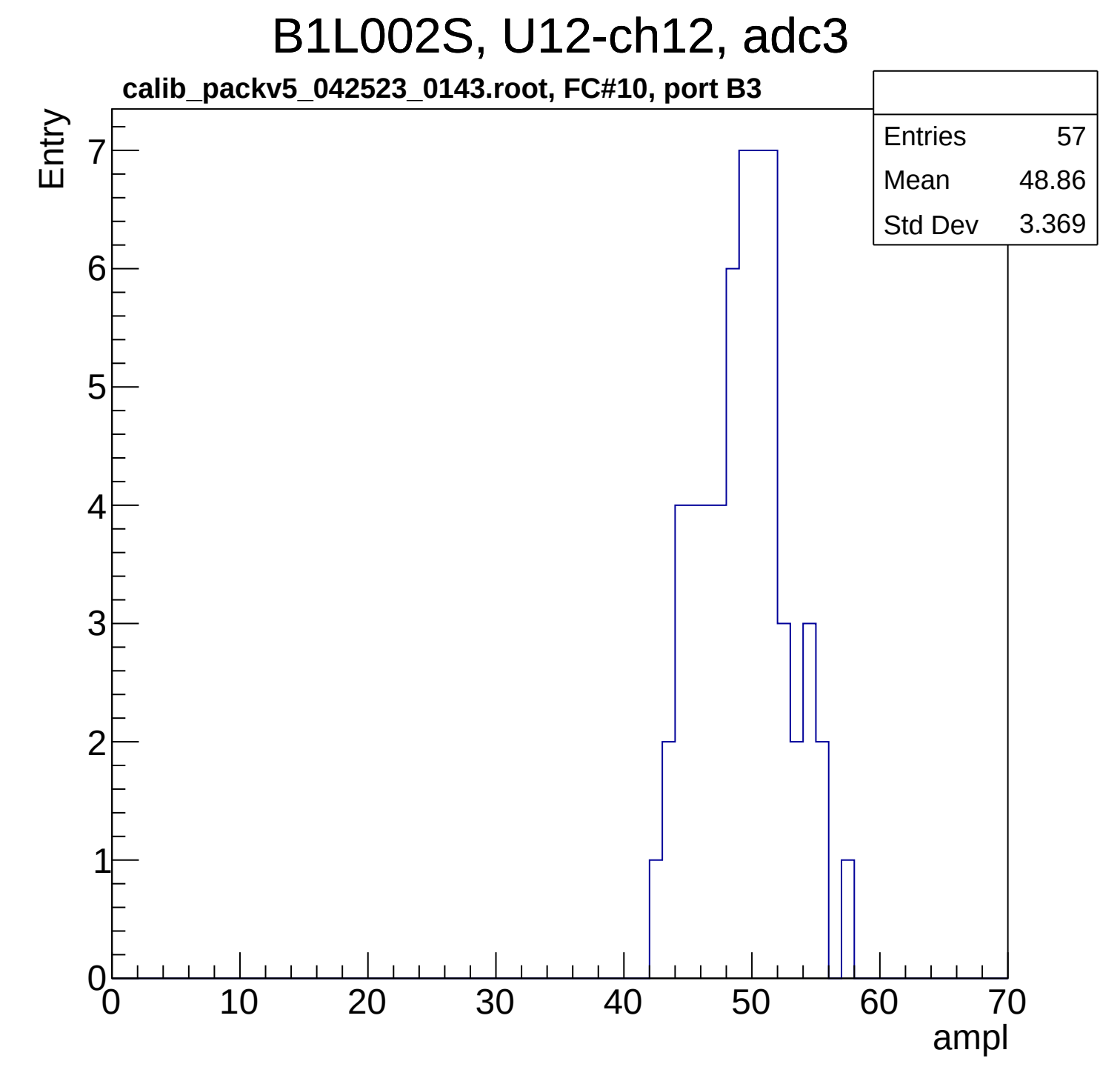
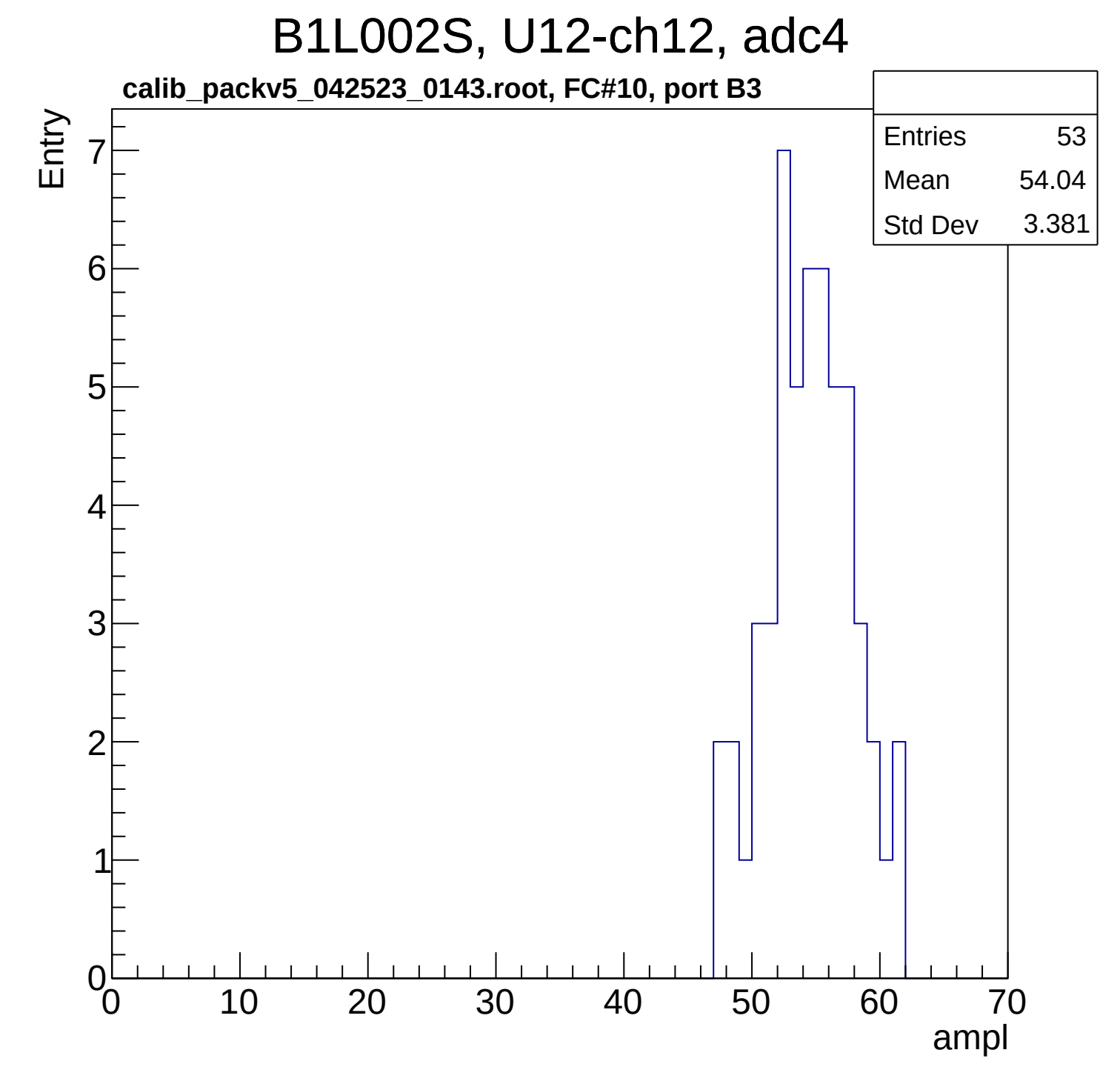

B1L002S, U12-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.86
Std Dev 3.369
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.04
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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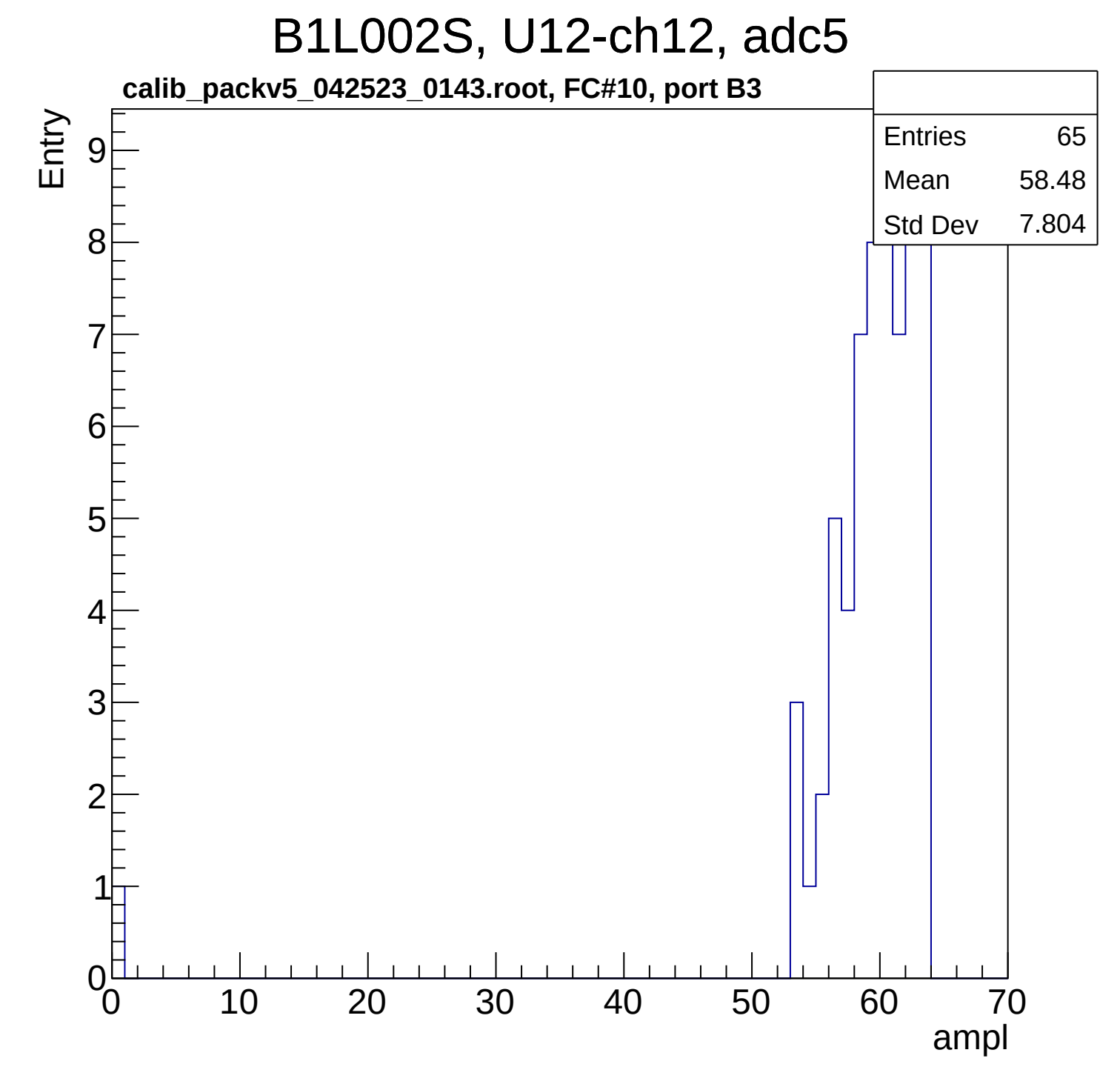
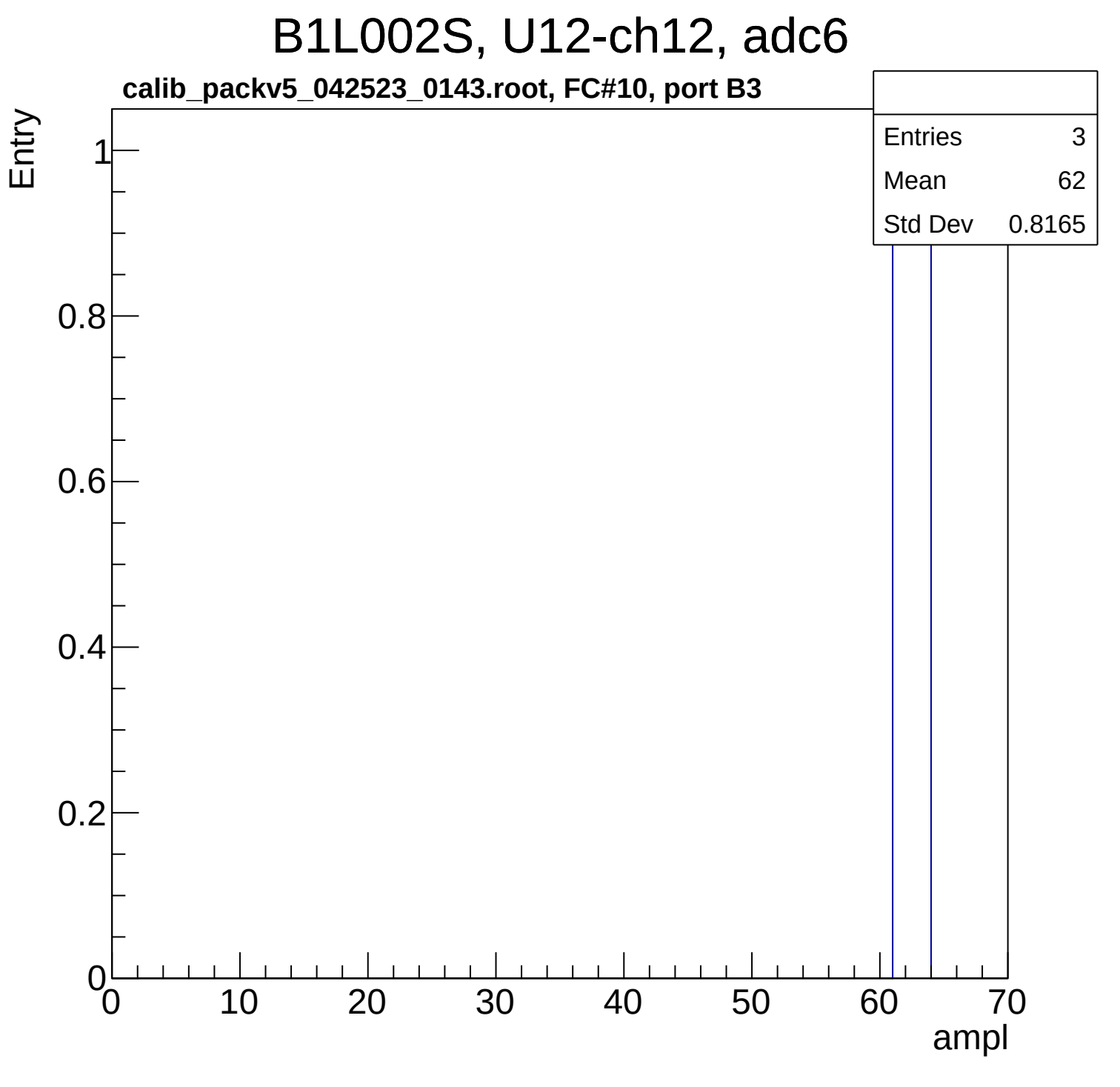
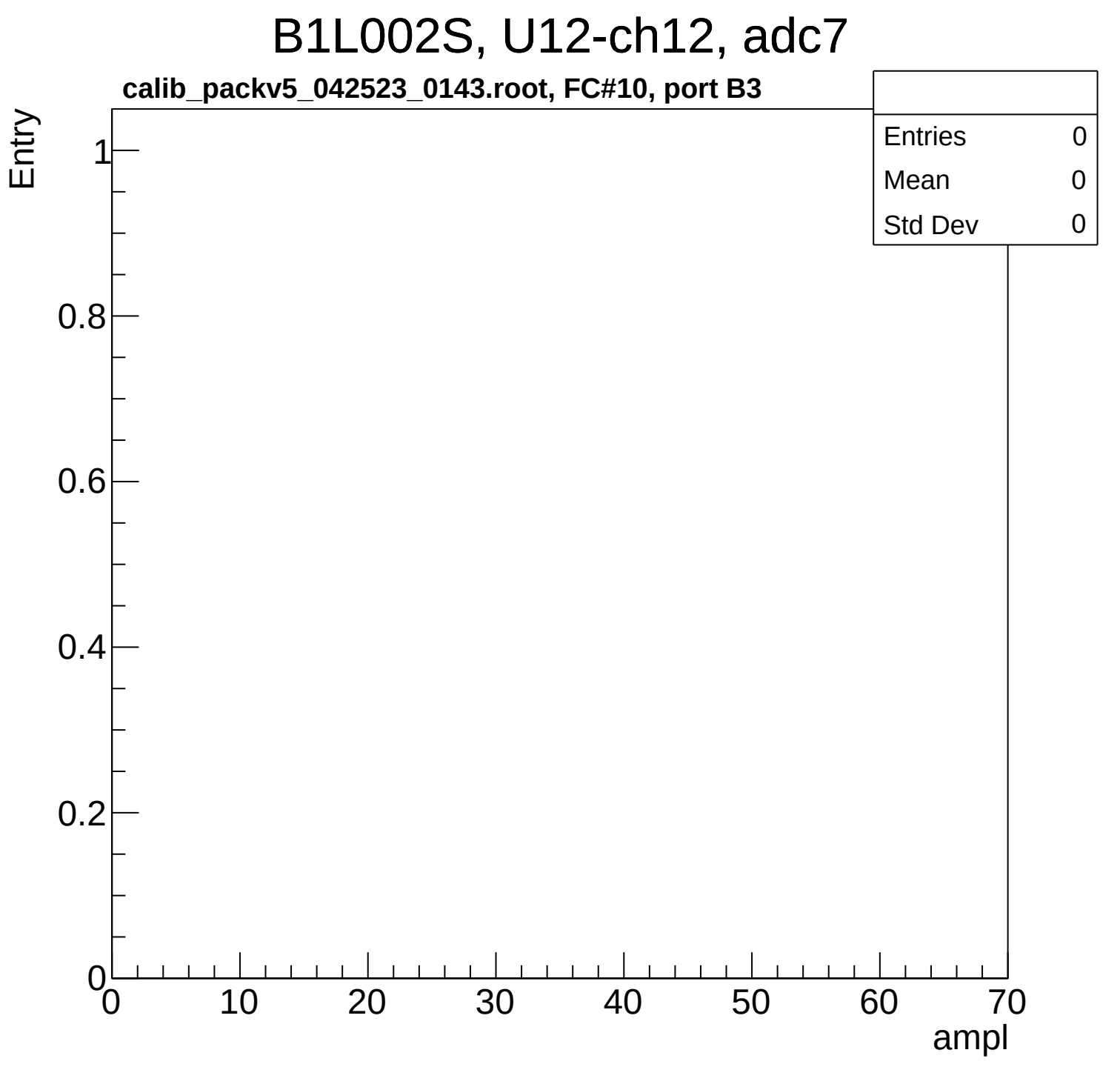
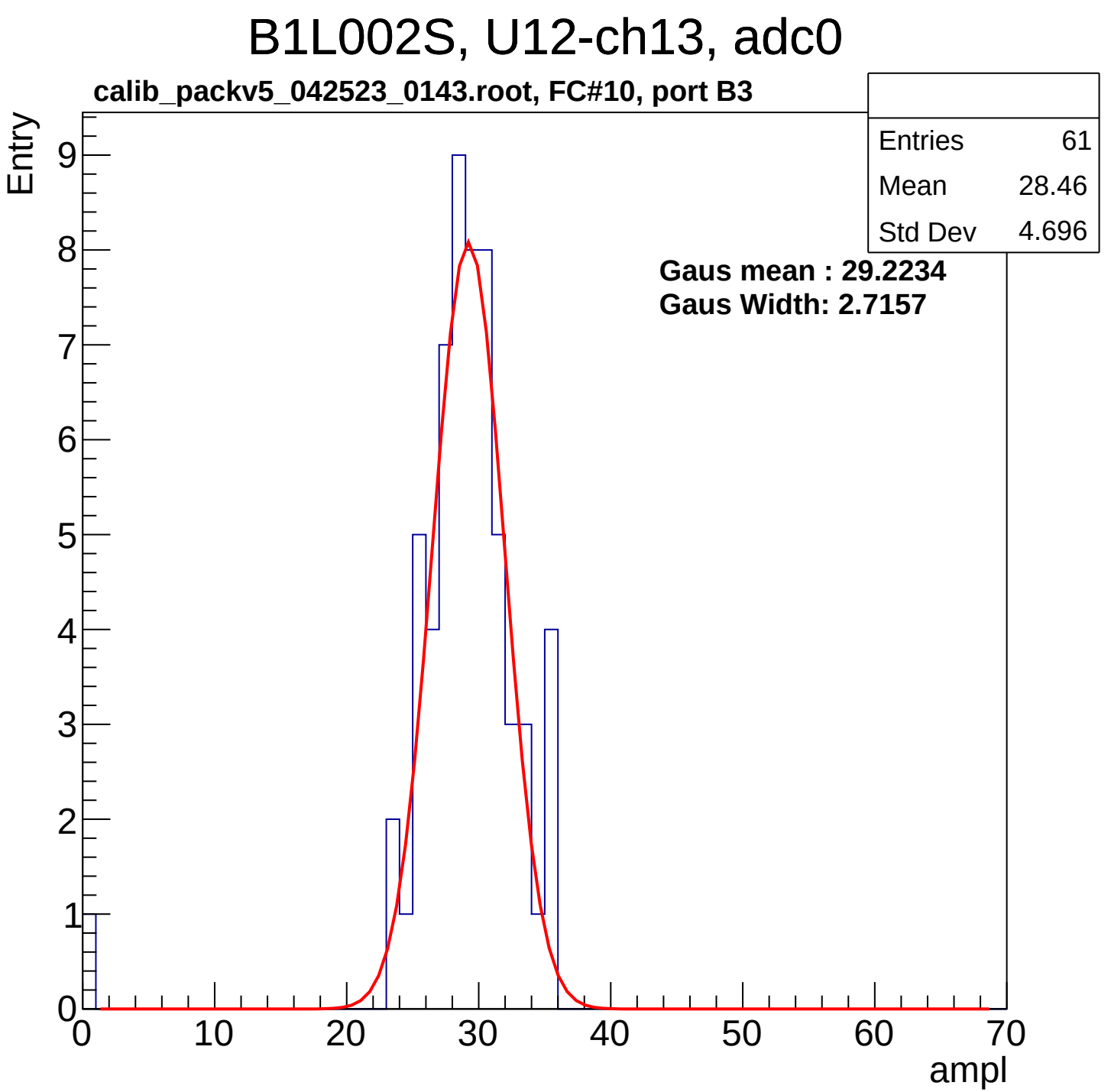

B1L002S, U12-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.48
Std Dev 7.804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.46
Std Dev 4.696
Gaus mean : 29.2234
Gaus Width: 2.7157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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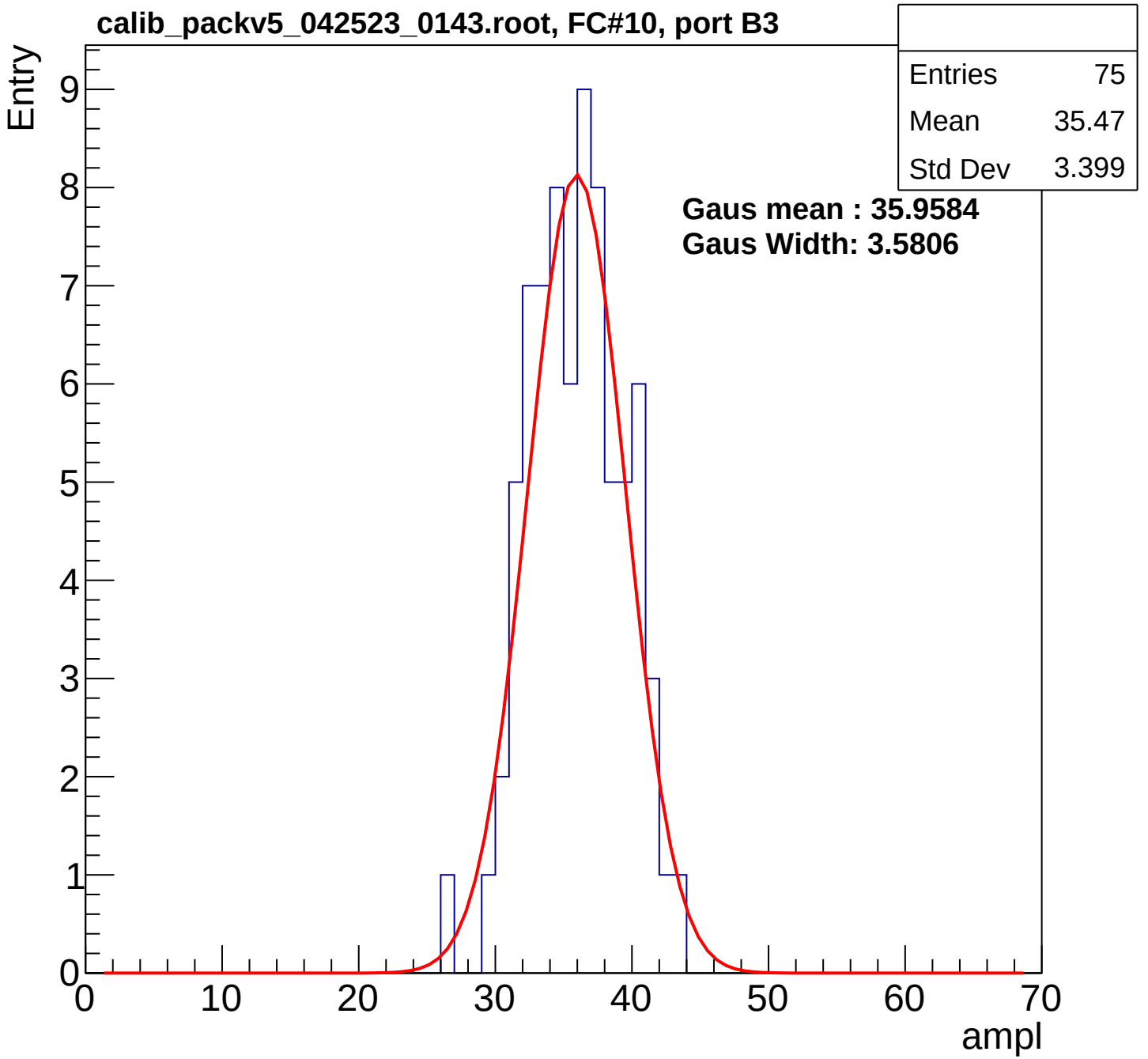
B1L002S, U12-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.47
Std Dev 3.399
Gaus mean : 35.9584
Gaus Width: 3.5806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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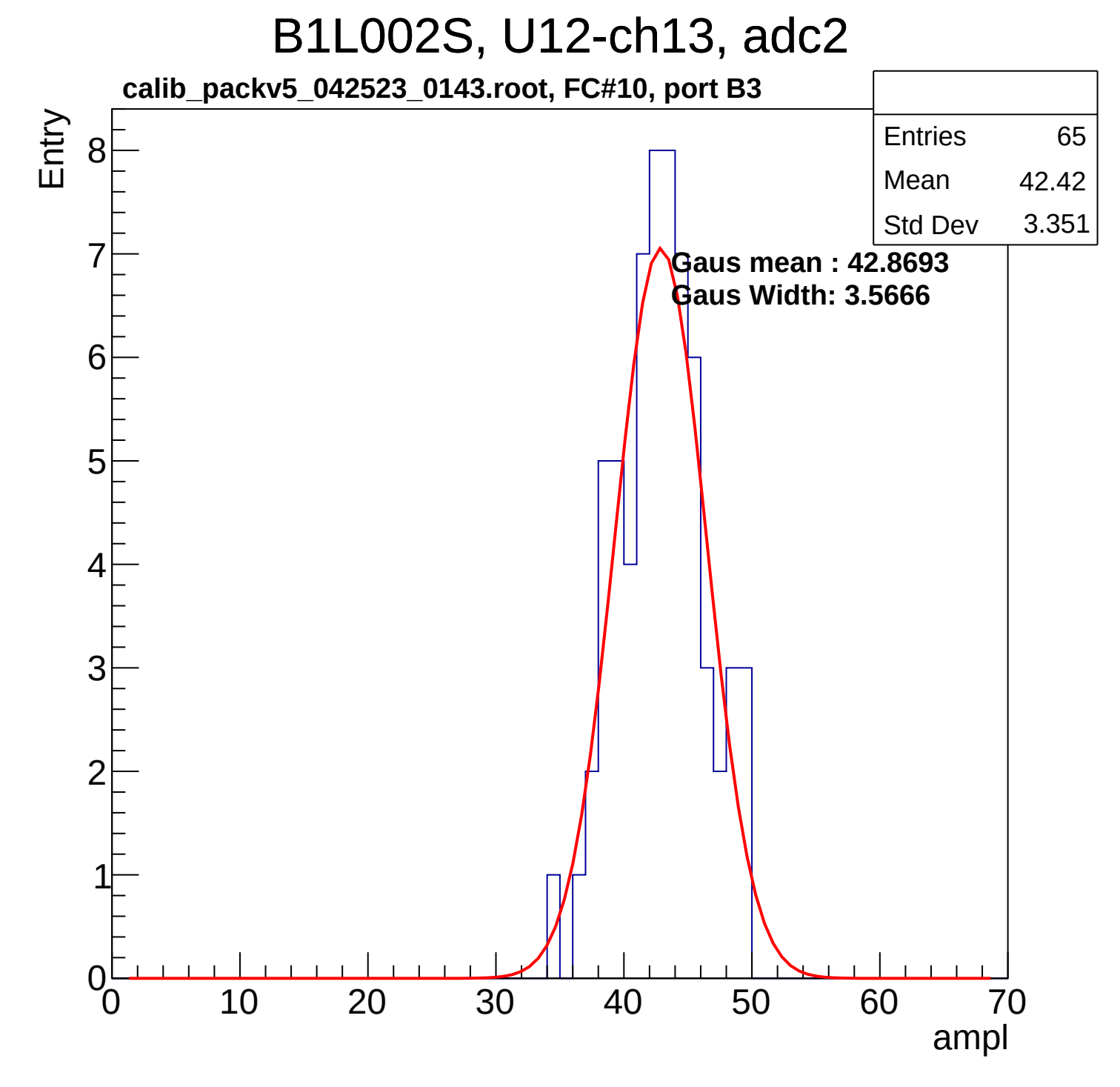
B1L002S, U12-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 42.42
Std Dev 3.351
Gaus mean : 42.8693
Gaus Width: 3.5666
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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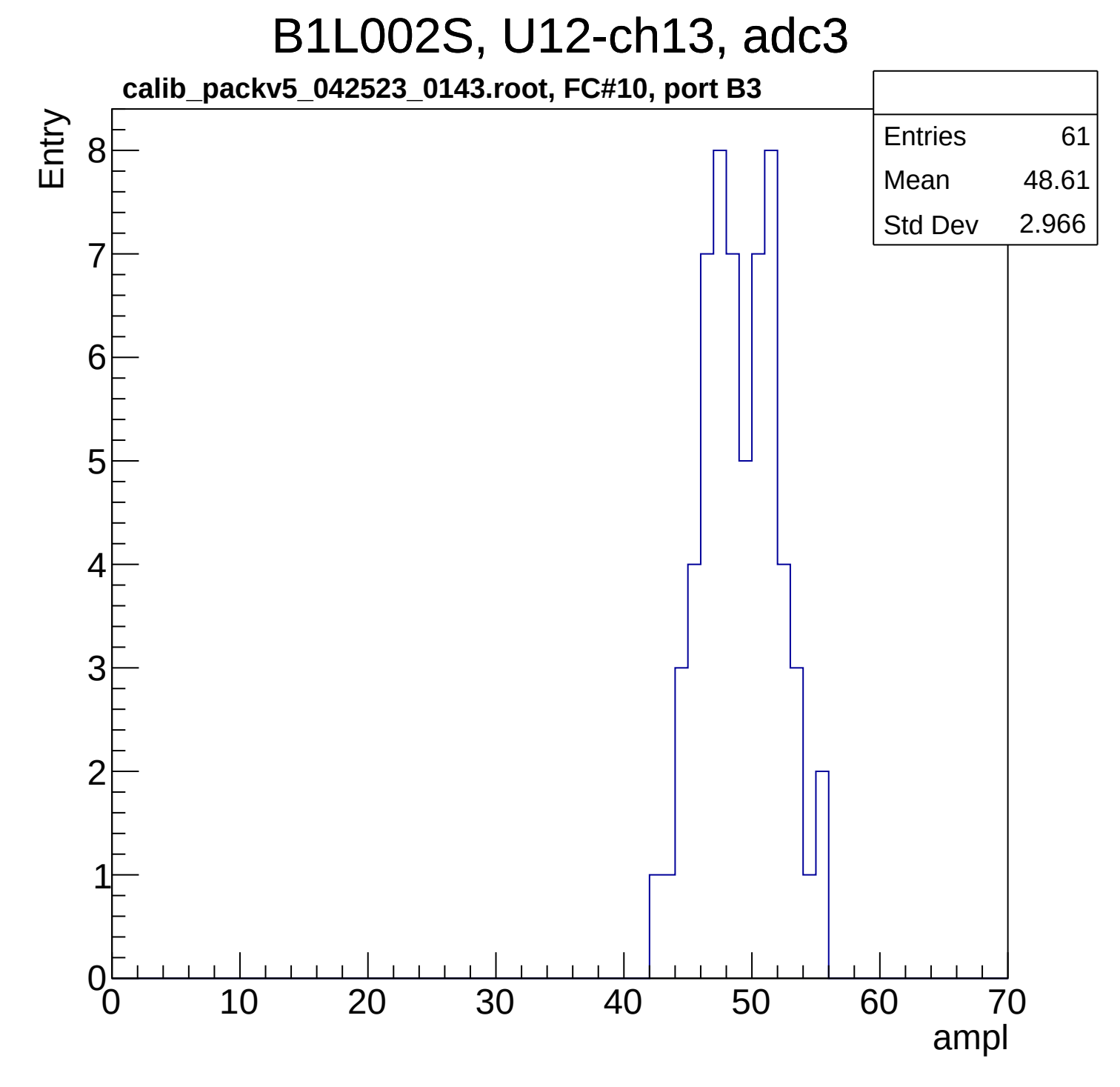
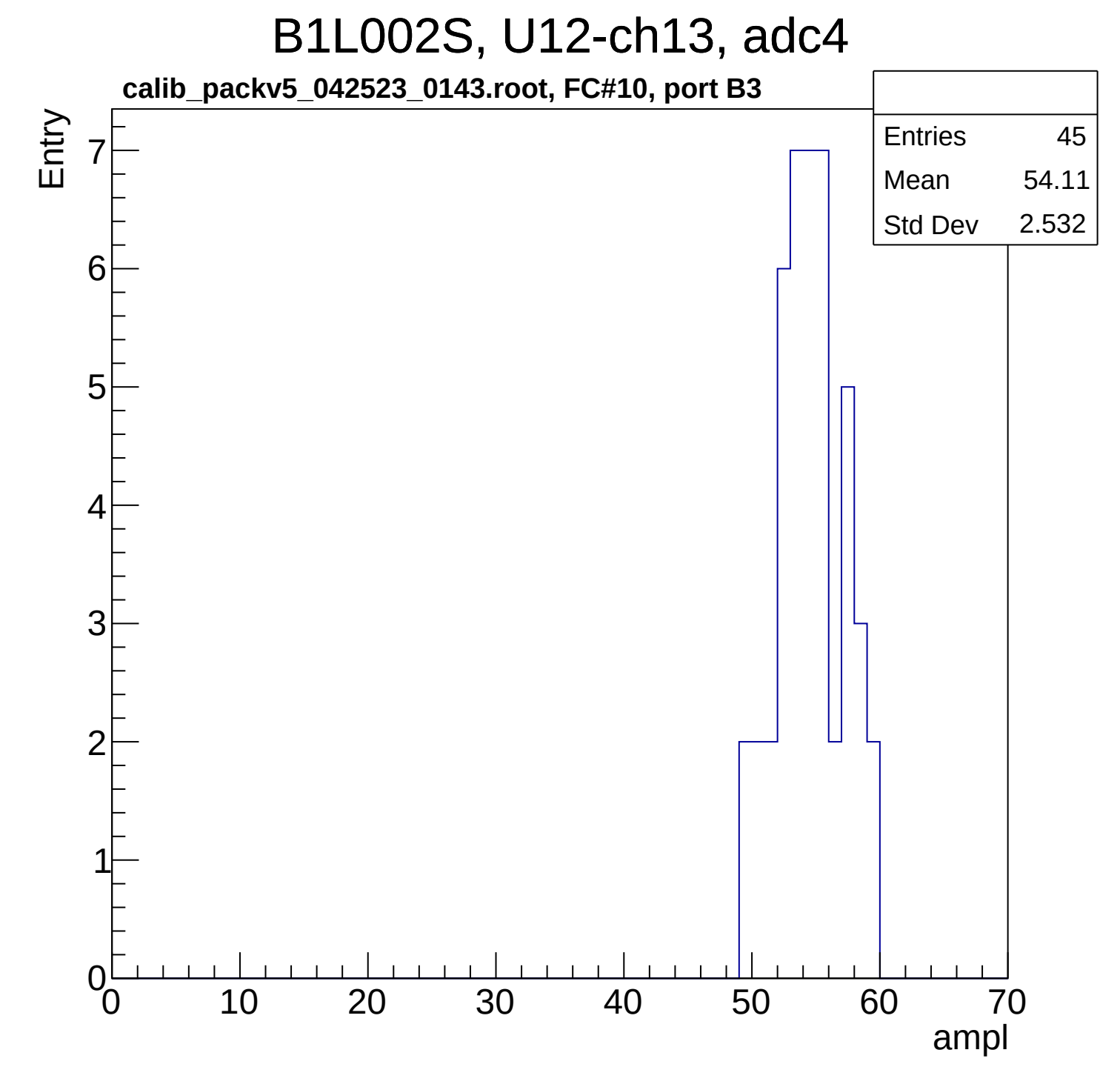

B1L002S, U12-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.61
Std Dev 2.966
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 54.11
Std Dev 2.532
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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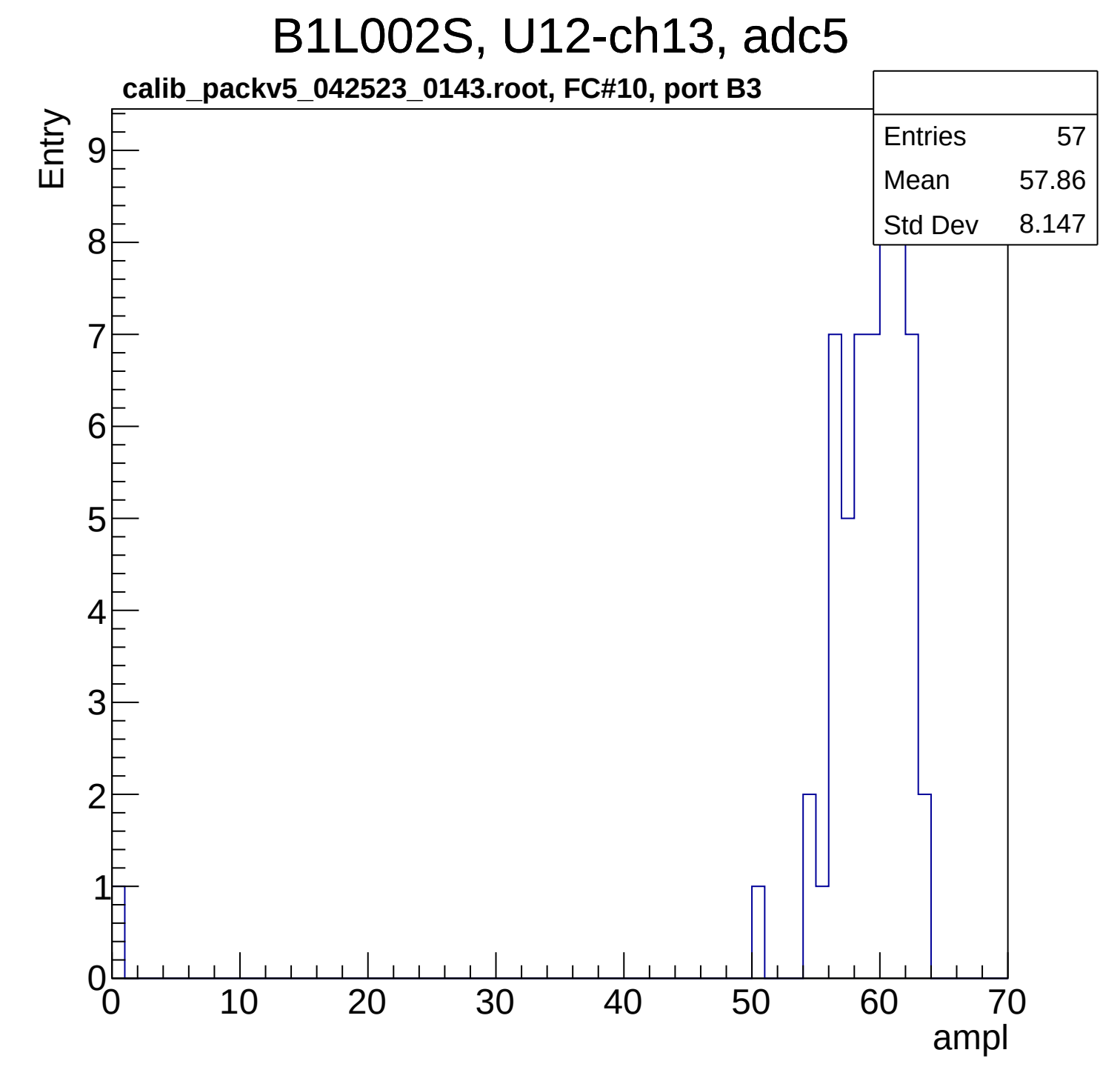
B1L002S, U12-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.86
Std Dev 8.147
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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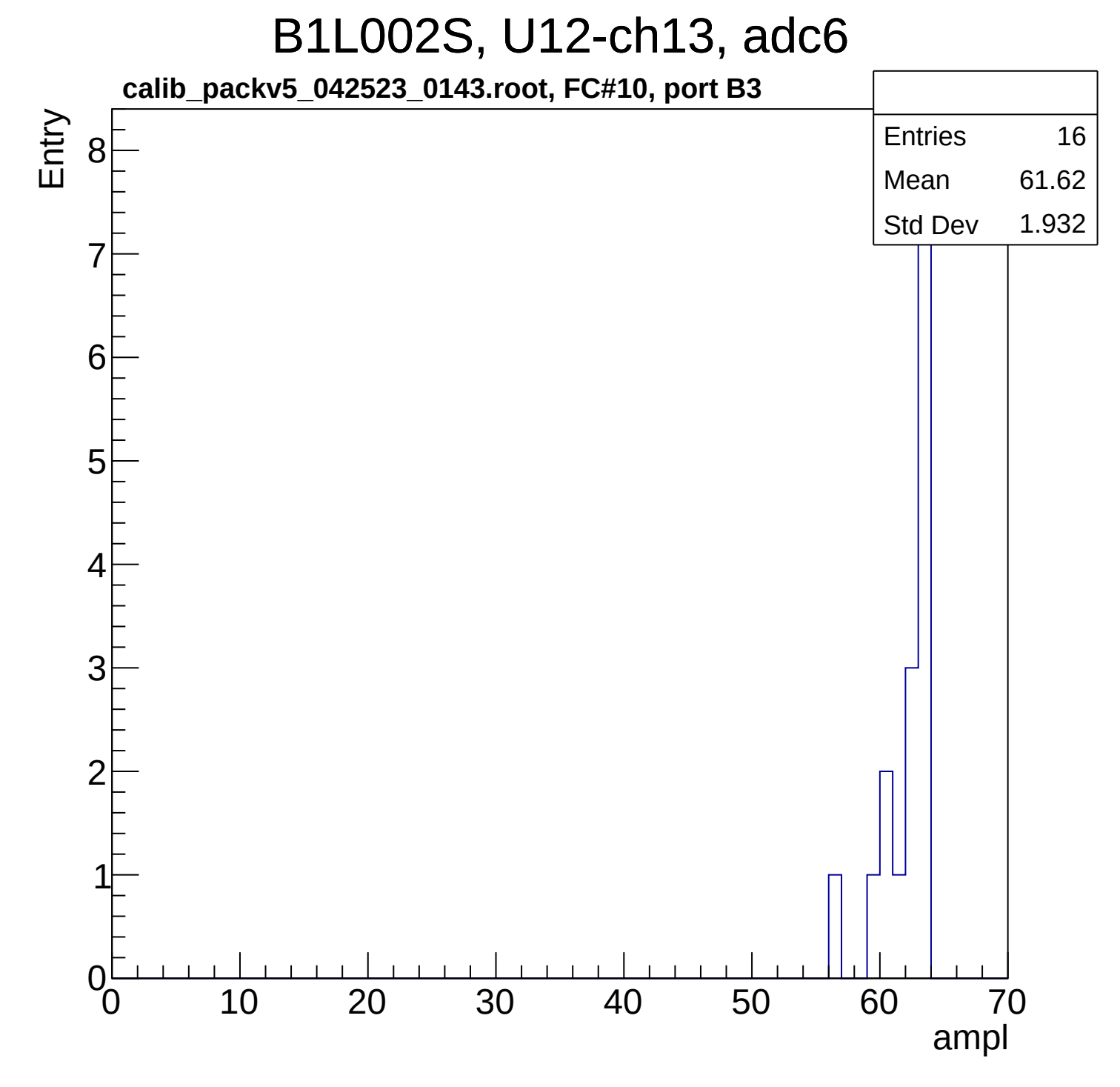
B1L002S, U12-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.62
Std Dev 1.932
Entry
ampl
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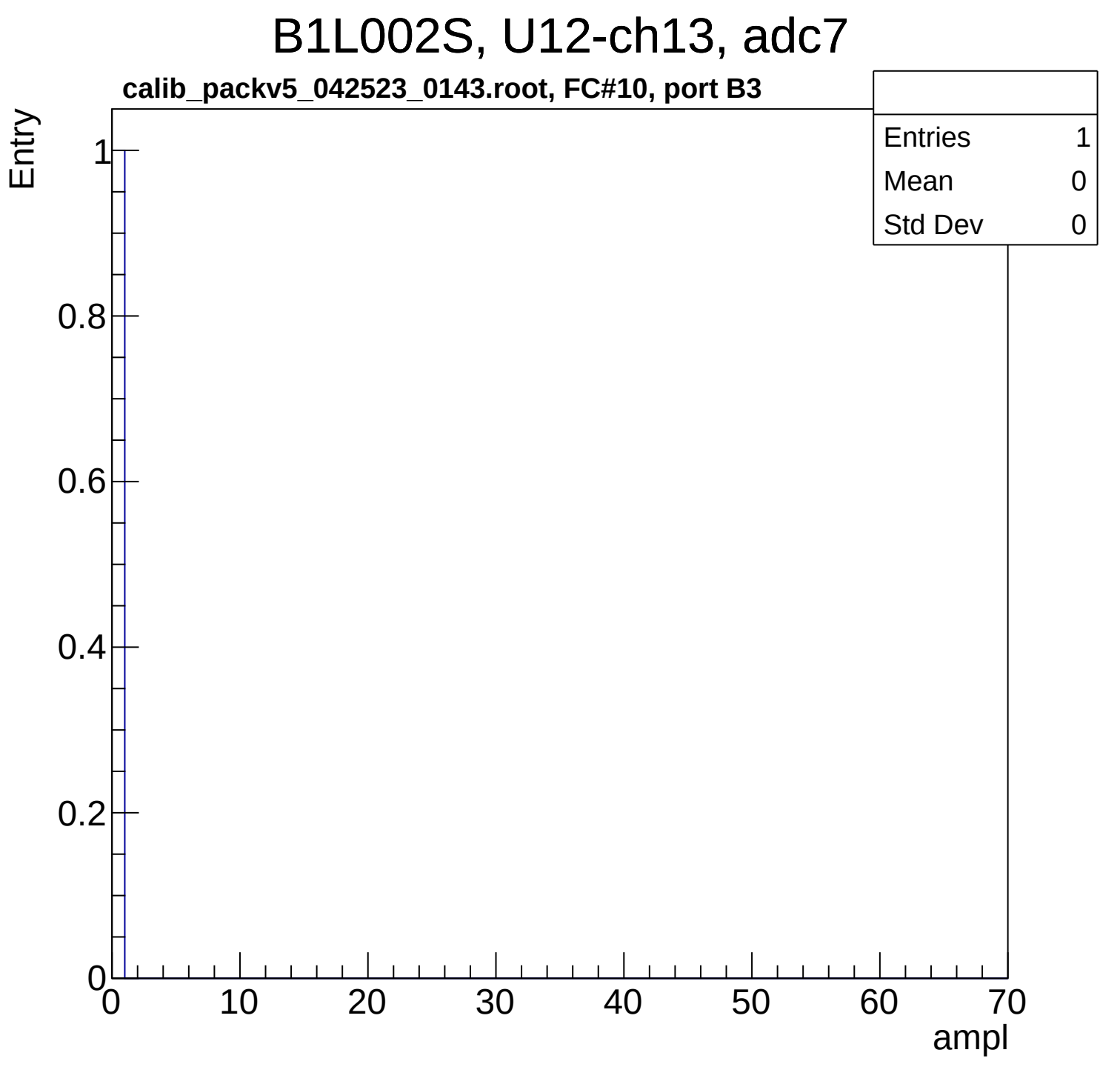
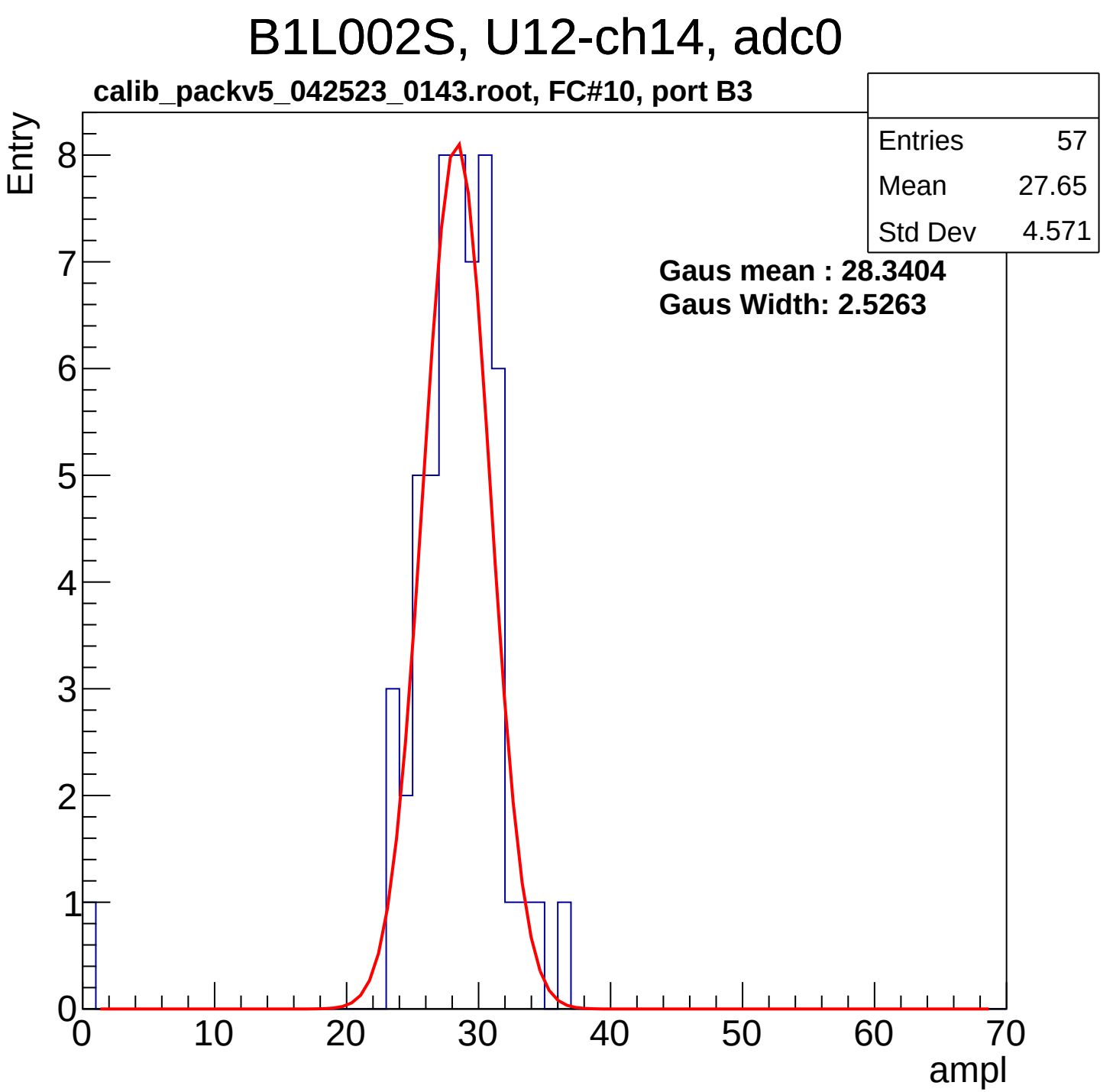
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.65
Std Dev 4.571
Gaus mean : 28.3404
Gaus Width: 2.5263
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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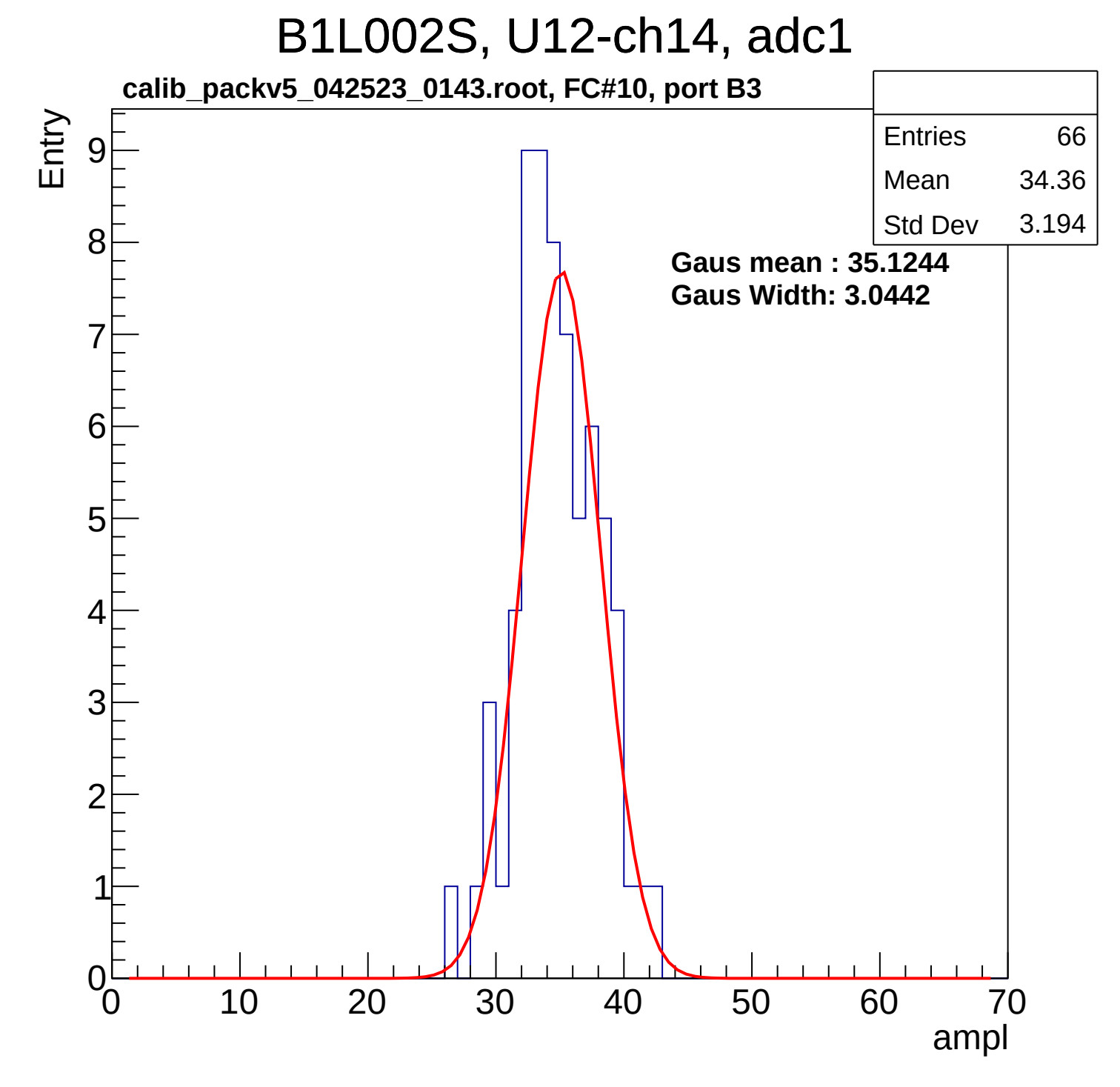
B1L002S, U12-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.36
Std Dev 3.194
Gaus mean : 35.1244
Gaus Width: 3.0442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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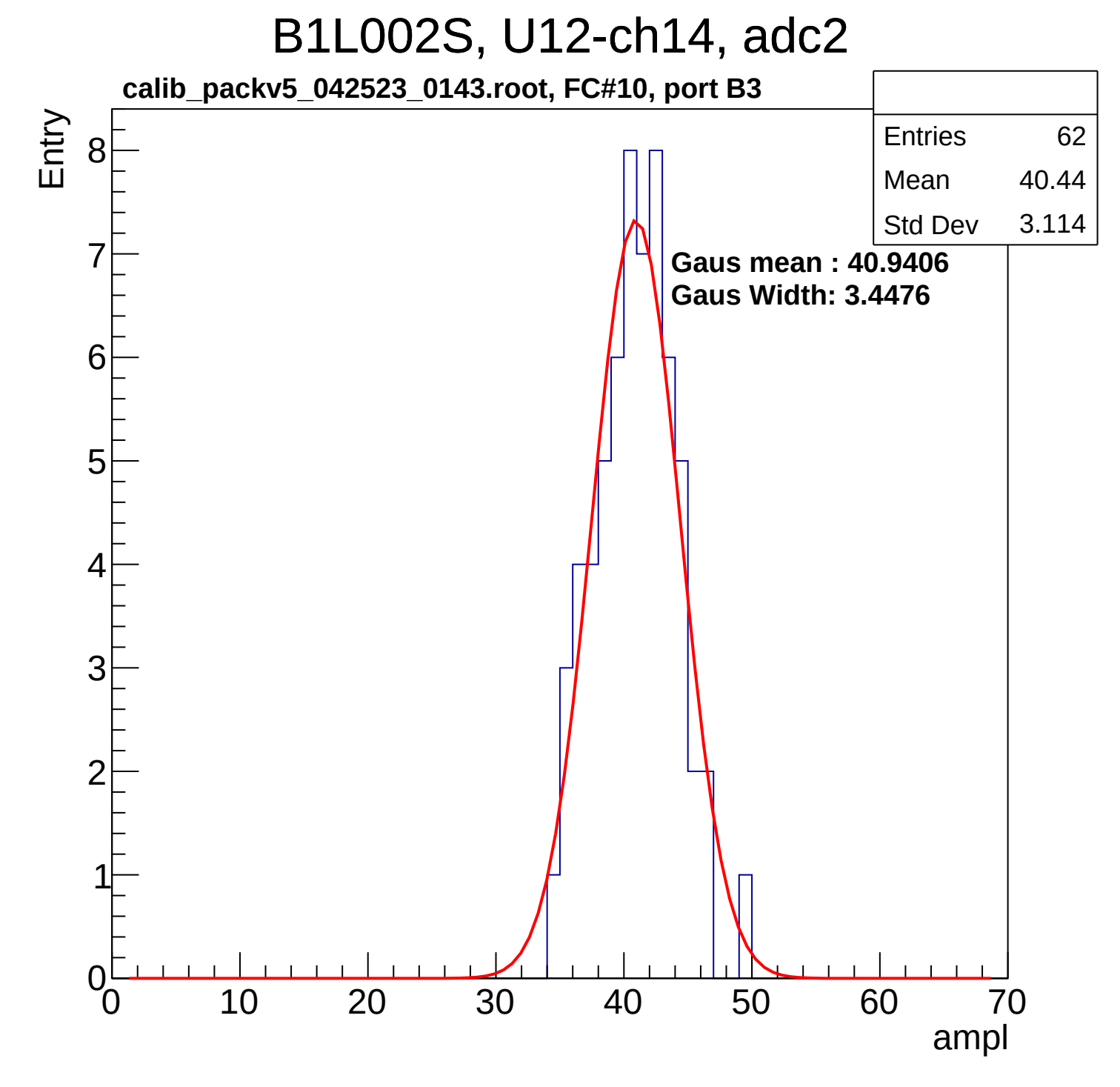
B1L002S, U12-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.44
Std Dev 3.114
Gaus mean : 40.9406
Gaus Width: 3.4476
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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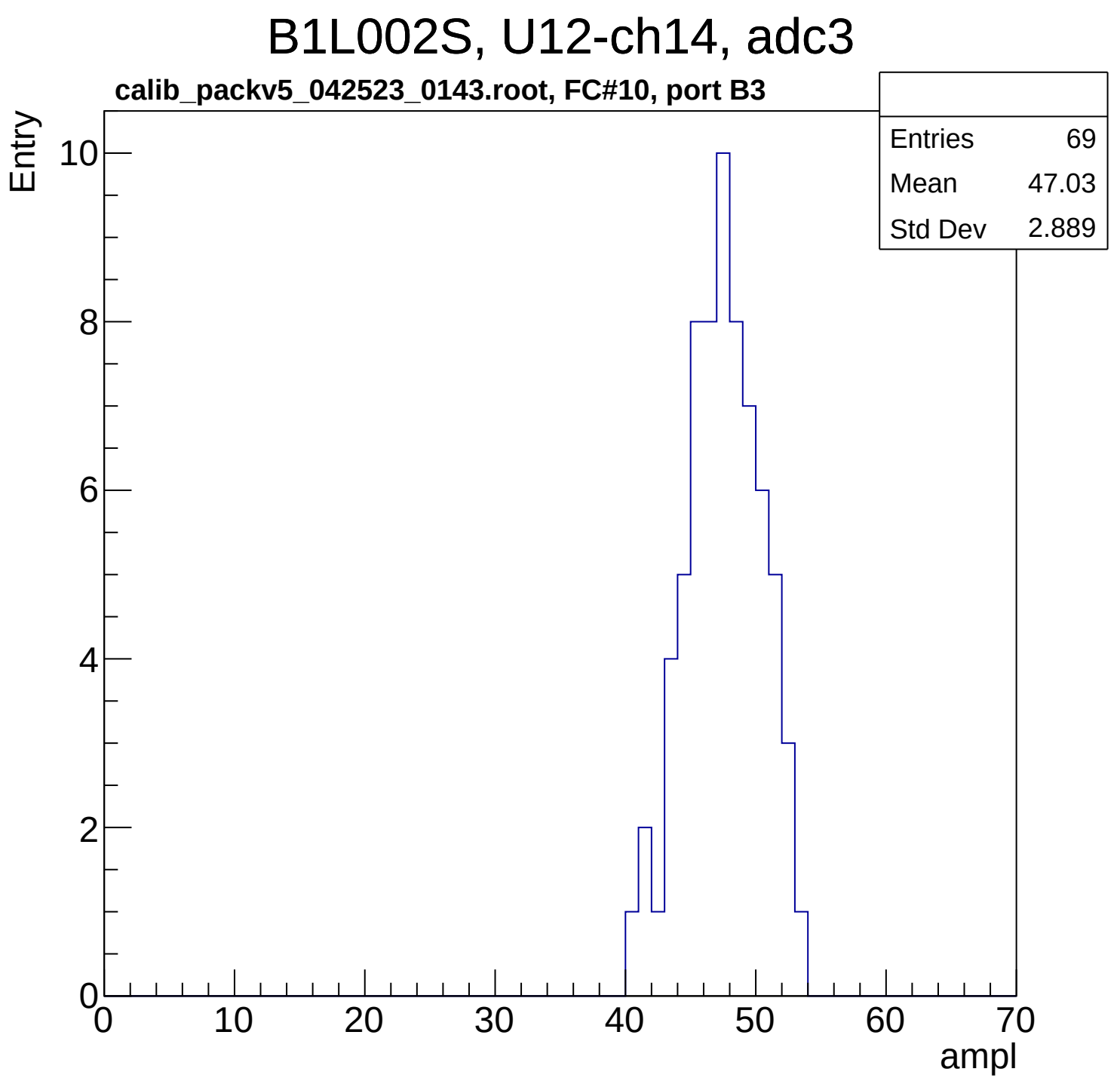
B1L002S, U12-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.03
Std Dev 2.889
Entry
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6
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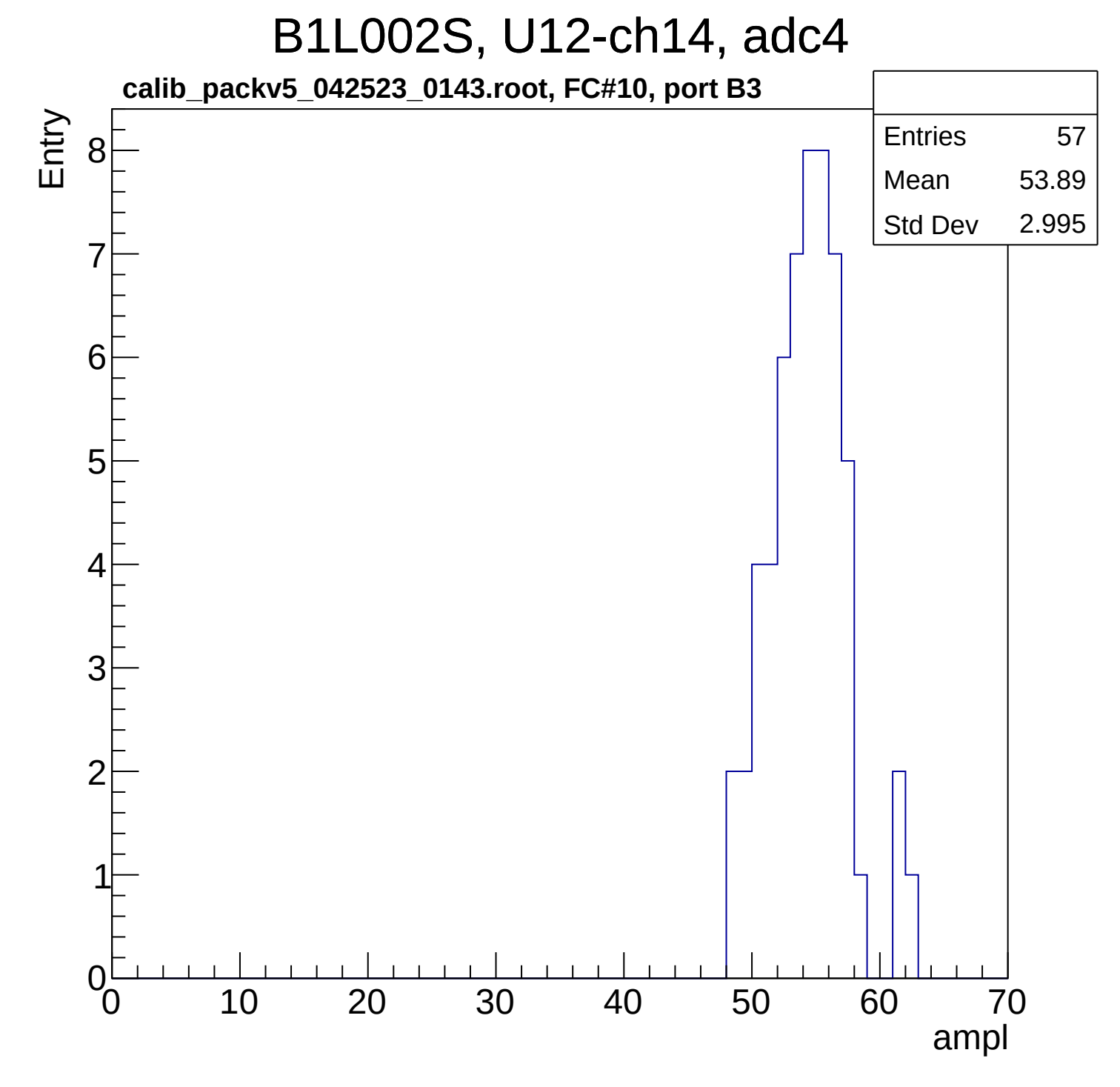
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.89
Std Dev 2.995
Entry
ampl
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2
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4
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7
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0
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20
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40
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60
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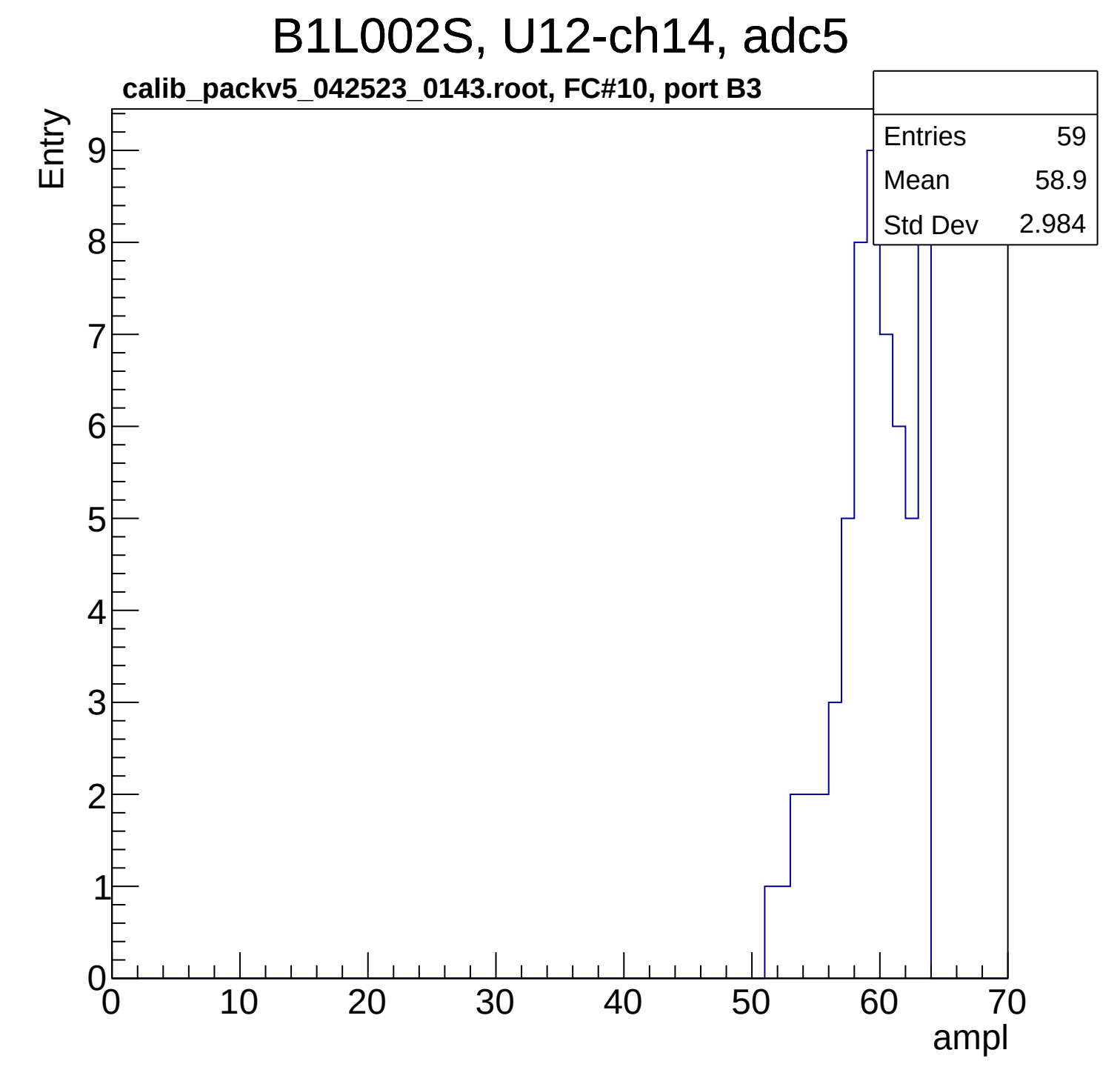
B1L002S, U12-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.9
Std Dev 2.984
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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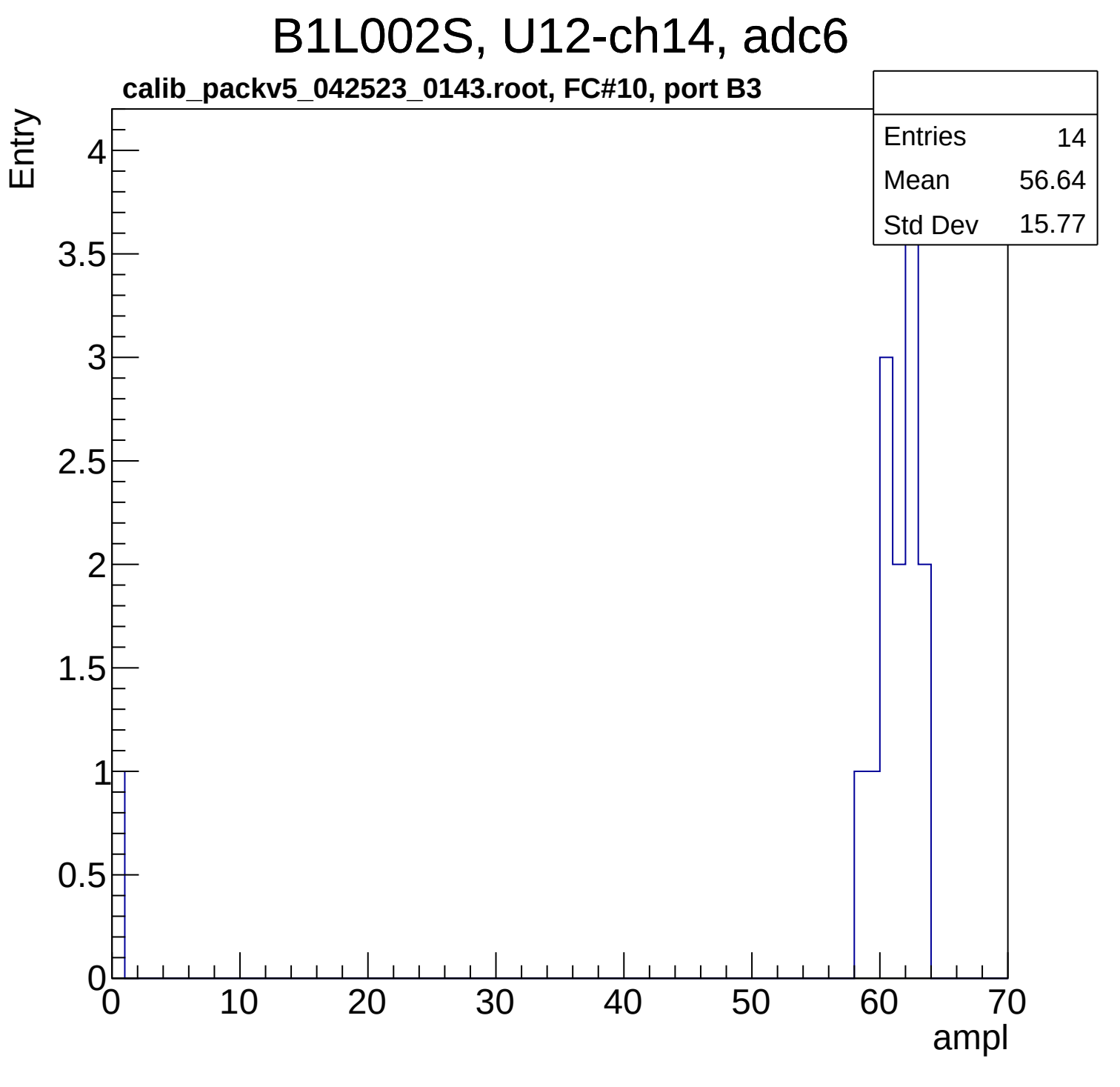
B1L002S, U12-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 56.64
Std Dev 15.77
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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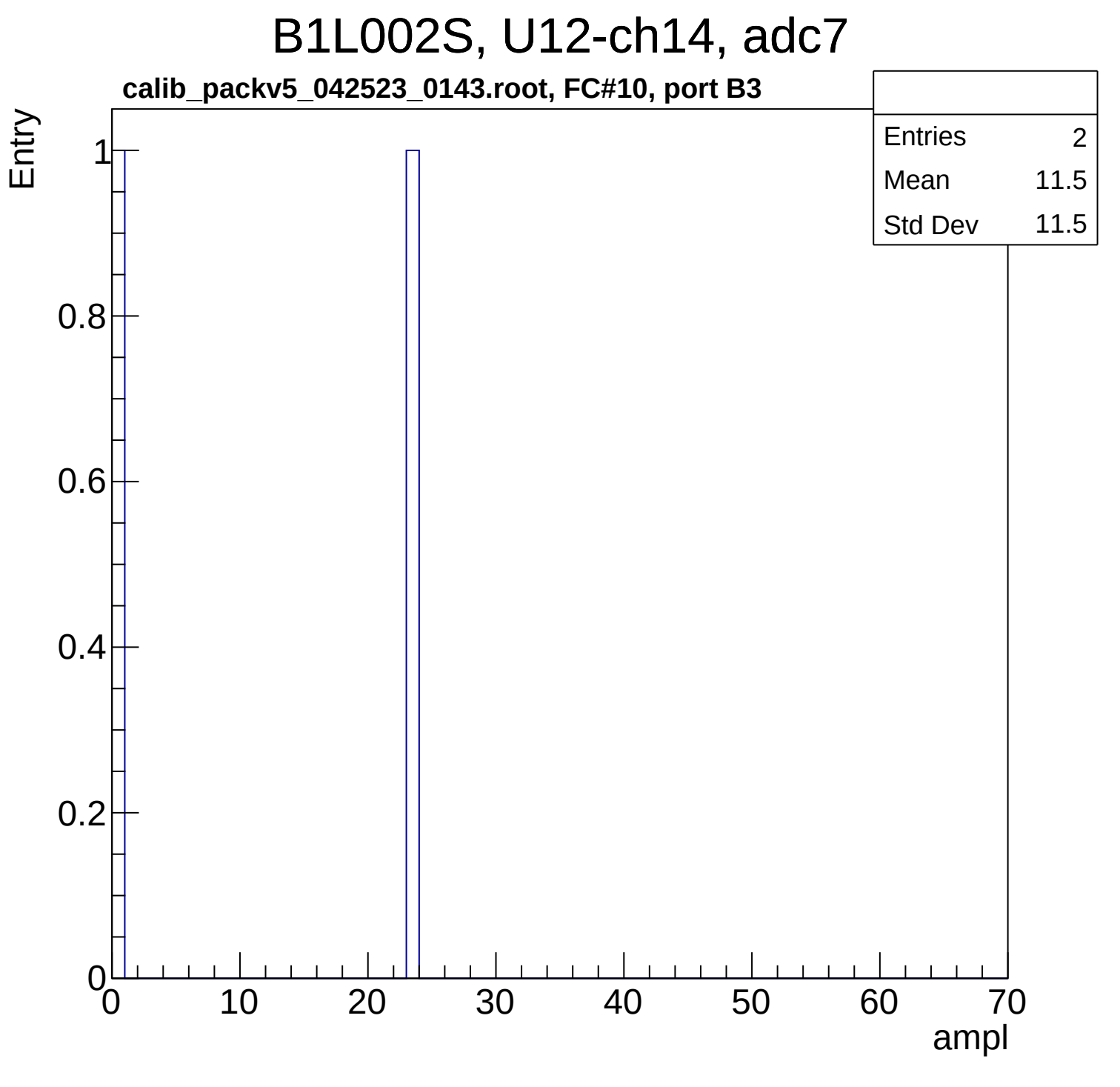
B1L002S, U12-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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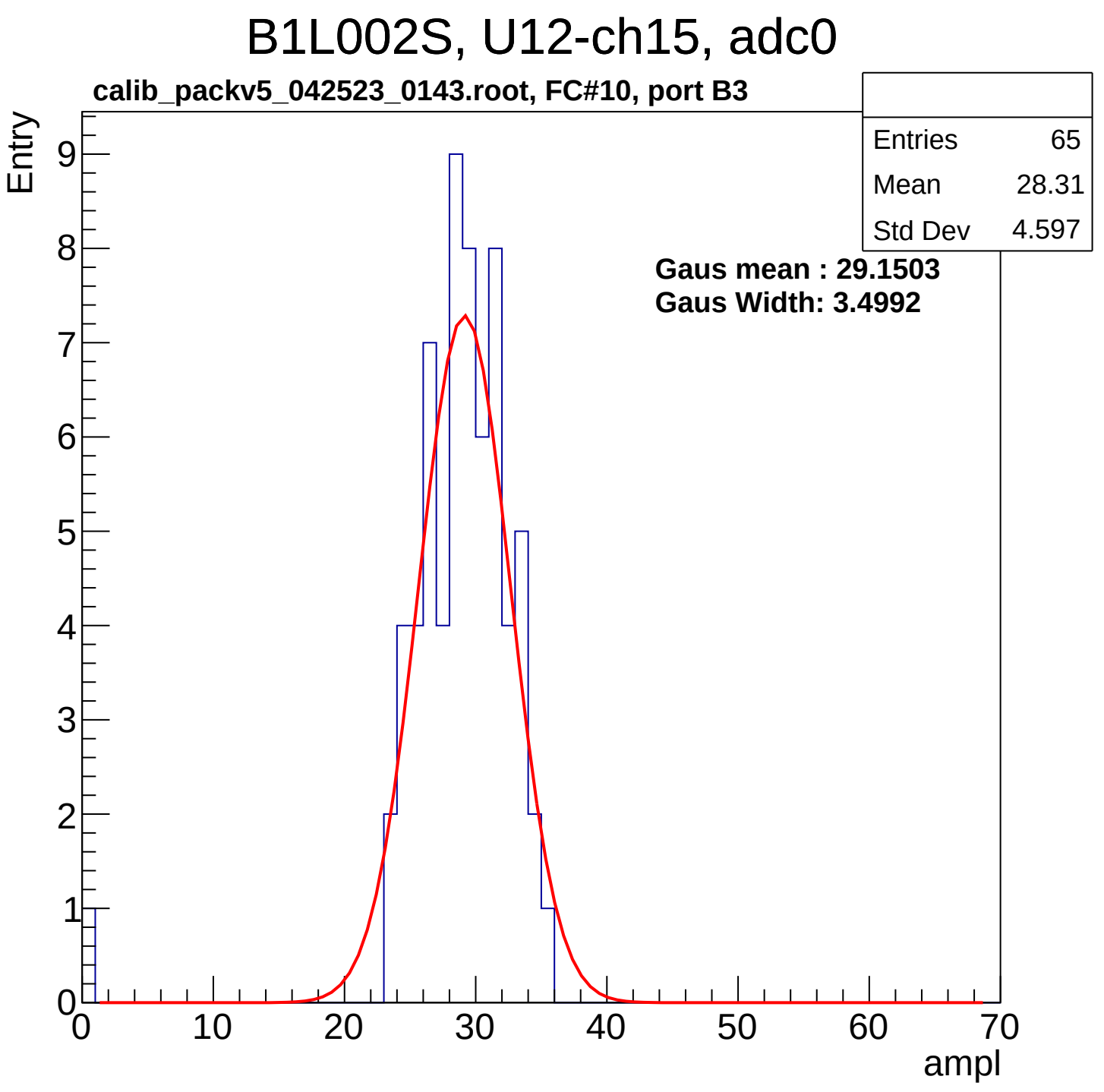
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.31
Std Dev 4.597
Gaus mean : 29.1503
Gaus Width: 3.4992
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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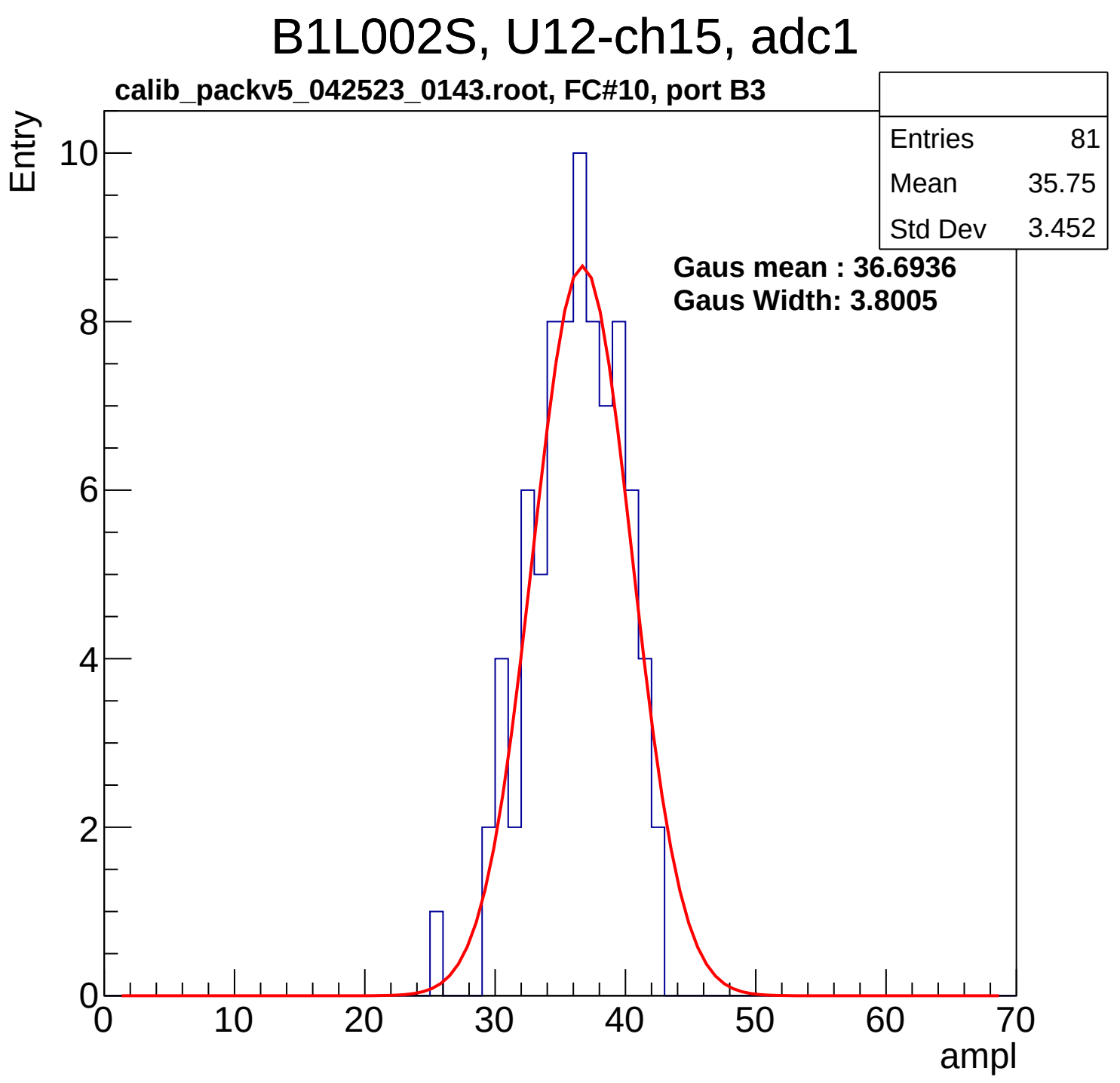
B1L002S, U12-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.75
Std Dev 3.452
Gaus mean : 36.6936
Gaus Width: 3.8005
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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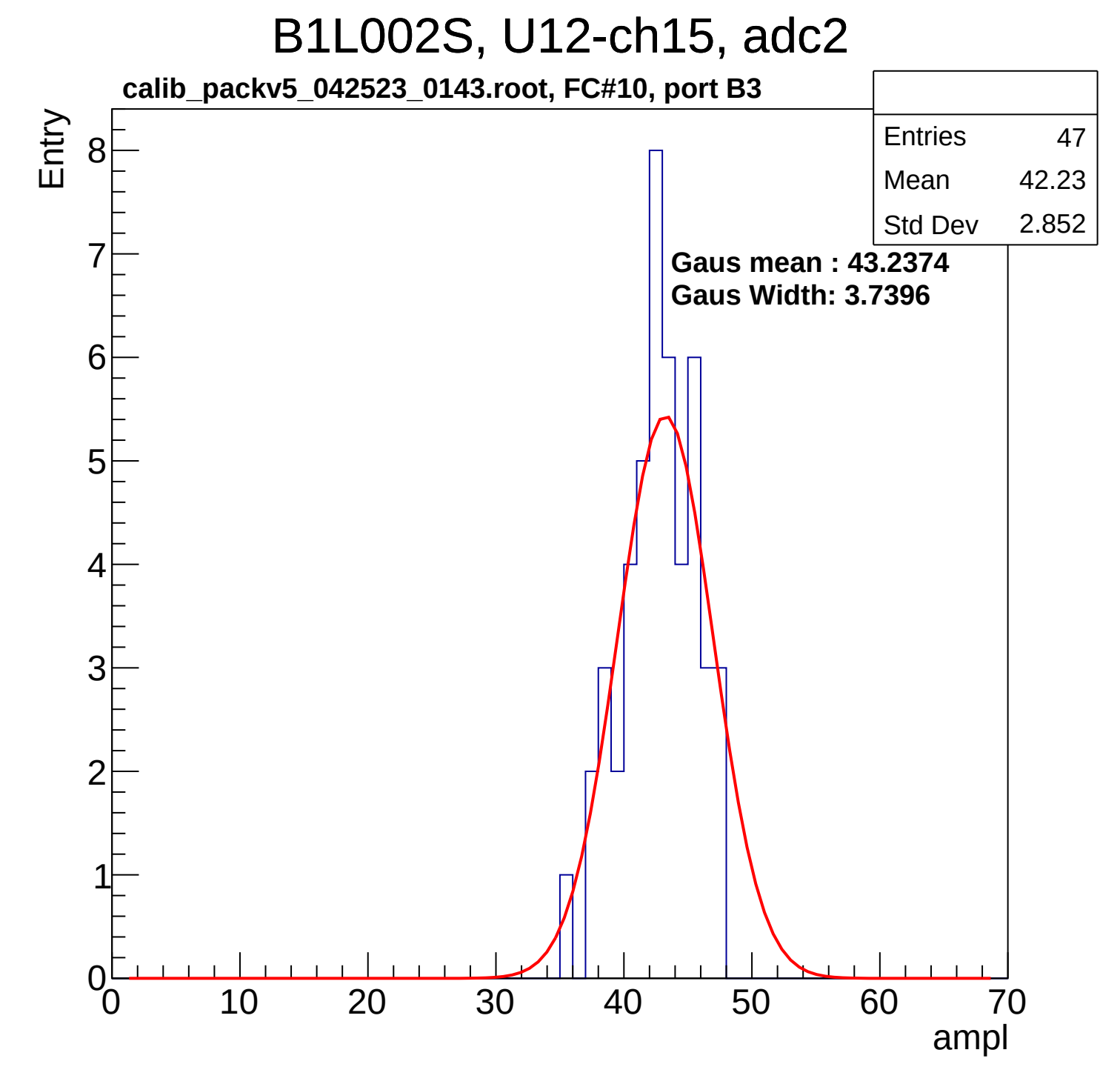
B1L002S, U12-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 42.23
Std Dev 2.852
Gaus mean : 43.2374
Gaus Width: 3.7396
Entry
ampl
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2
3
4
5
6
7
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0
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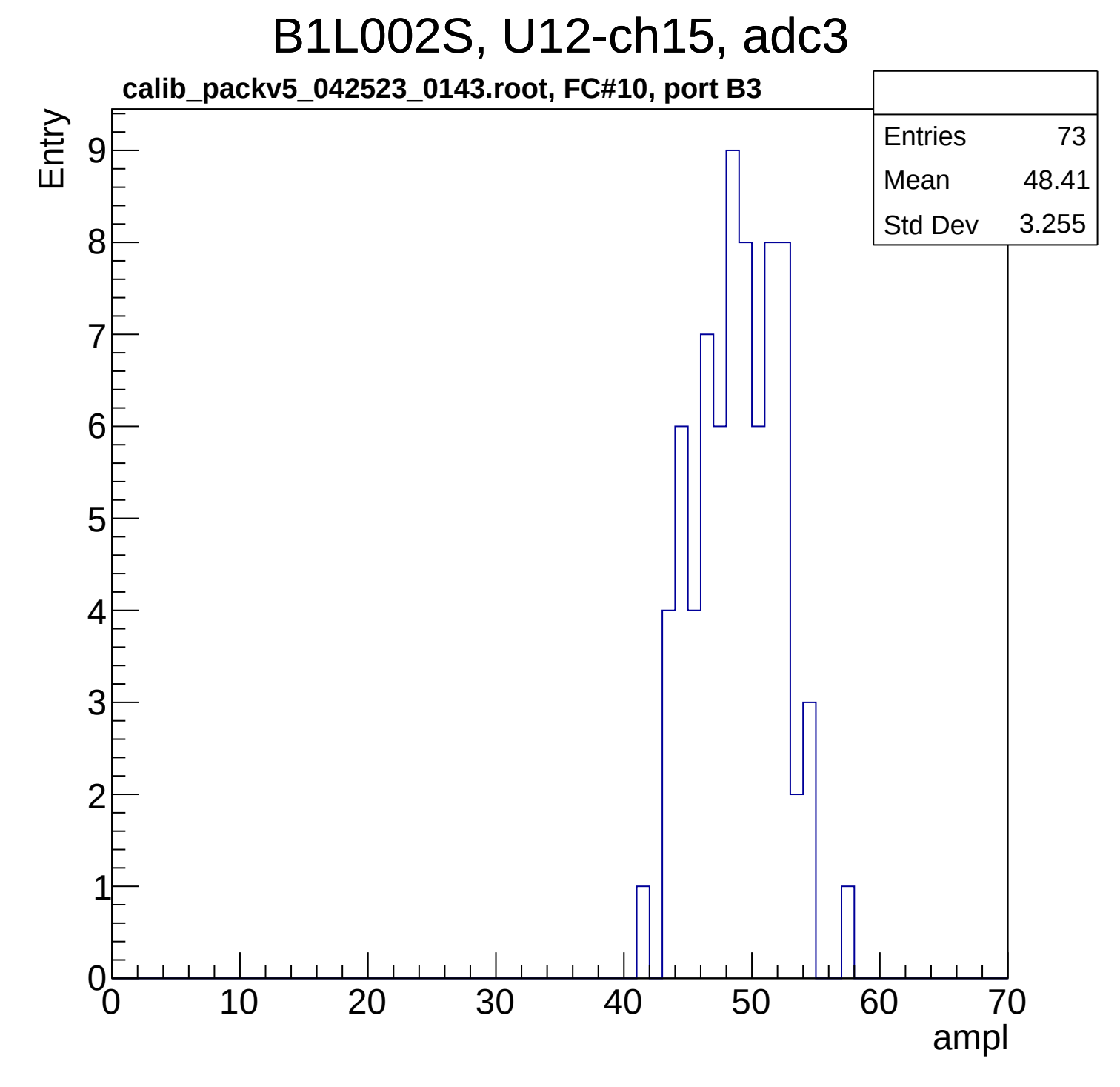
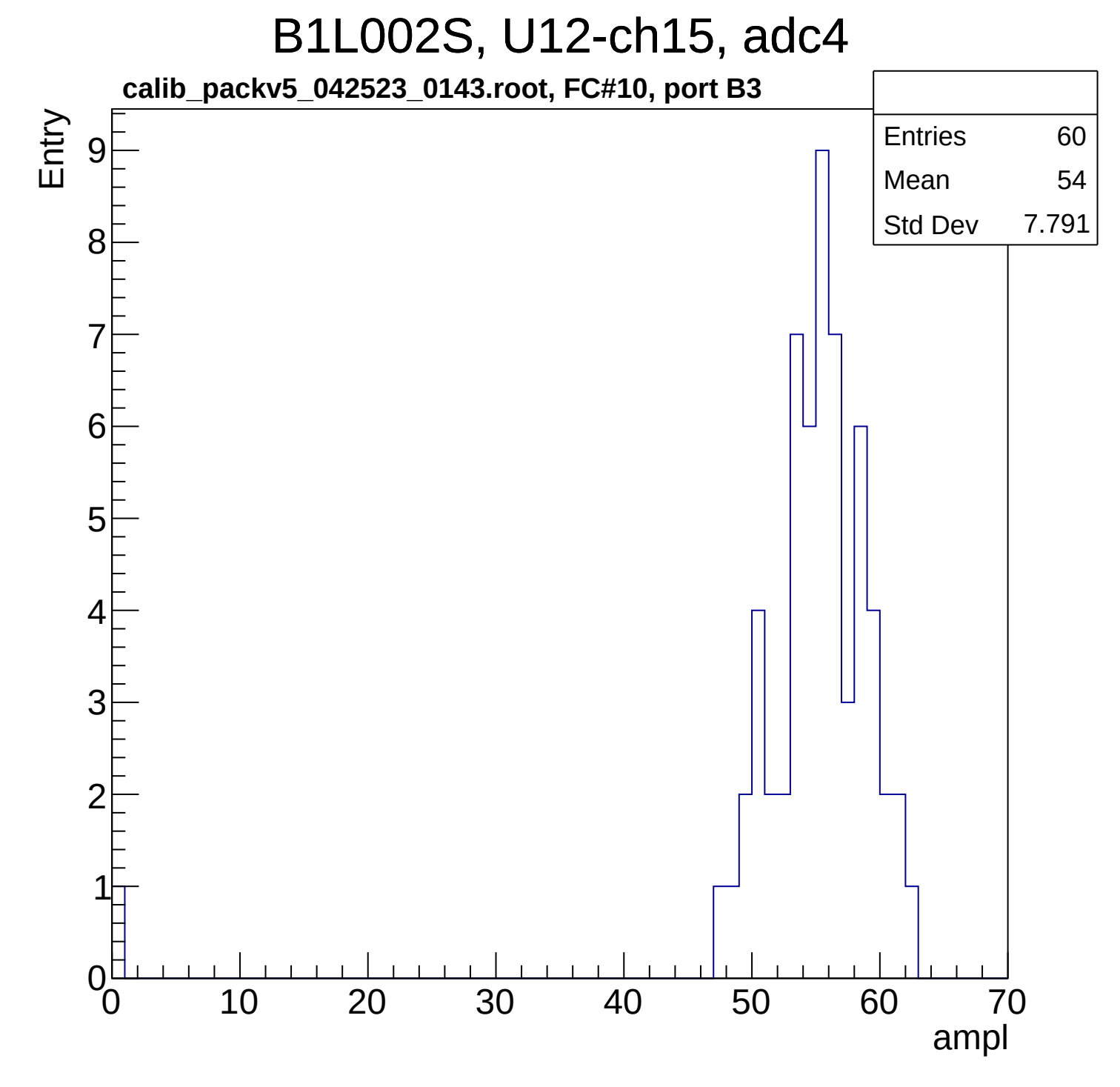

B1L002S, U12-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.41
Std Dev 3.255
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54
Std Dev 7.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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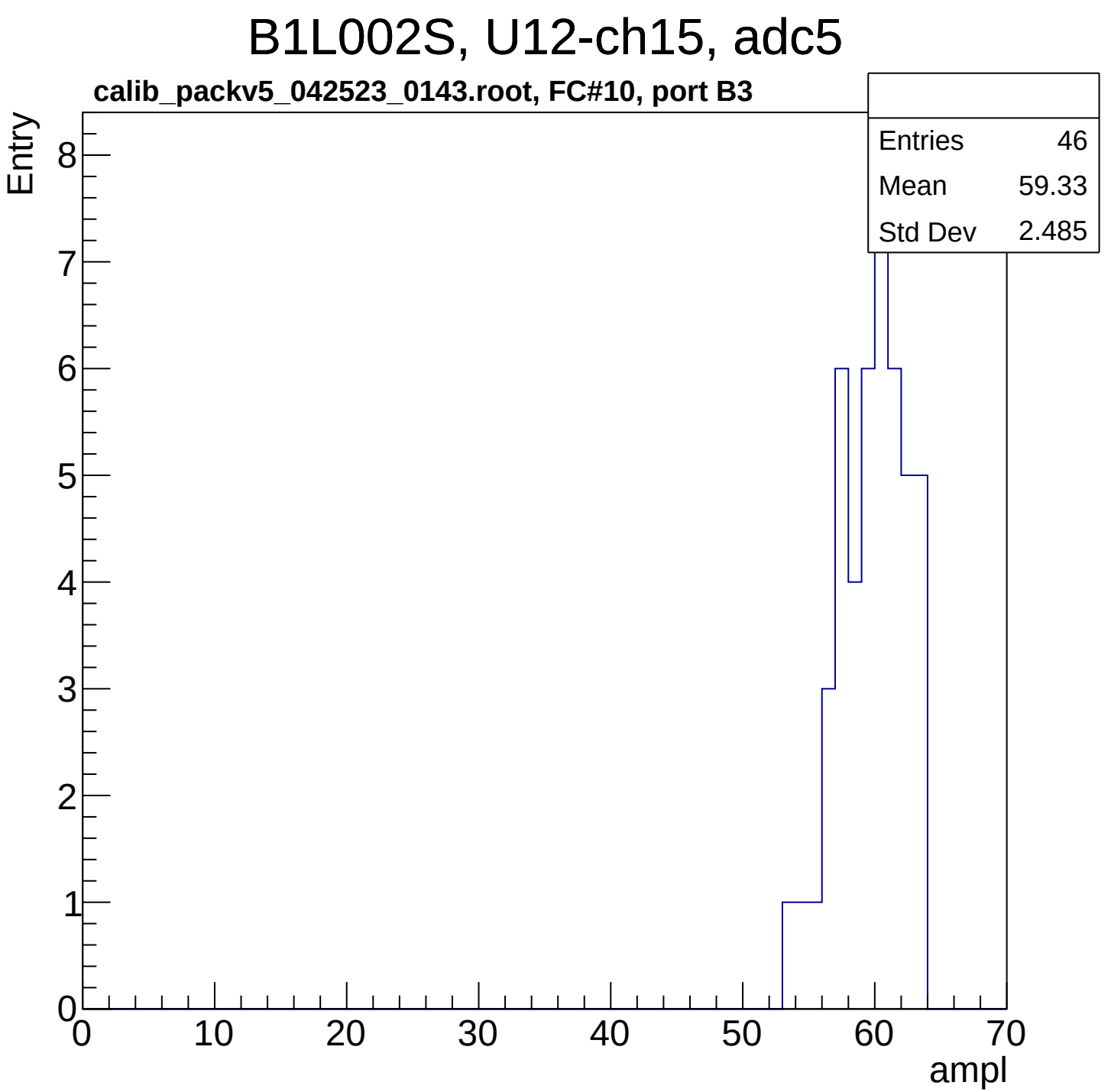
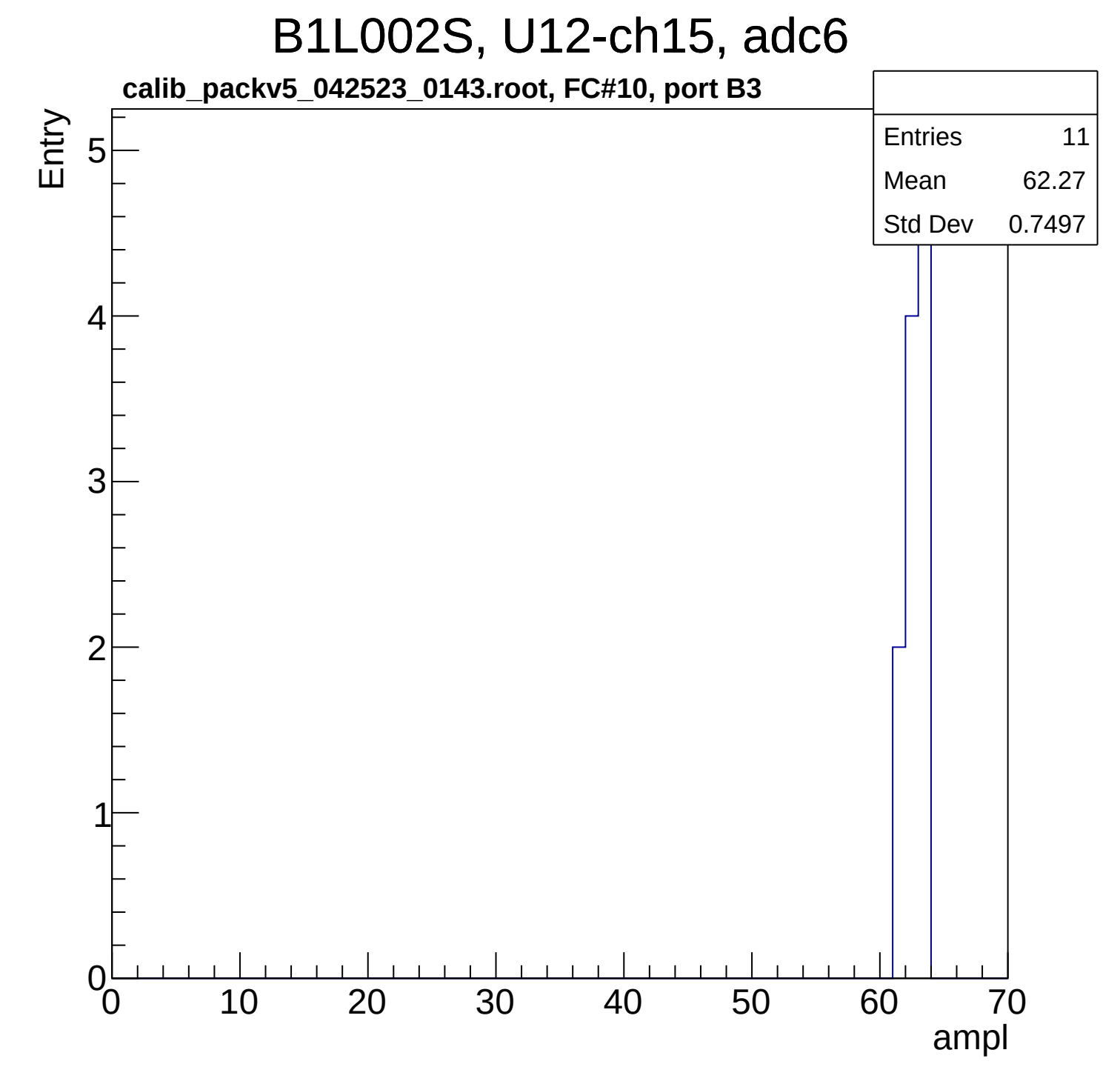
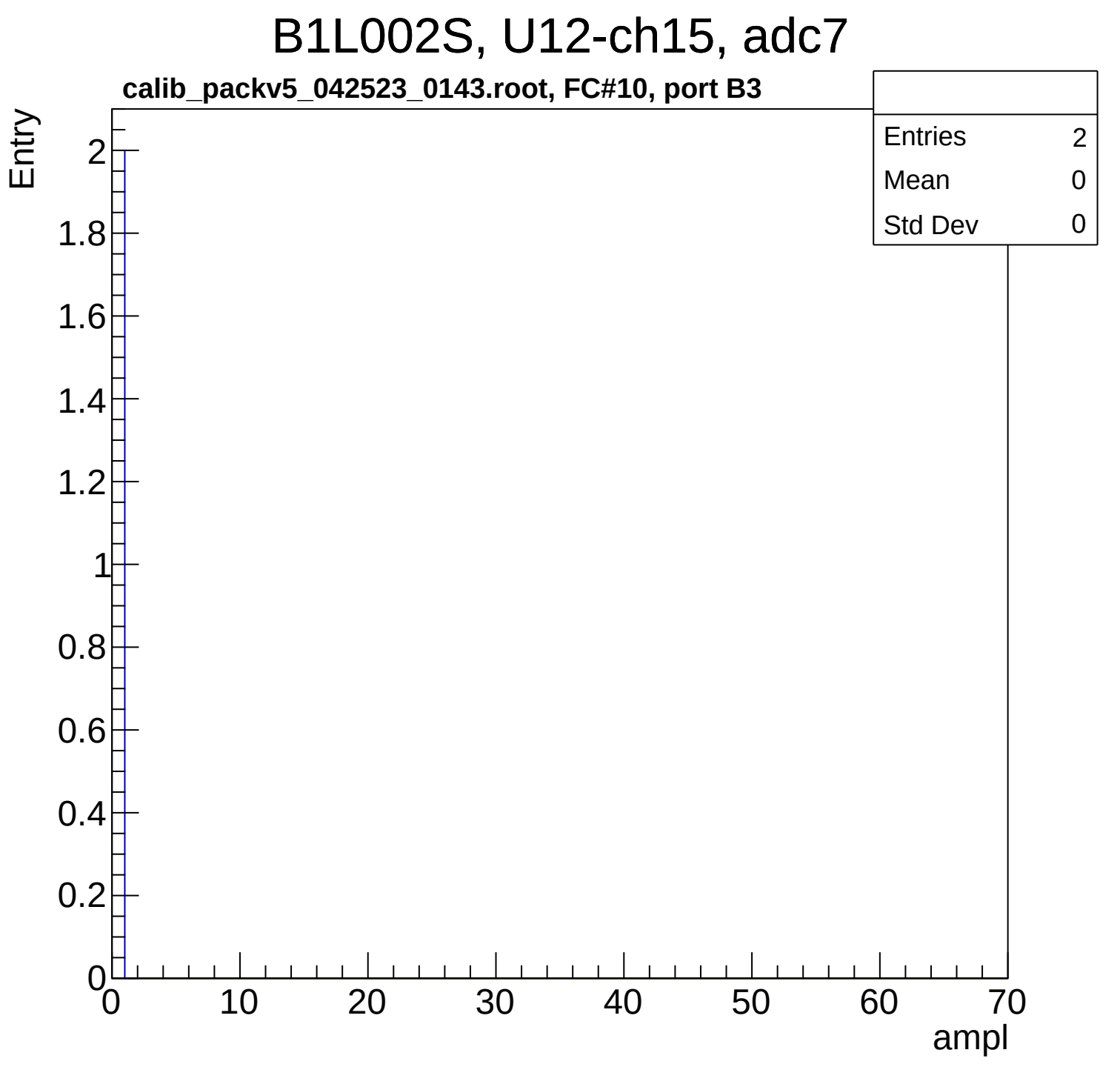

B1L002S, U12-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 59.33
Std Dev 2.485
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.27
Std Dev 0.7497
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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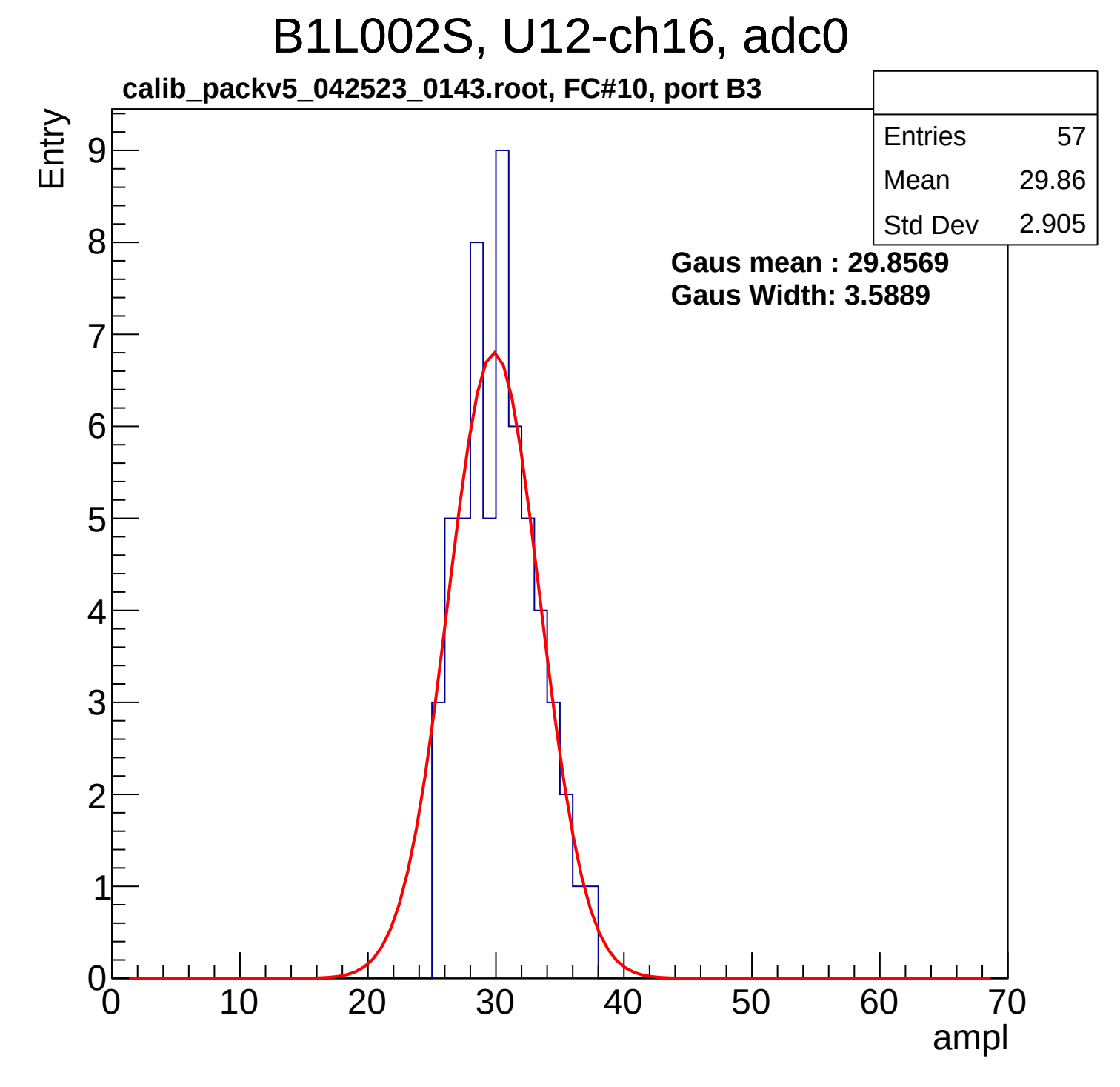
B1L002S, U12-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 29.86
Std Dev 2.905
Gaus mean : 29.8569
Gaus Width: 3.5889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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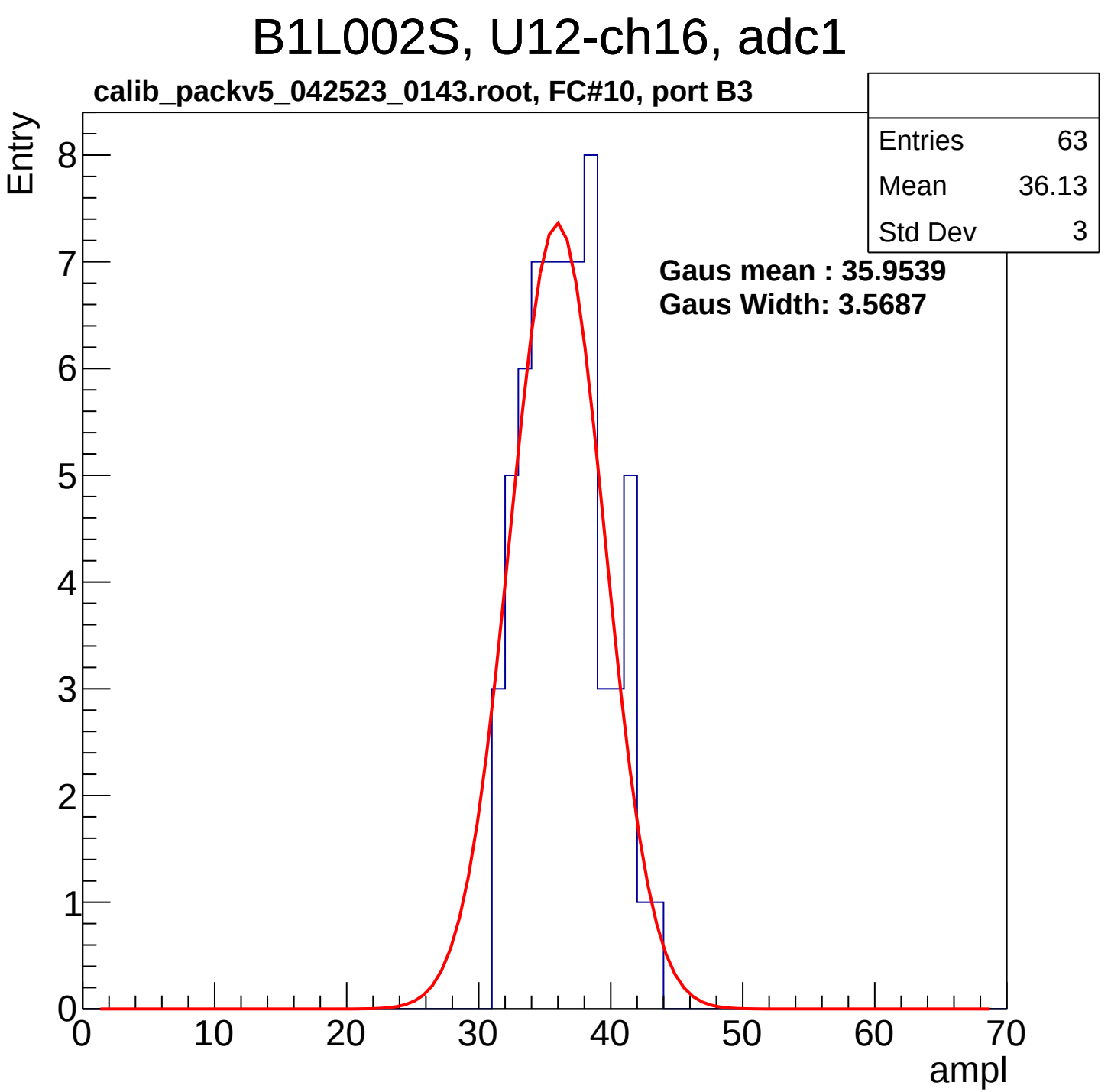
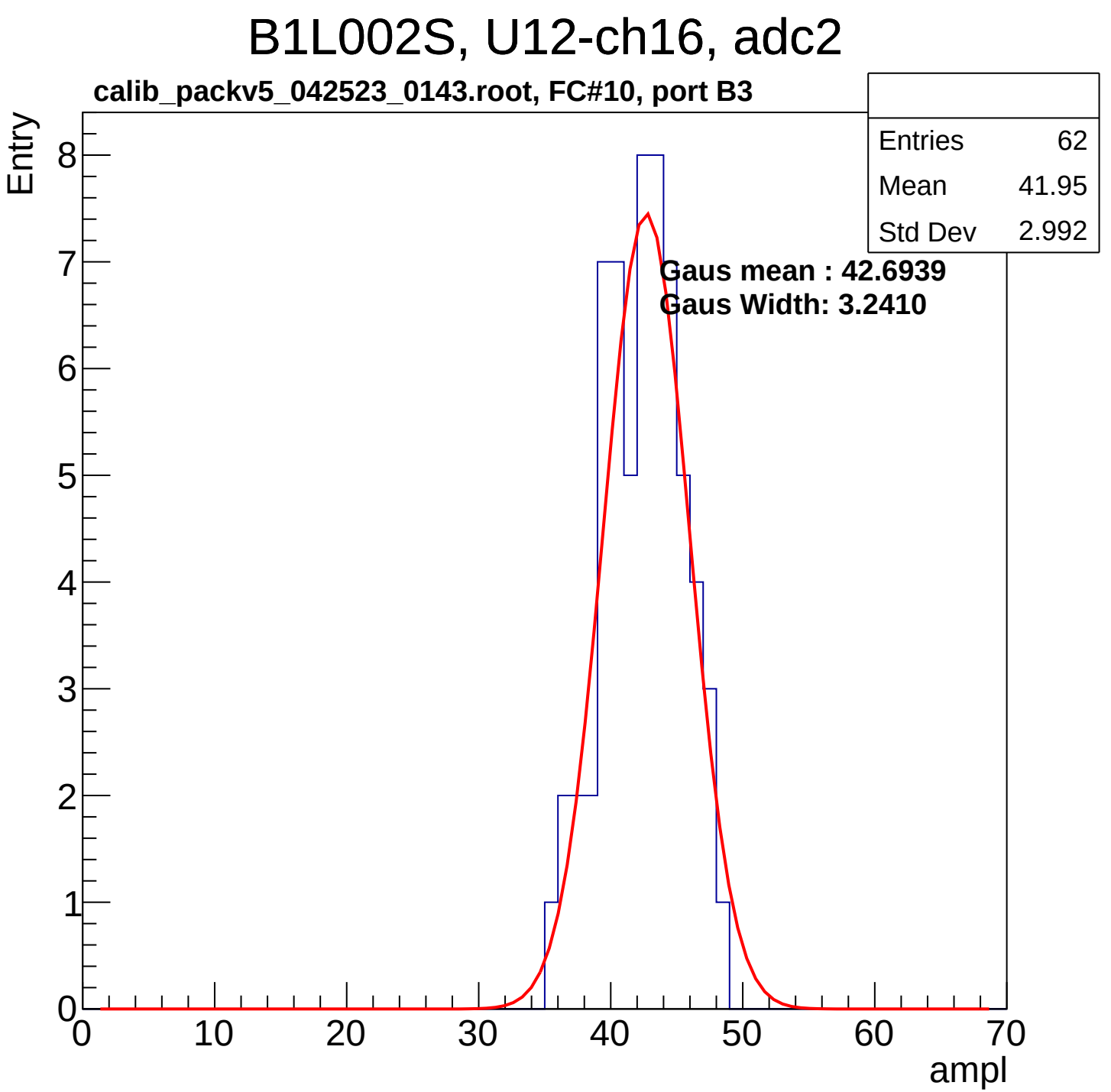
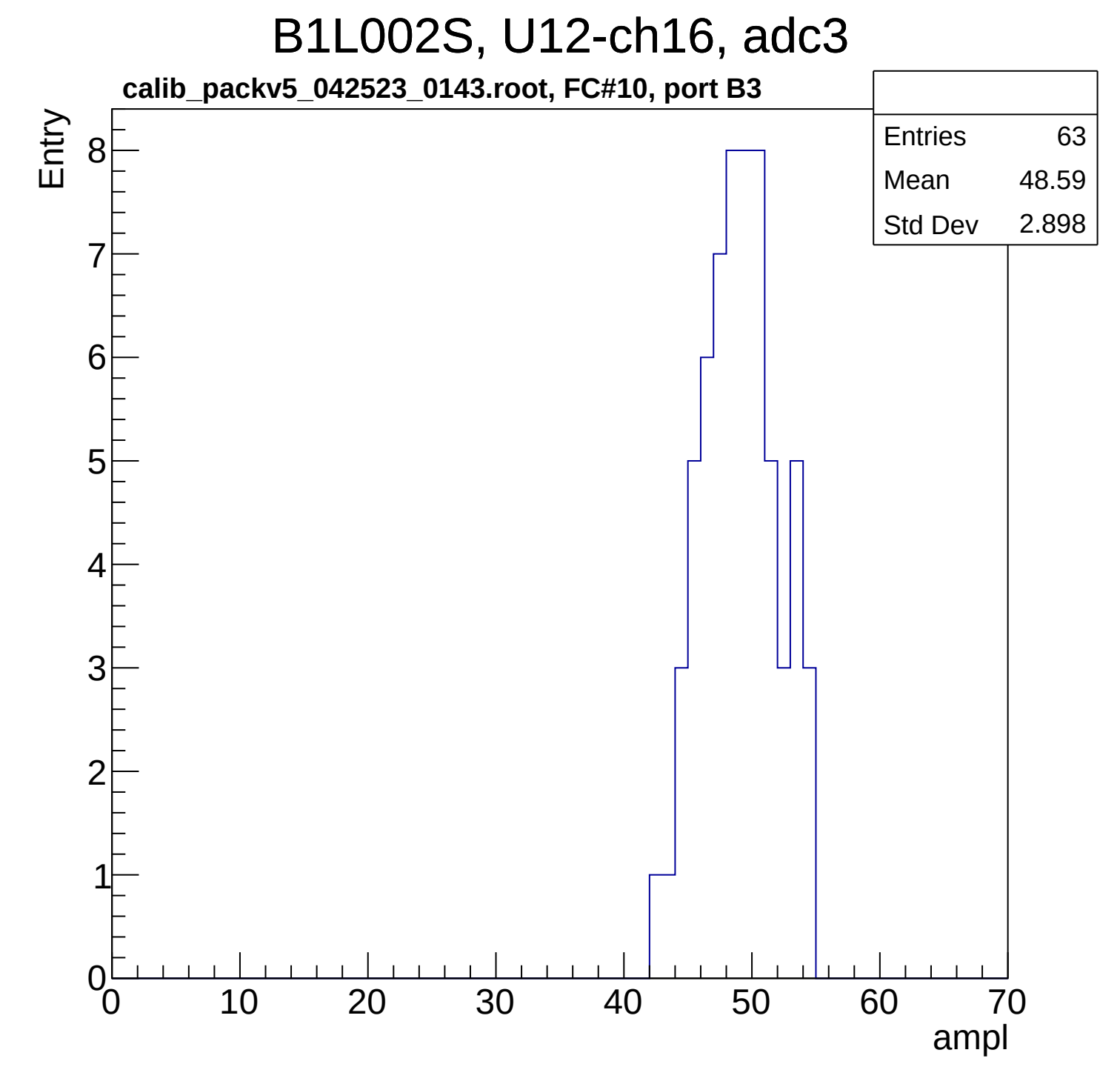
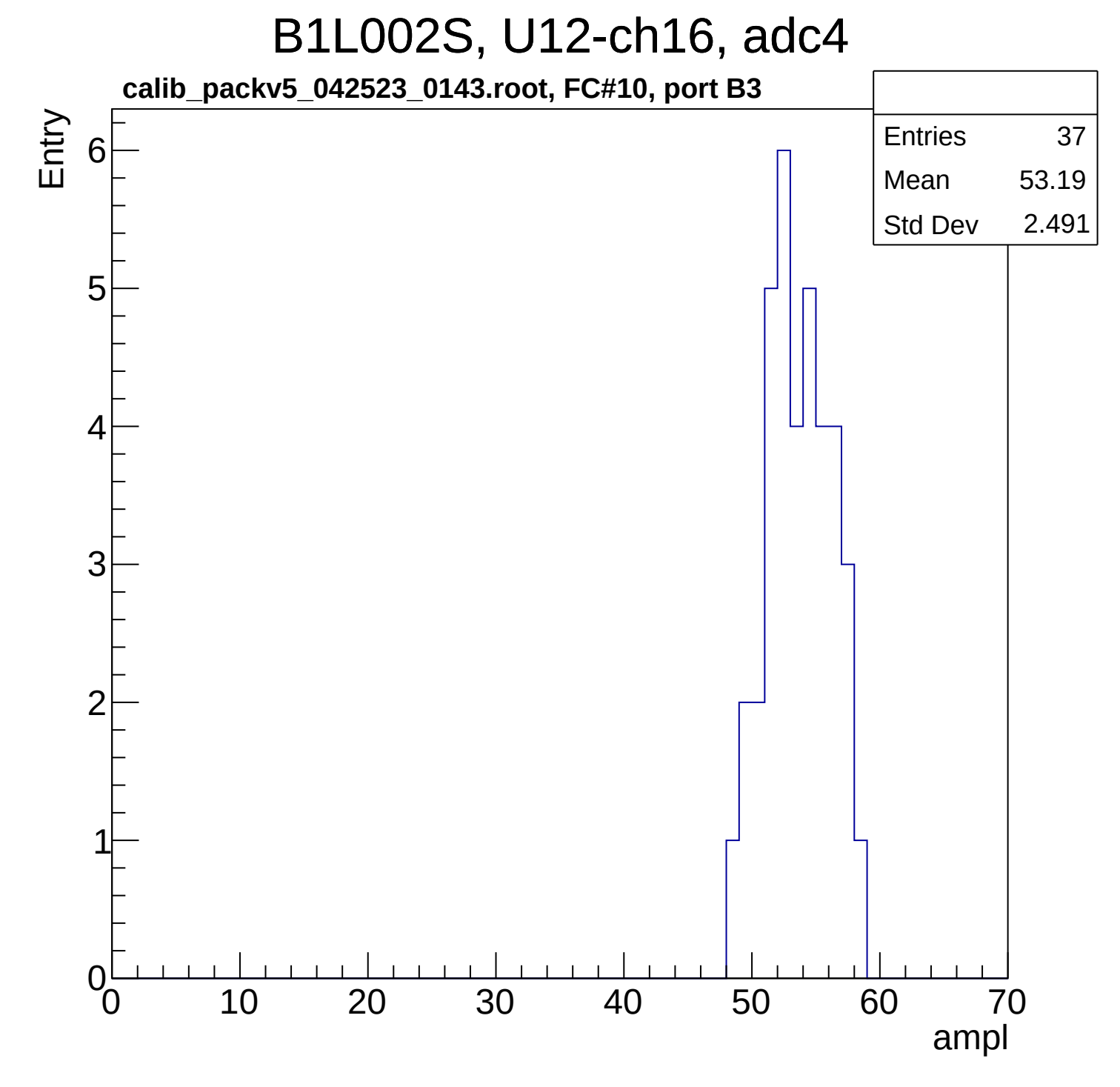
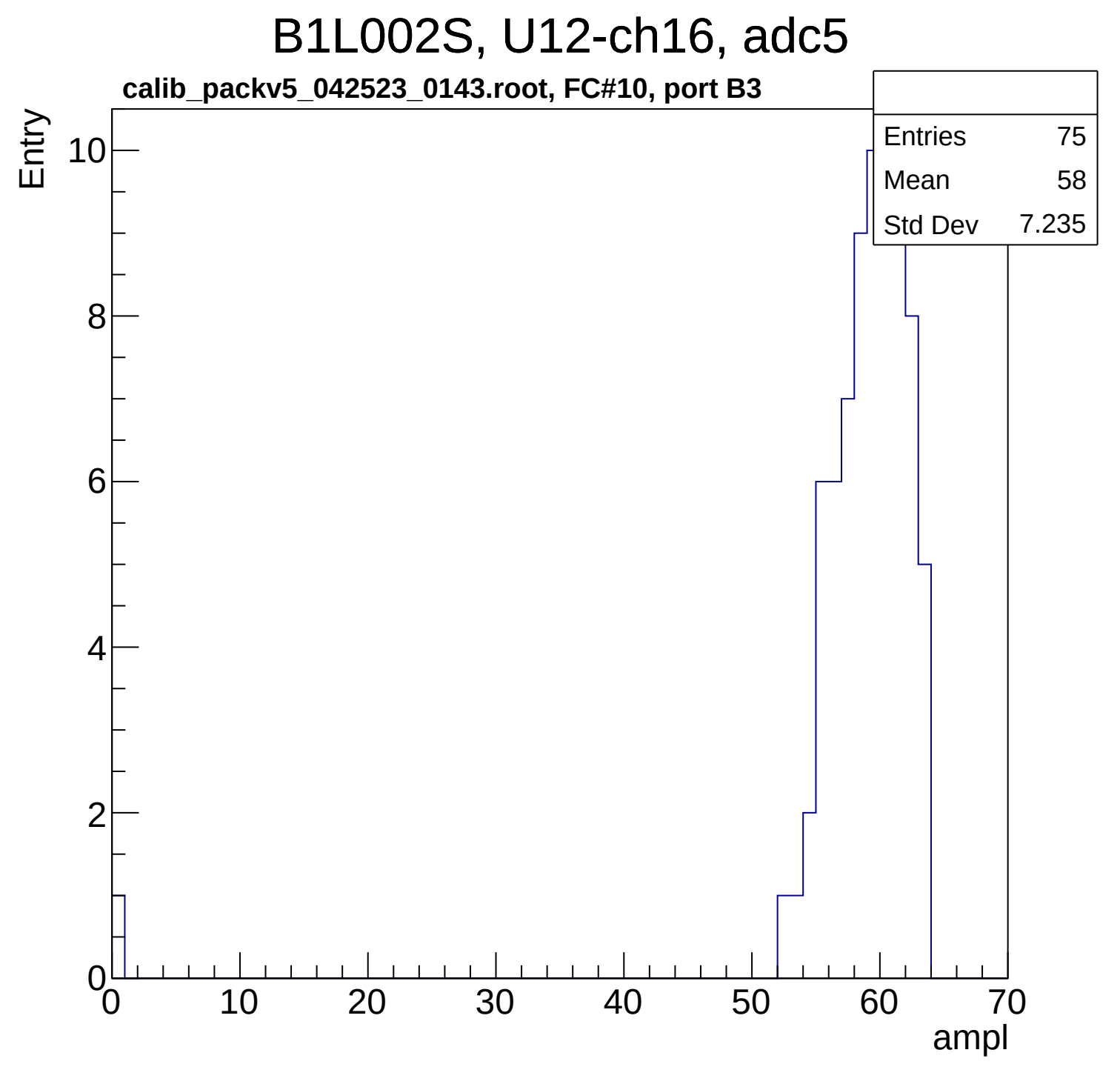
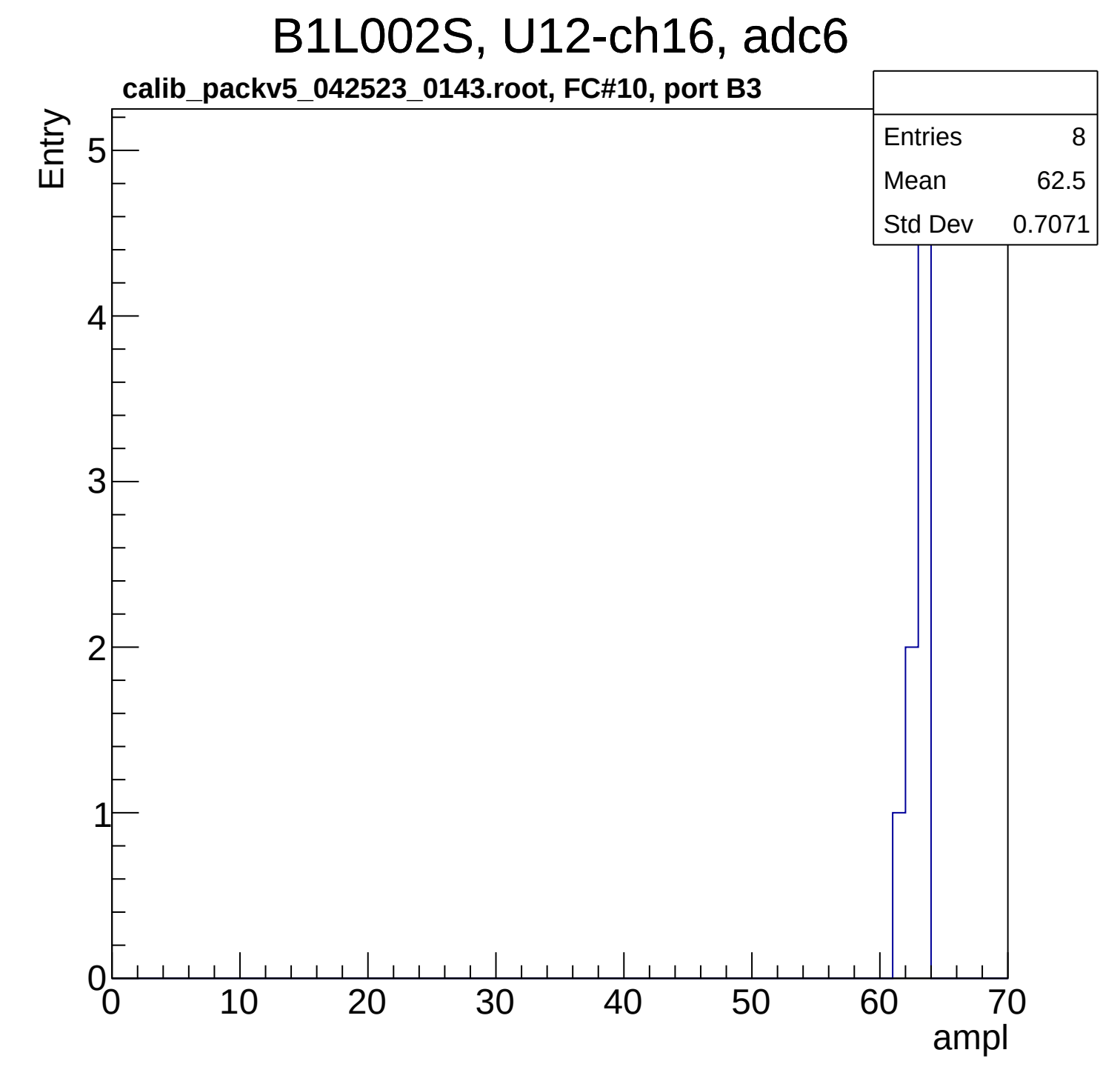

B1L002S, U12-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.13
Std Dev 3
Gaus mean : 35.9539
Gaus Width: 3.5687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

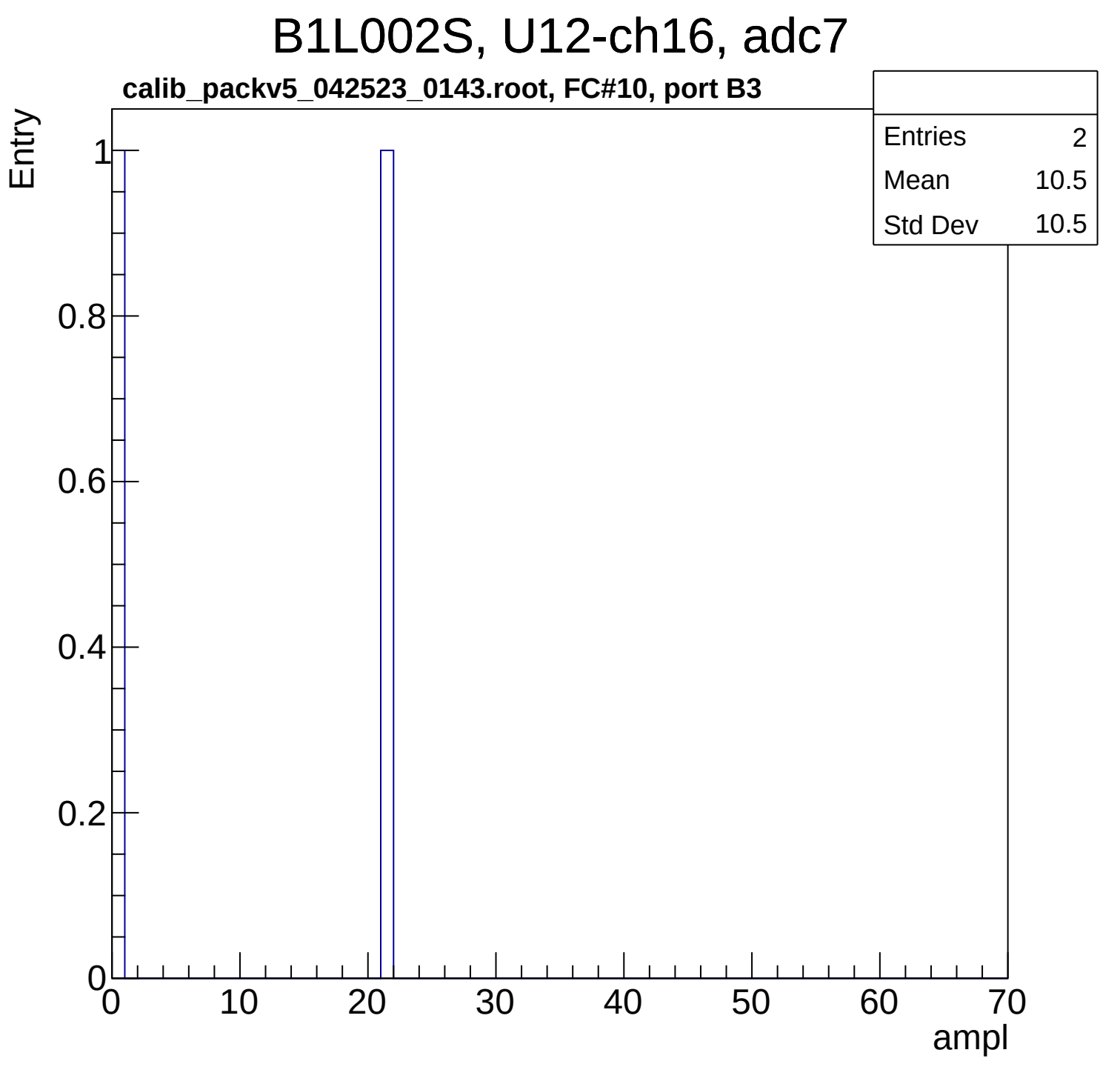

B1L002S, U12-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.95
Std Dev 2.992
Gaus mean : 42.6939
Gaus Width: 3.2410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

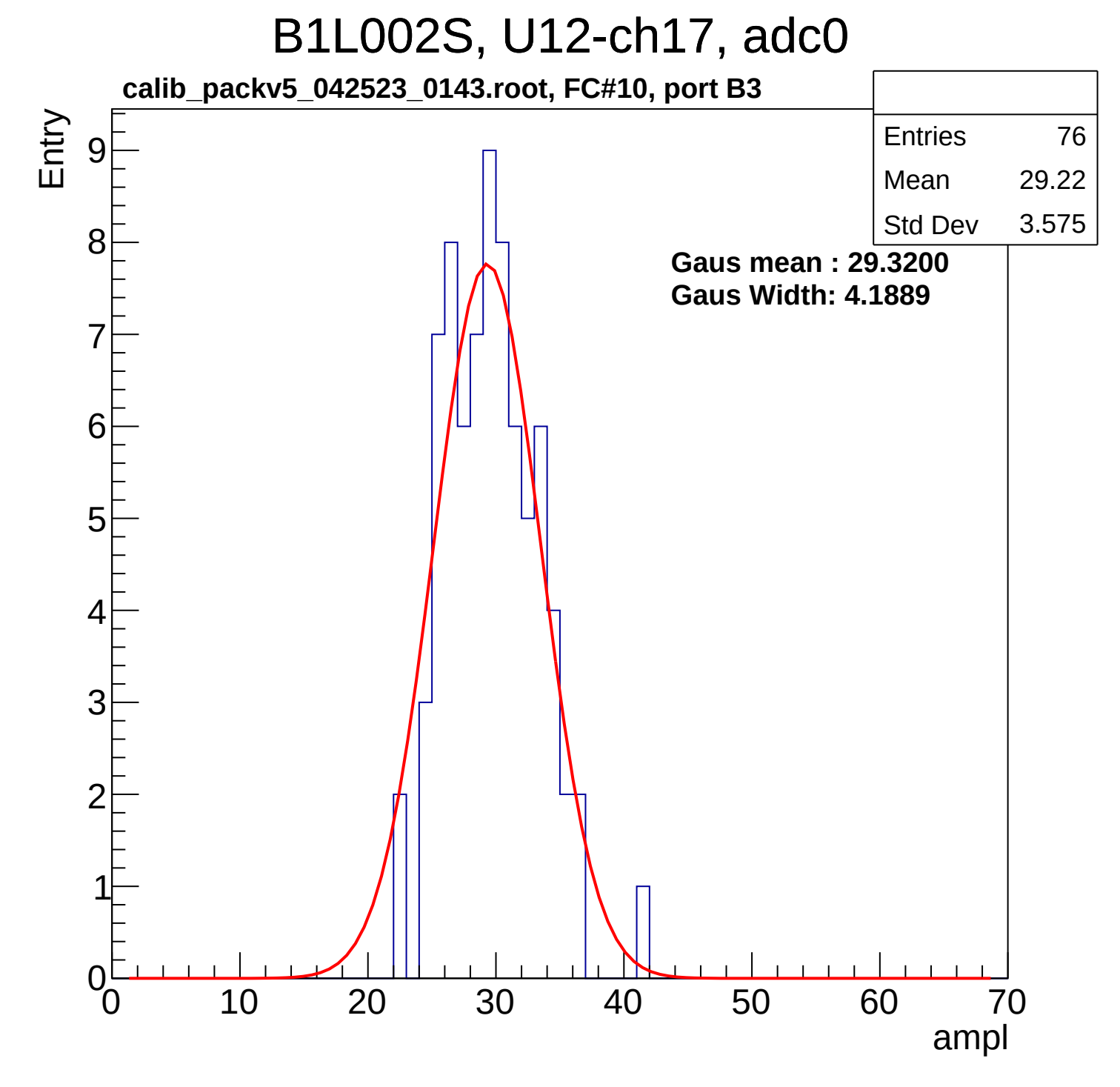

B1L002S, U12-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.59
Std Dev 2.898
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

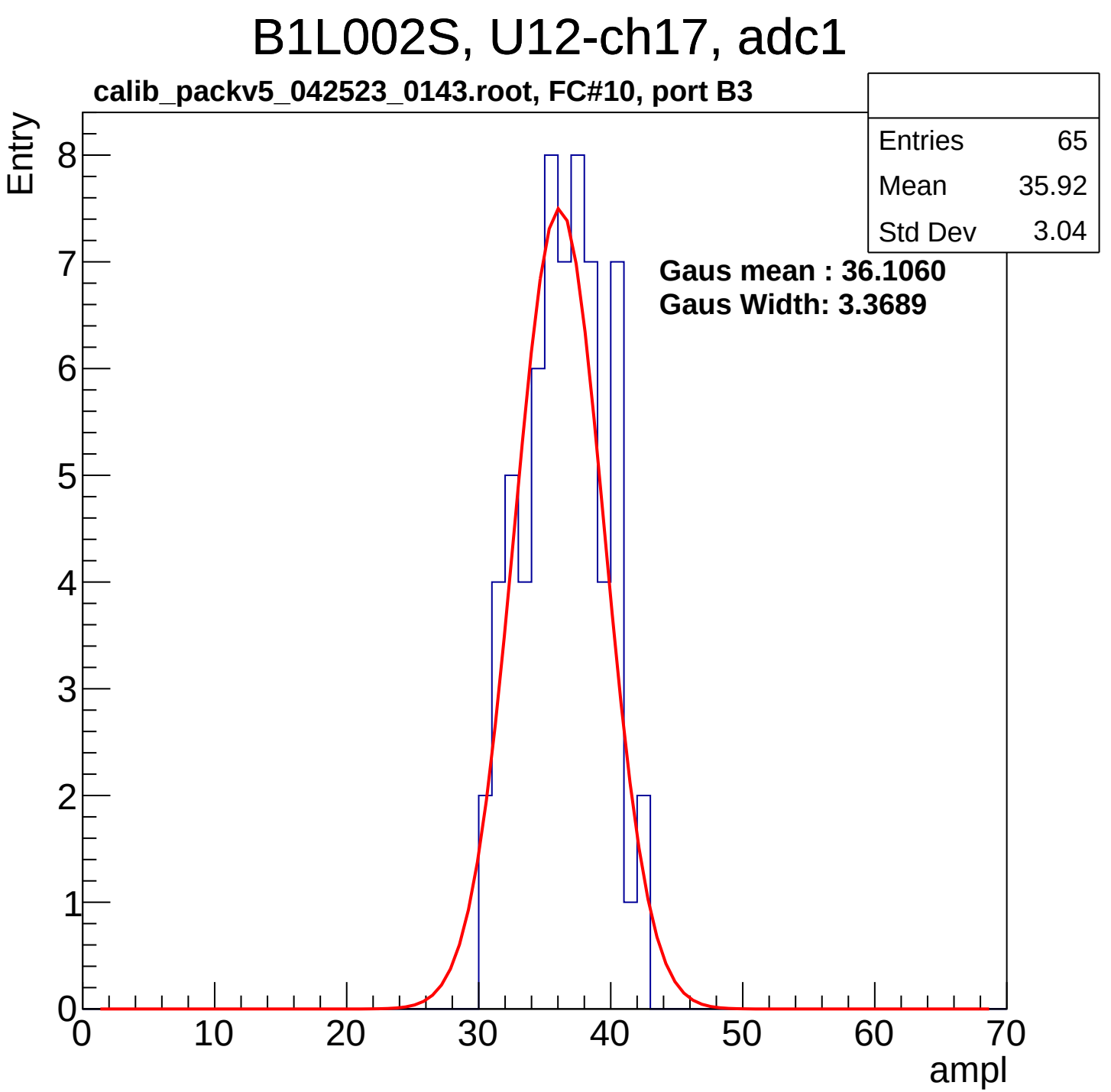

B1L002S, U12-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 53.19
Std Dev 2.491
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

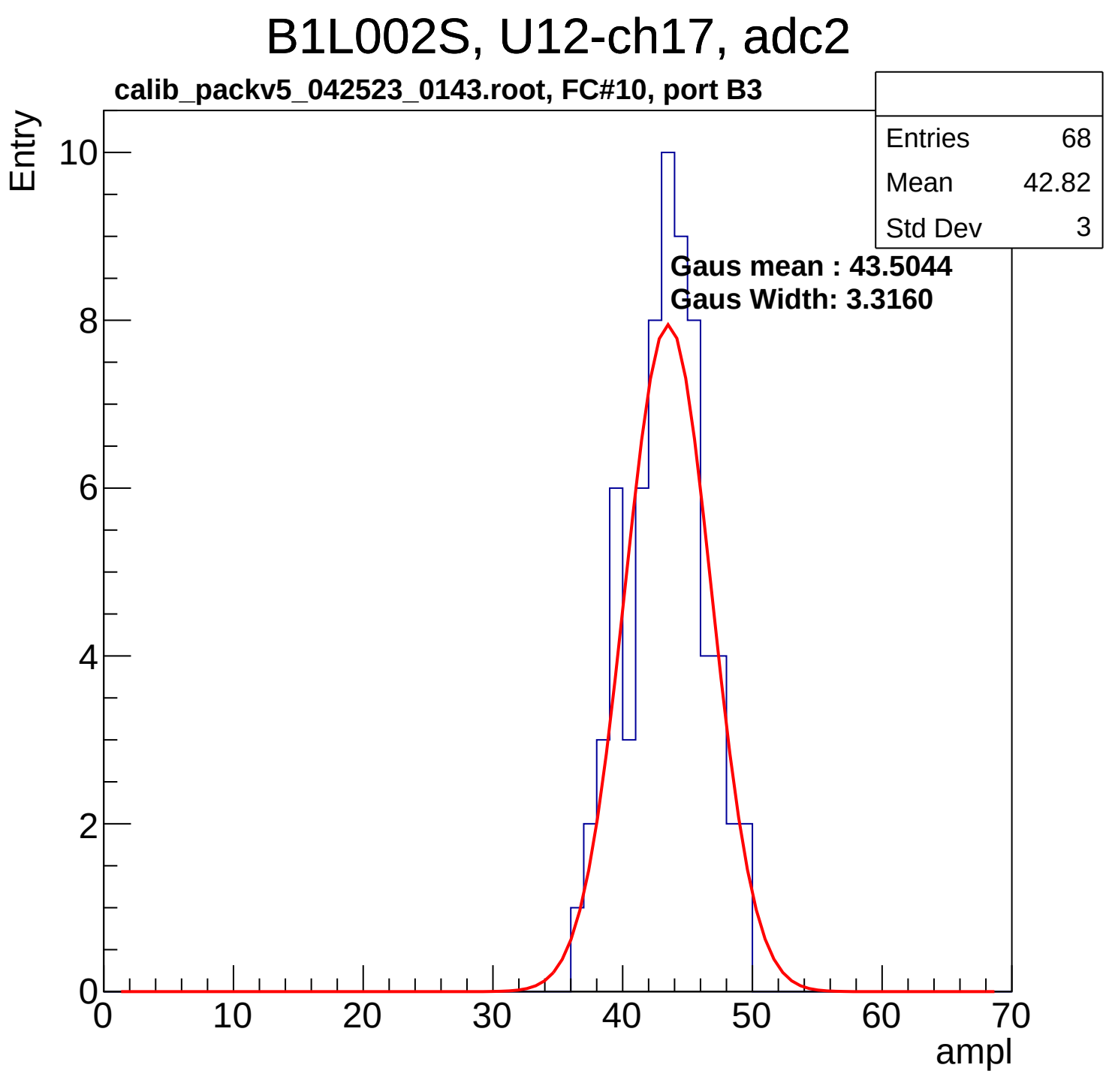

B1L002S, U12-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 58
Std Dev 7.235
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

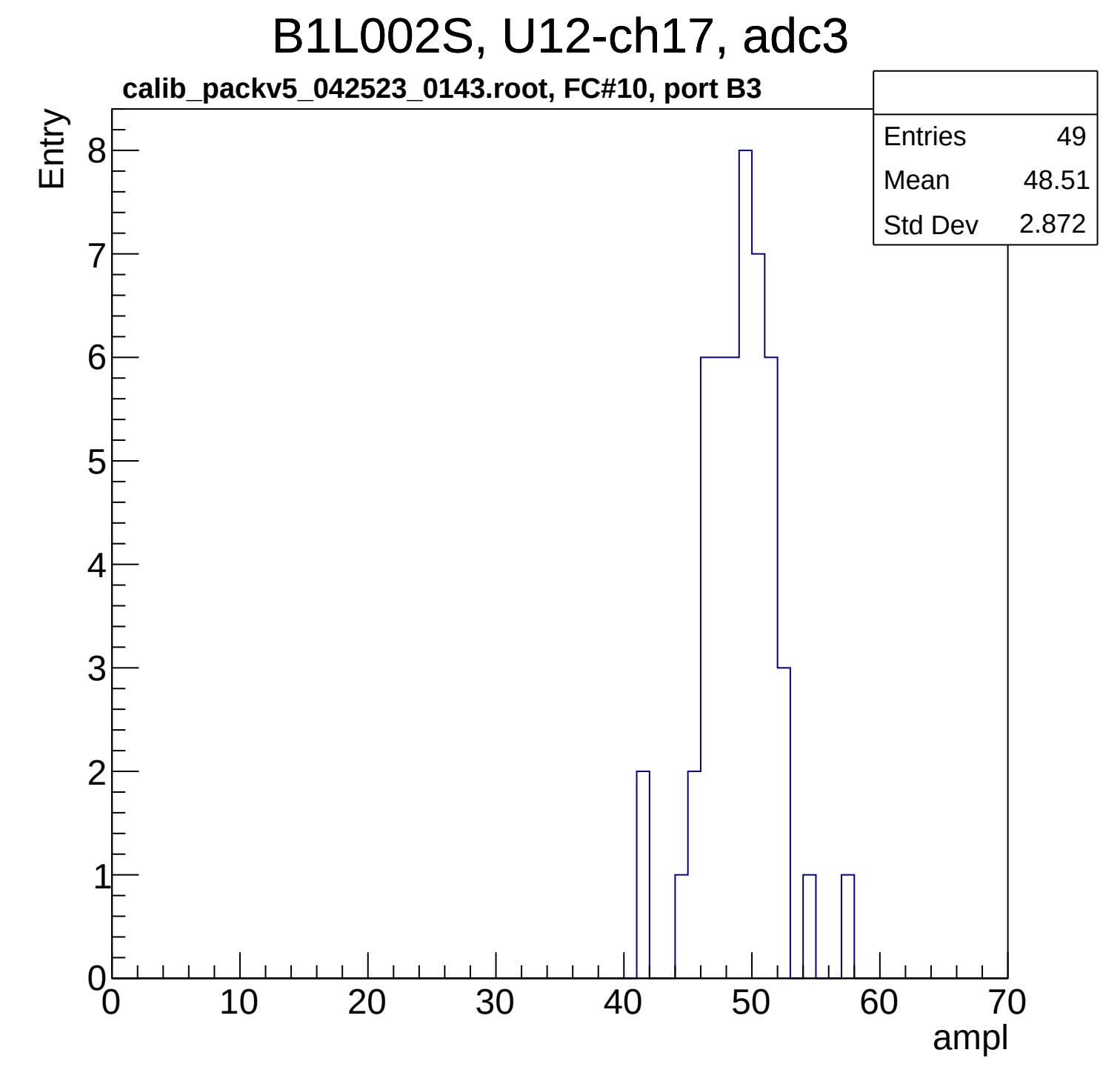

B1L002S, U12-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

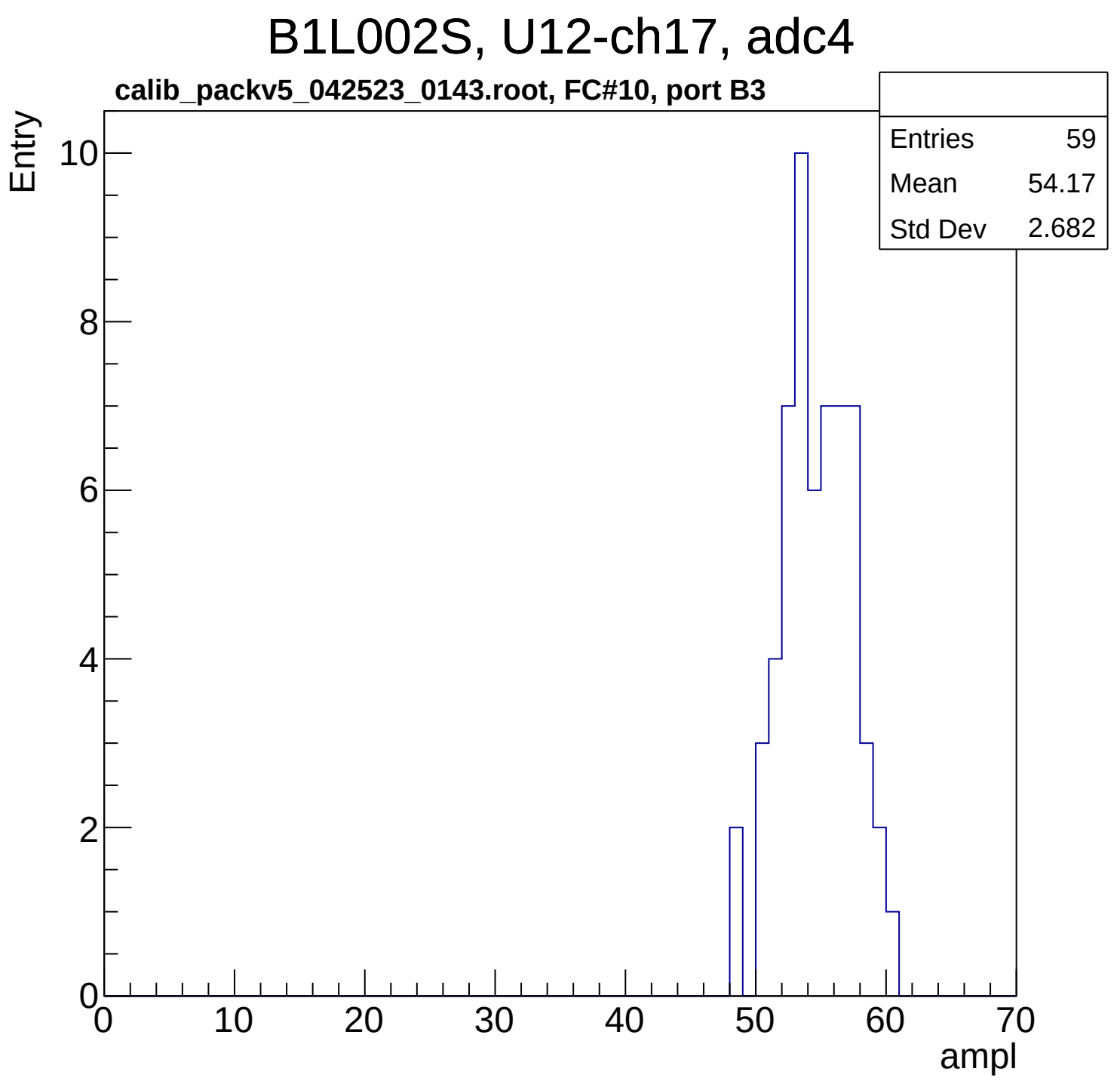

B1L002S, U12-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

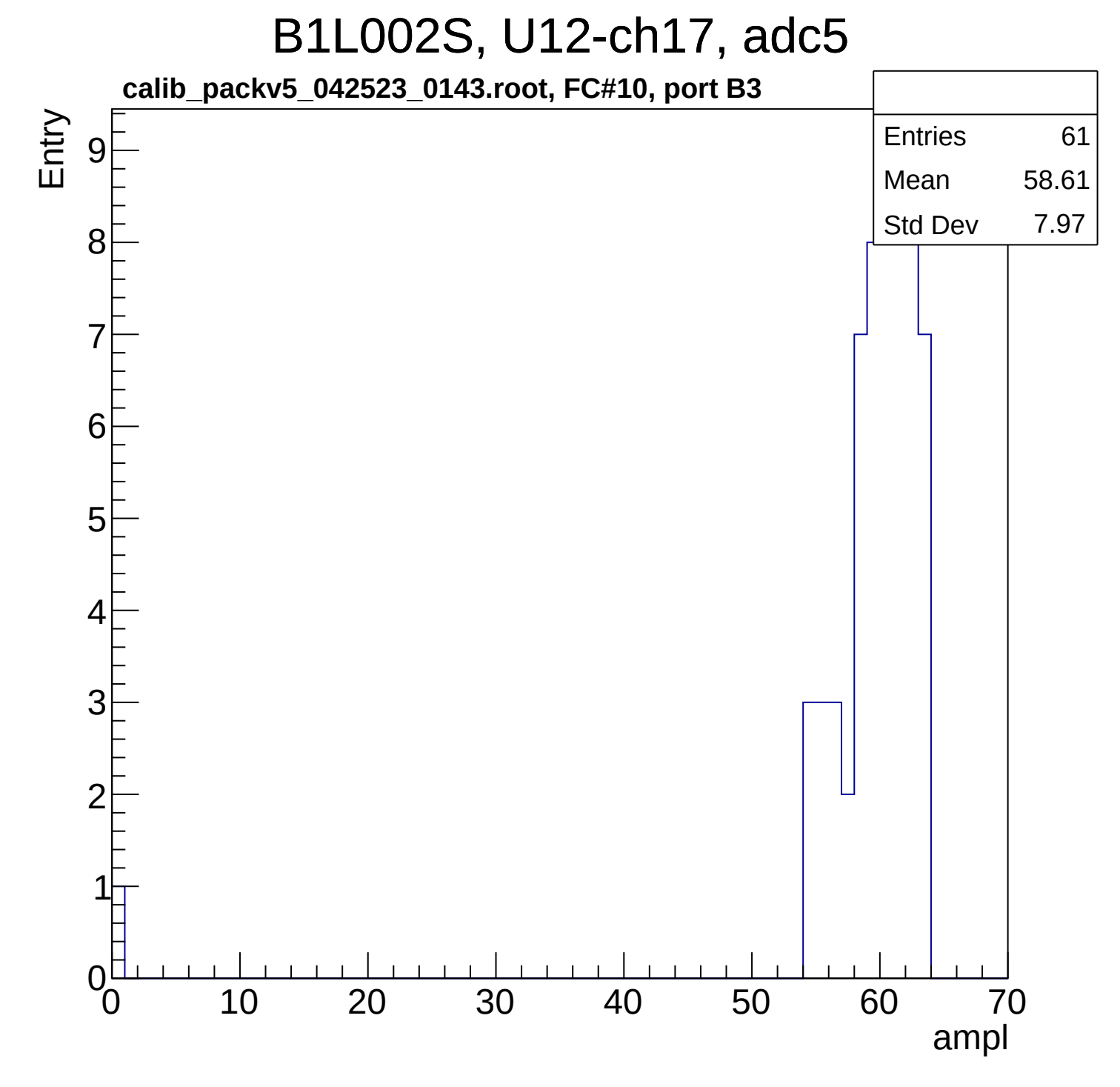

B1L002S, U12-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 29.22
Std Dev 3.575
Gaus mean : 29.3200
Gaus Width: 4.1889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

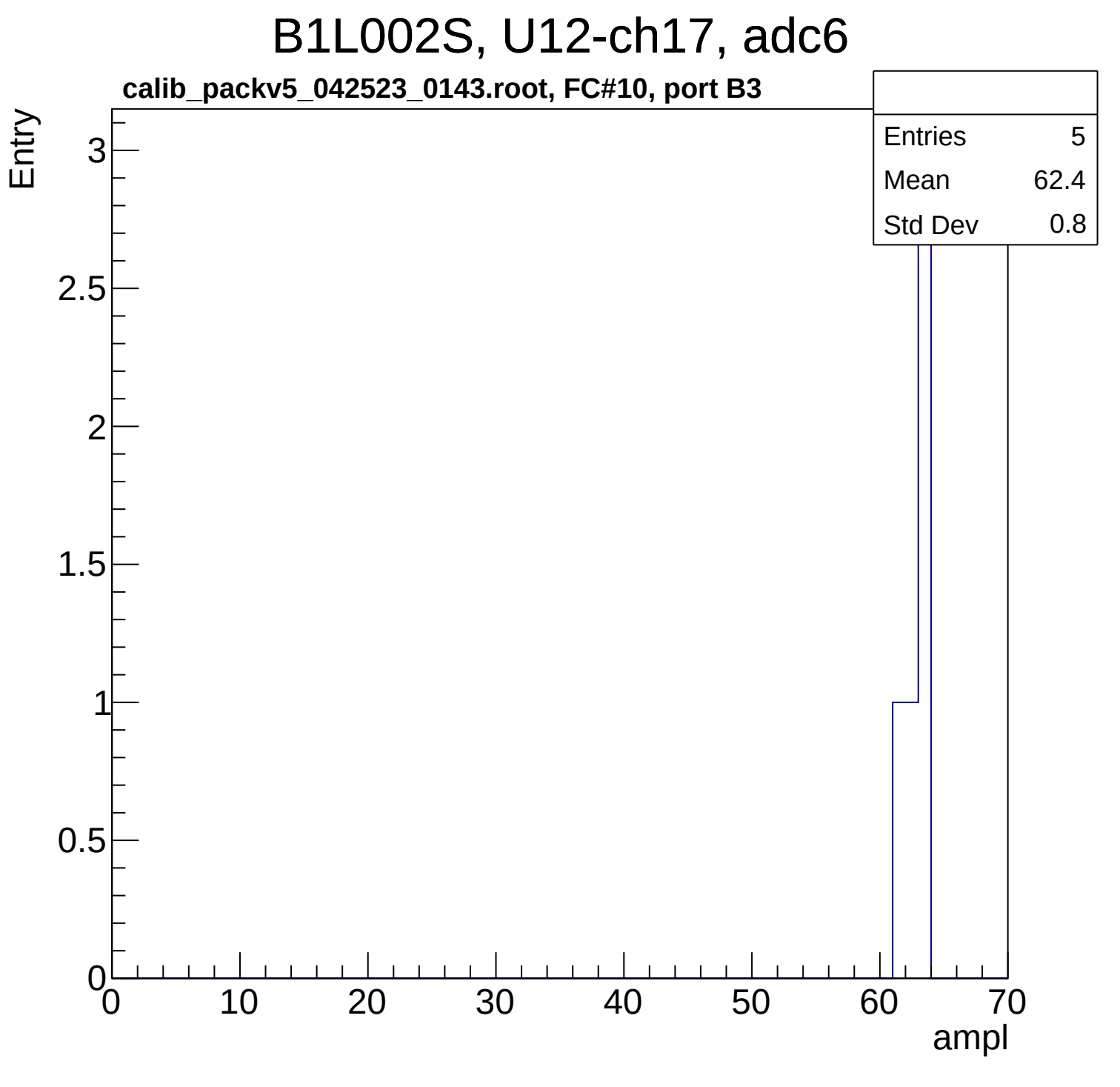

B1L002S, U12-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.92
Std Dev 3.04
Gaus mean : 36.1060
Gaus Width: 3.3689
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

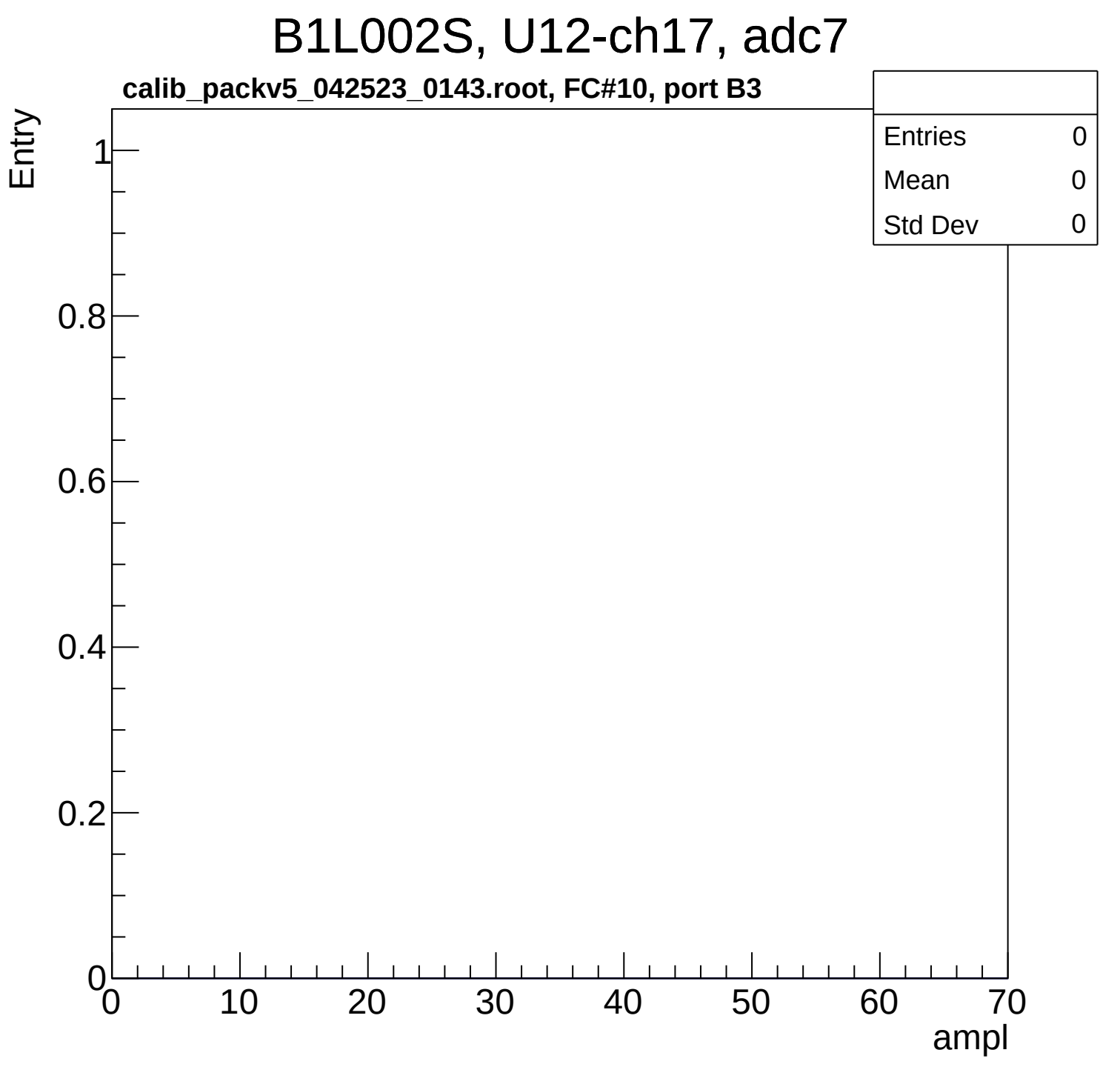

B1L002S, U12-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42.82
Std Dev 3
Gaus mean : 43.5044
Gaus Width: 3.3160
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

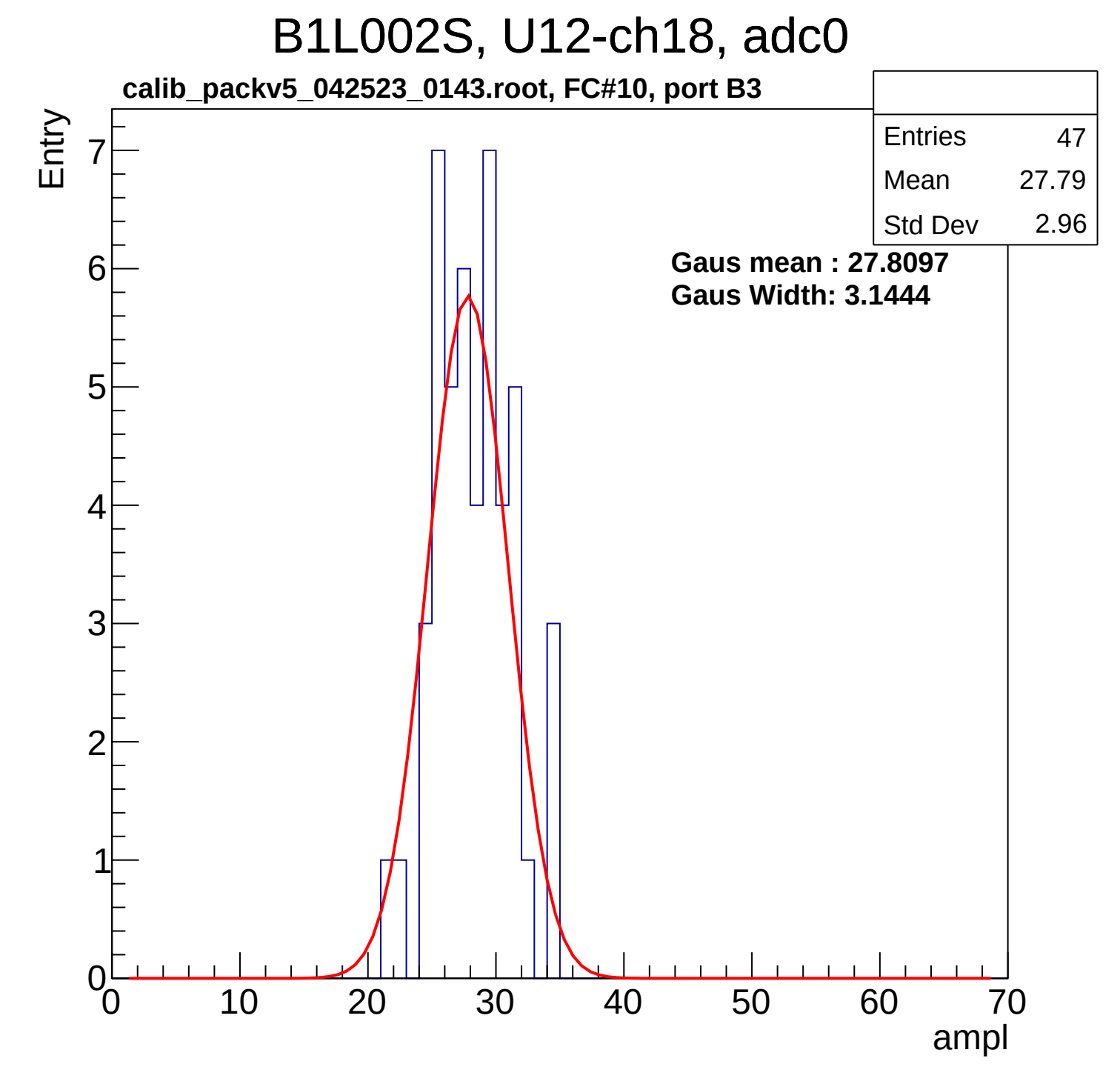

B1L002S, U12-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 48.51
Std Dev 2.872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

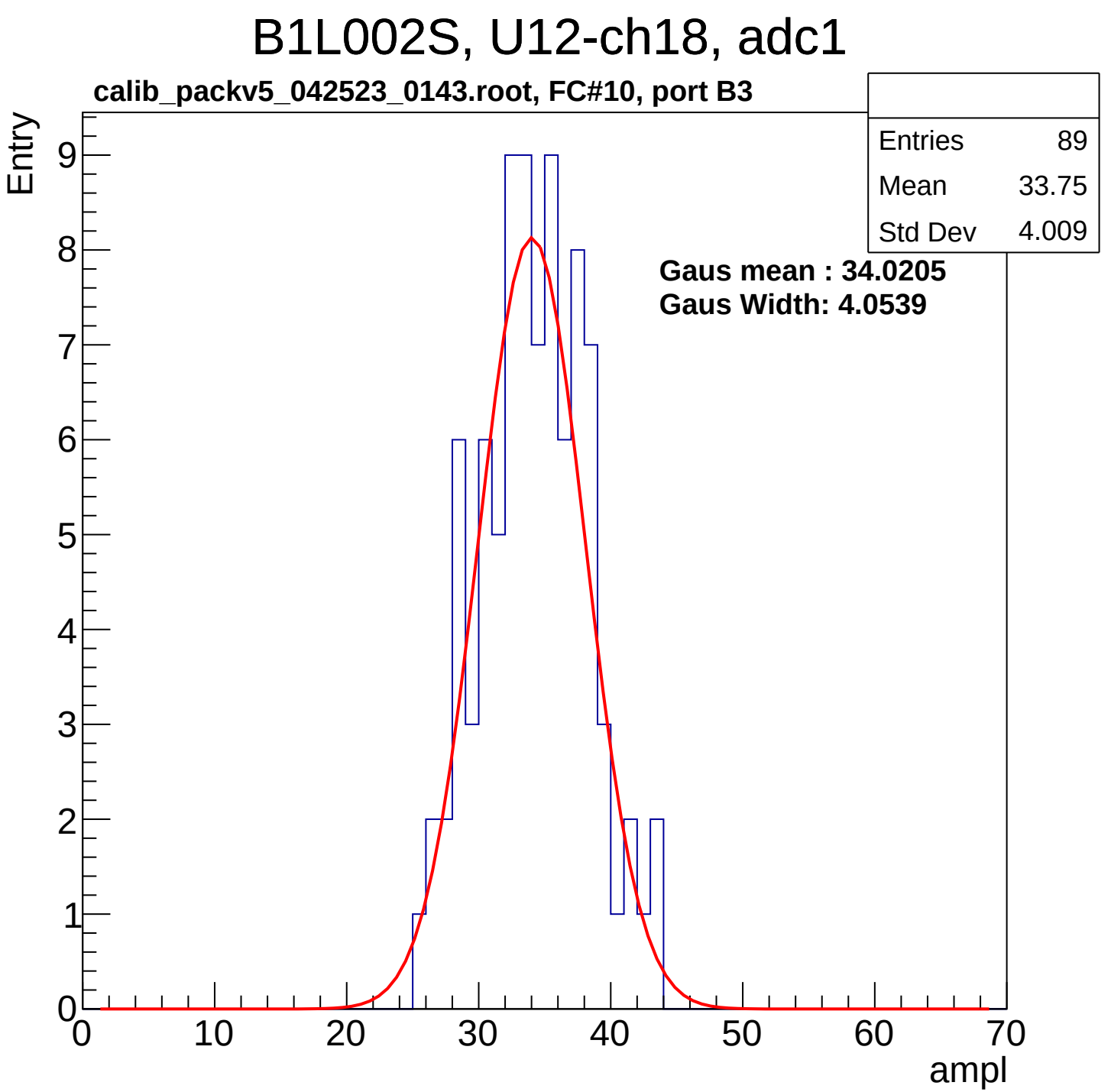

B1L002S, U12-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.17
Std Dev 2.682
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

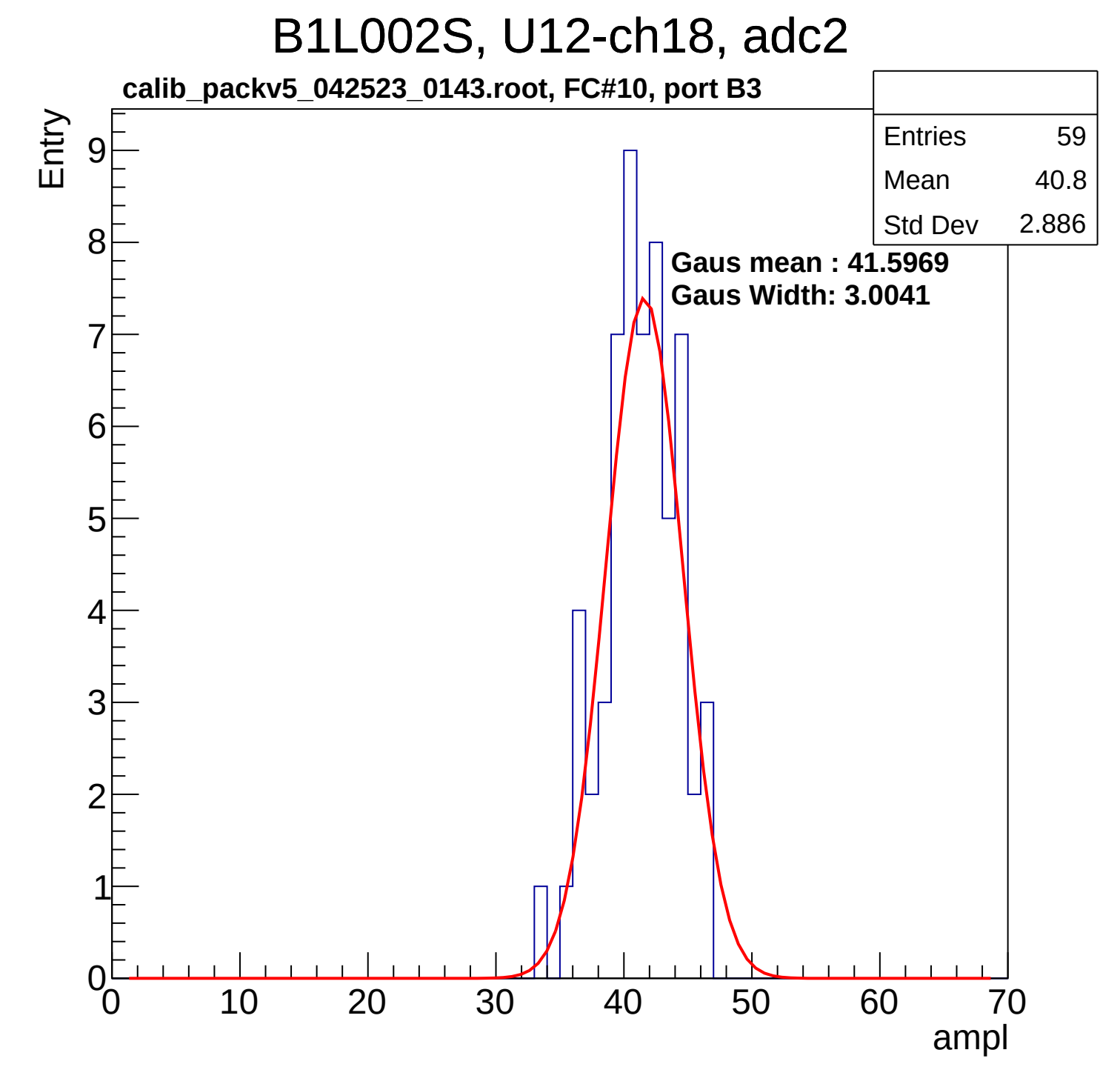

B1L002S, U12-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.61
Std Dev 7.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

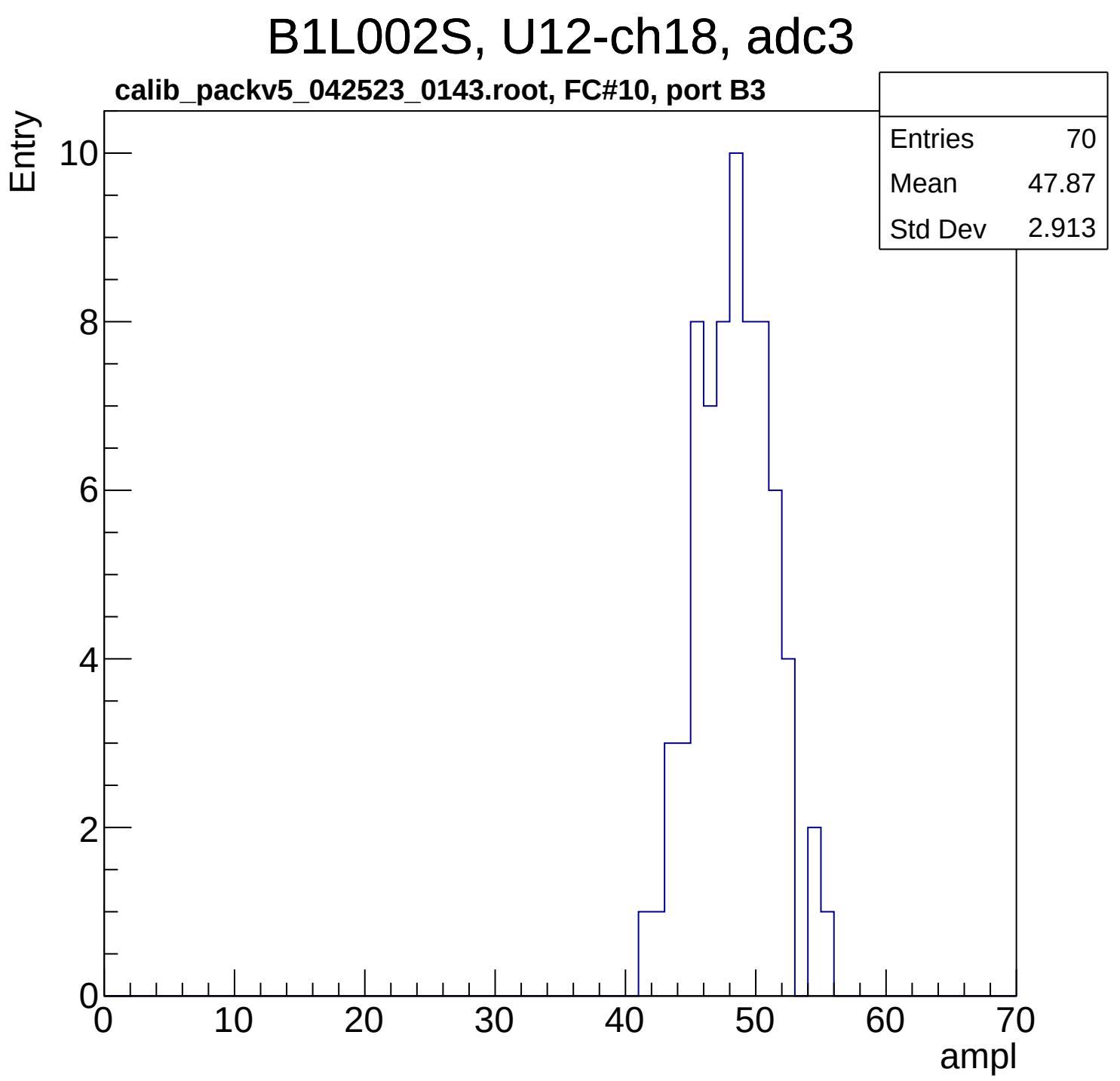

B1L002S, U12-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

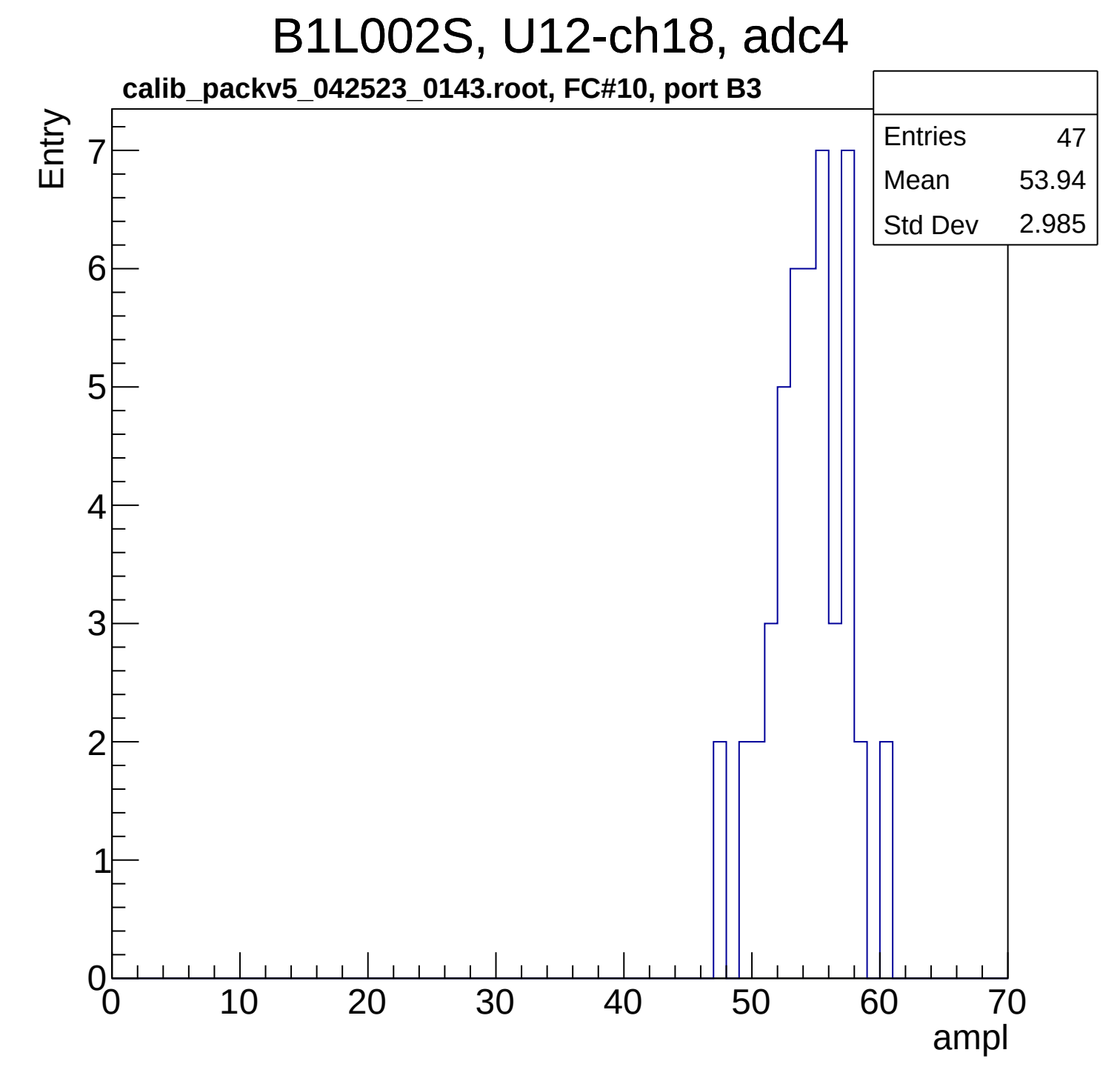

B1L002S, U12-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

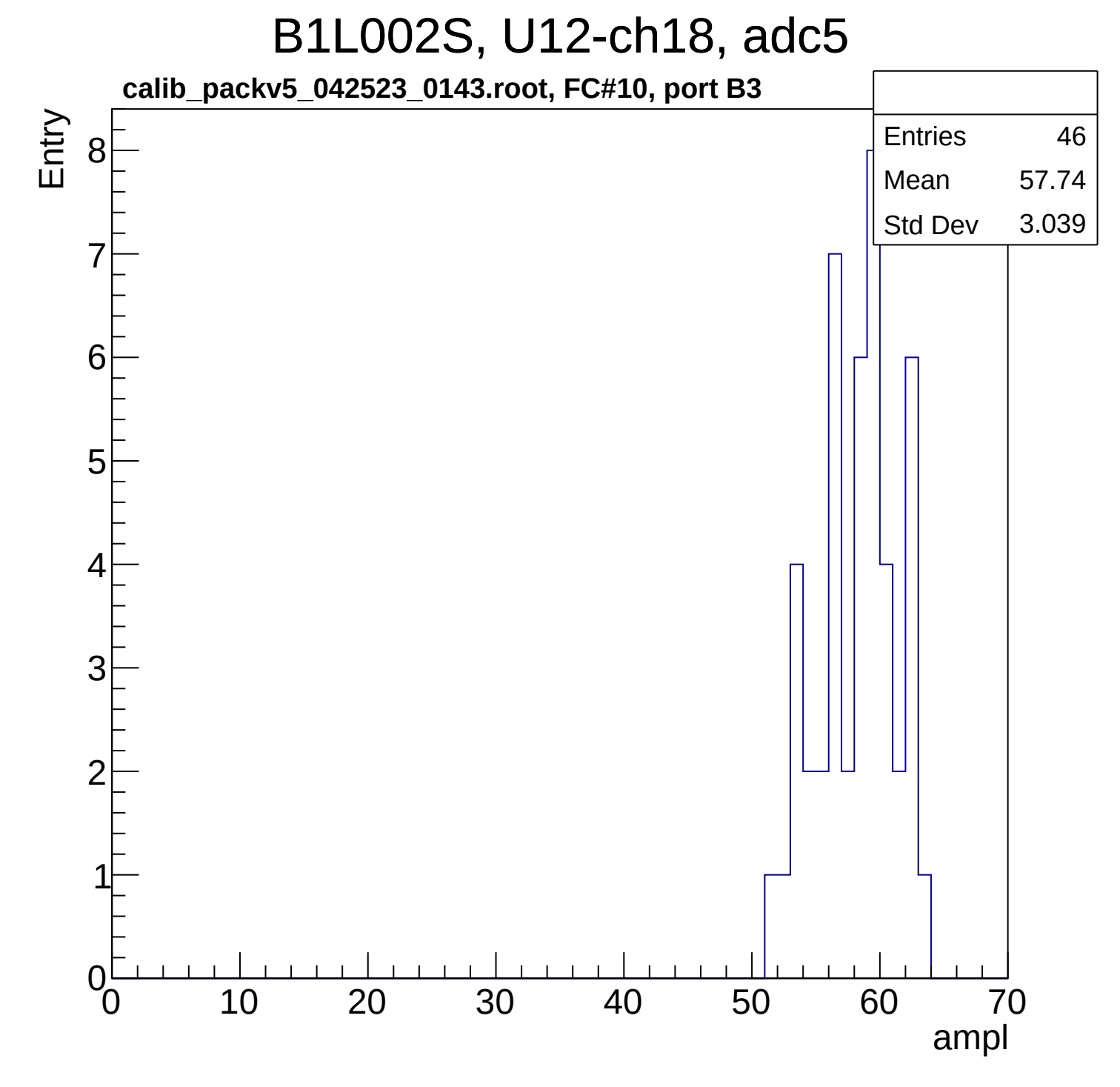

B1L002S, U12-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 27.79
Std Dev 2.96
Gaus mean : 27.8097
Gaus Width: 3.1444
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 89
Mean 33.75
Std Dev 4.009
Gaus mean : 34.0205
Gaus Width: 4.0539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.8
Std Dev 2.886
Gaus mean : 41.5969
Gaus Width: 3.0041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.87
Std Dev 2.913
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 53.94
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.74
Std Dev 3.039
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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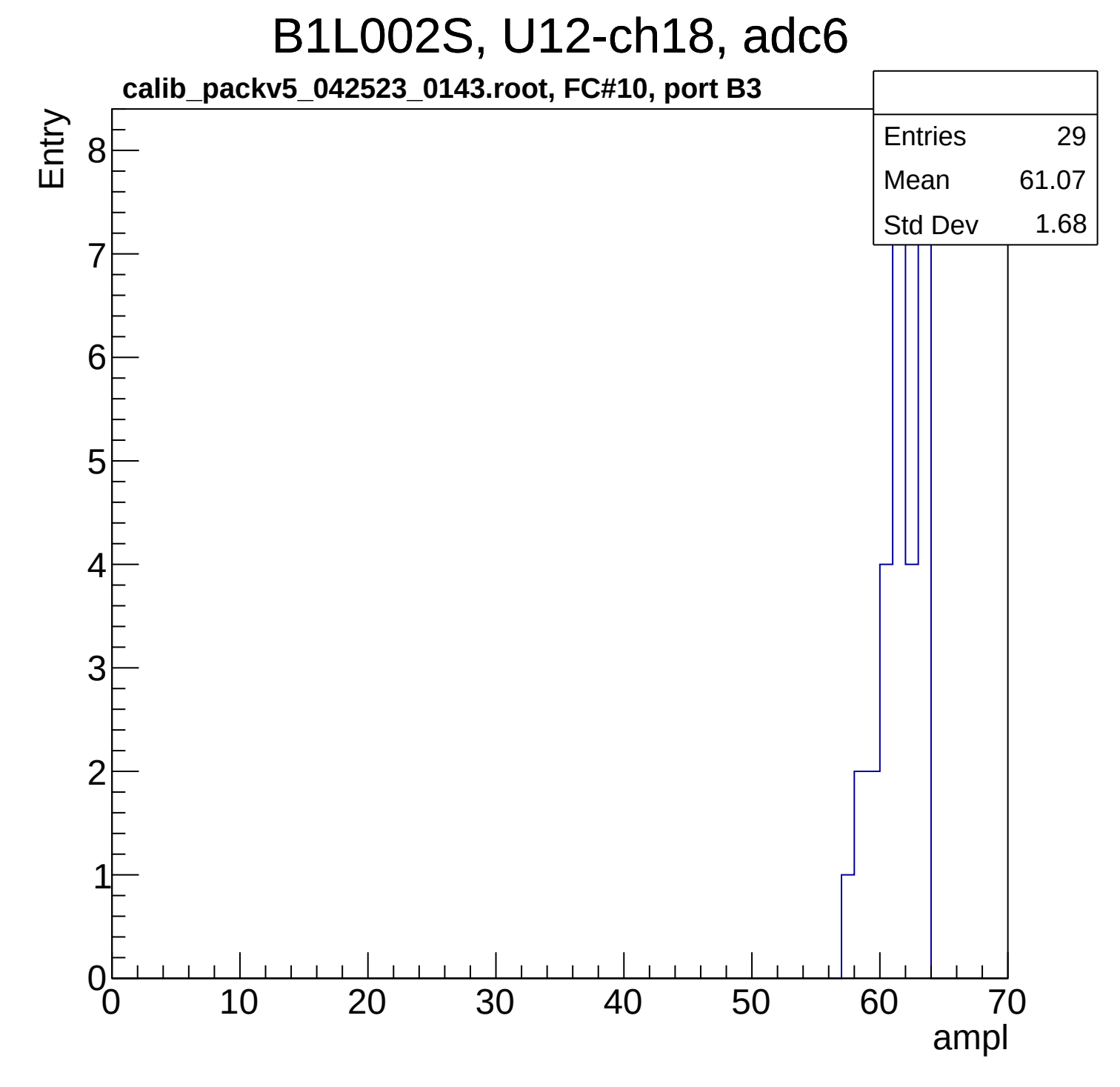
B1L002S, U12-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 61.07
Std Dev 1.68
Entry
ampl
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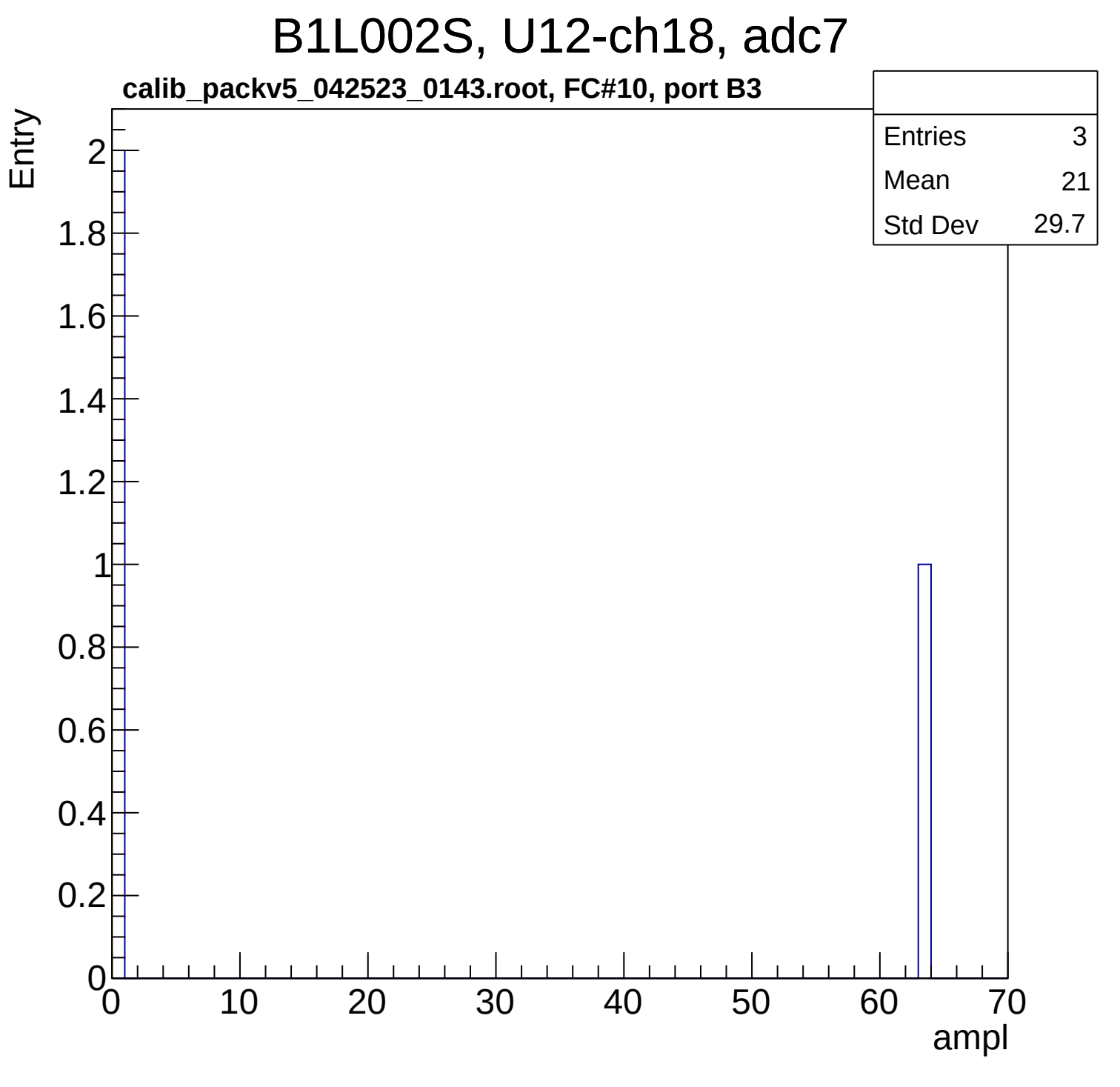
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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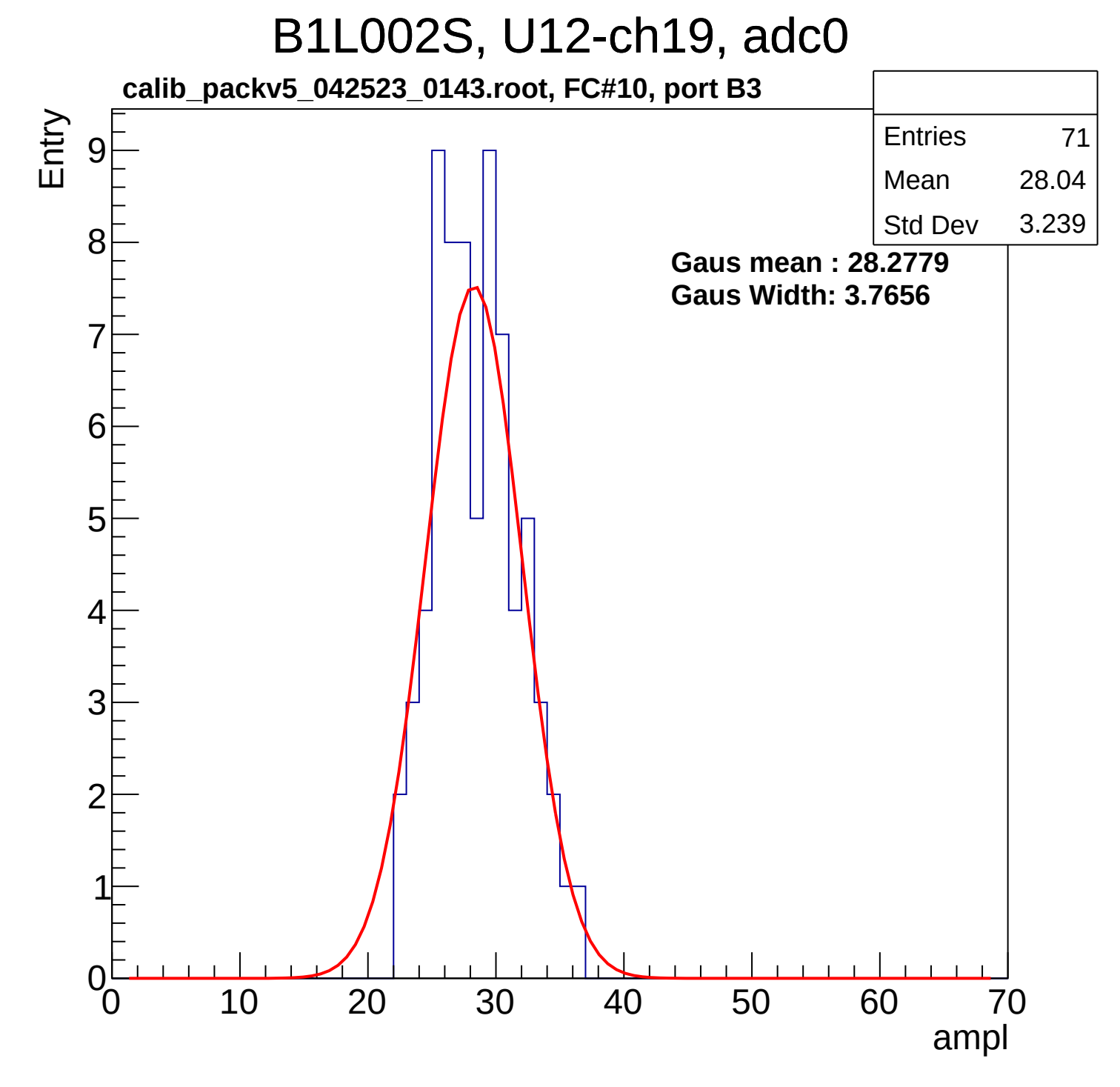
B1L002S, U12-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.04
Std Dev 3.239
Gaus mean : 28.2779
Gaus Width: 3.7656
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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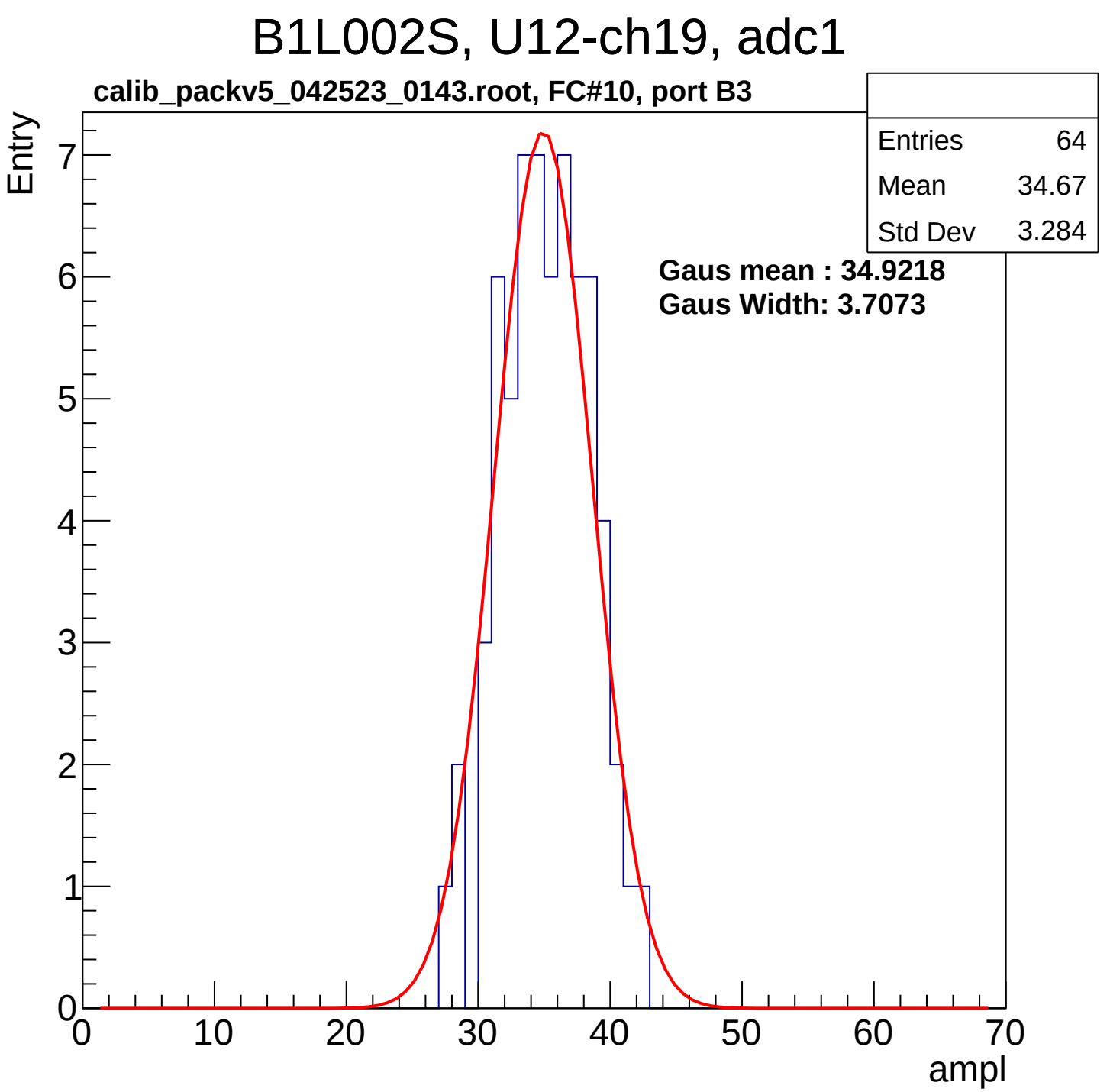
B1L002S, U12-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.67
Std Dev 3.284
Gaus mean : 34.9218
Gaus Width: 3.7073
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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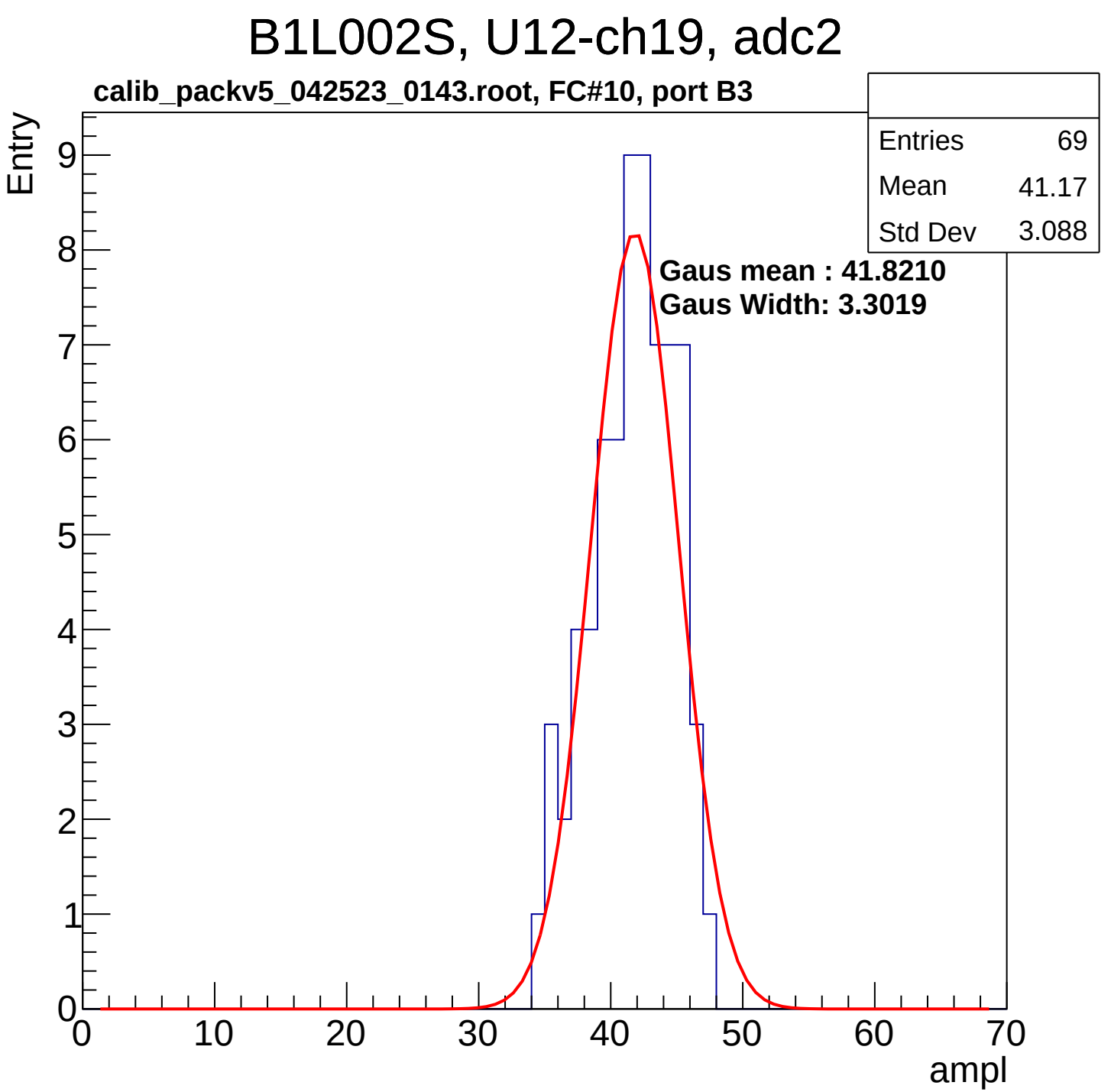
B1L002S, U12-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.17
Std Dev 3.088
Gaus mean : 41.8210
Gaus Width: 3.3019
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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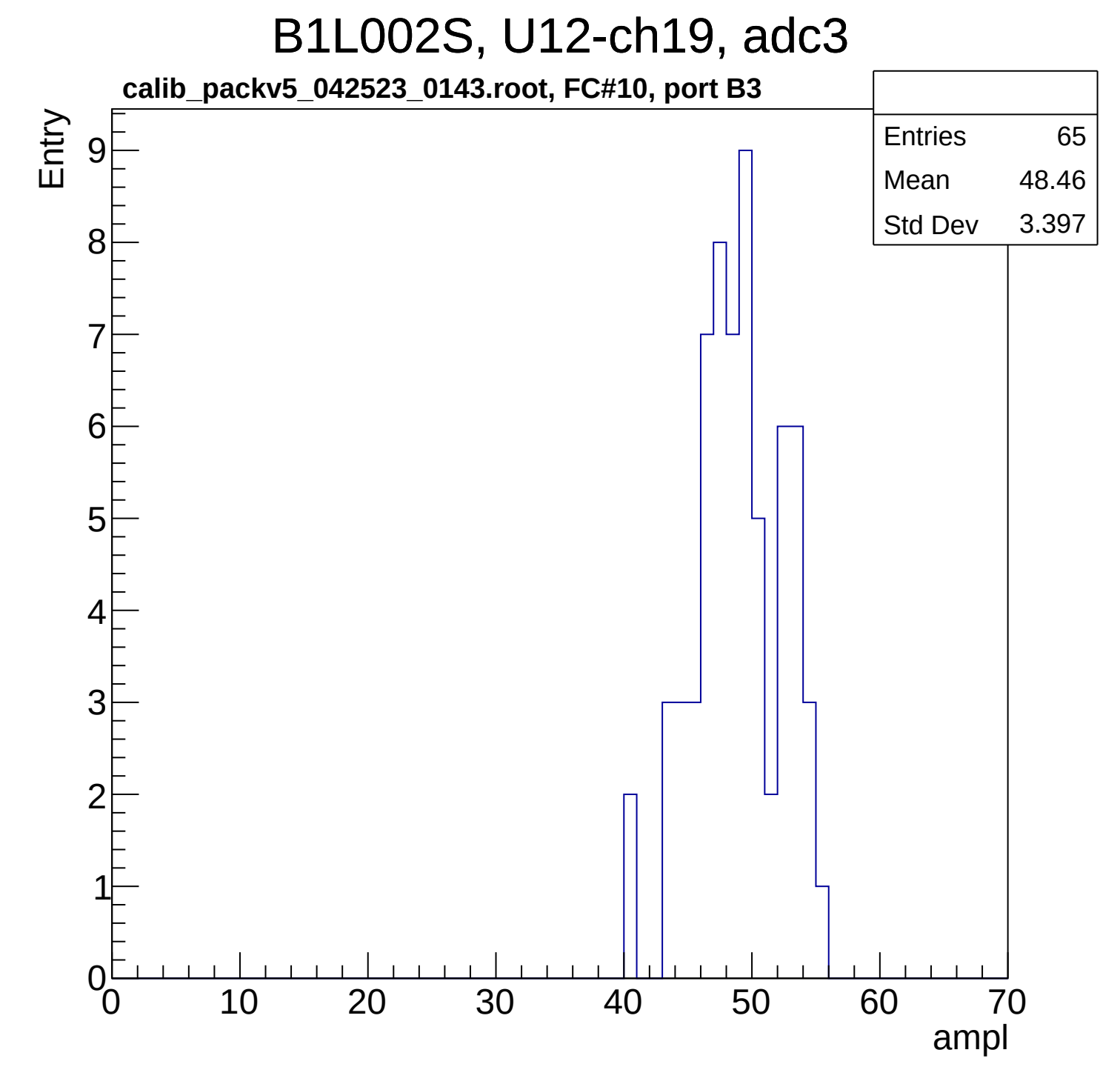
B1L002S, U12-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.46
Std Dev 3.397
Entry
ampl
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2
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4
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0
10
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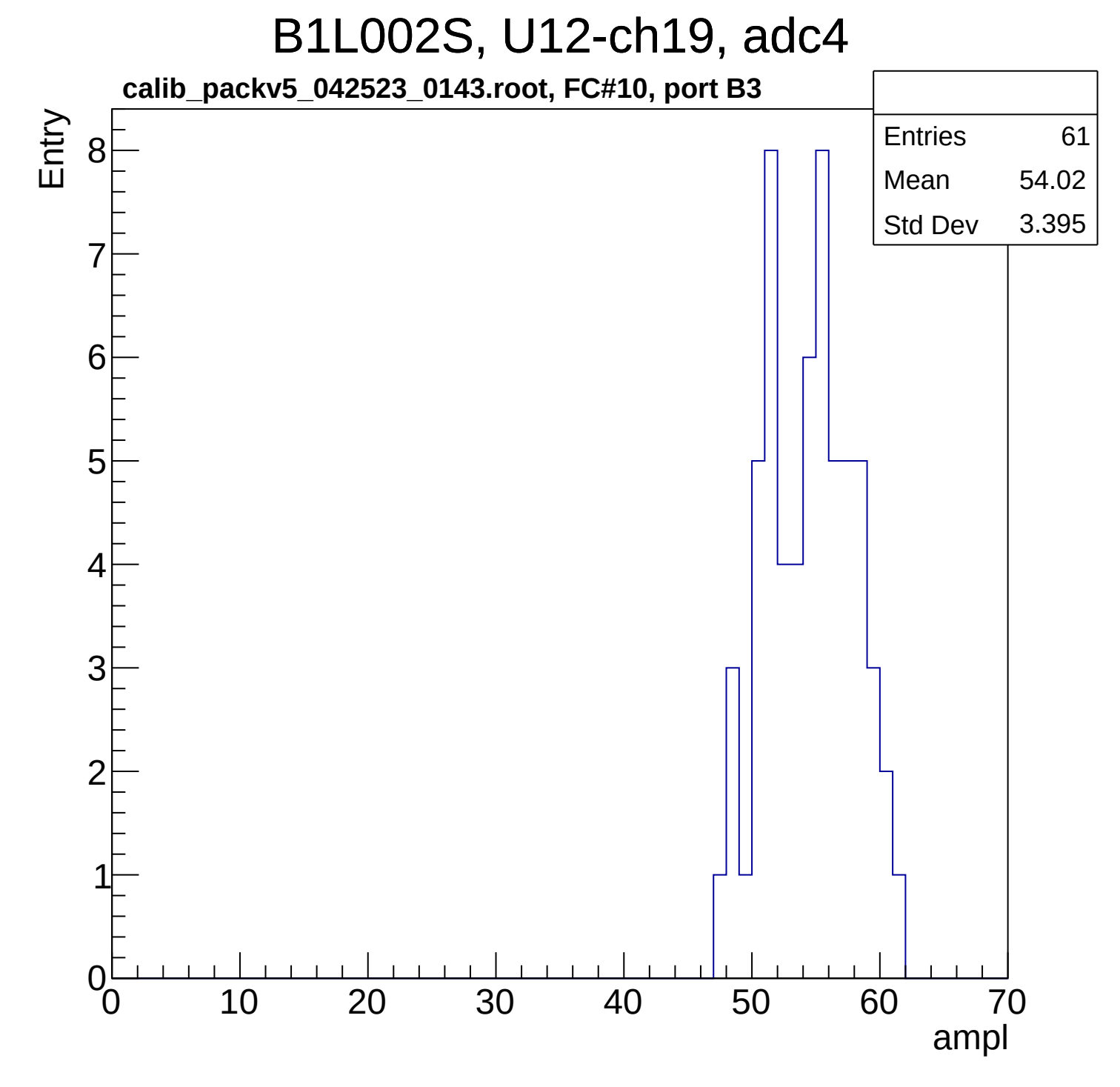
B1L002S, U12-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.02
Std Dev 3.395
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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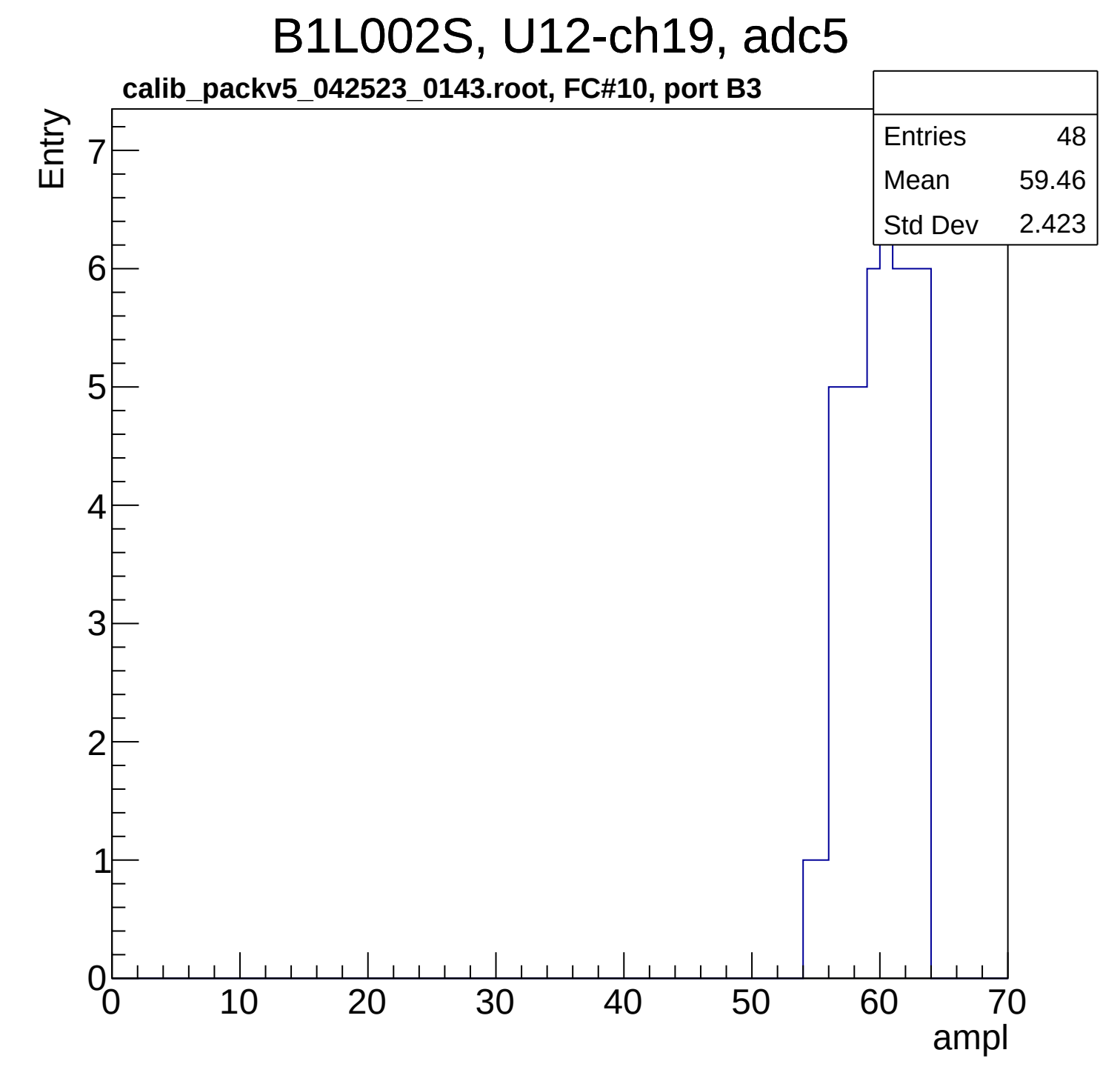
B1L002S, U12-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.46
Std Dev 2.423
Entry
0
1
2
3
4
5
6
7
0
10
20
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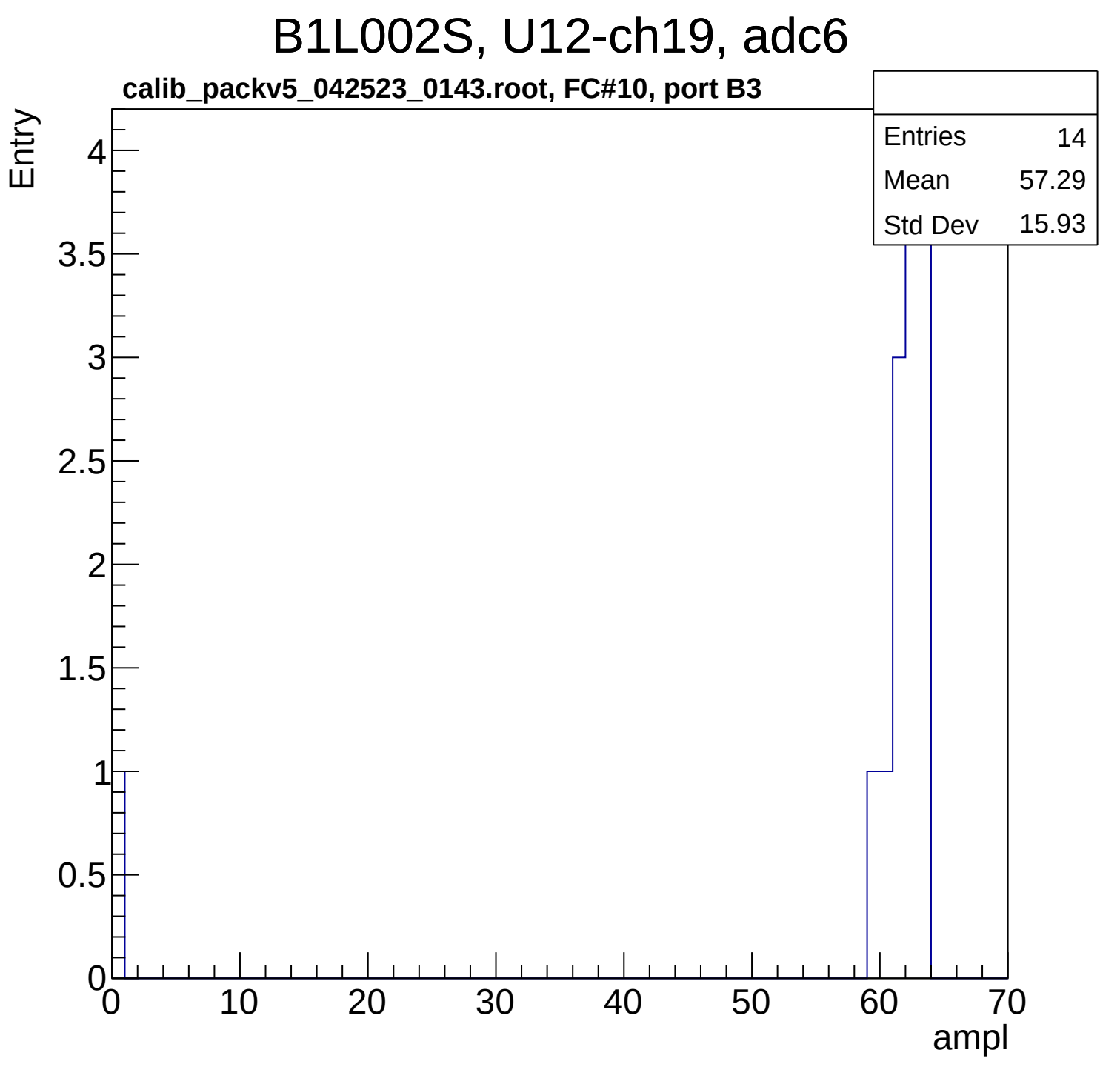
40
50
60
70
ampl


B1L002S, U12-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.29
Std Dev 15.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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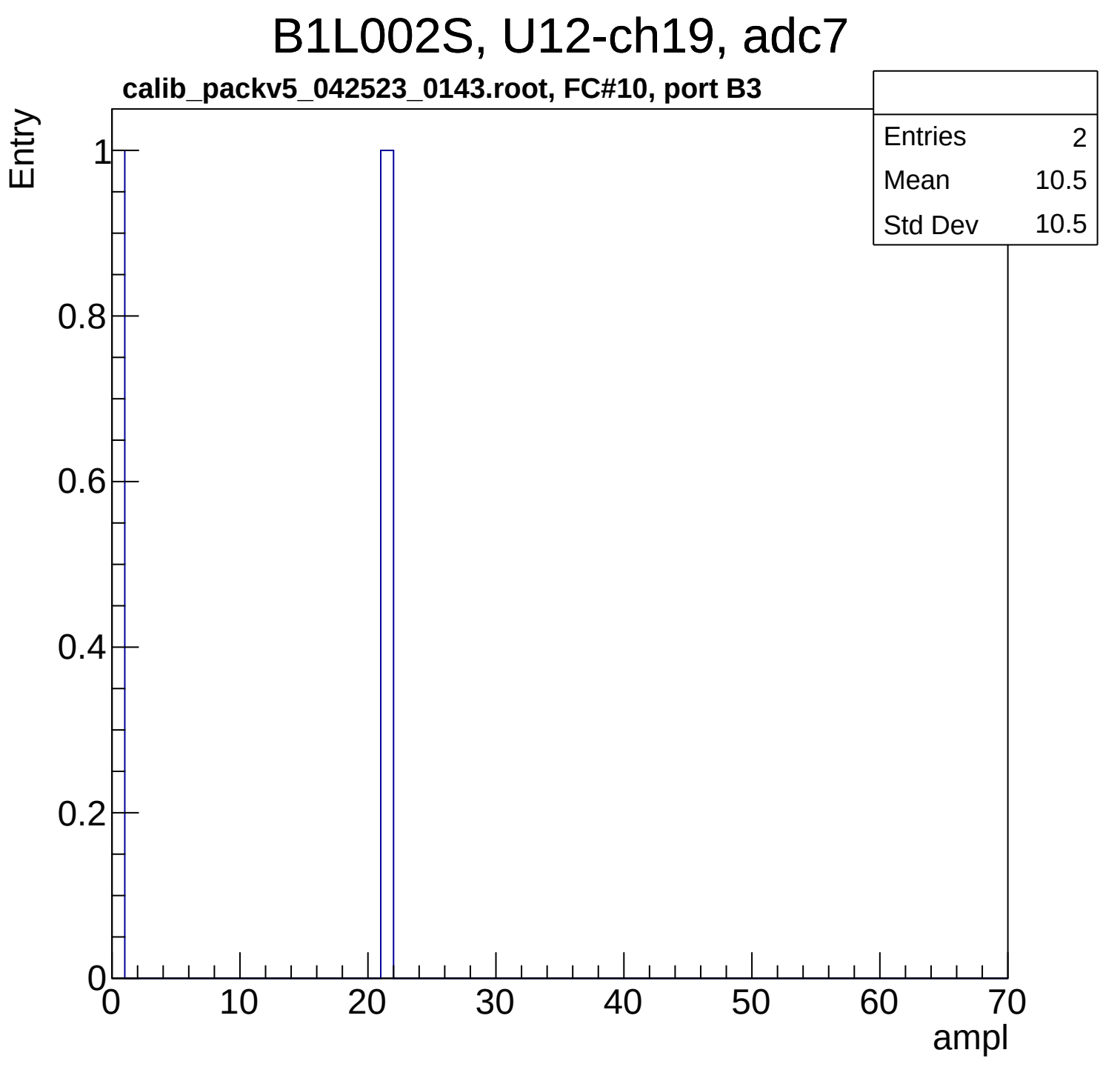
B1L002S, U12-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
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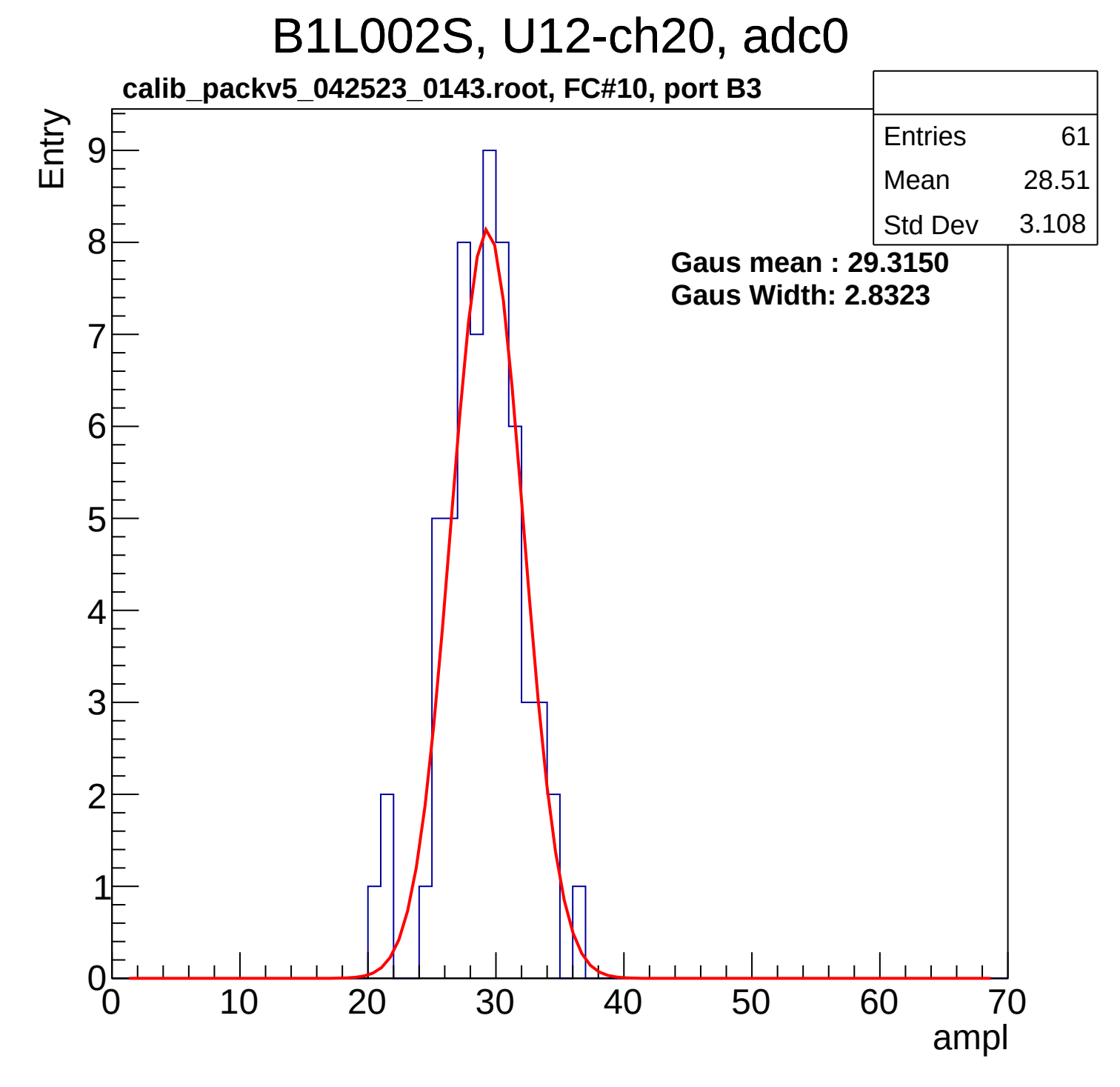
60
70
ampl


B1L002S, U12-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.51
Std Dev 3.108
Gaus mean : 29.3150
Gaus Width: 2.8323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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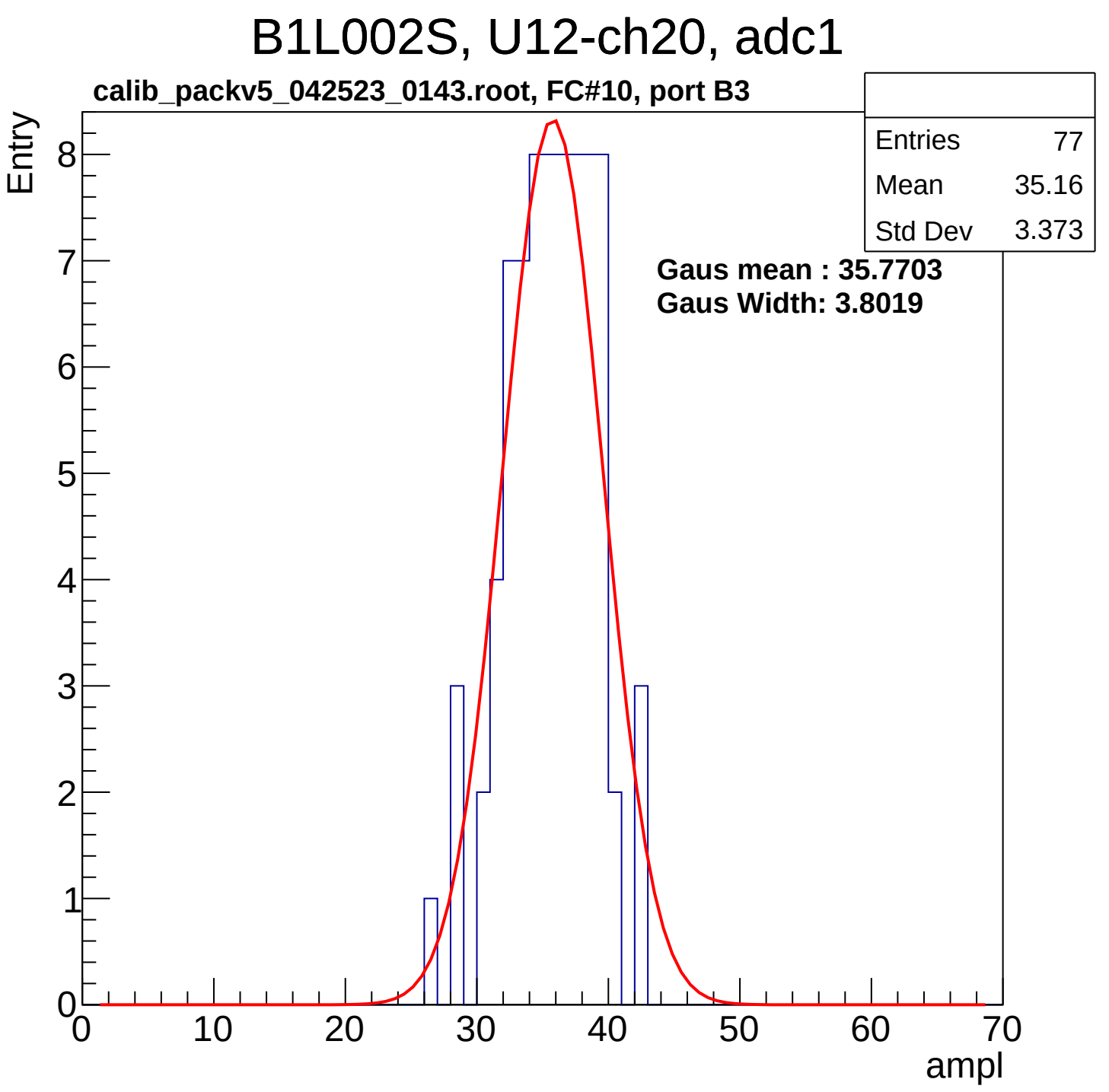
B1L002S, U12-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.16
Std Dev 3.373
Gaus mean : 35.7703
Gaus Width: 3.8019
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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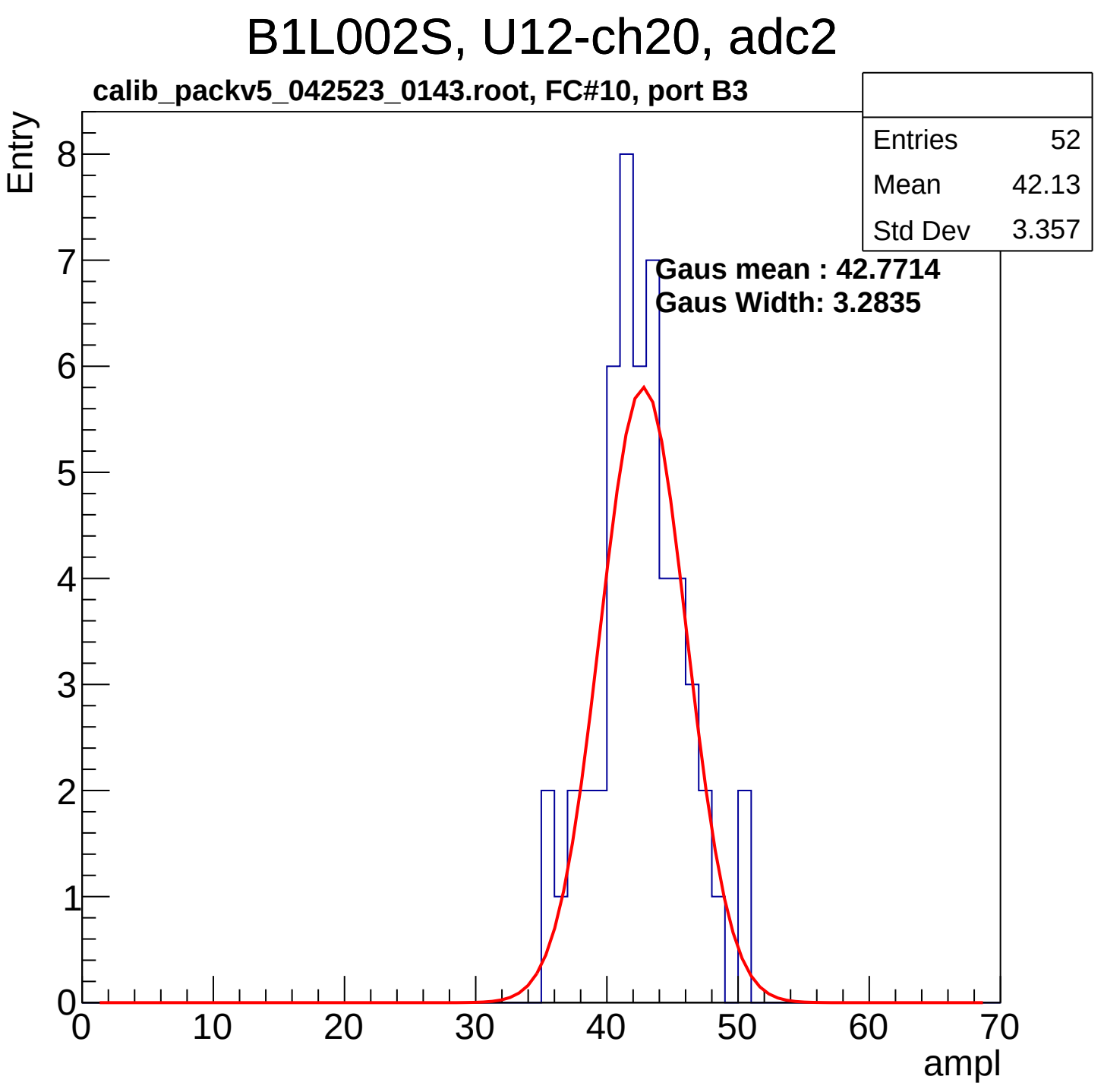
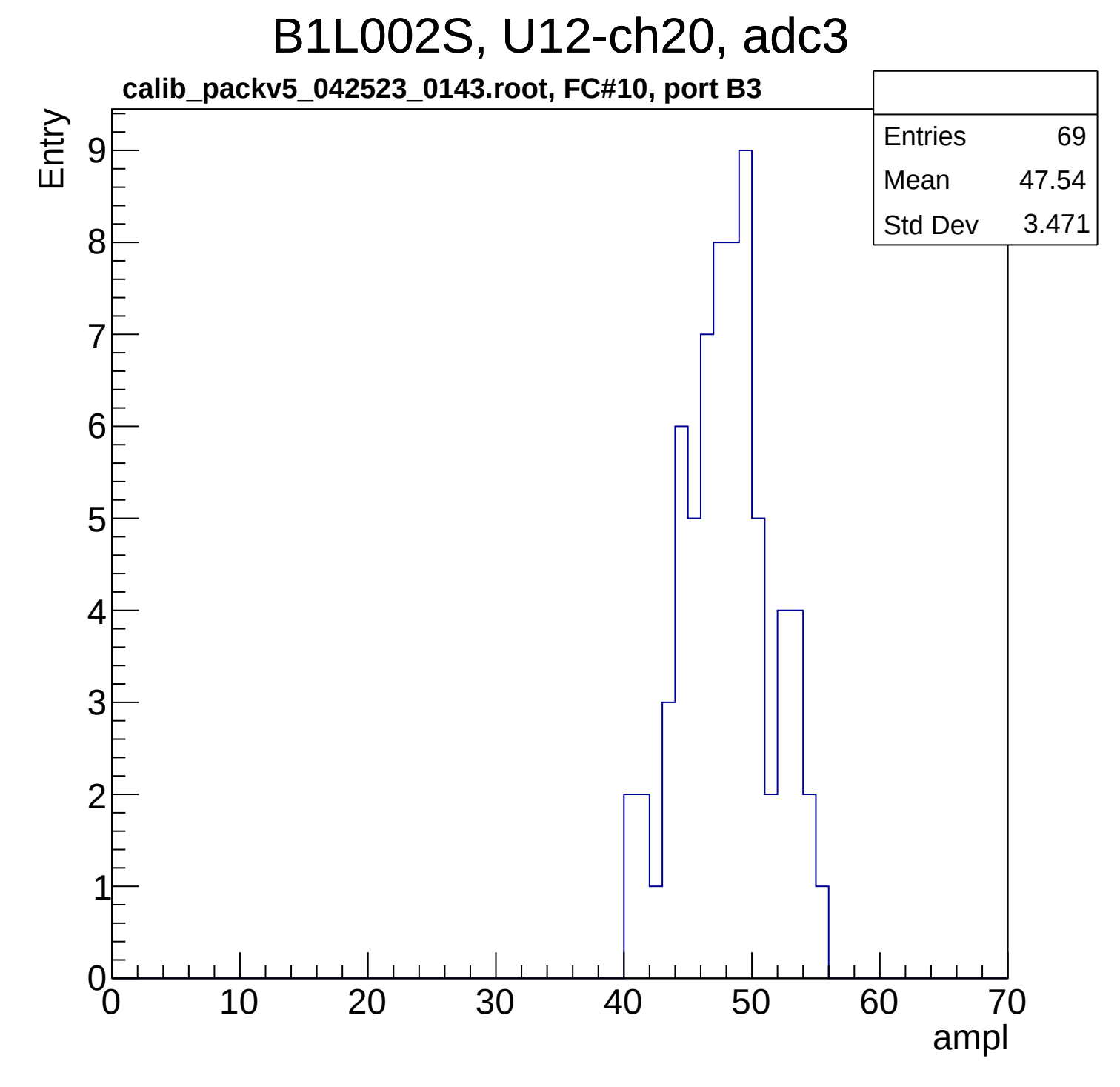

B1L002S, U12-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 42.13
Std Dev 3.357
Gaus mean : 42.7714
Gaus Width: 3.2835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.54
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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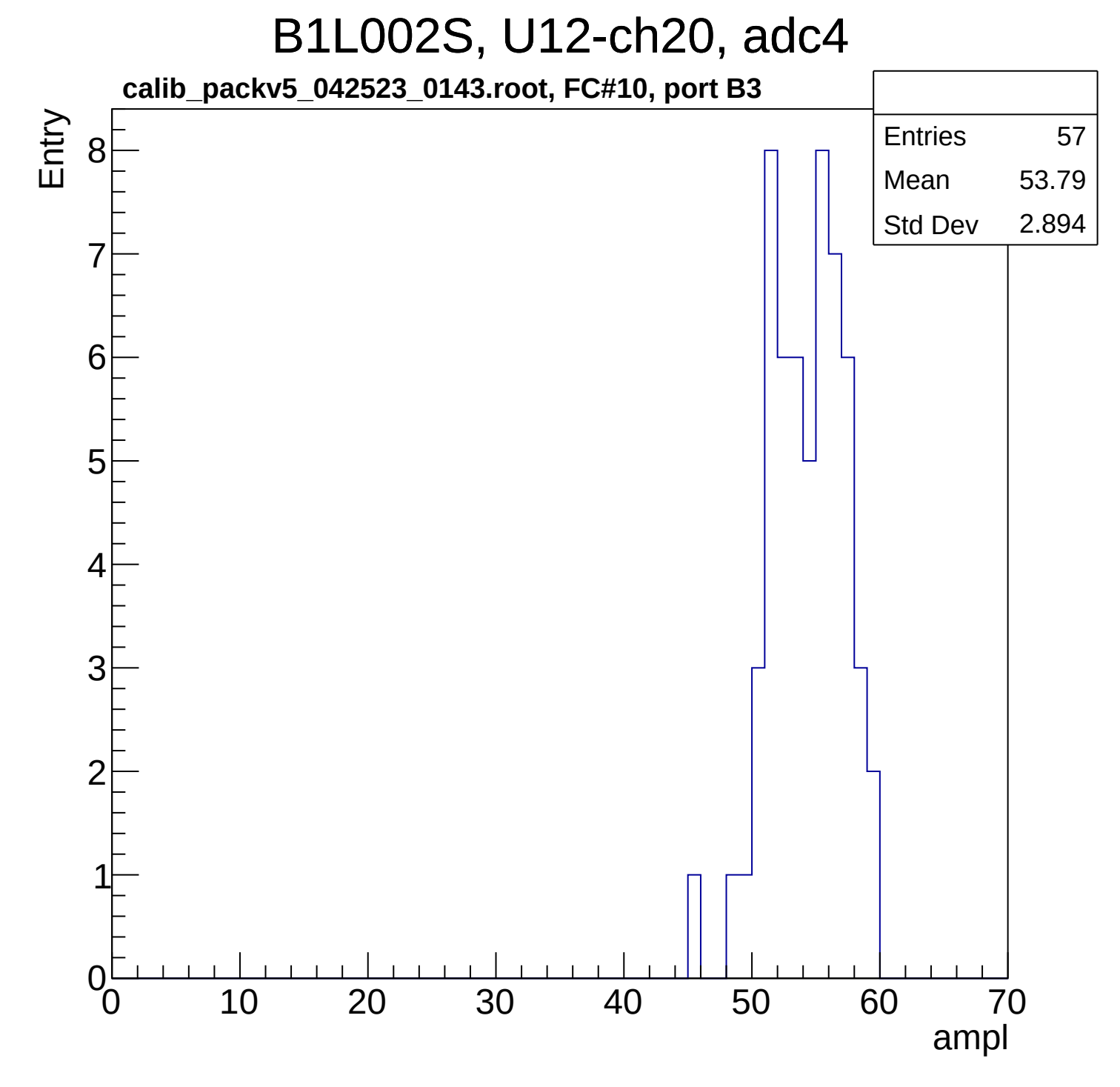
40
50
60
70


B1L002S, U12-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.79
Std Dev 2.894
Entry
ampl
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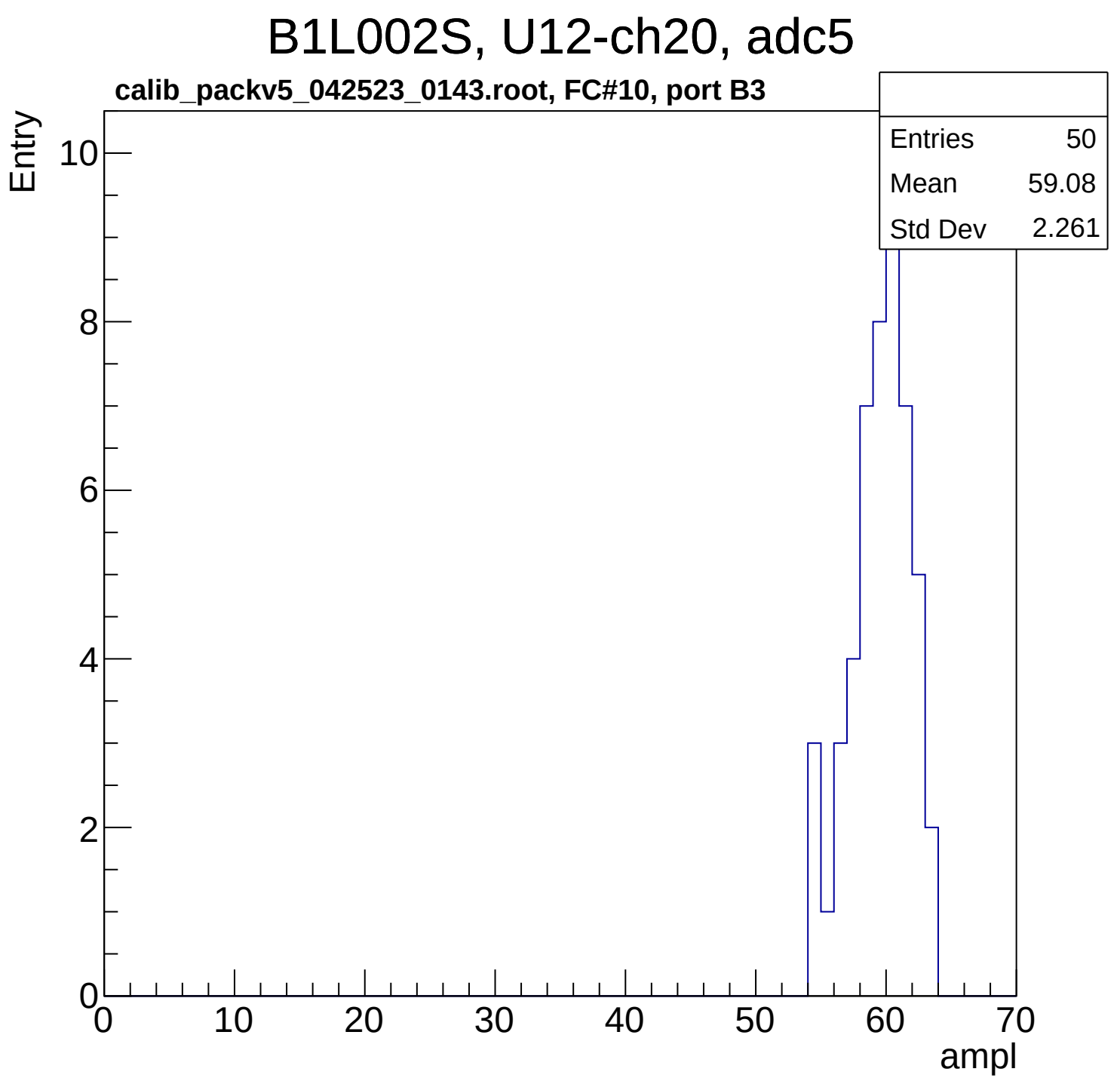
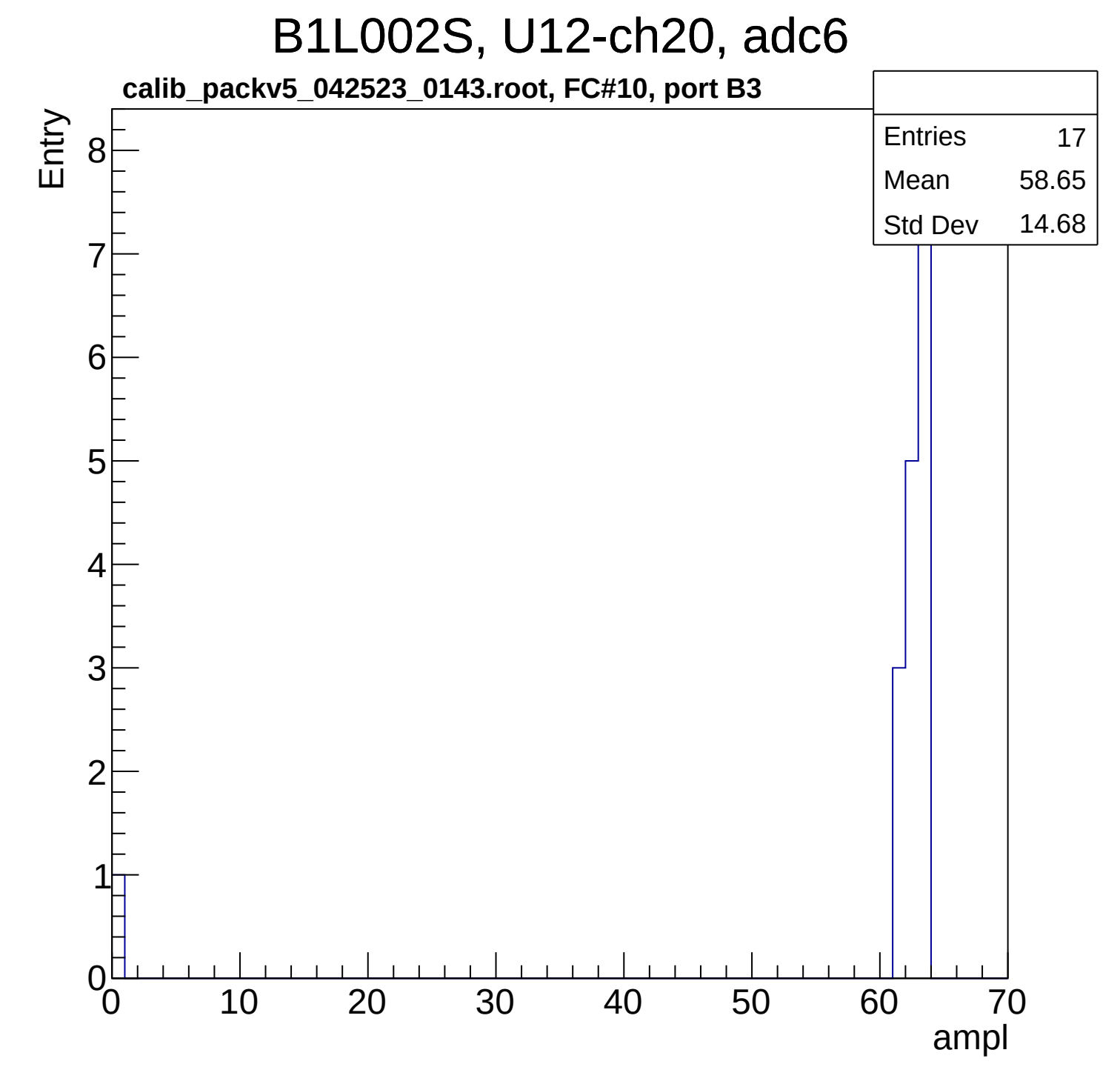
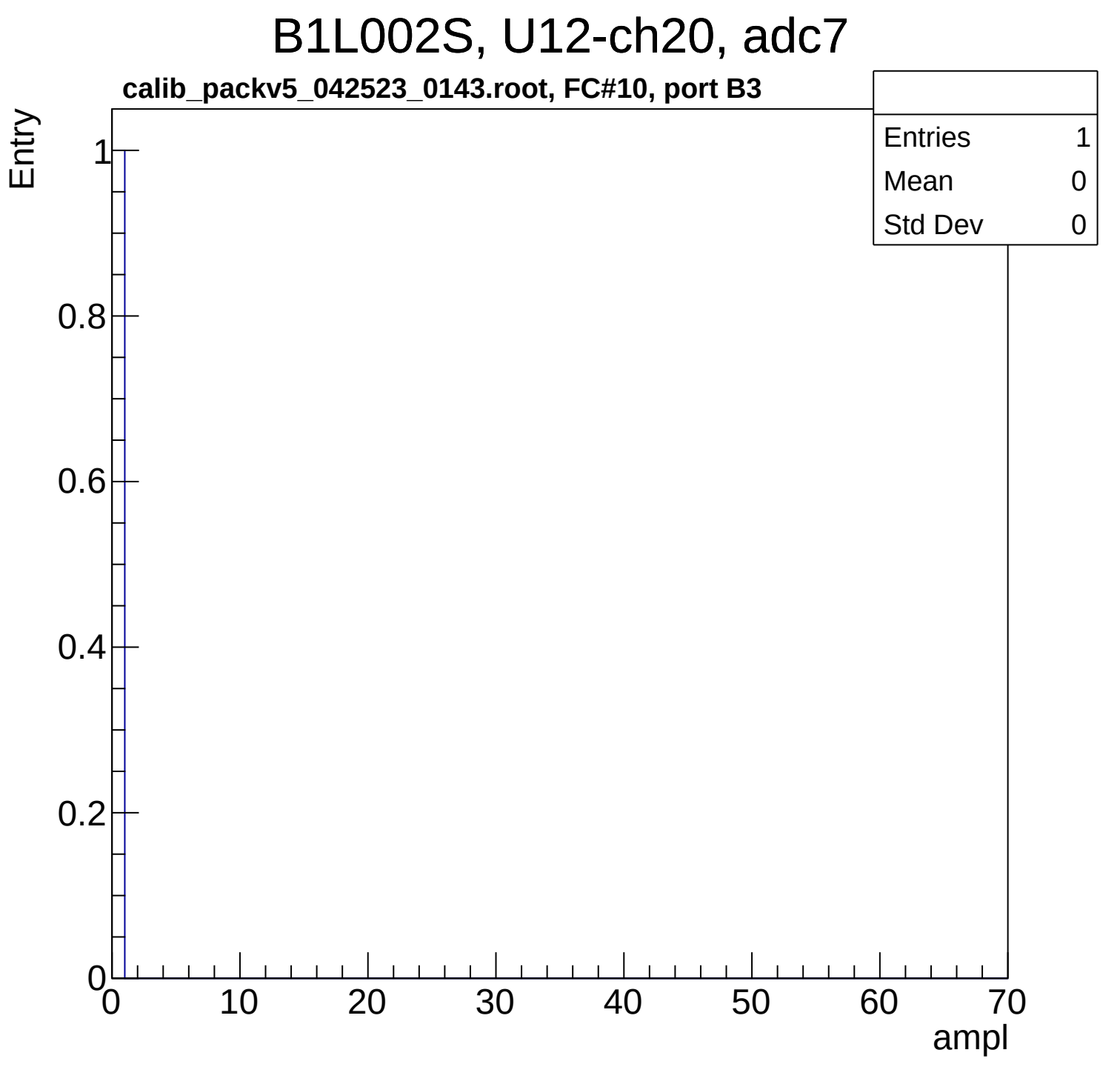
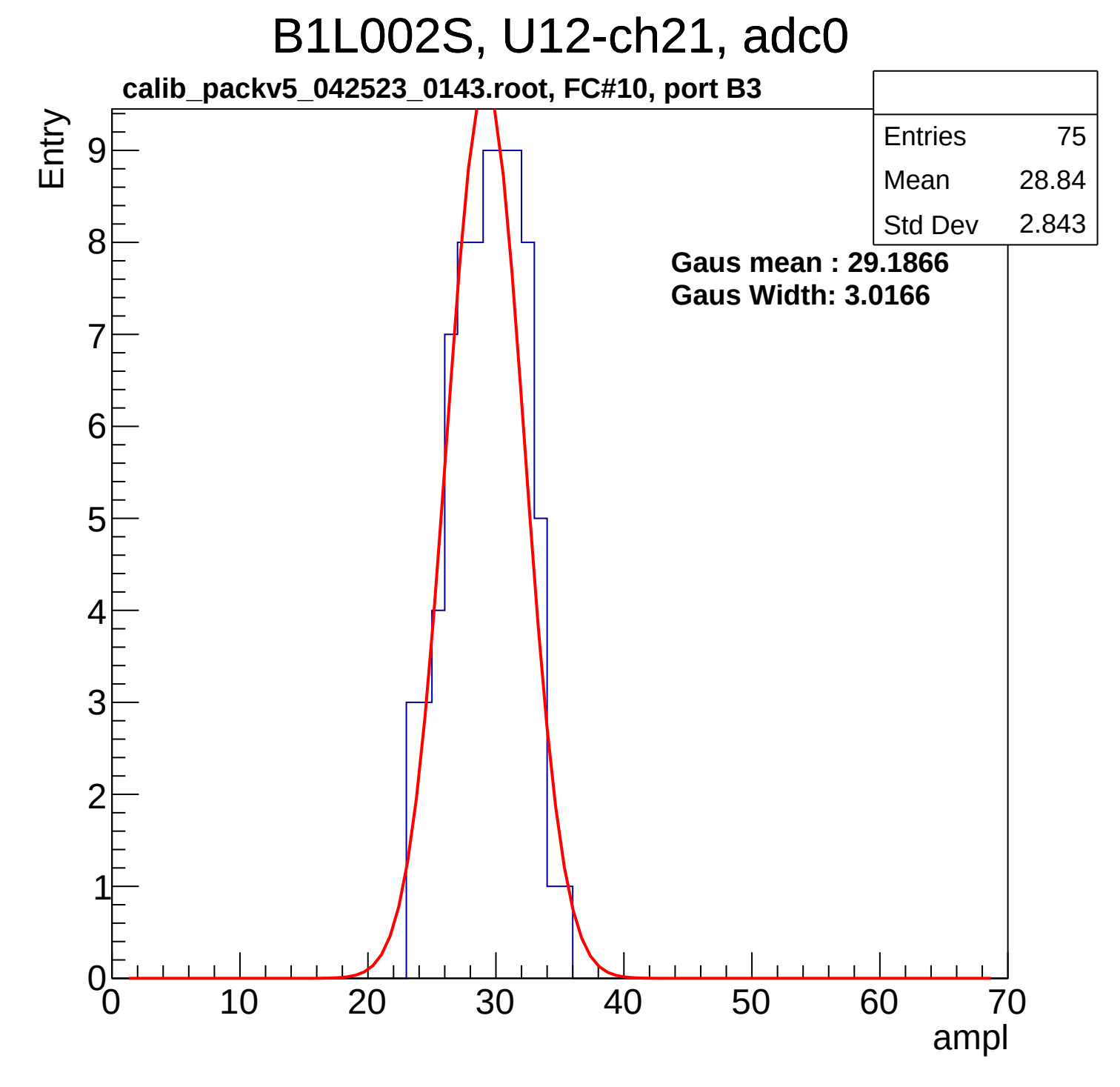
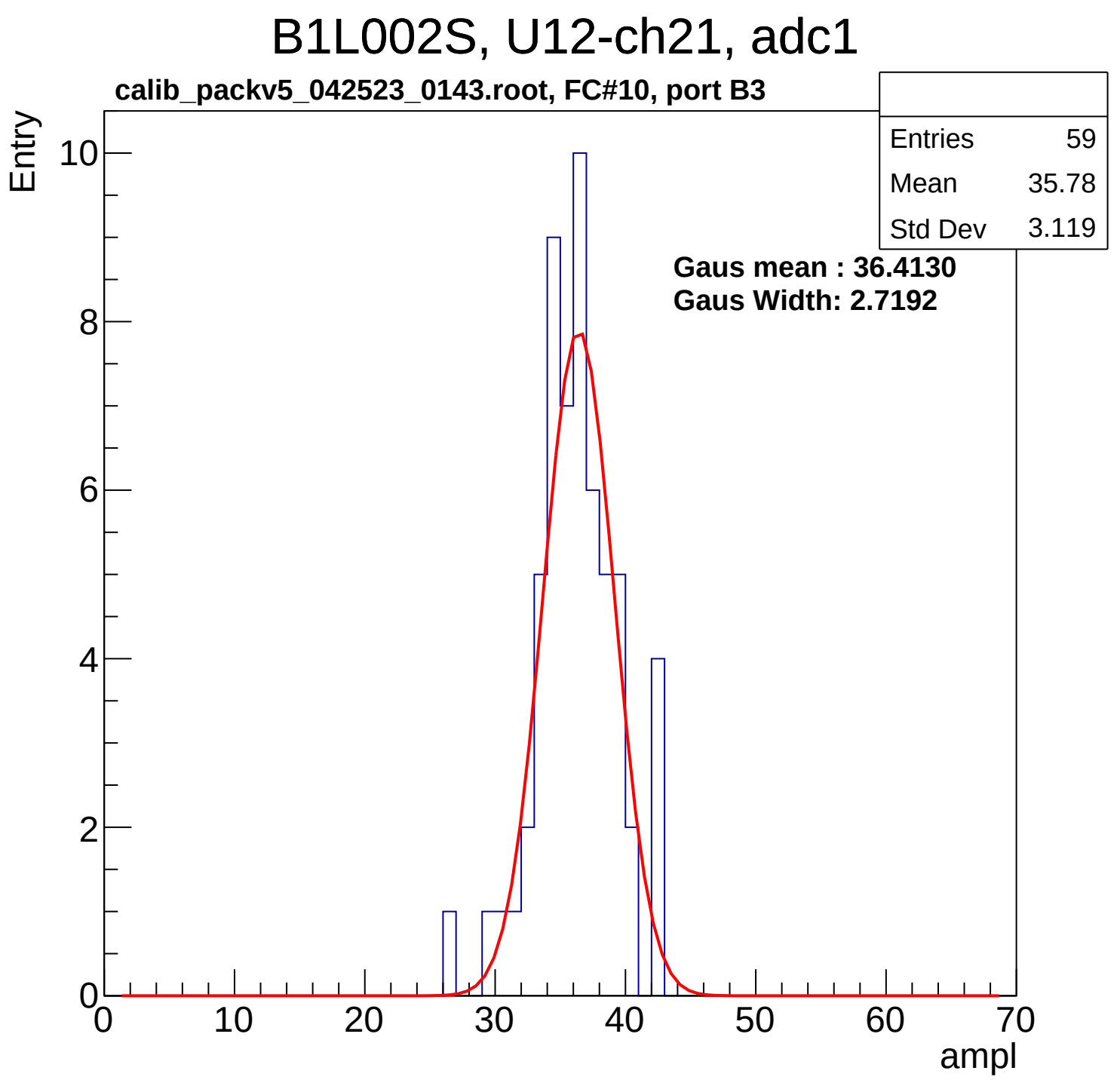
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

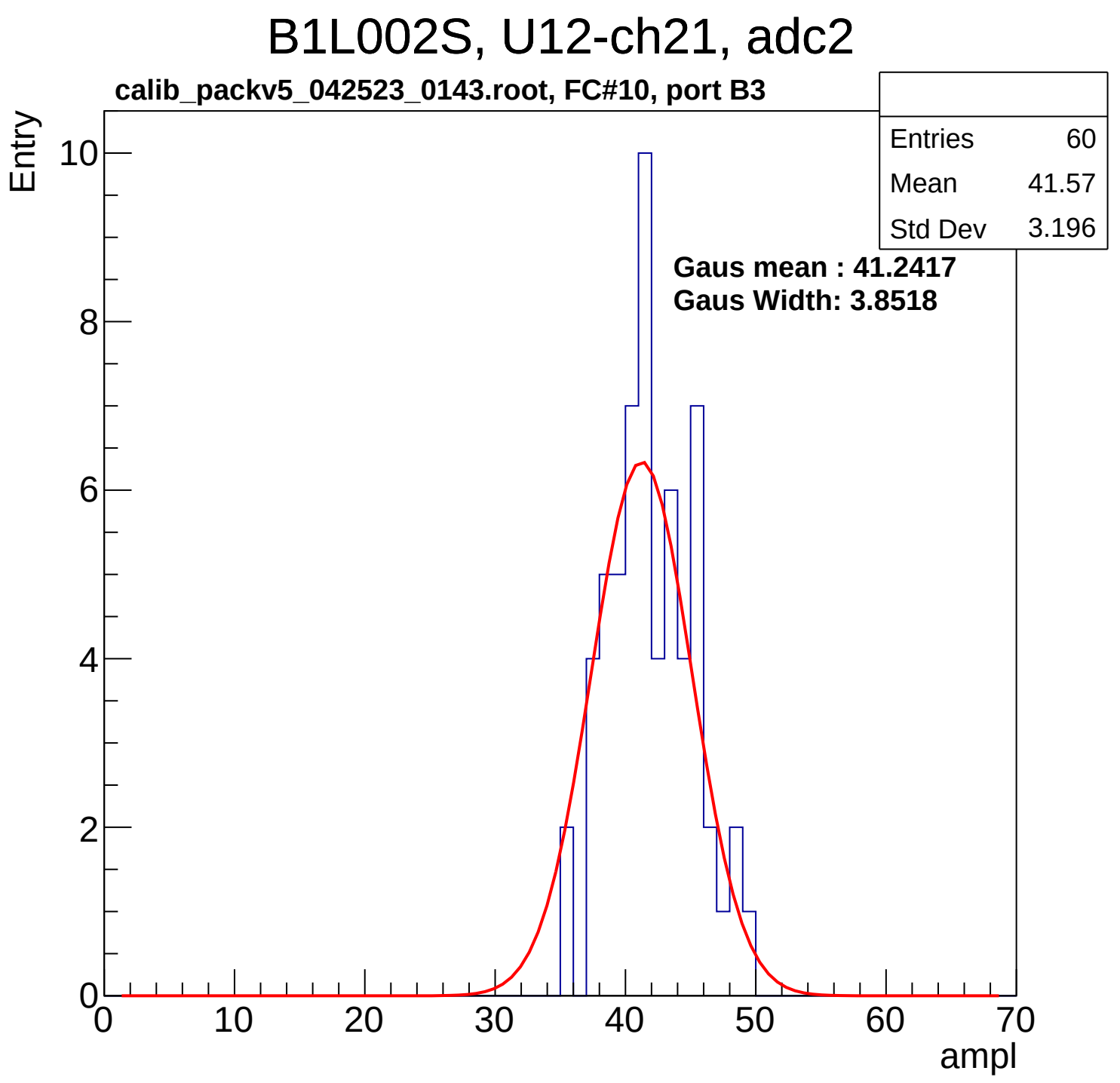

B1L002S, U12-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 59.08
Std Dev 2.261
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

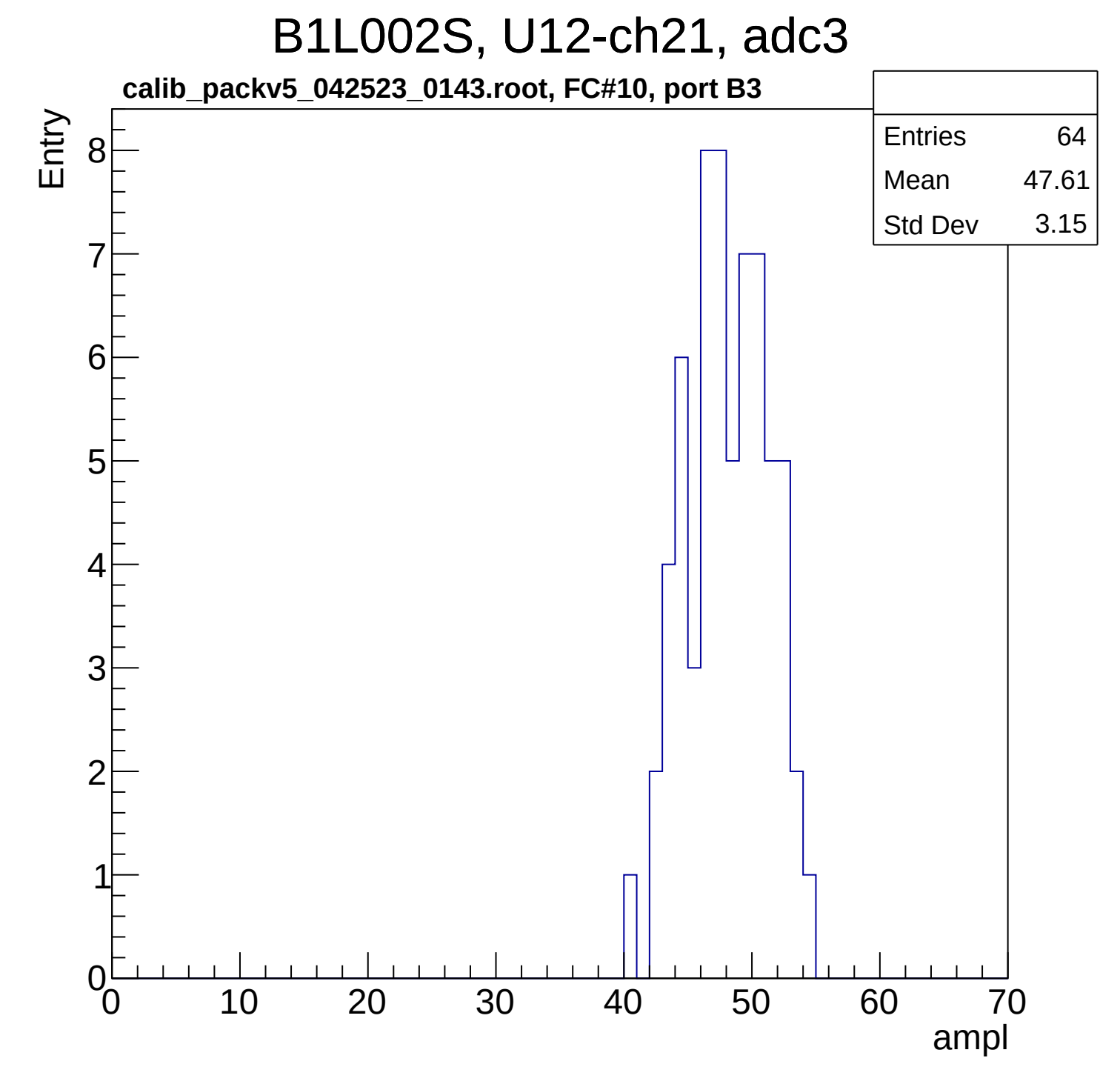

B1L002S, U12-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.65
Std Dev 14.68
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.84
Std Dev 2.843
Gaus mean : 29.1866
Gaus Width: 3.0166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.78
Std Dev 3.119
Gaus mean : 36.4130
Gaus Width: 2.7192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.57
Std Dev 3.196
Gaus mean : 41.2417
Gaus Width: 3.8518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.61
Std Dev 3.15
Entry
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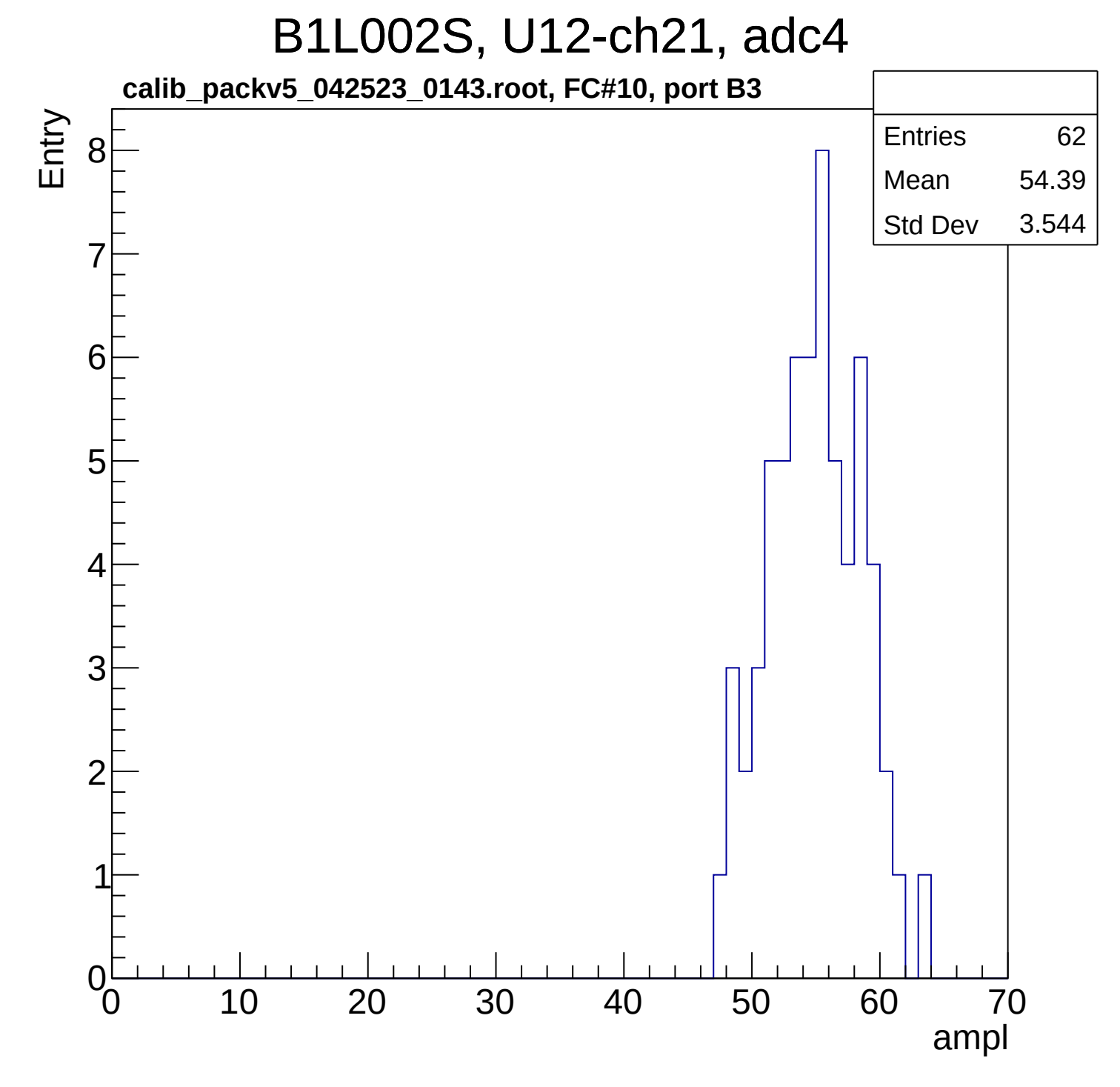
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.39
Std Dev 3.544
Entry
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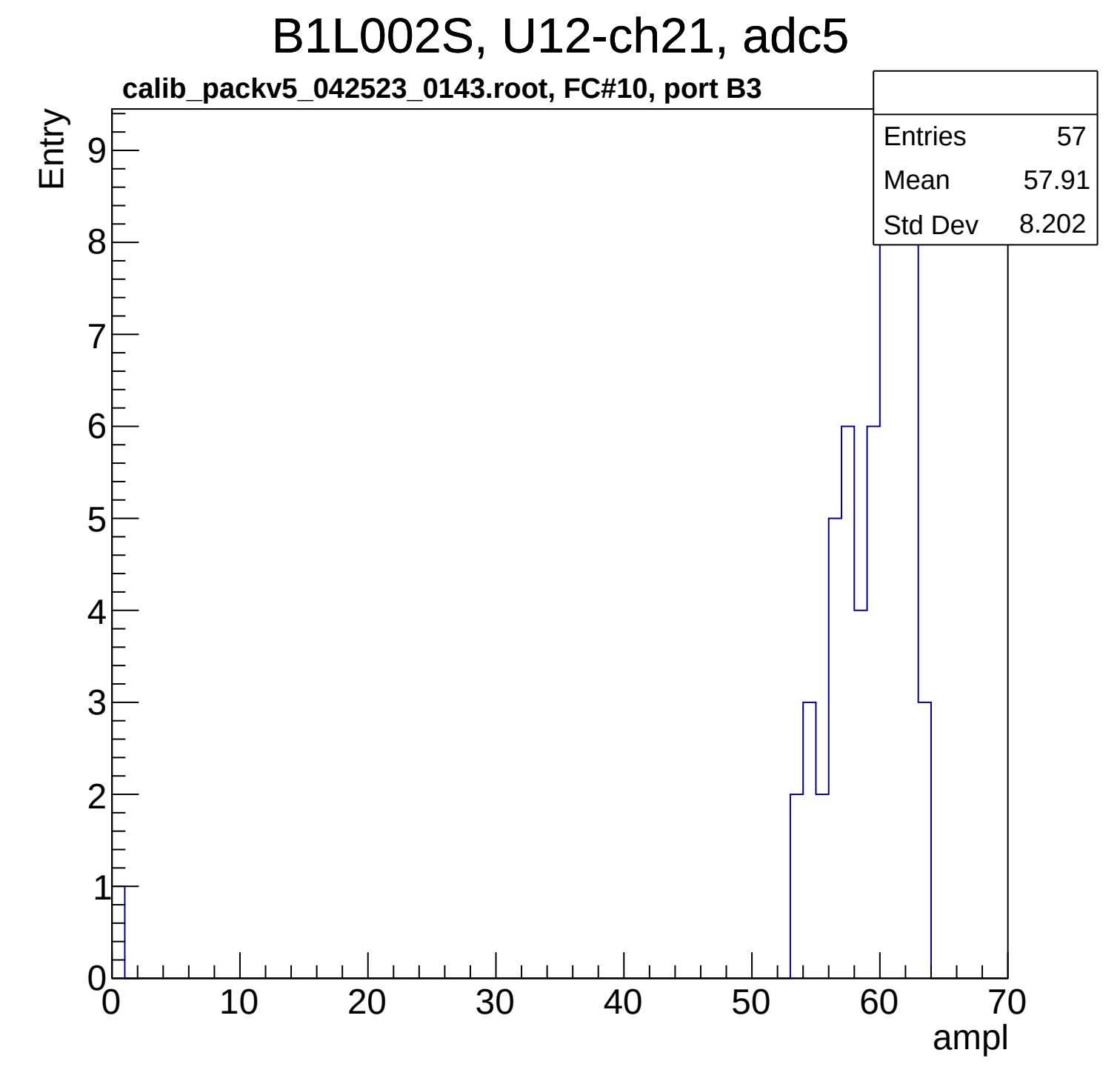
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.91
Std Dev 8.202
Entry
ampl
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2
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4
5
6
7
8
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0
10
20
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60
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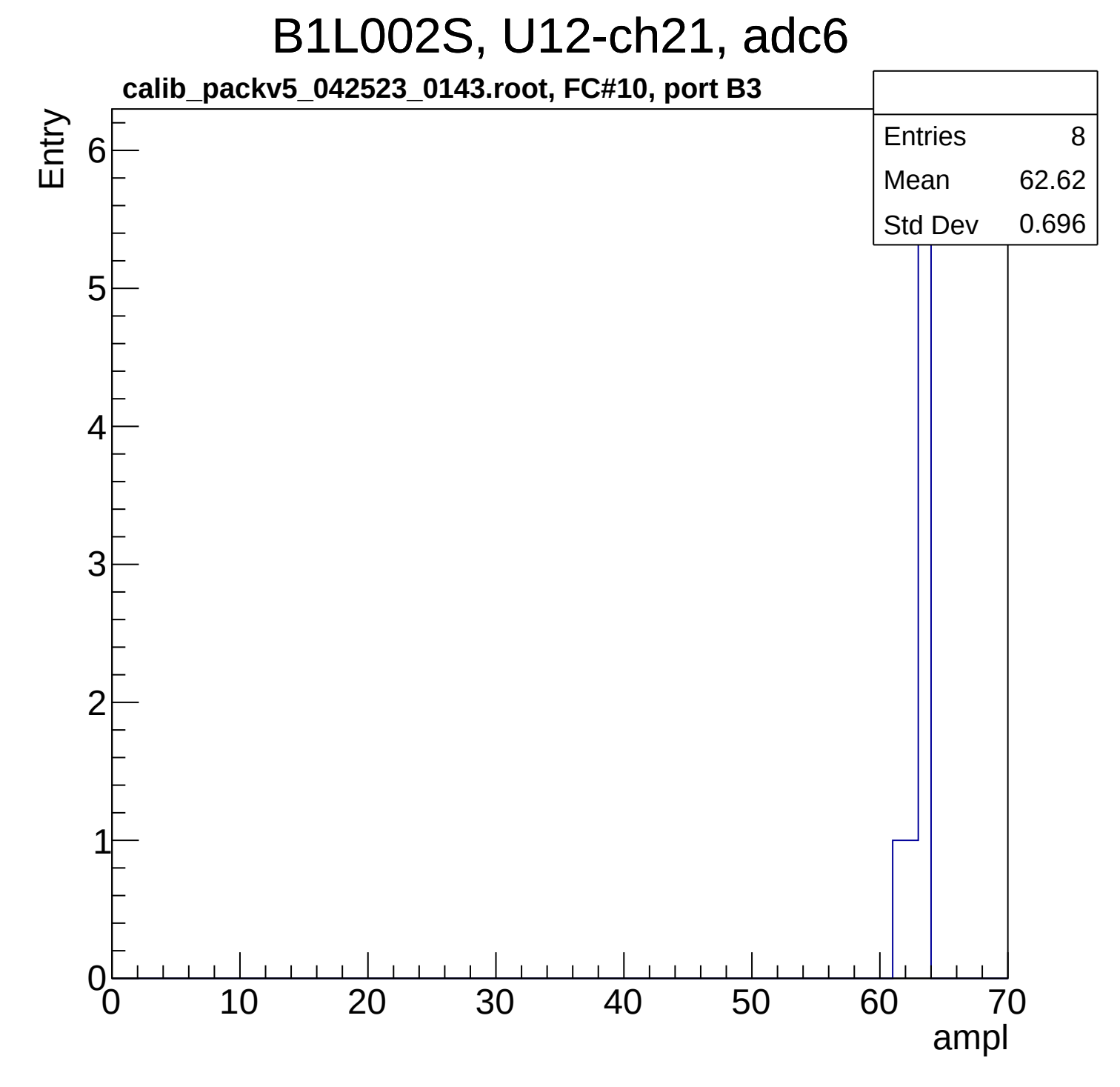
B1L002S, U12-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.62
Std Dev 0.696
Entry
ampl
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0
10
20
30
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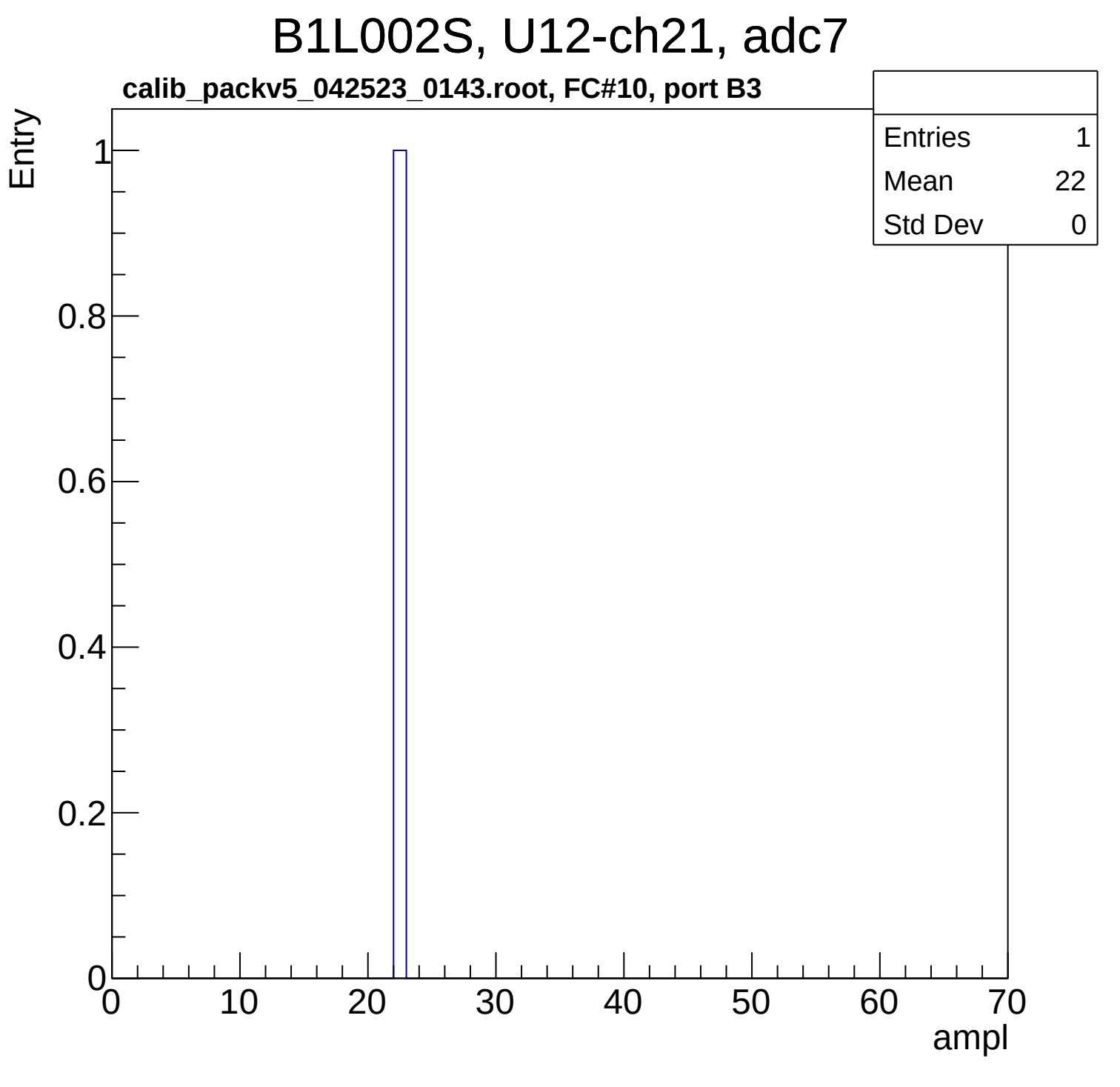
50
60
70


B1L002S, U12-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
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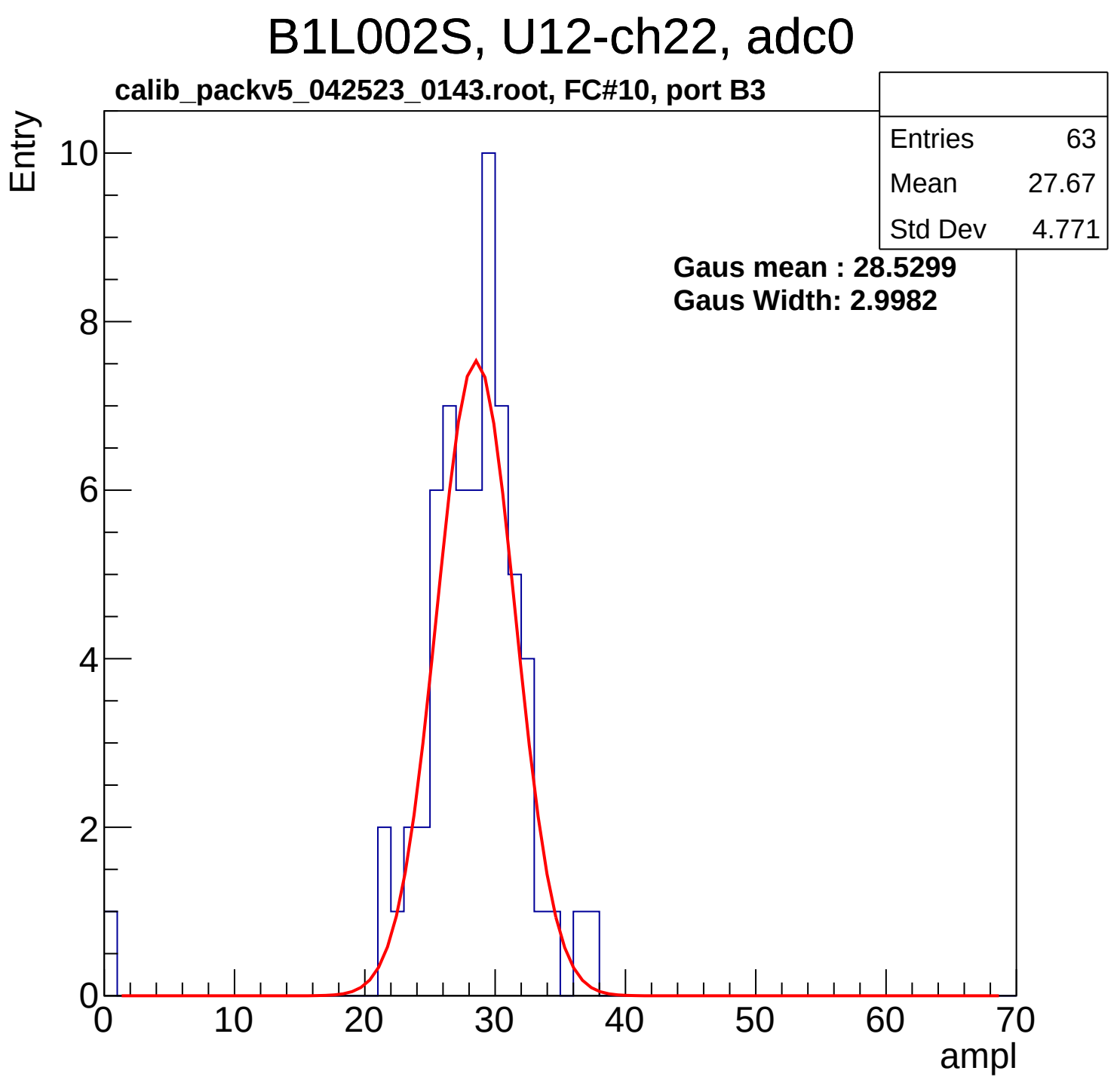
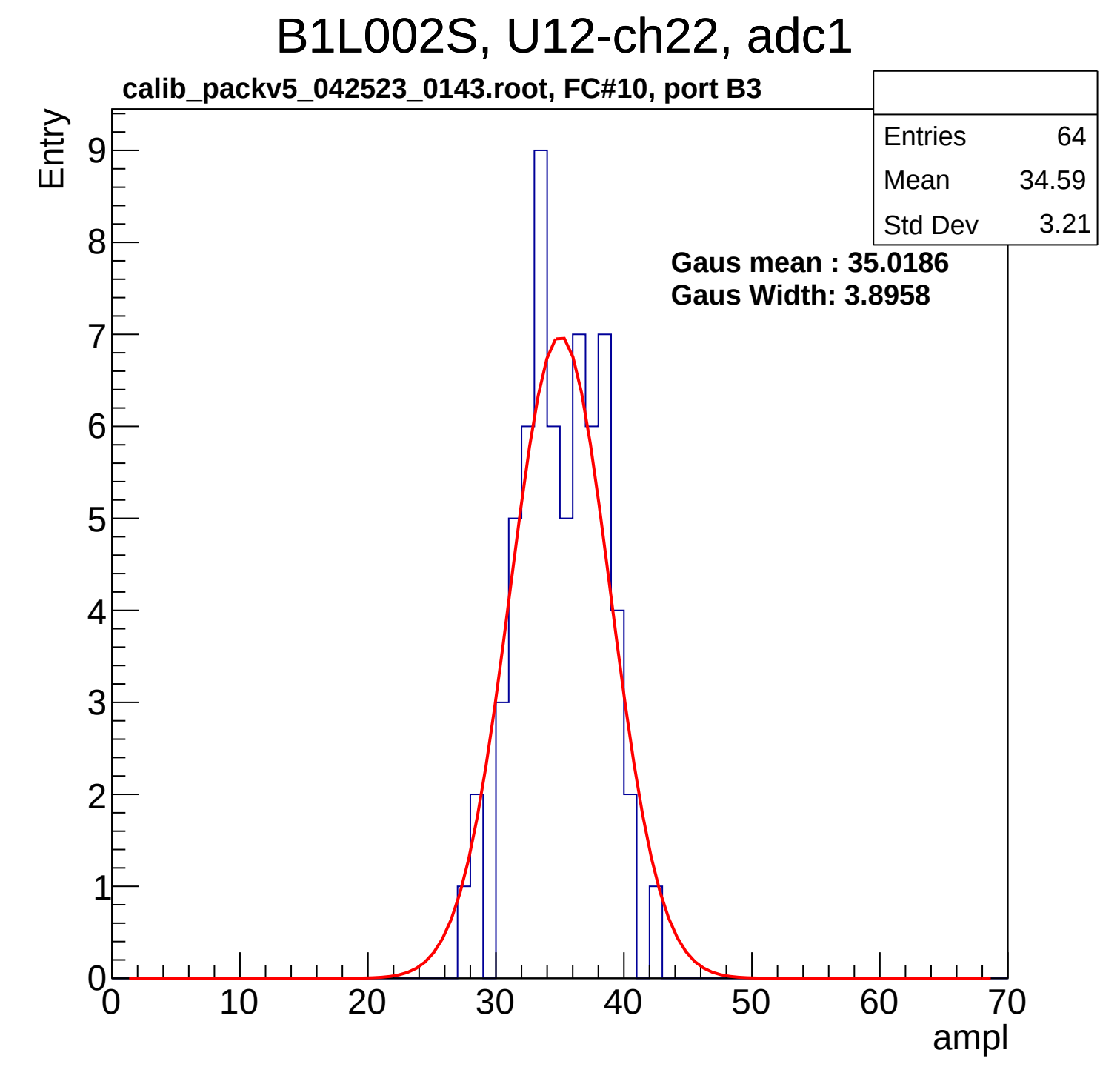
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.67
Std Dev 4.771
Gaus mean : 28.5299
Gaus Width: 2.9982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.59
Std Dev 3.21
Gaus mean : 35.0186
Gaus Width: 3.8958
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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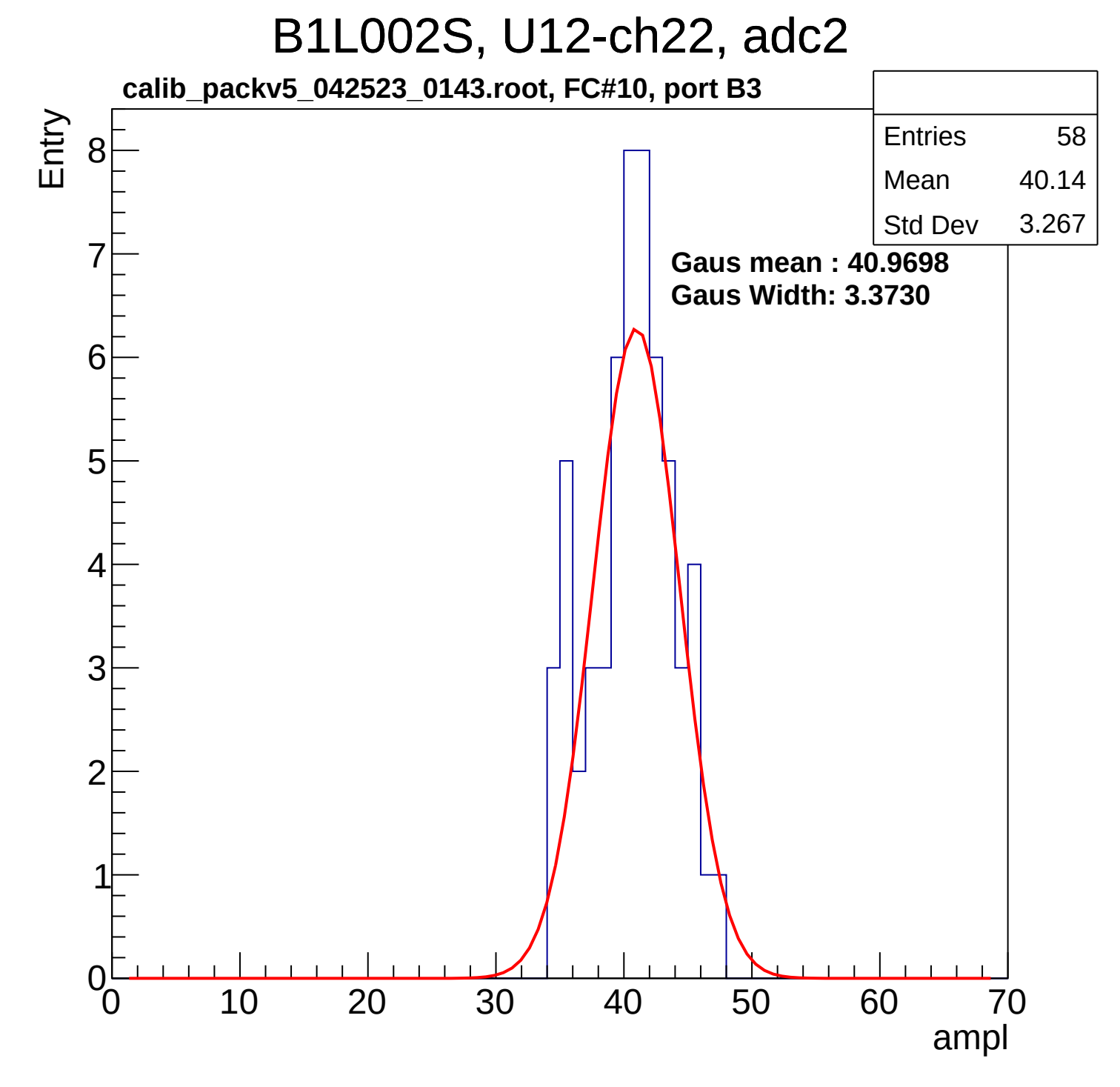
B1L002S, U12-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.14
Std Dev 3.267
Gaus mean : 40.9698
Gaus Width: 3.3730
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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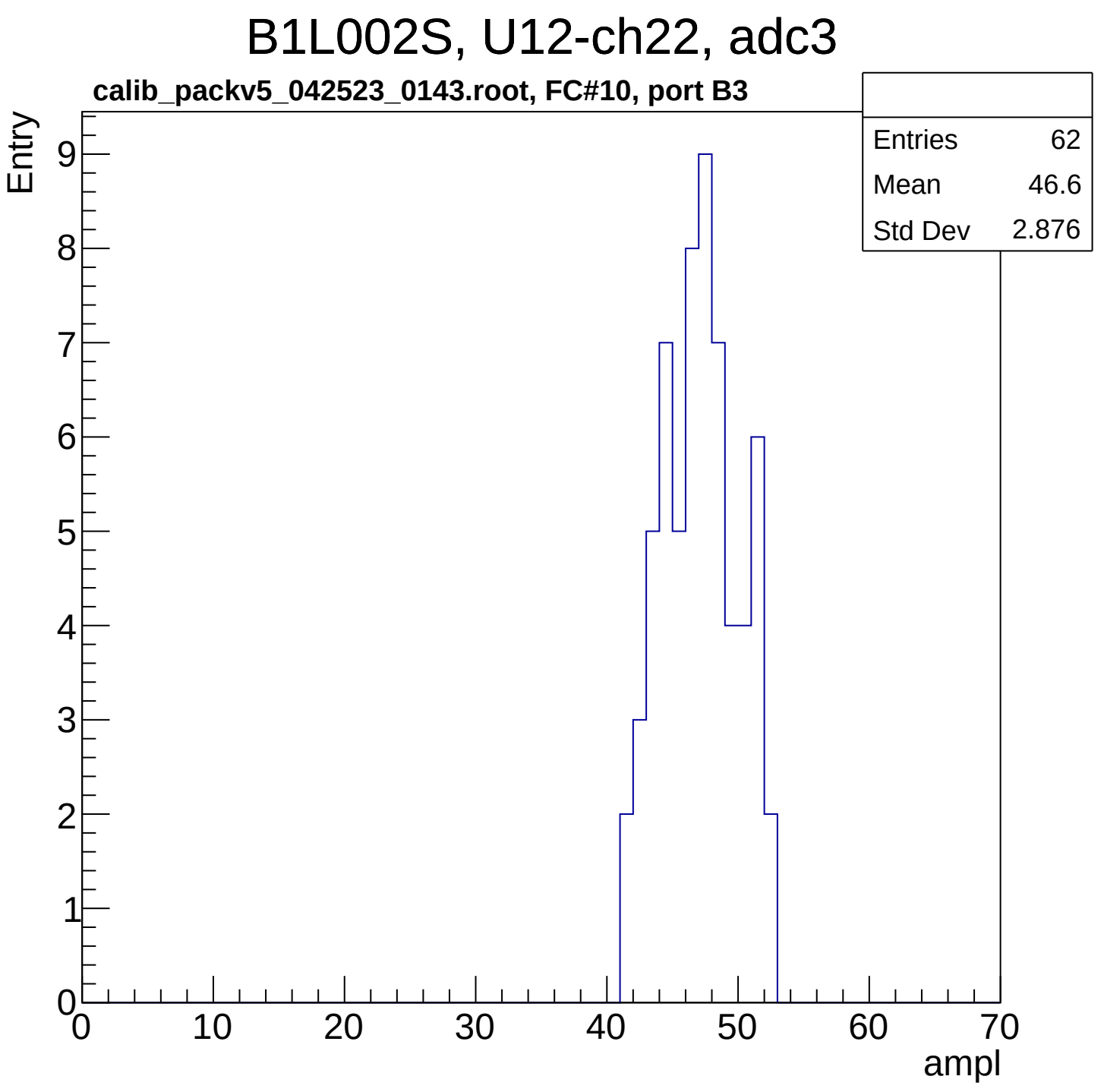
B1L002S, U12-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.6
Std Dev 2.876
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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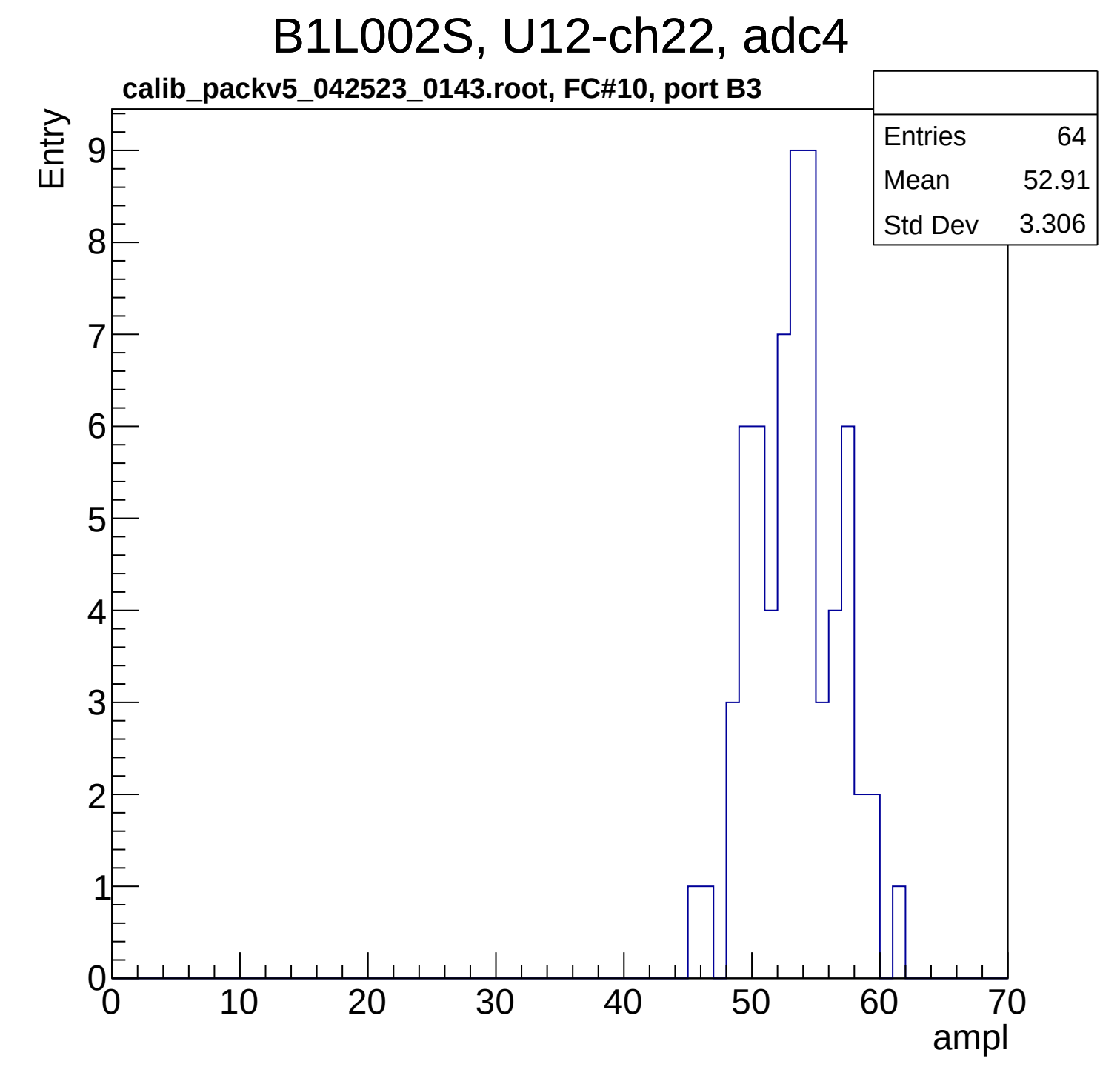
B1L002S, U12-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.91
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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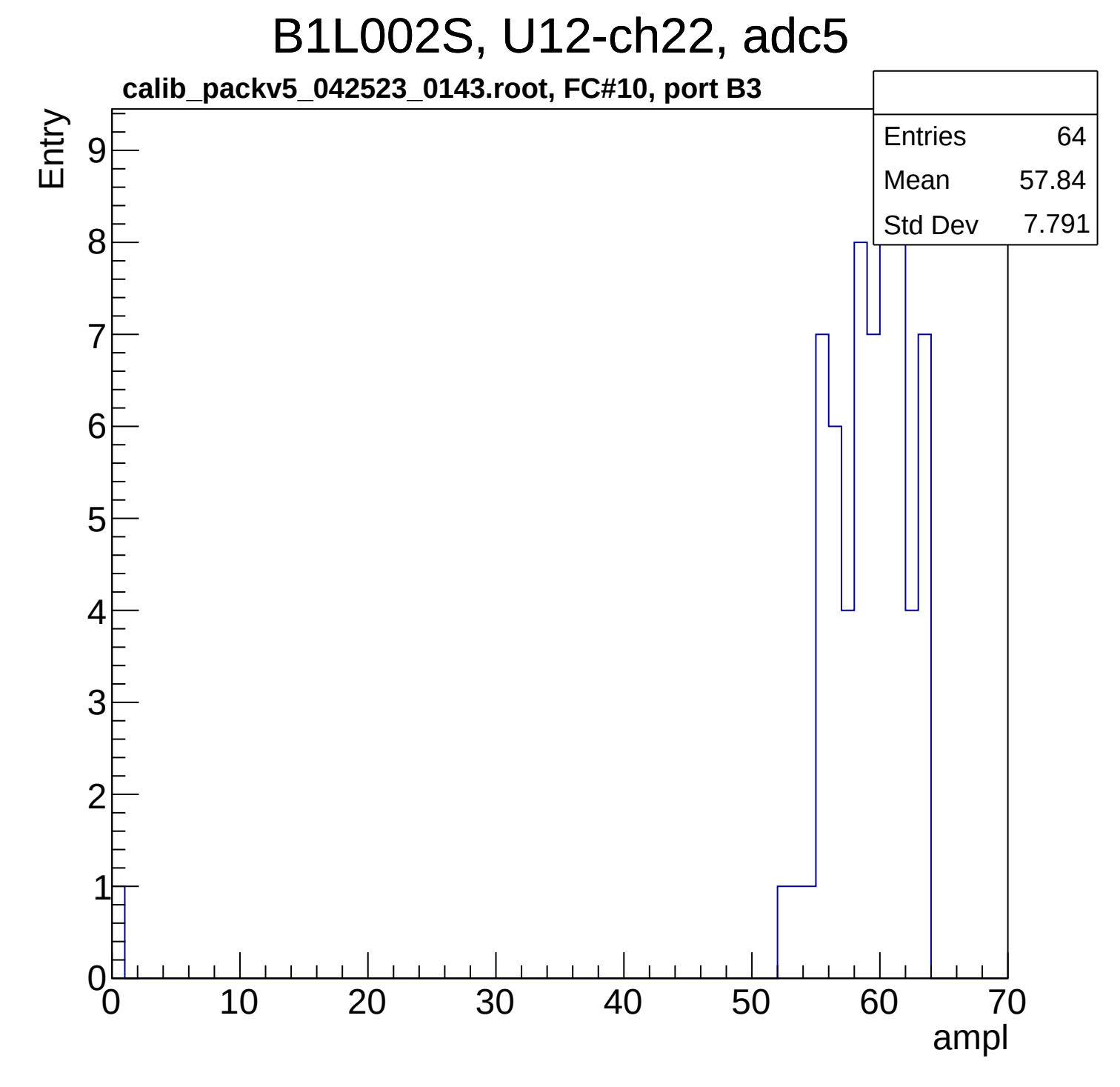
B1L002S, U12-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.84
Std Dev 7.791
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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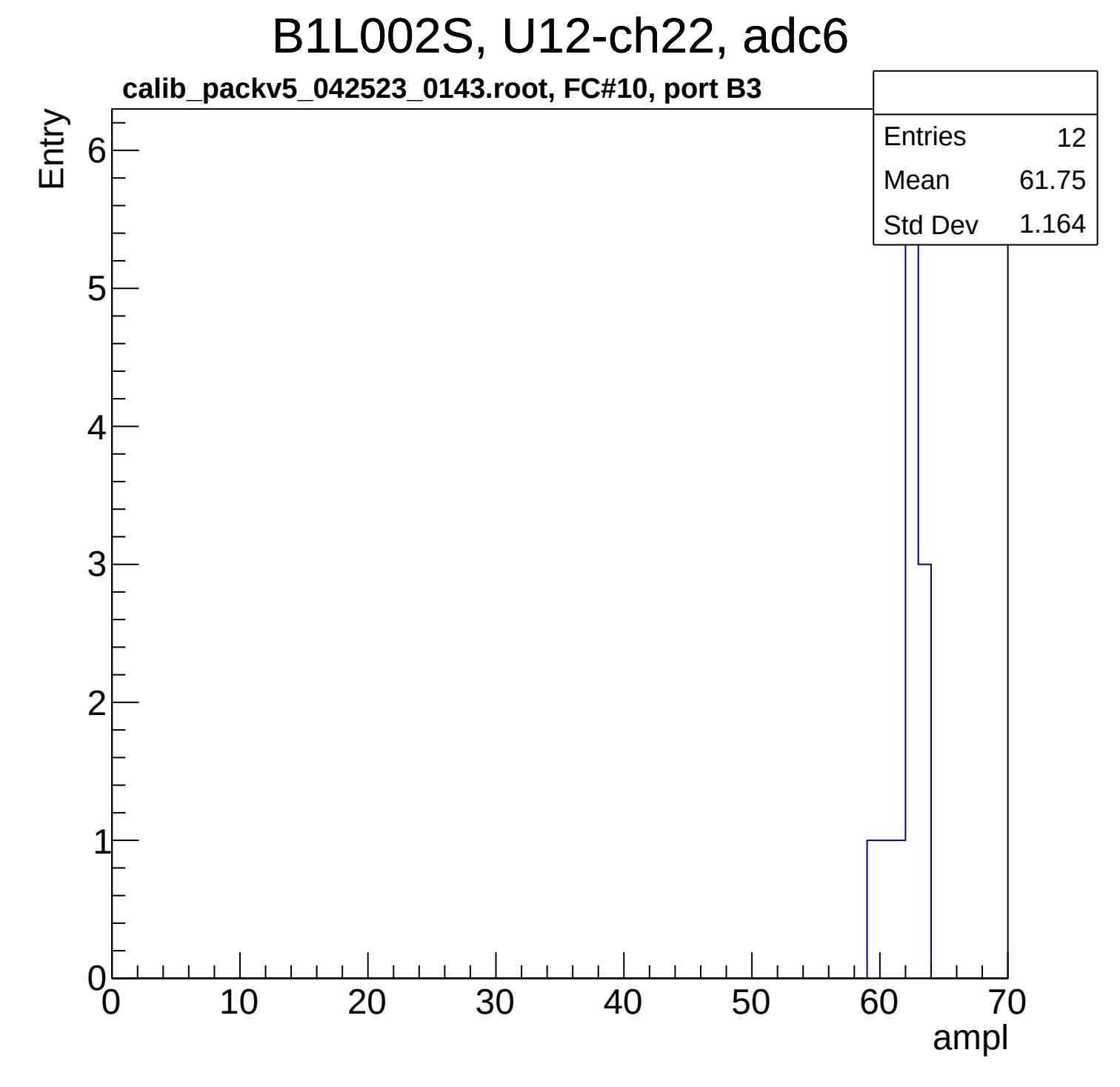
B1L002S, U12-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.75
Std Dev 1.164
Entry
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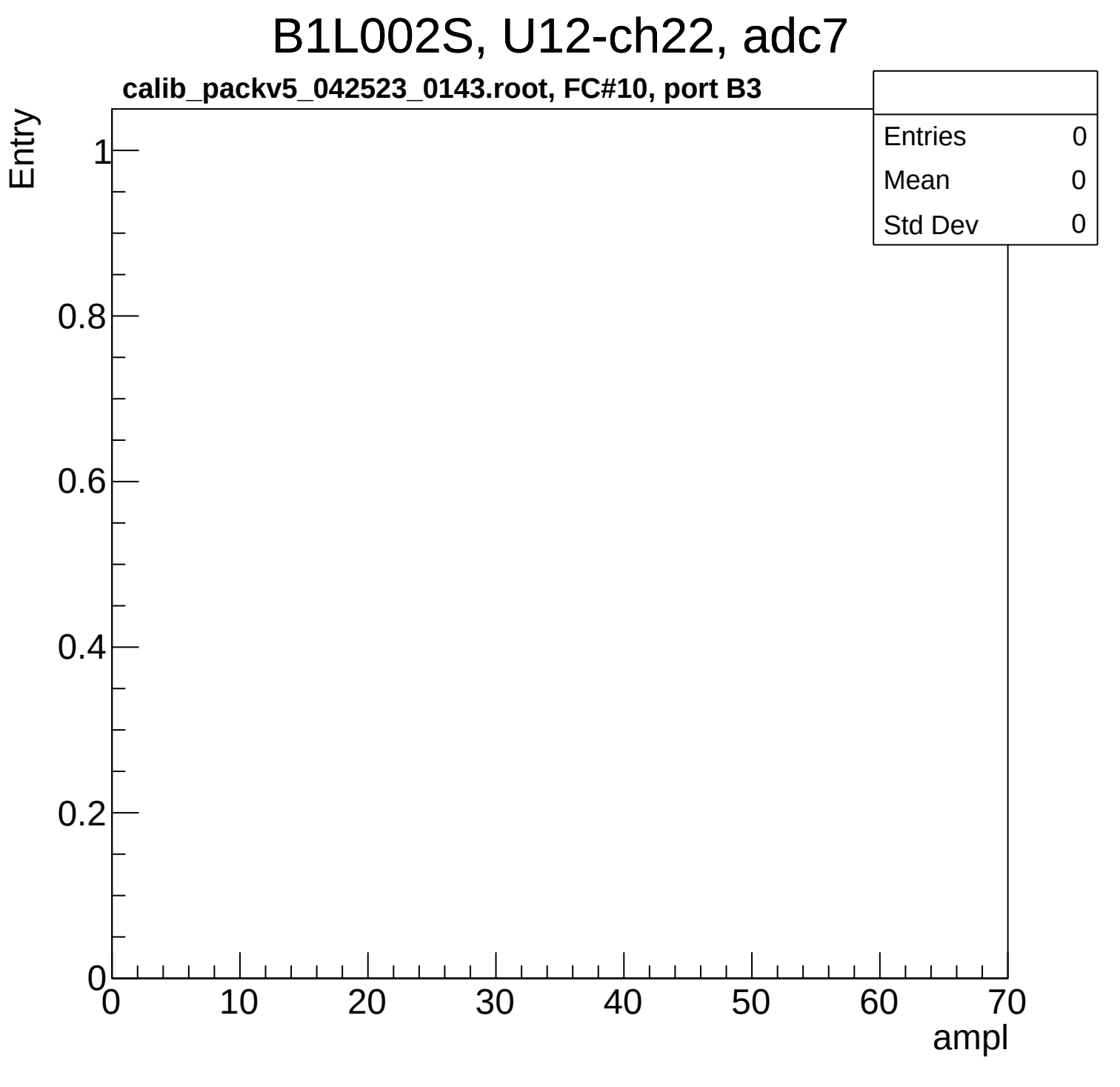
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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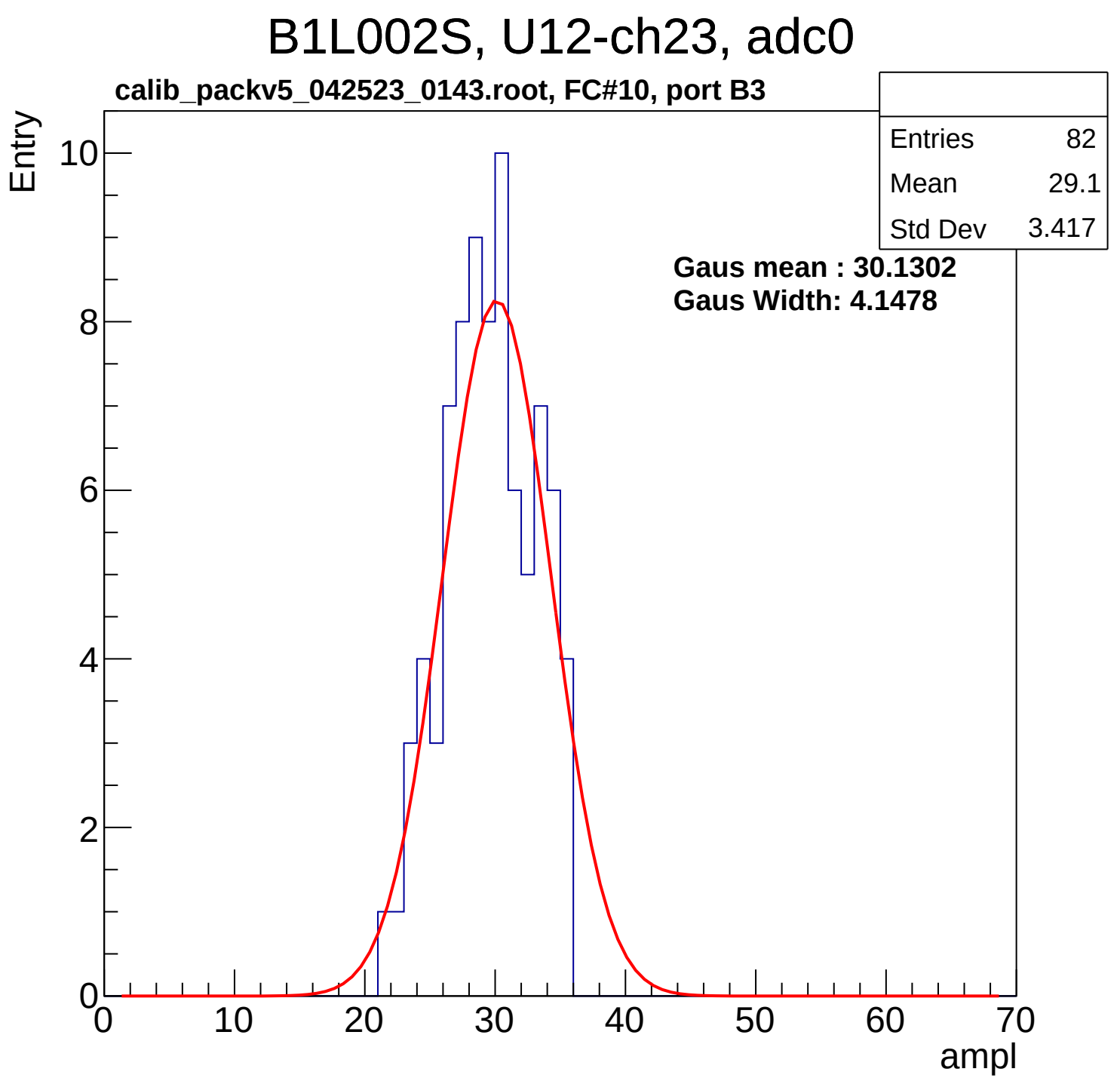
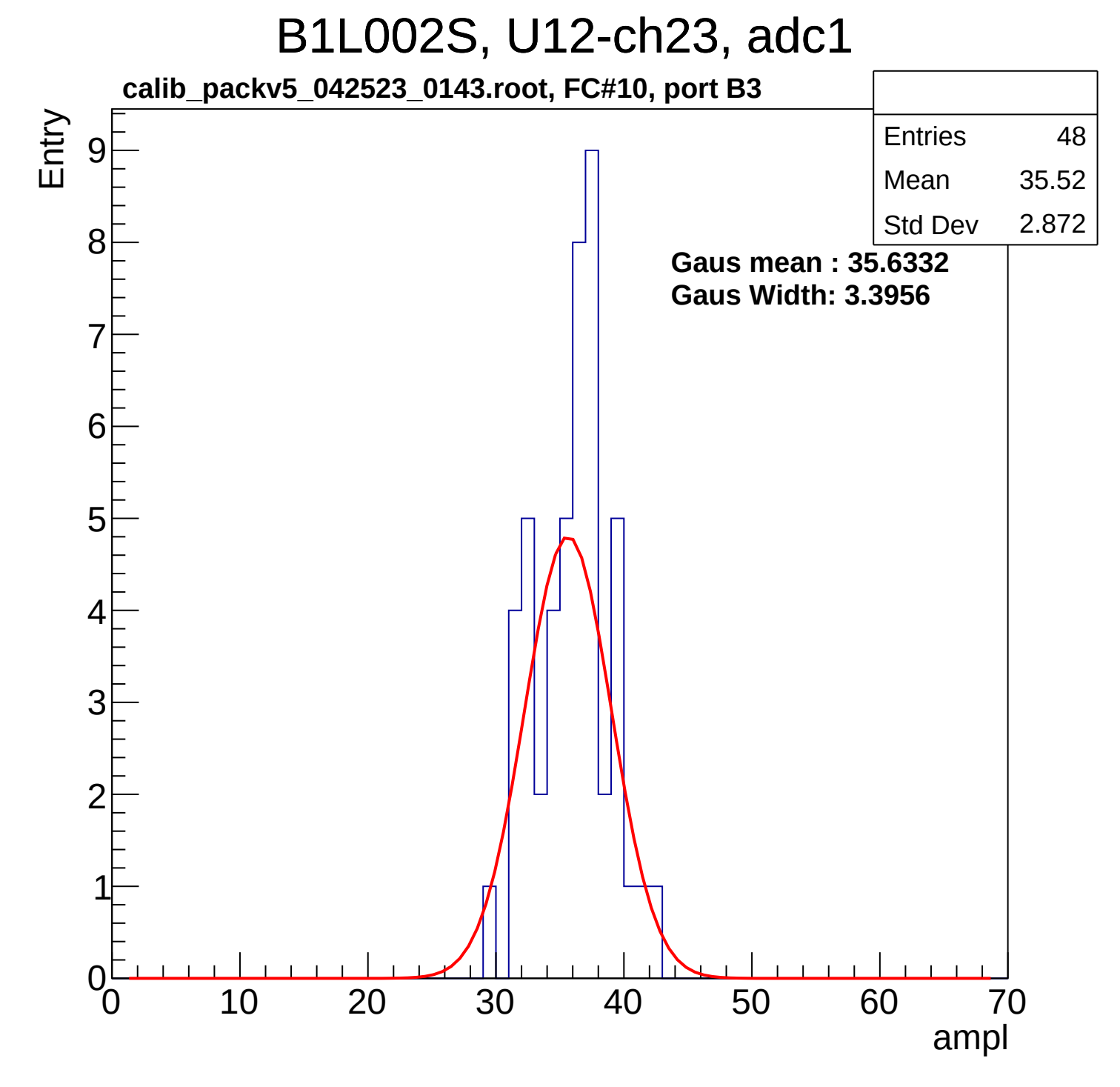
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 29.1
Std Dev 3.417
Gaus mean : 30.1302
Gaus Width: 4.1478
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 35.52
Std Dev 2.872
Gaus mean : 35.6332
Gaus Width: 3.3956
Entry
ampl
0
10
20
30
40
50
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1
2
3
4
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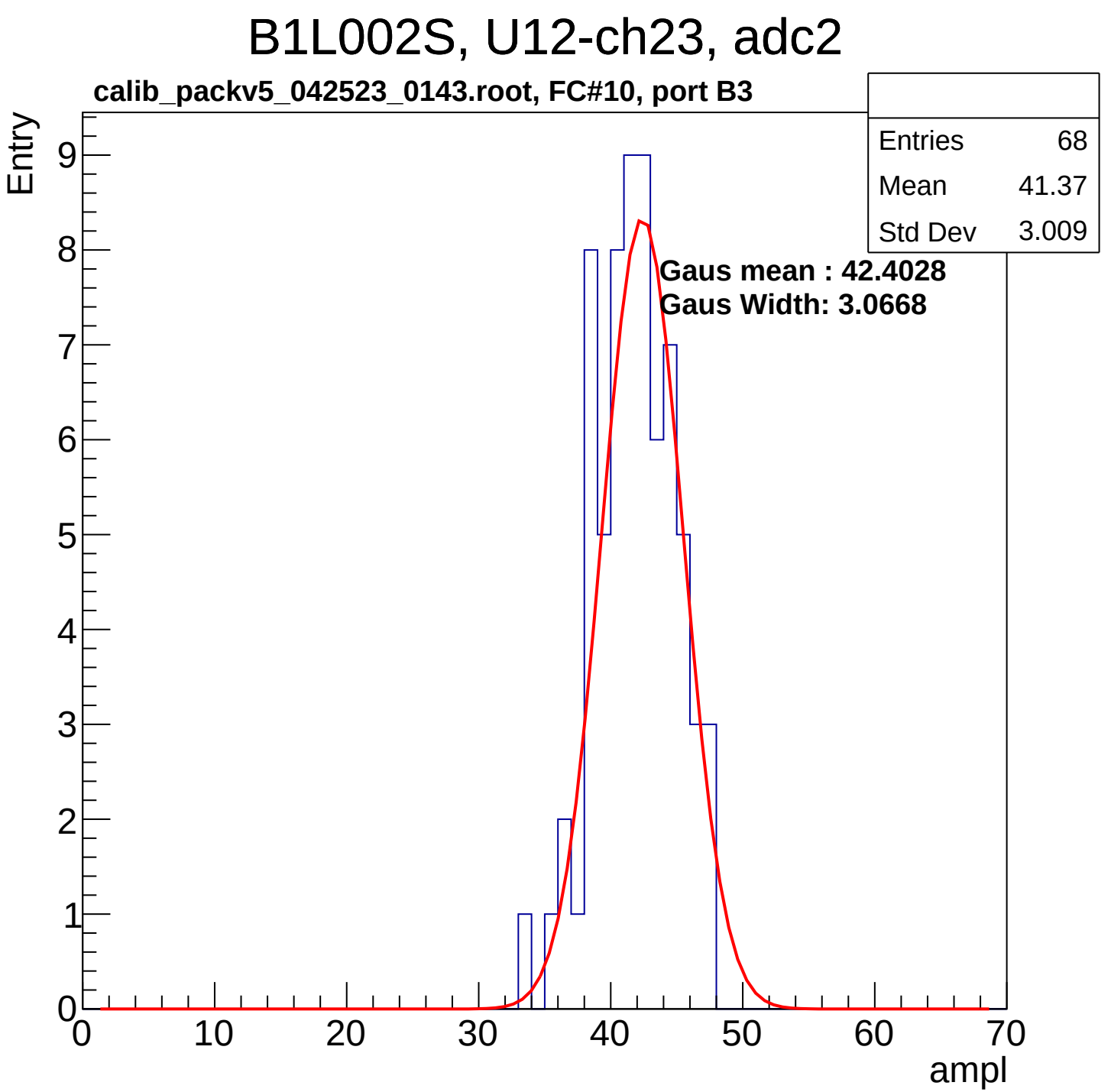
6
7
8
9


B1L002S, U12-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.37
Std Dev 3.009
Gaus mean : 42.4028
Gaus Width: 3.0668
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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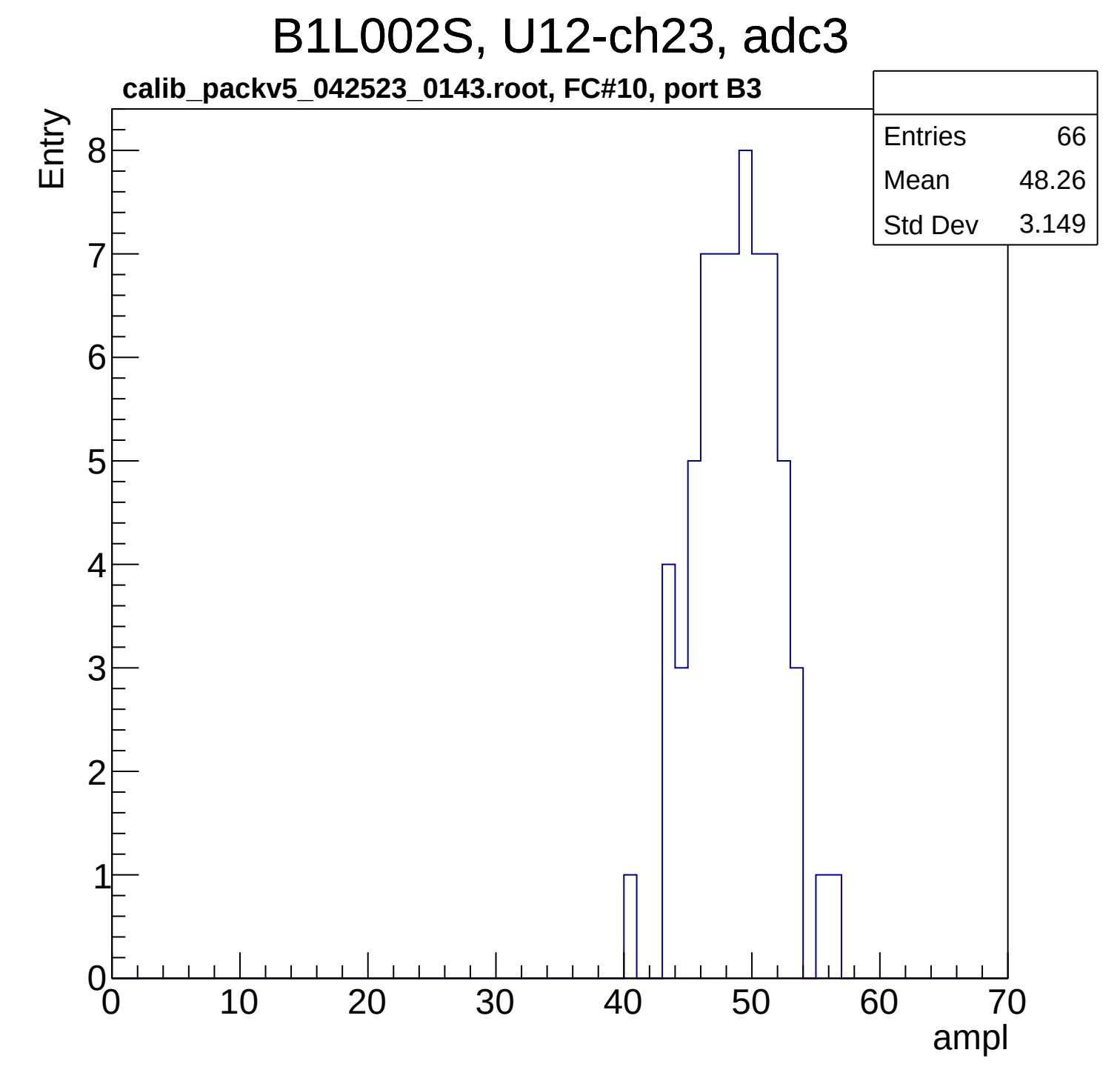
B1L002S, U12-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.26
Std Dev 3.149
Entry
ampl
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3
4
5
6
7
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0
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60
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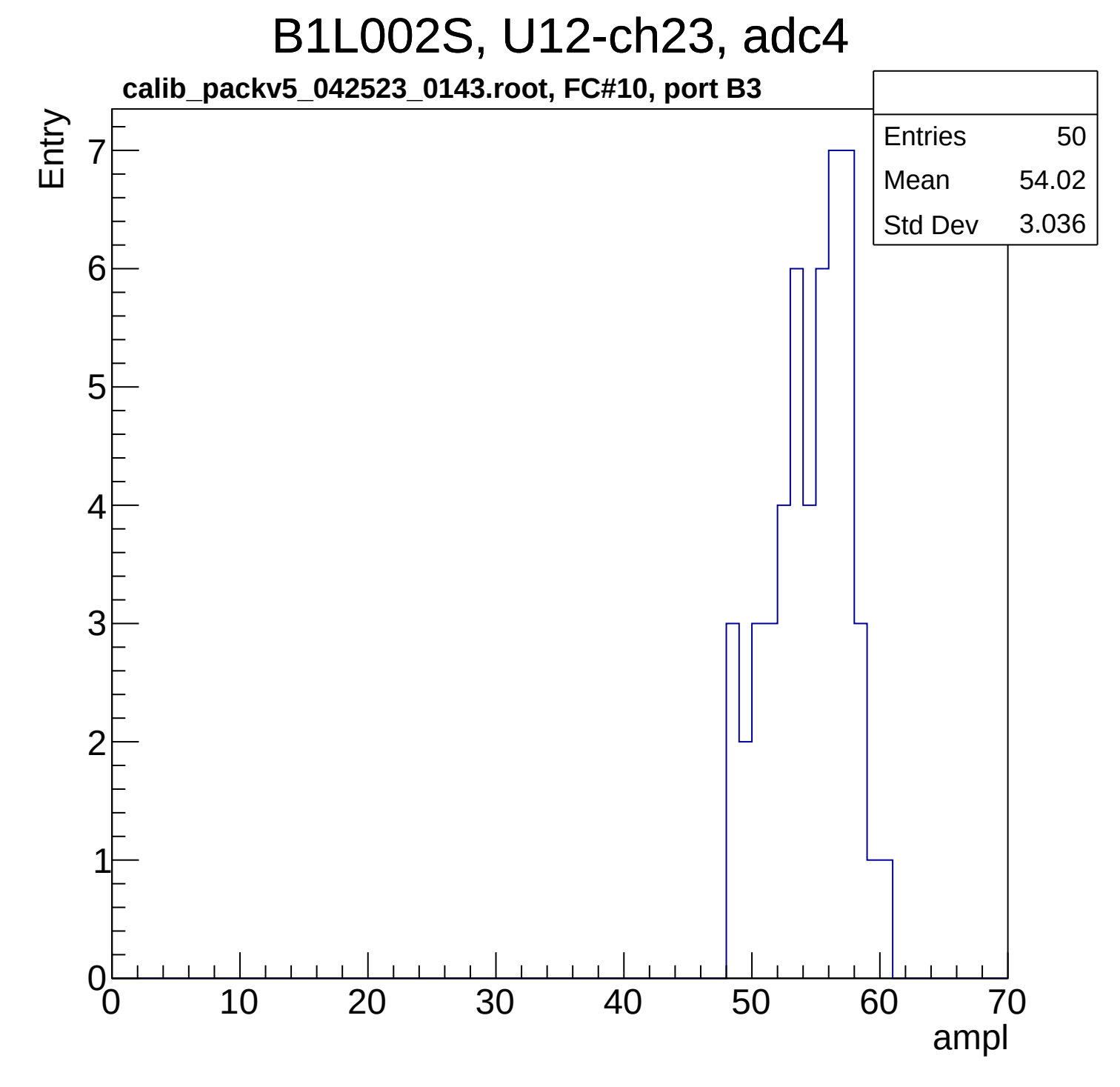
B1L002S, U12-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 54.02
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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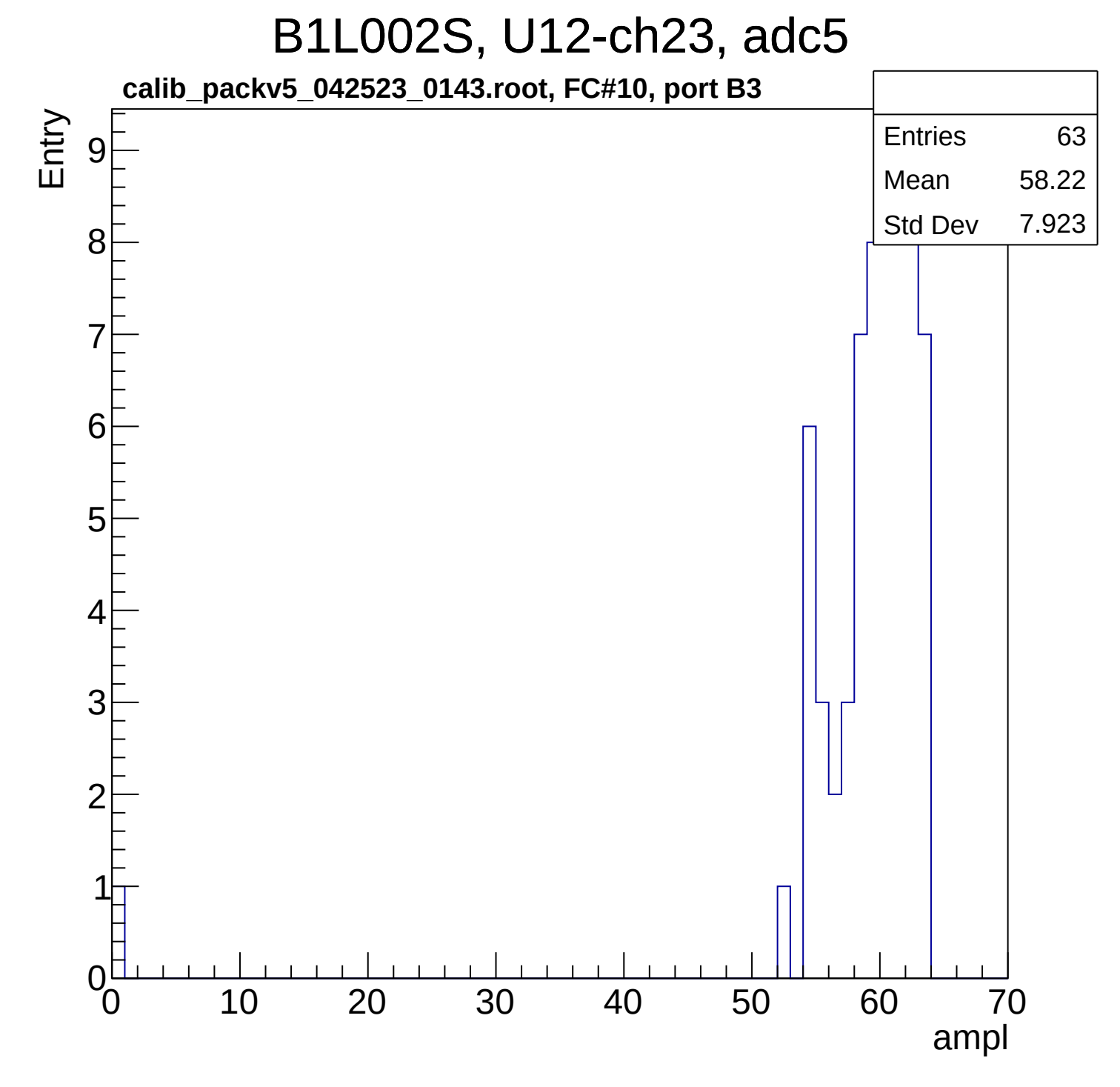
B1L002S, U12-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.22
Std Dev 7.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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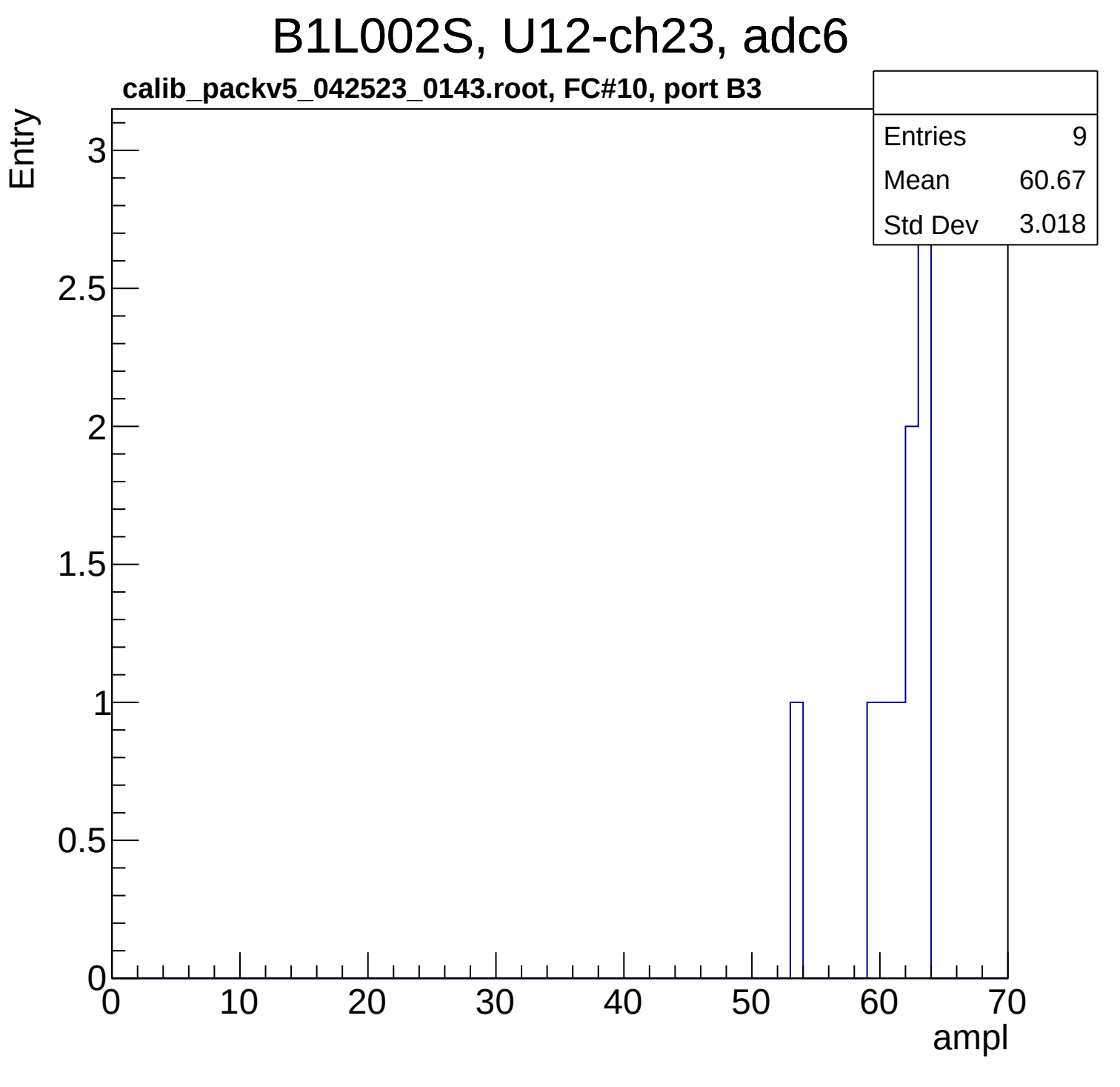
B1L002S, U12-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 60.67
Std Dev 3.018
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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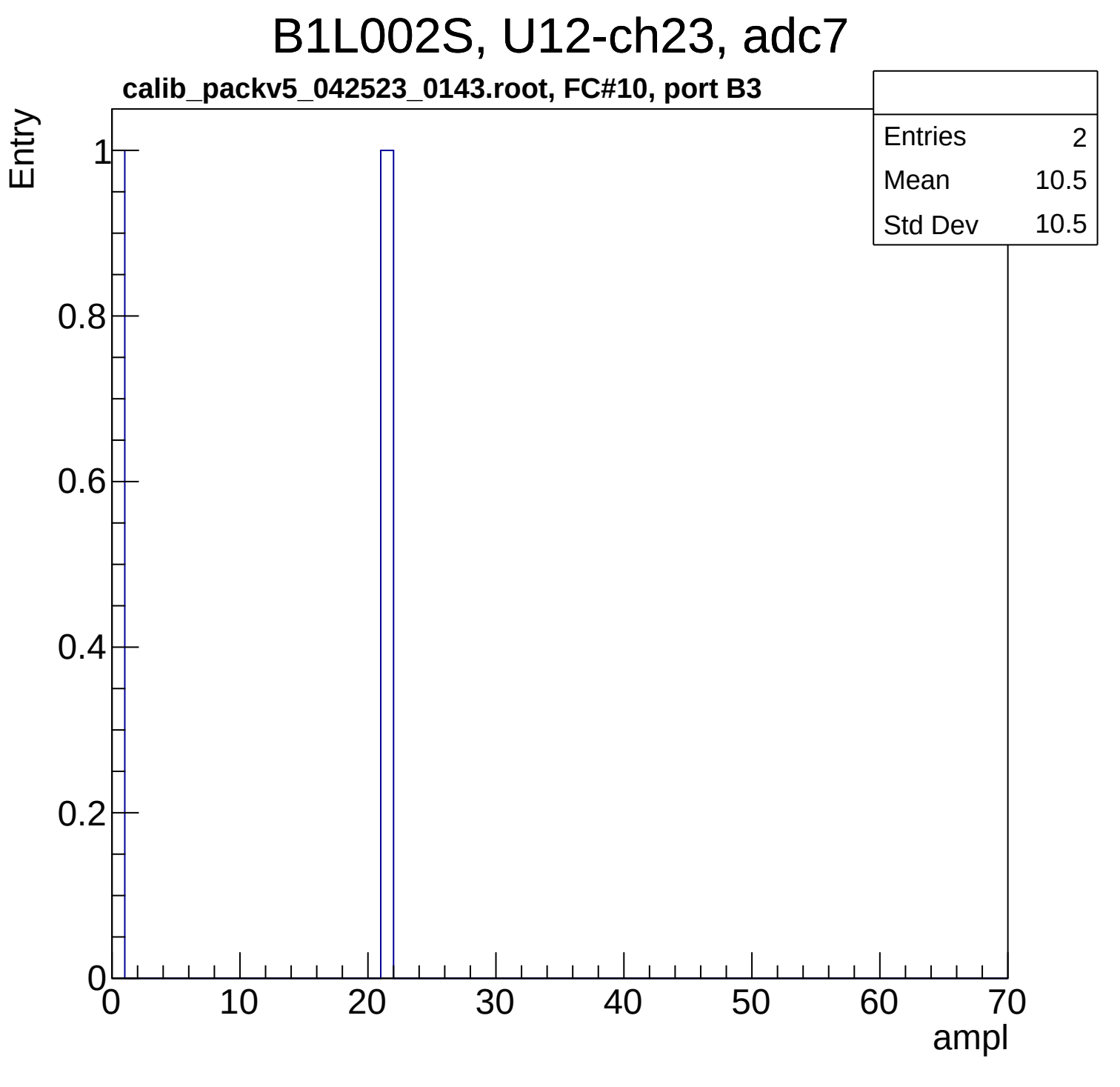
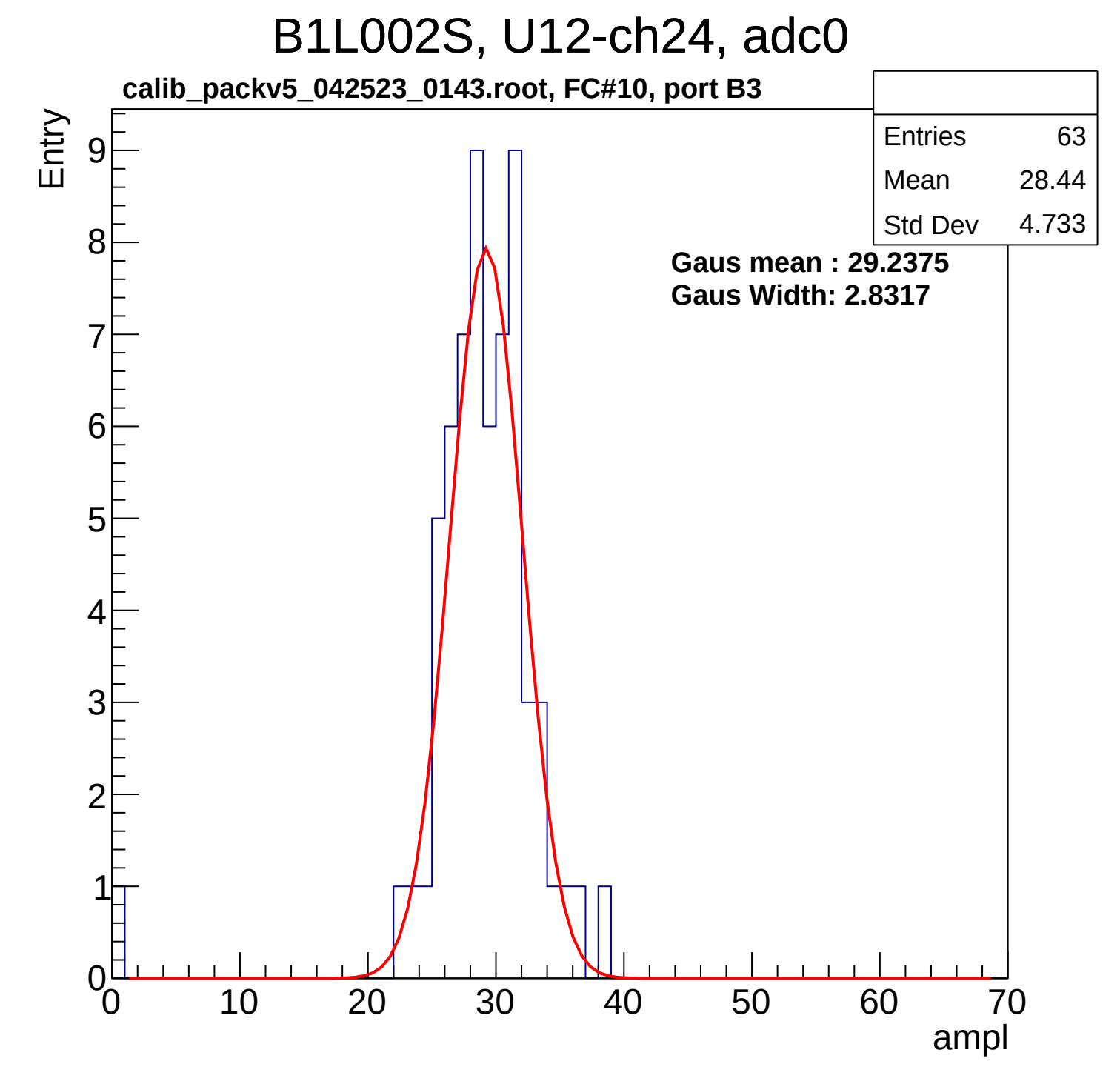

B1L002S, U12-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.44
Std Dev 4.733
Gaus mean : 29.2375
Gaus Width: 2.8317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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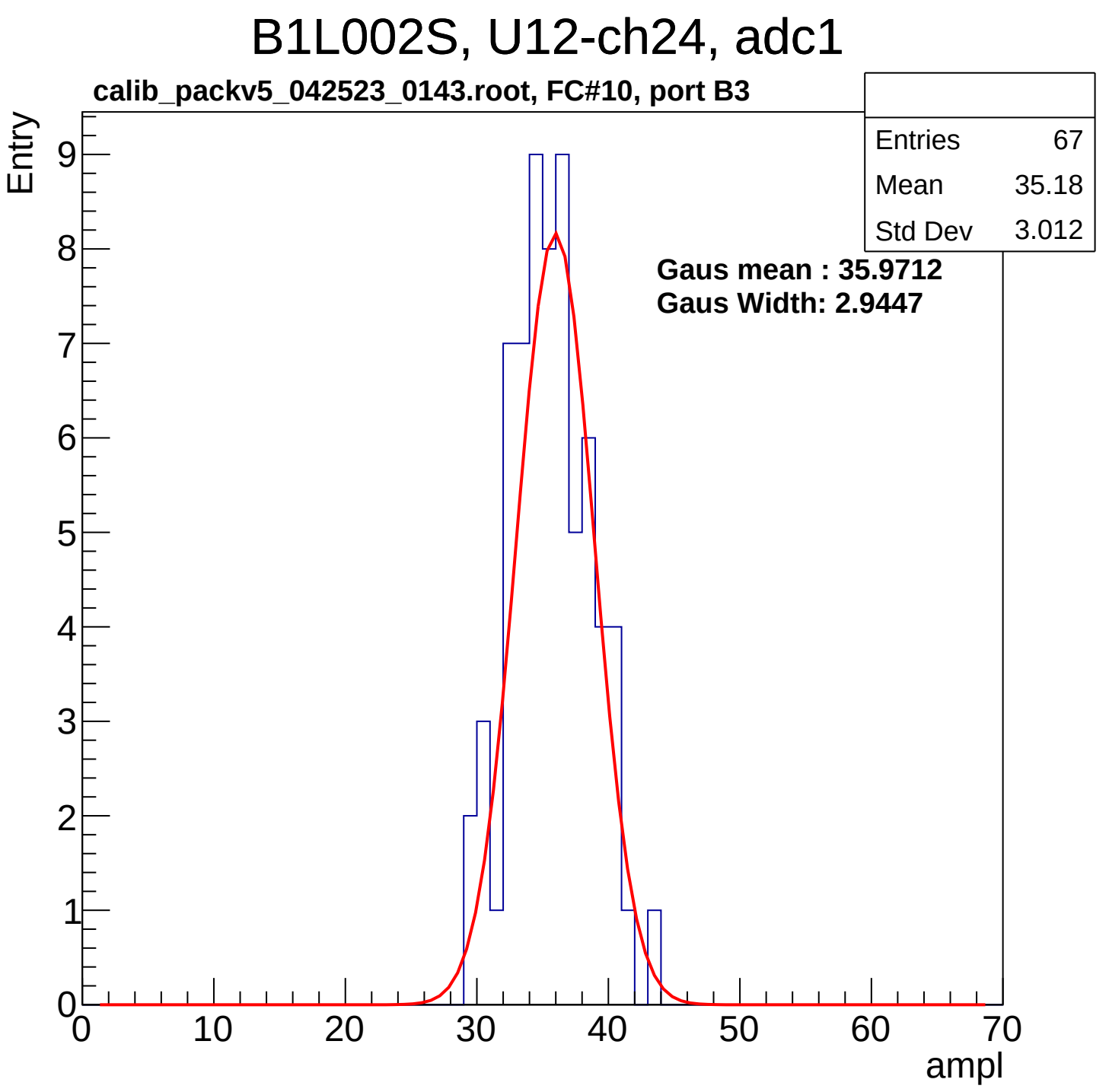
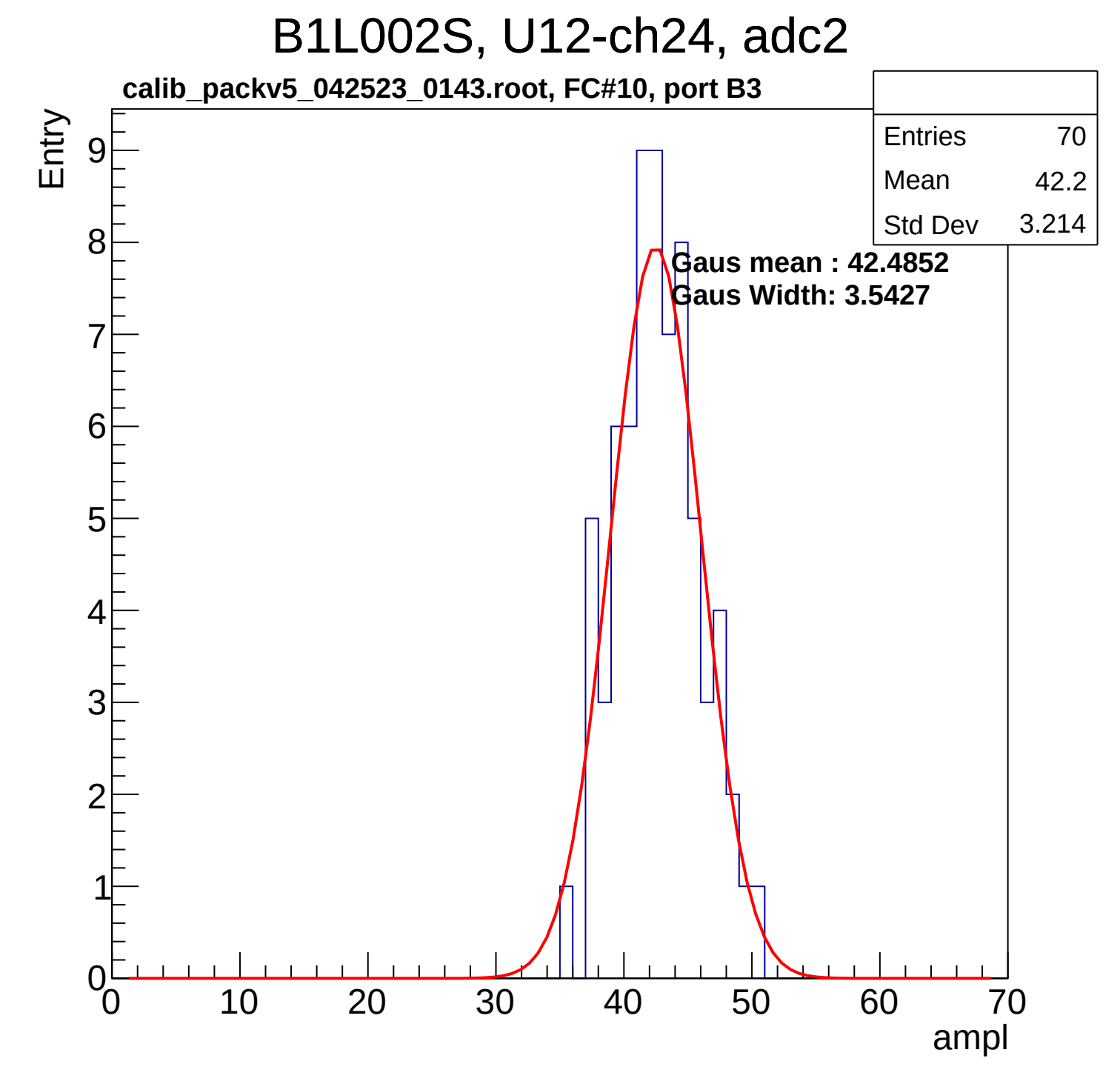
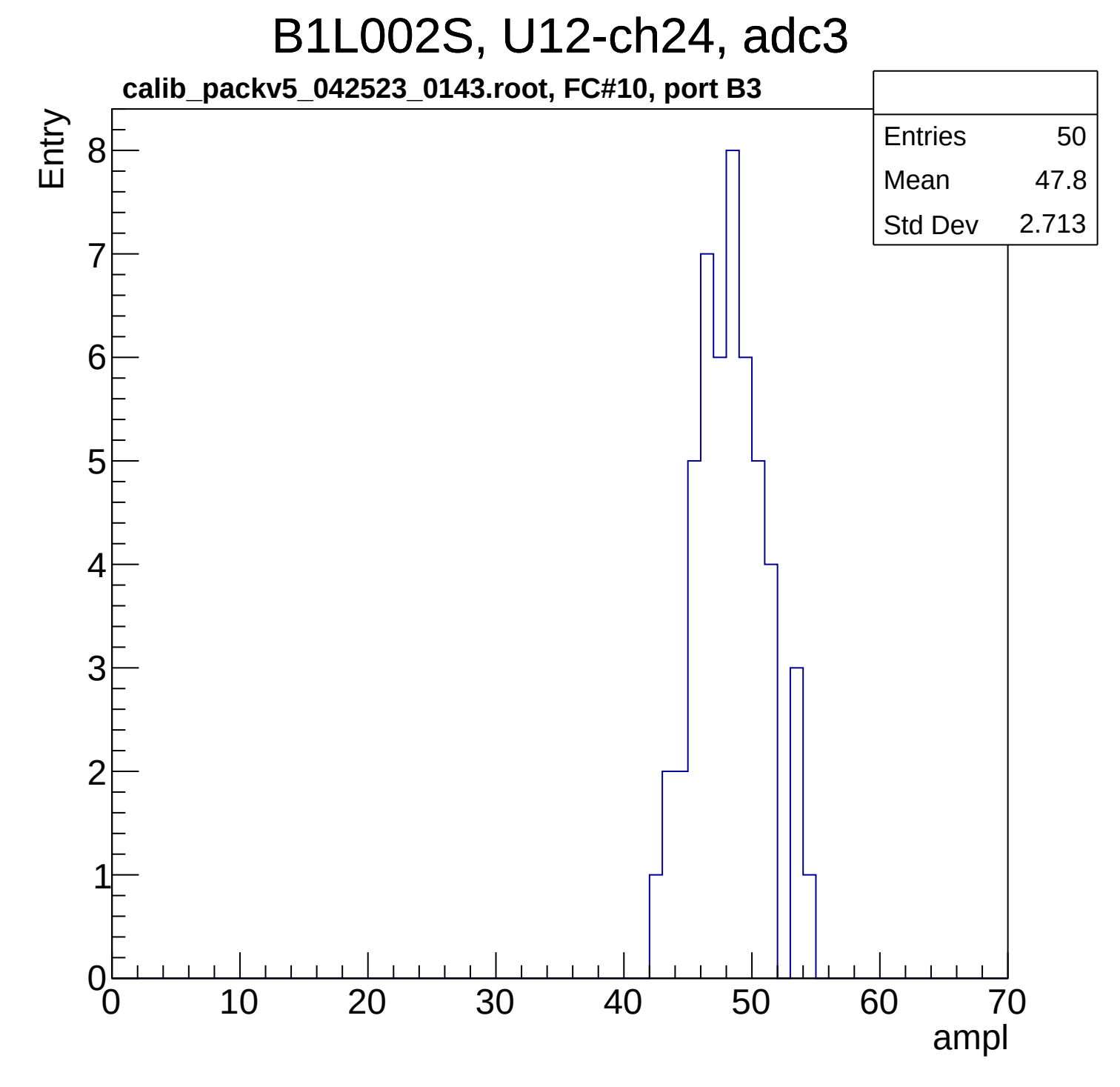

B1L002S, U12-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.18
Std Dev 3.012
Gaus mean : 35.9712
Gaus Width: 2.9447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 42.2
Std Dev 3.214
Gaus mean : 42.4852
Gaus Width: 3.5427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 47.8
Std Dev 2.713
Entry
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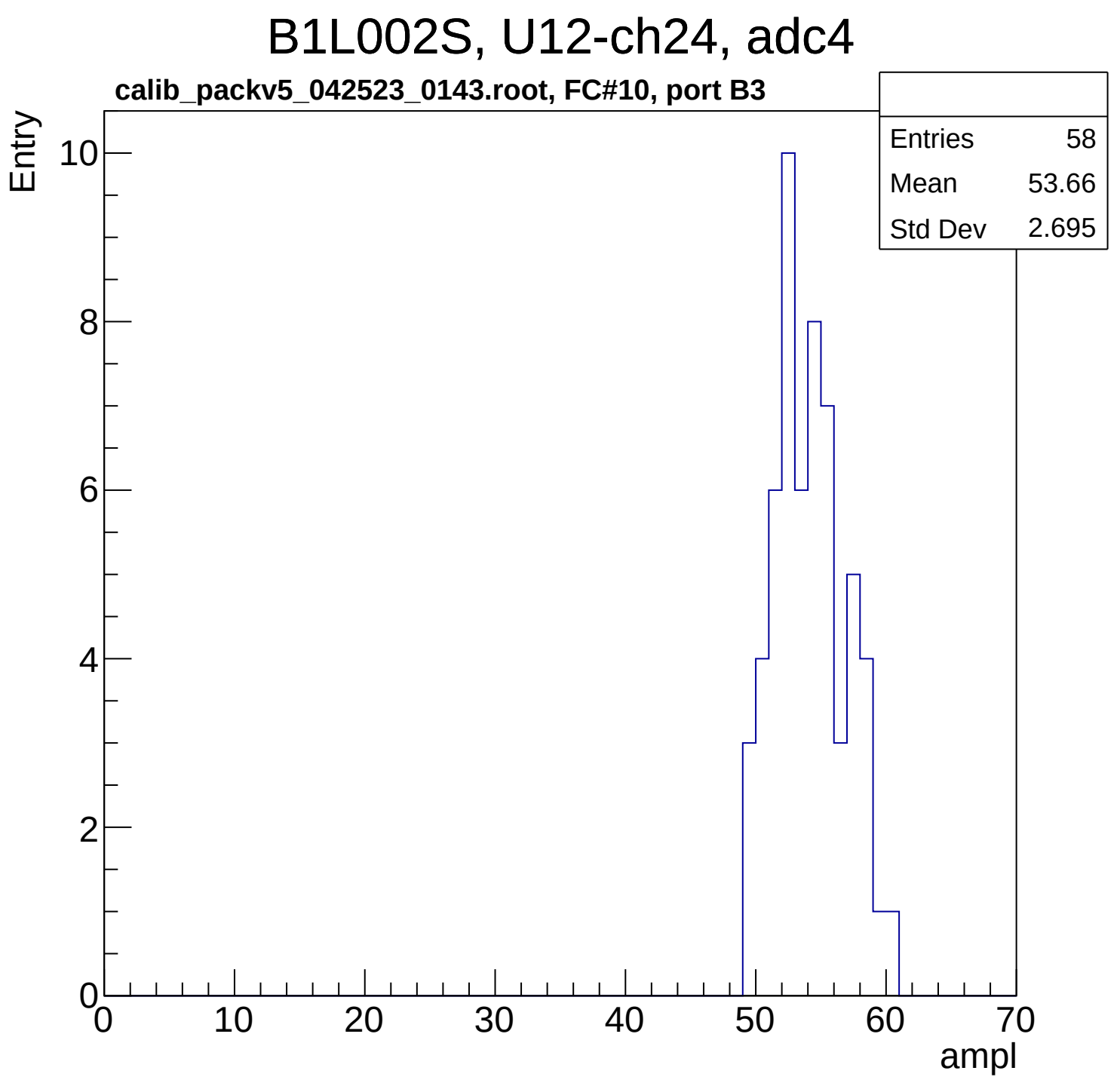
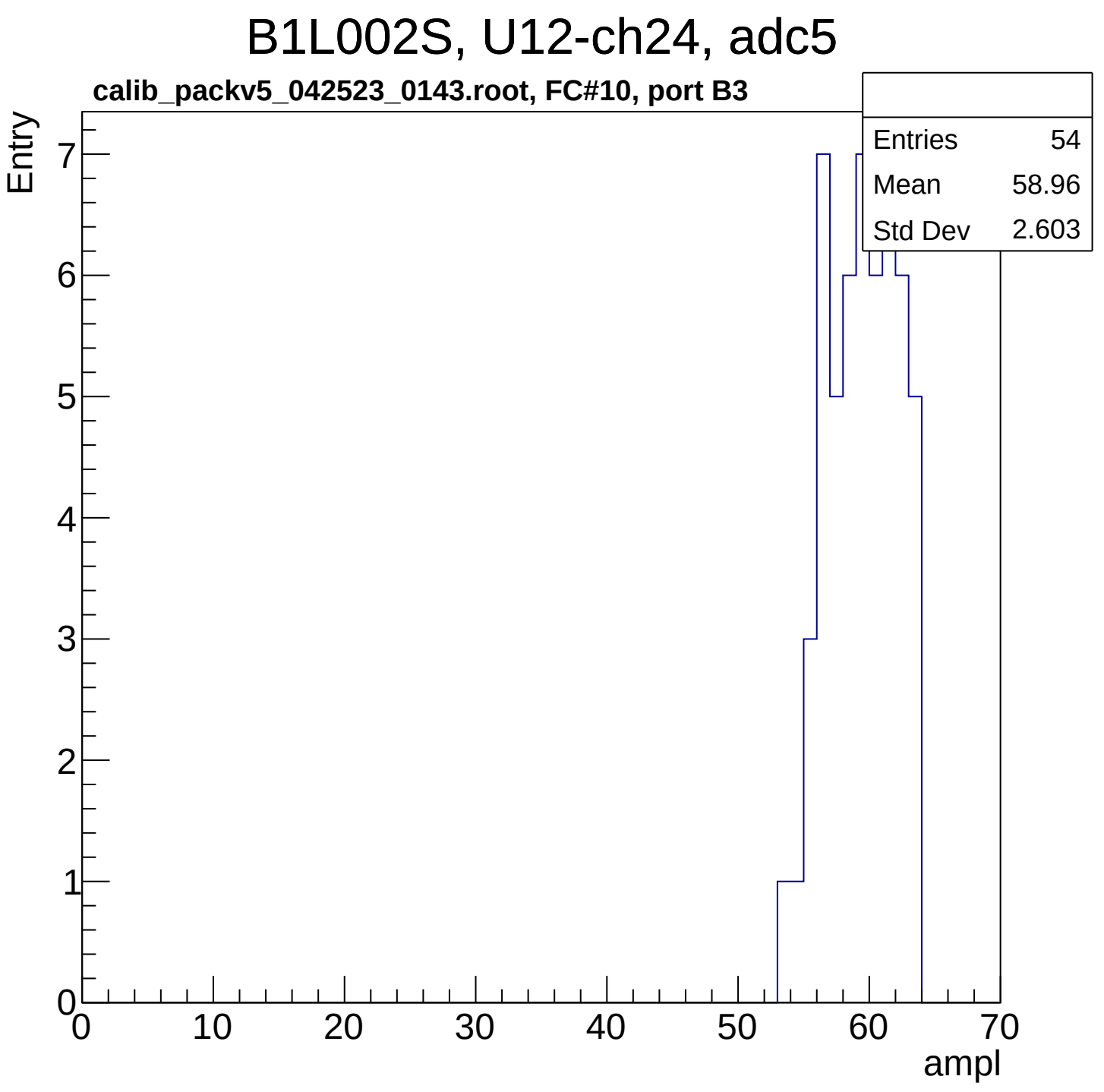
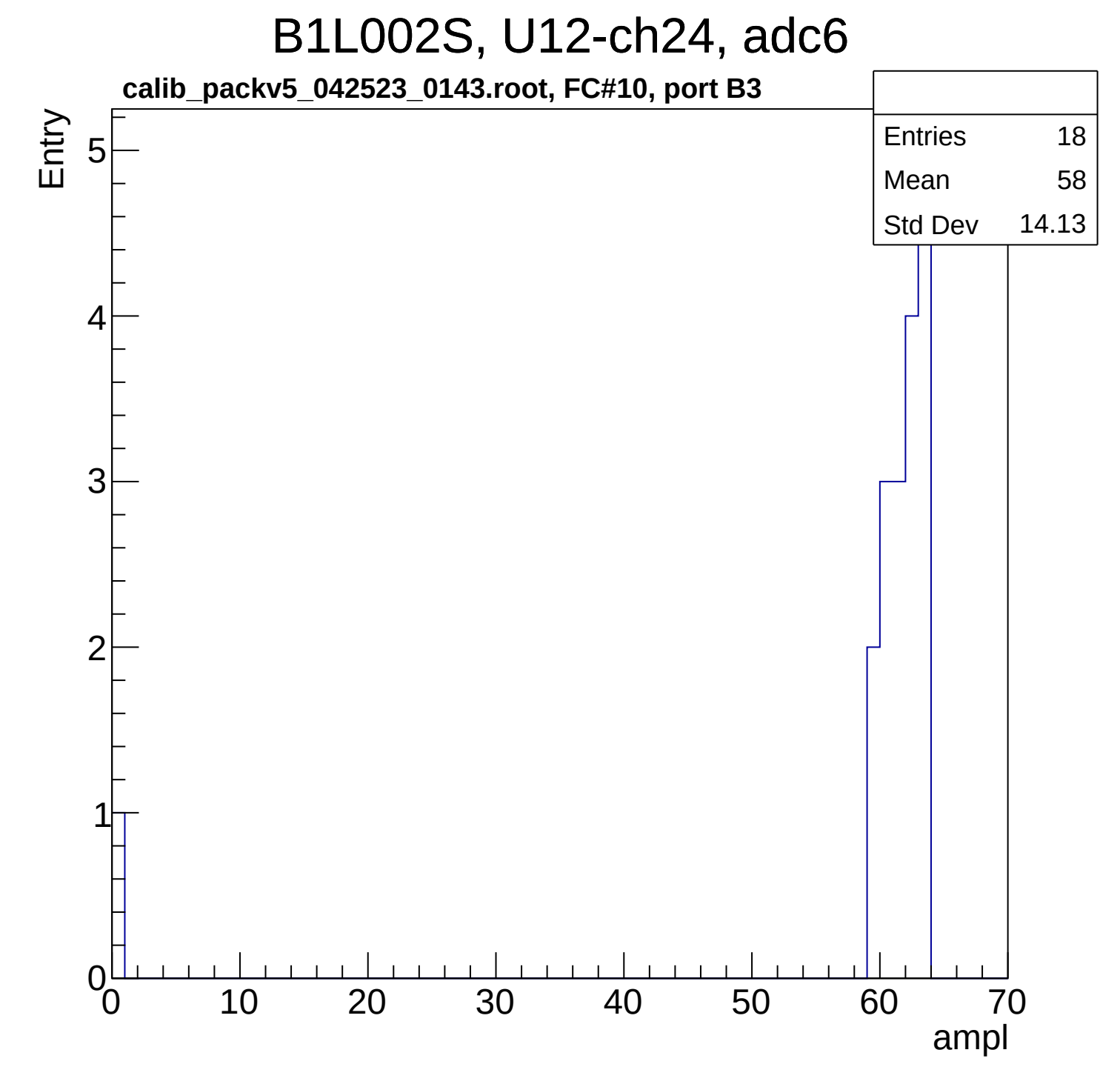
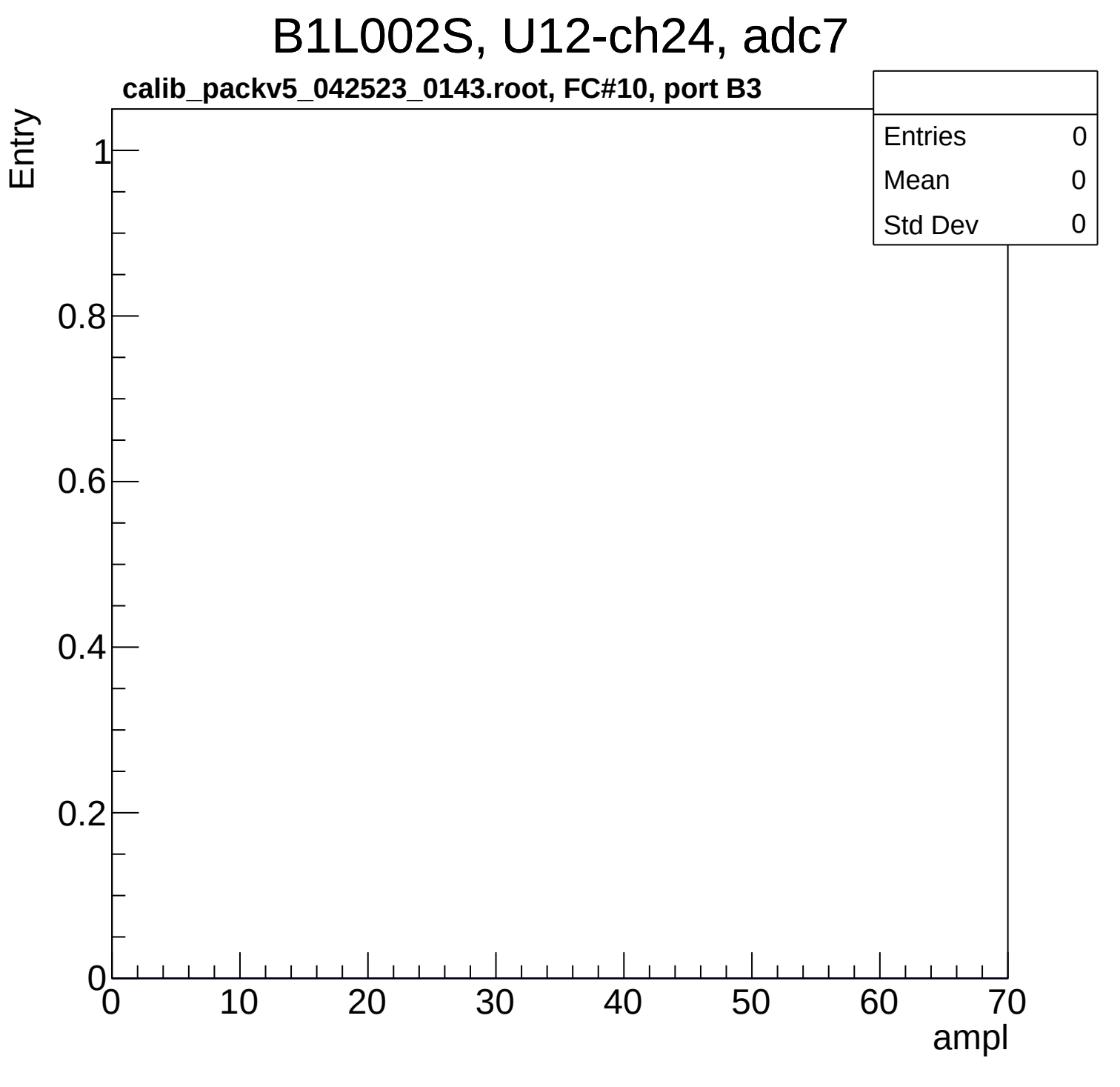
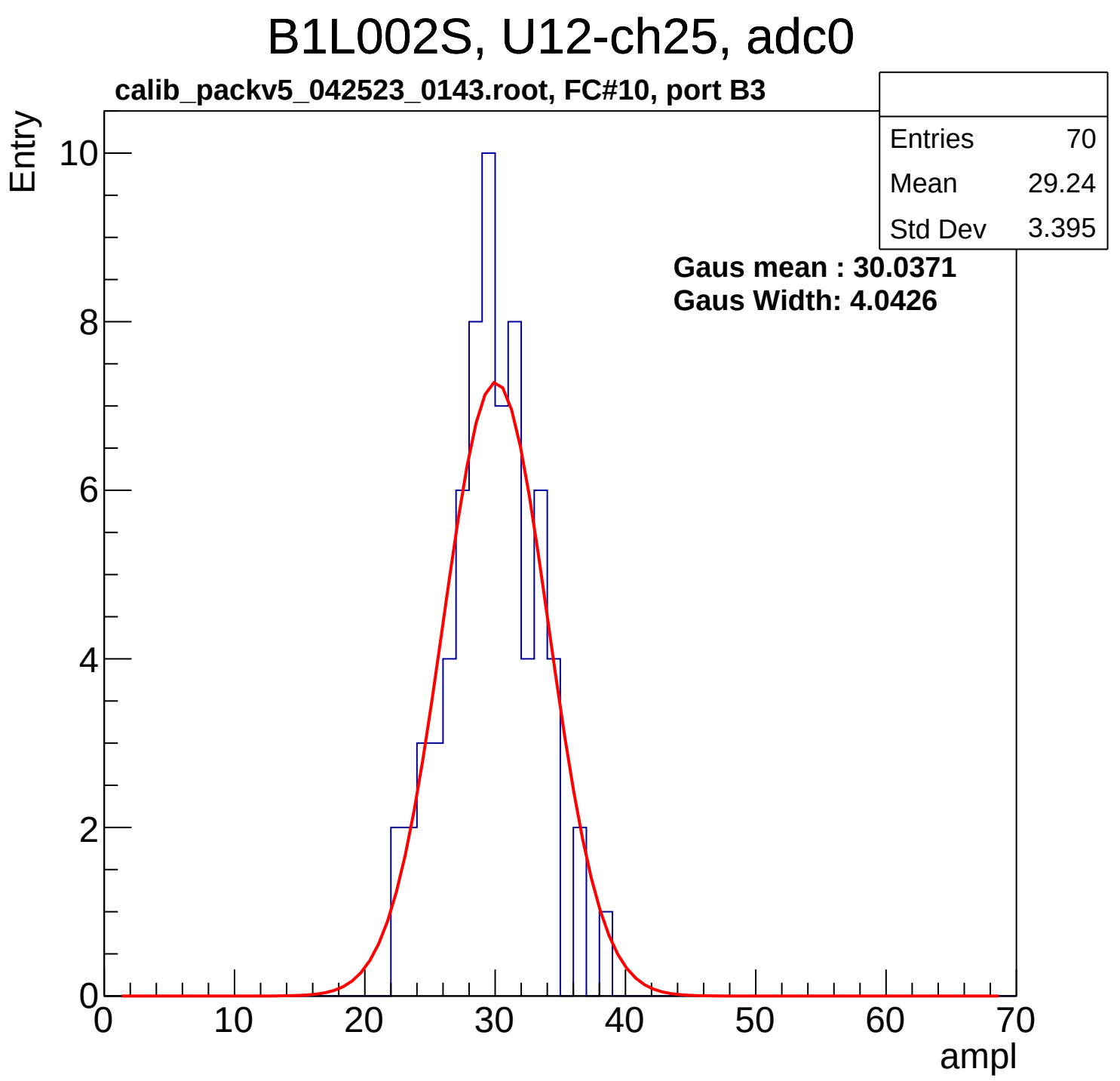
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

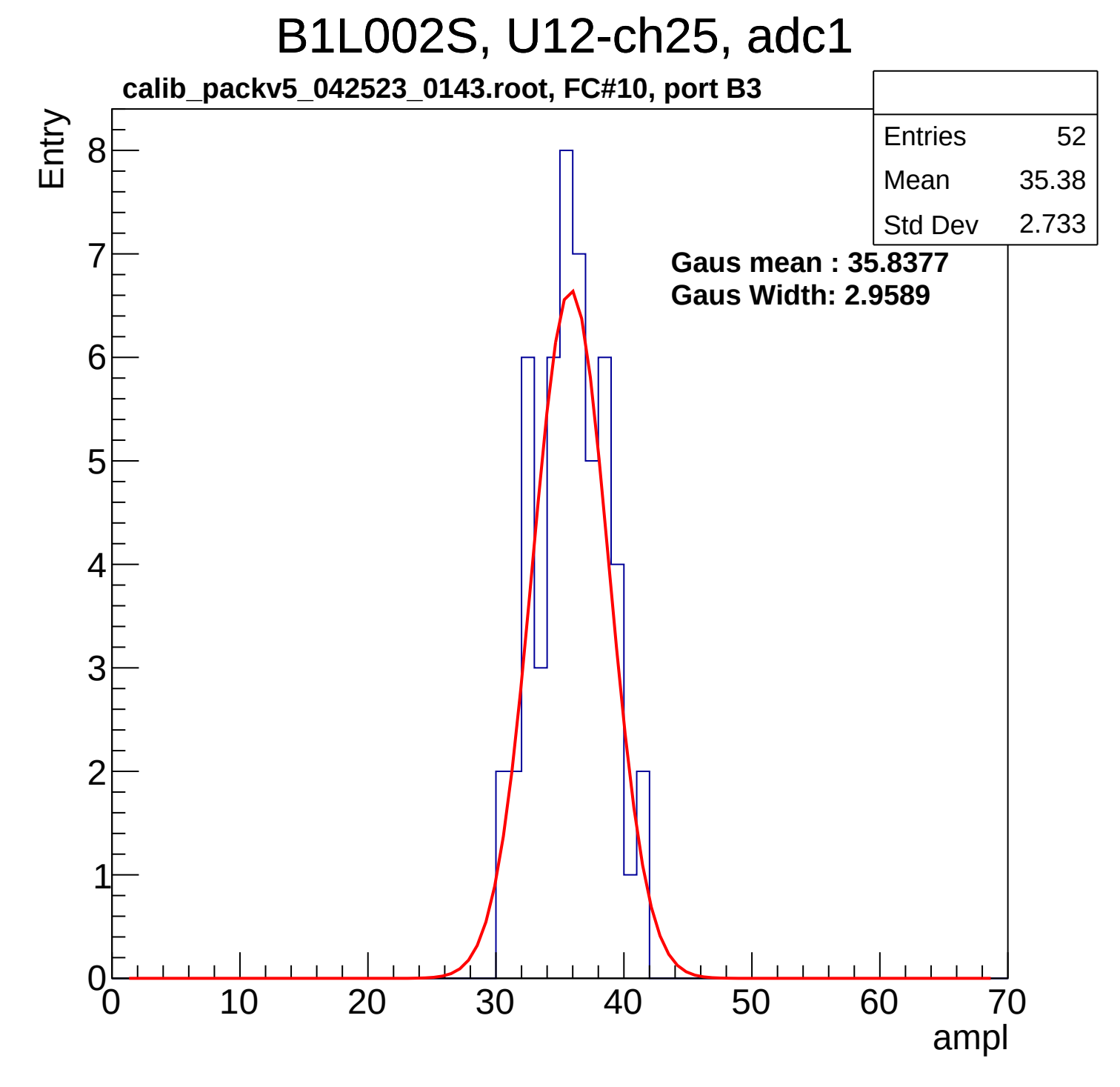

B1L002S, U12-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.66
Std Dev 2.695
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

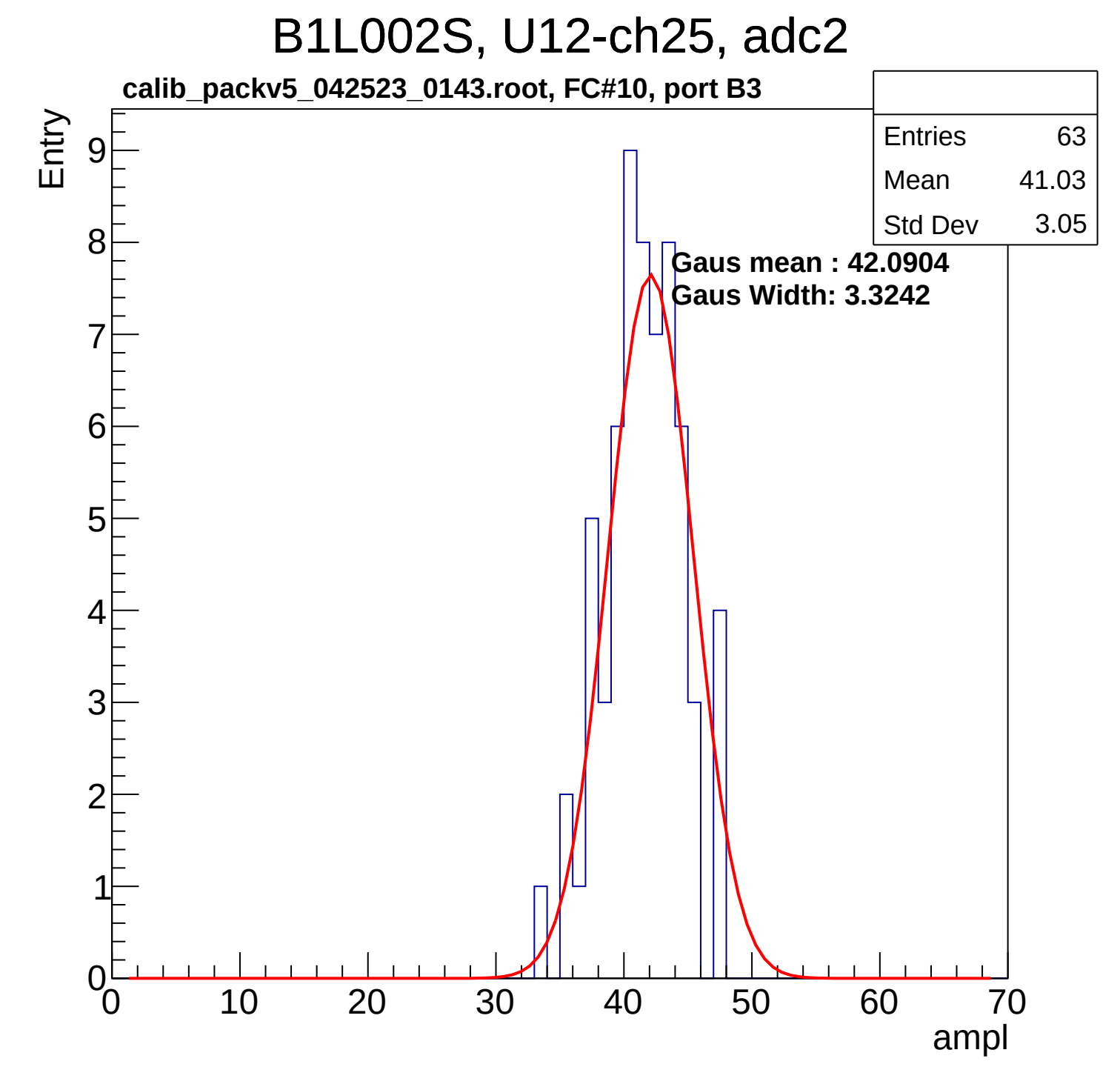

B1L002S, U12-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.96
Std Dev 2.603
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

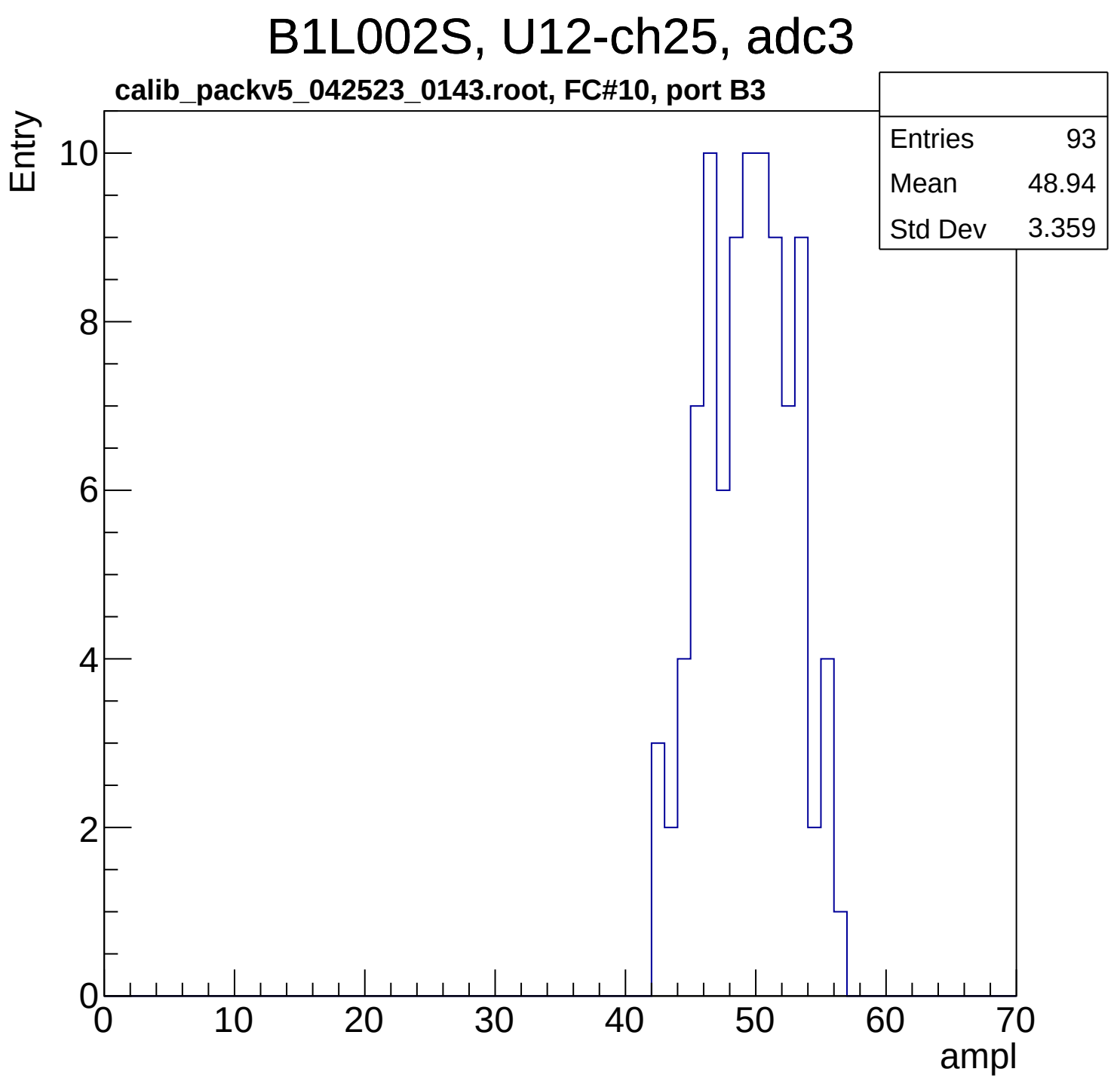

B1L002S, U12-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58
Std Dev 14.13
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

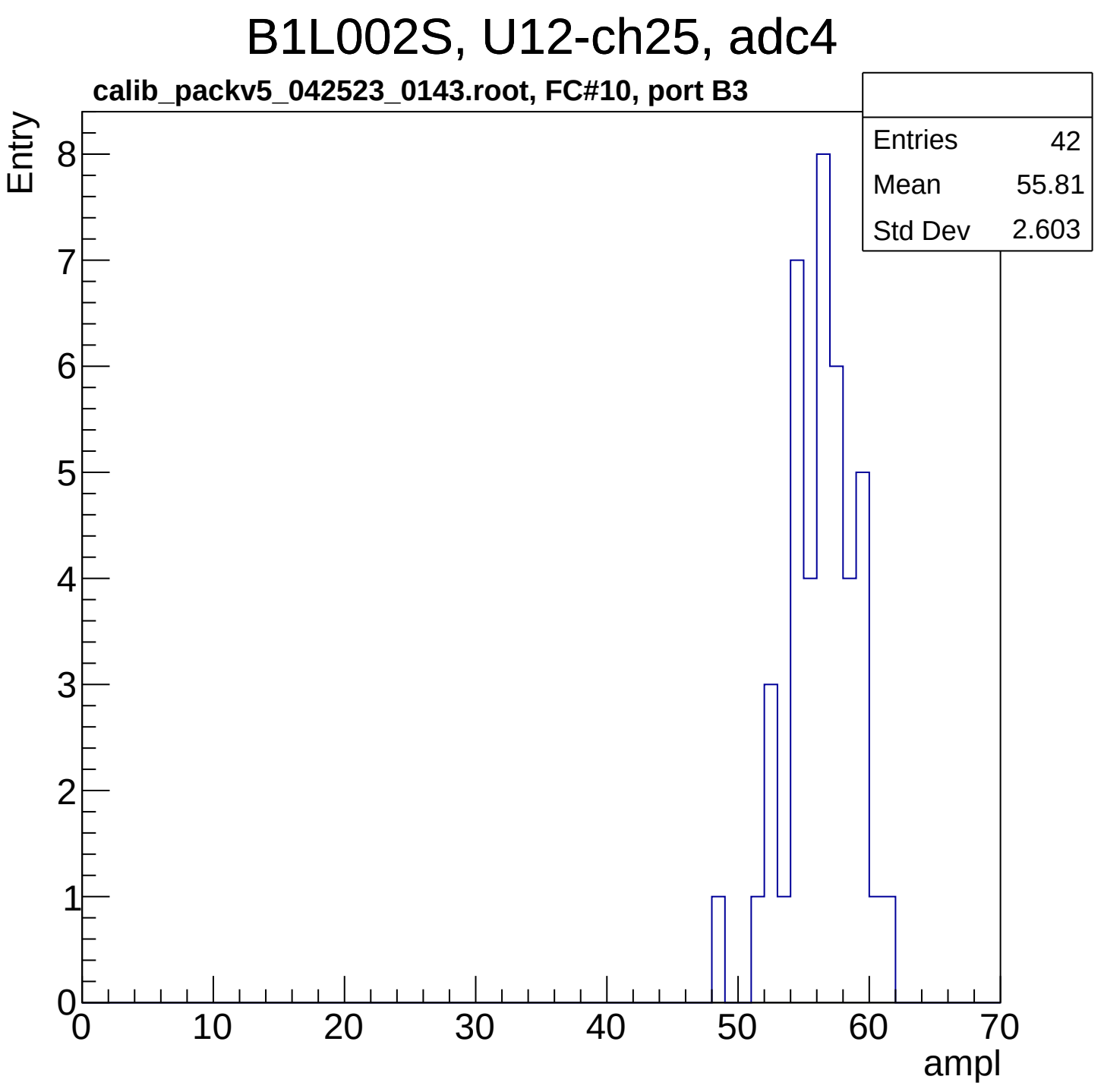

B1L002S, U12-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.24
Std Dev 3.395
Gaus mean : 30.0371
Gaus Width: 4.0426
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.38
Std Dev 2.733
Gaus mean : 35.8377
Gaus Width: 2.9589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.03
Std Dev 3.05
Gaus mean : 42.0904
Gaus Width: 3.3242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 93
Mean 48.94
Std Dev 3.359
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 55.81
Std Dev 2.603
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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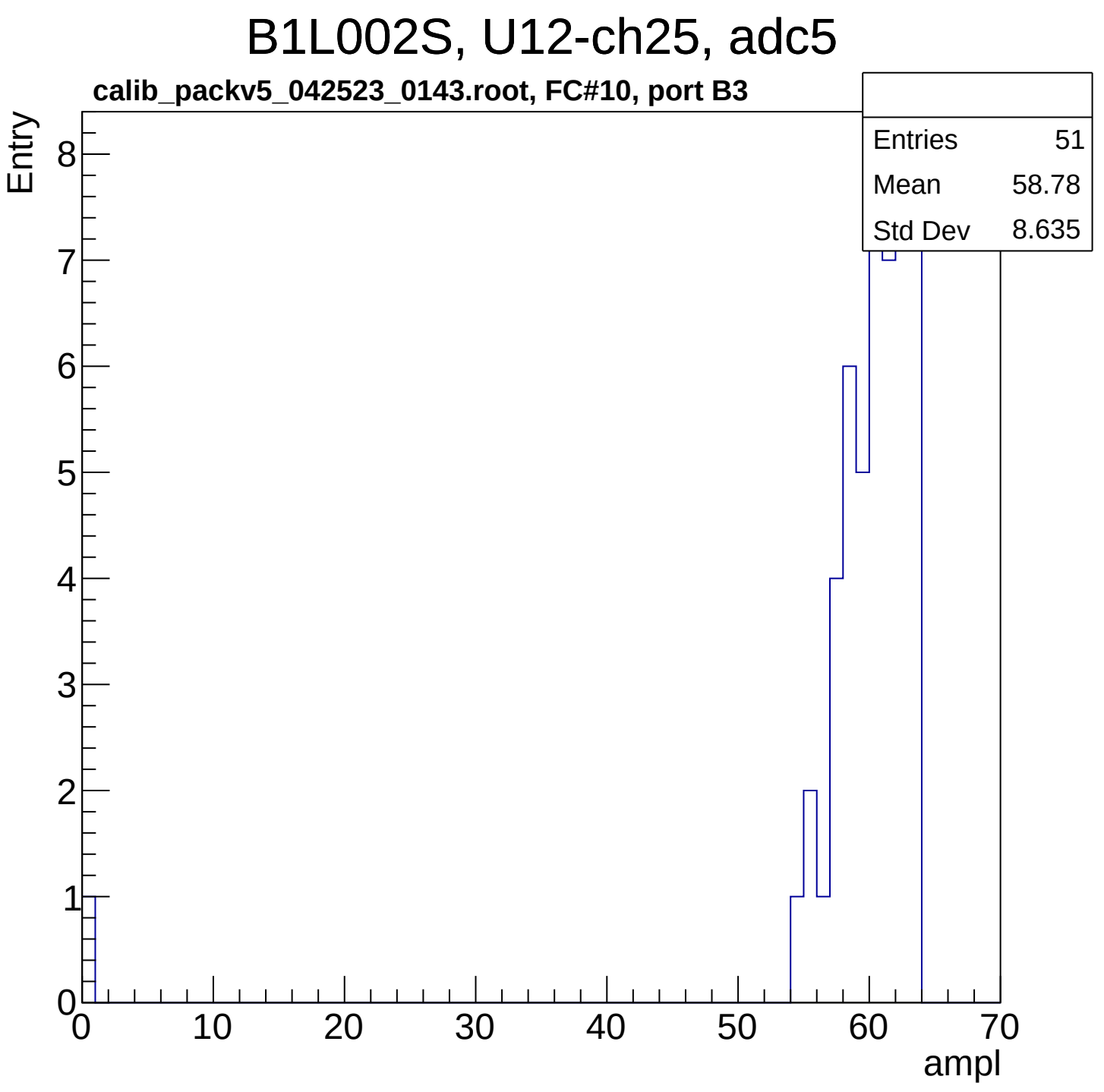
B1L002S, U12-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.78
Std Dev 8.635
Entry
ampl
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2
3
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6
7
8
0
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60
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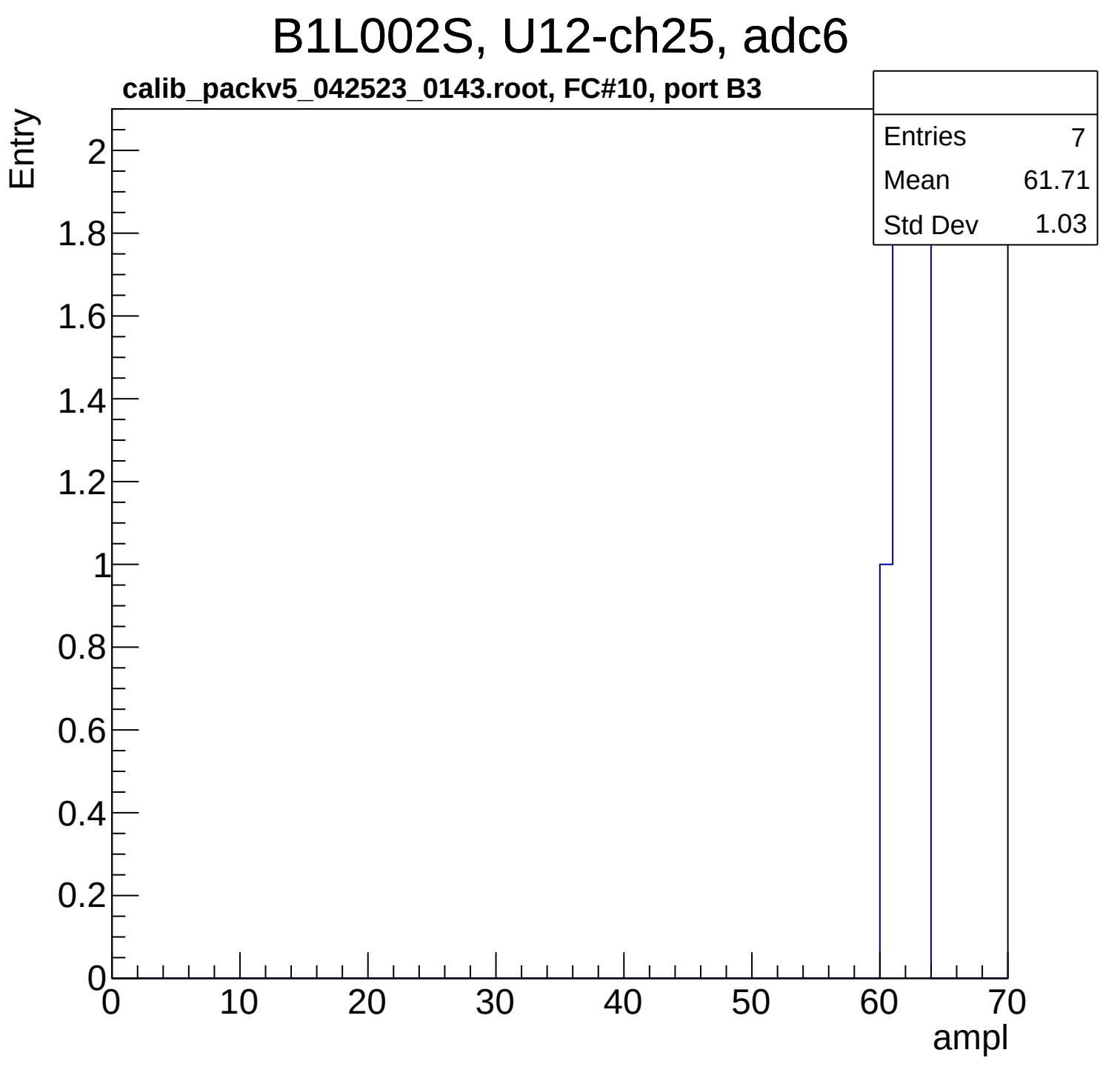
B1L002S, U12-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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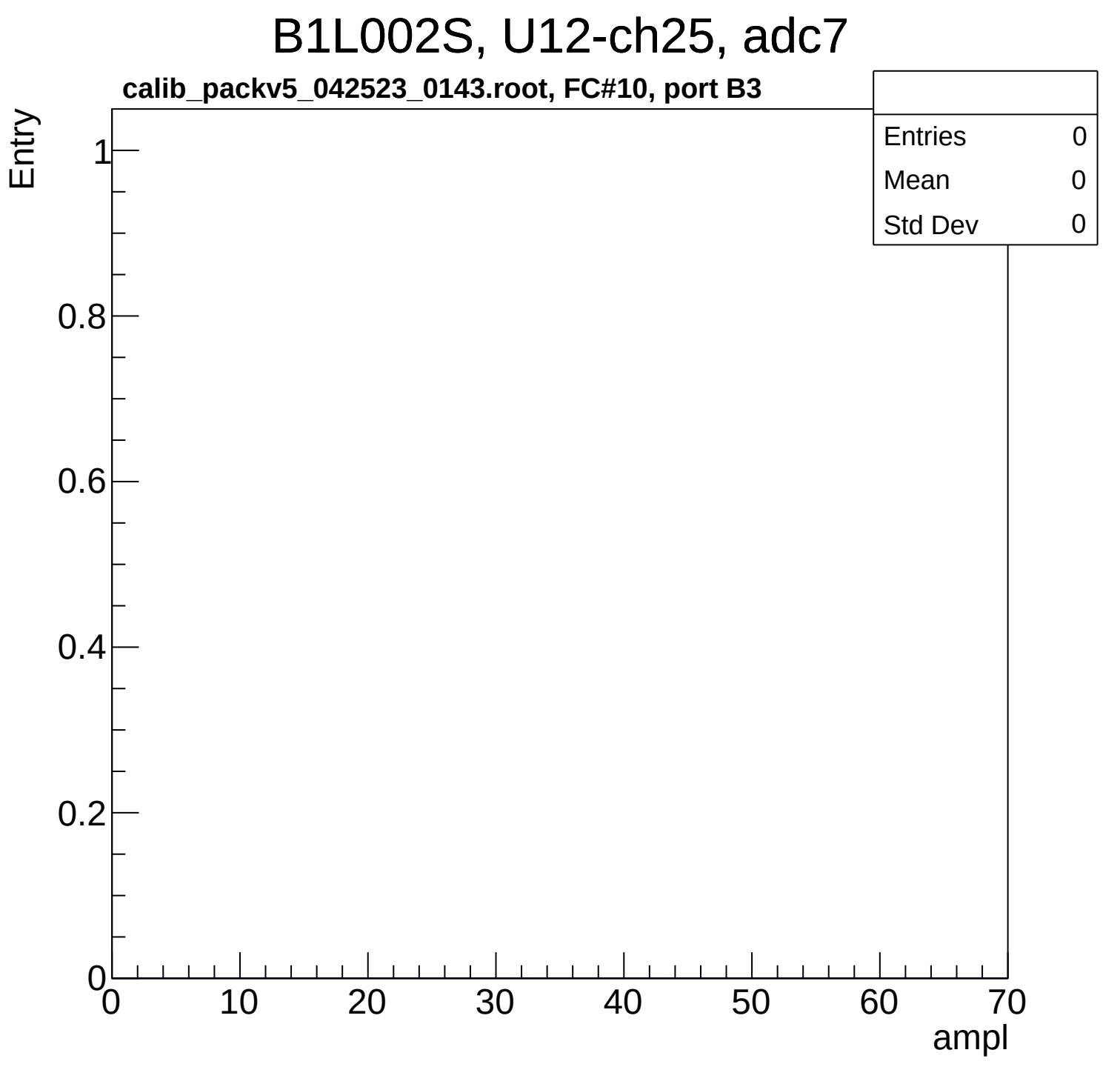
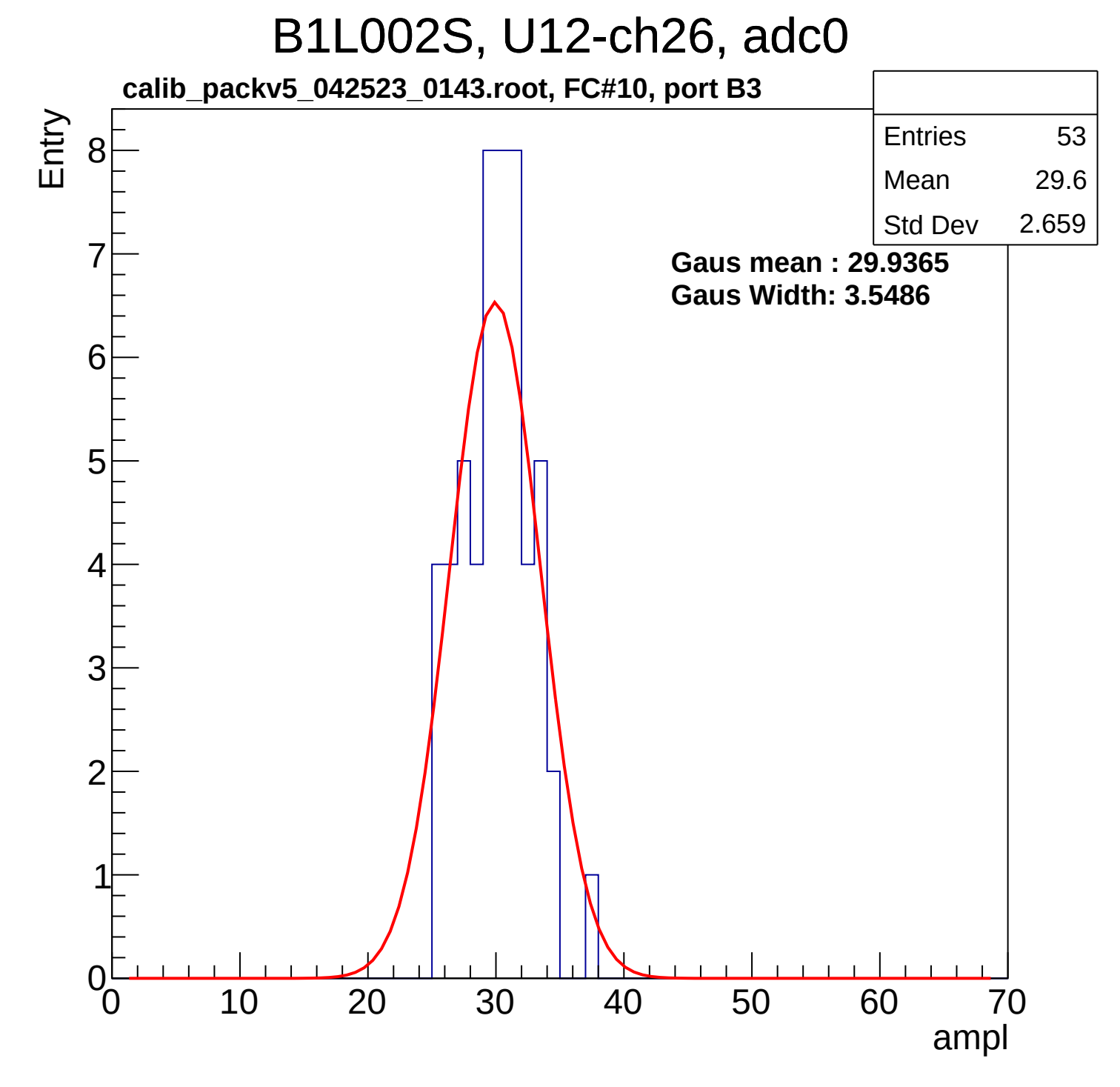
40
50
60
70


B1L002S, U12-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 29.6
Std Dev 2.659
Gaus mean : 29.9365
Gaus Width: 3.5486
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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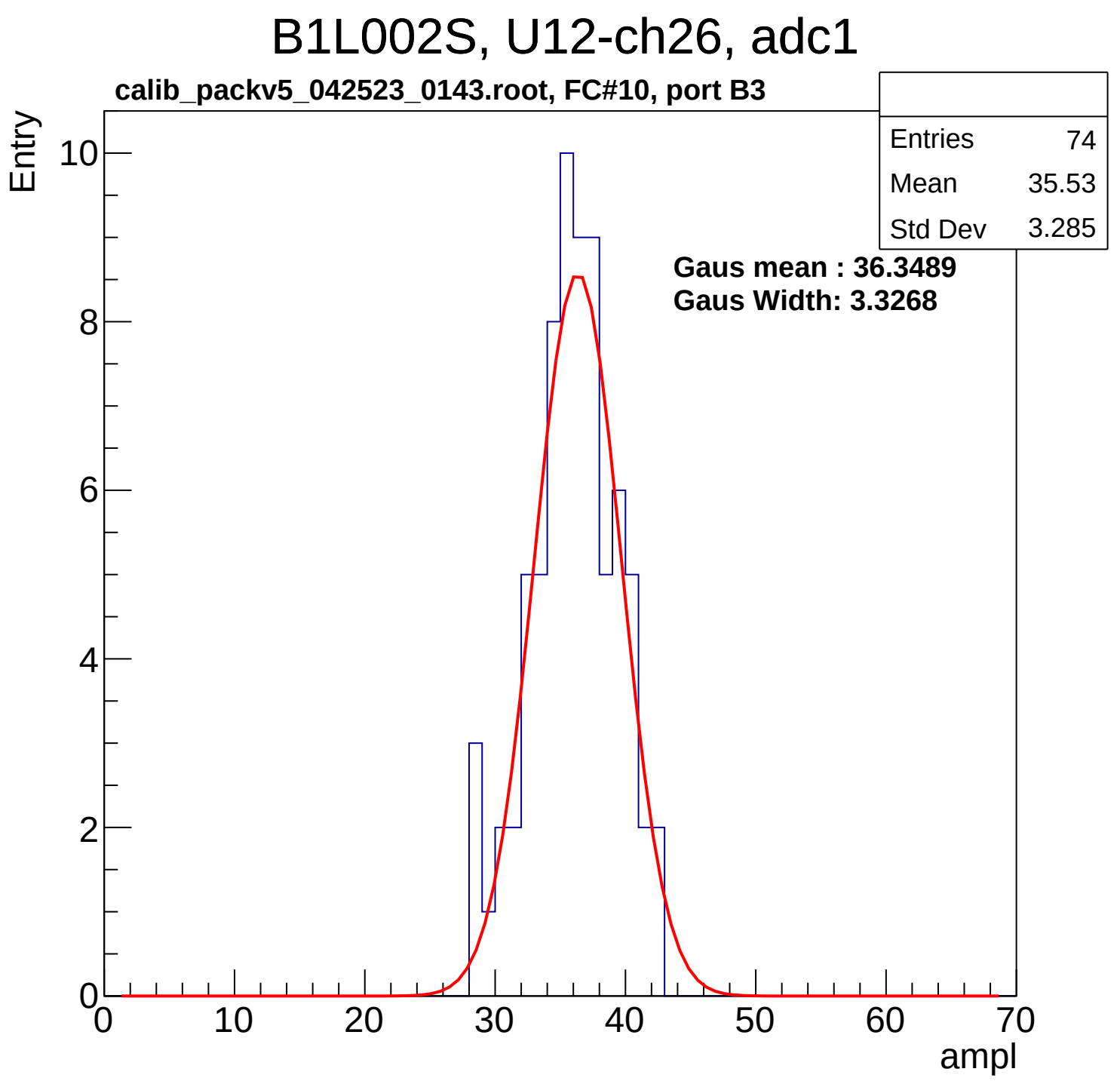
B1L002S, U12-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.53
Std Dev 3.285
Gaus mean : 36.3489
Gaus Width: 3.3268
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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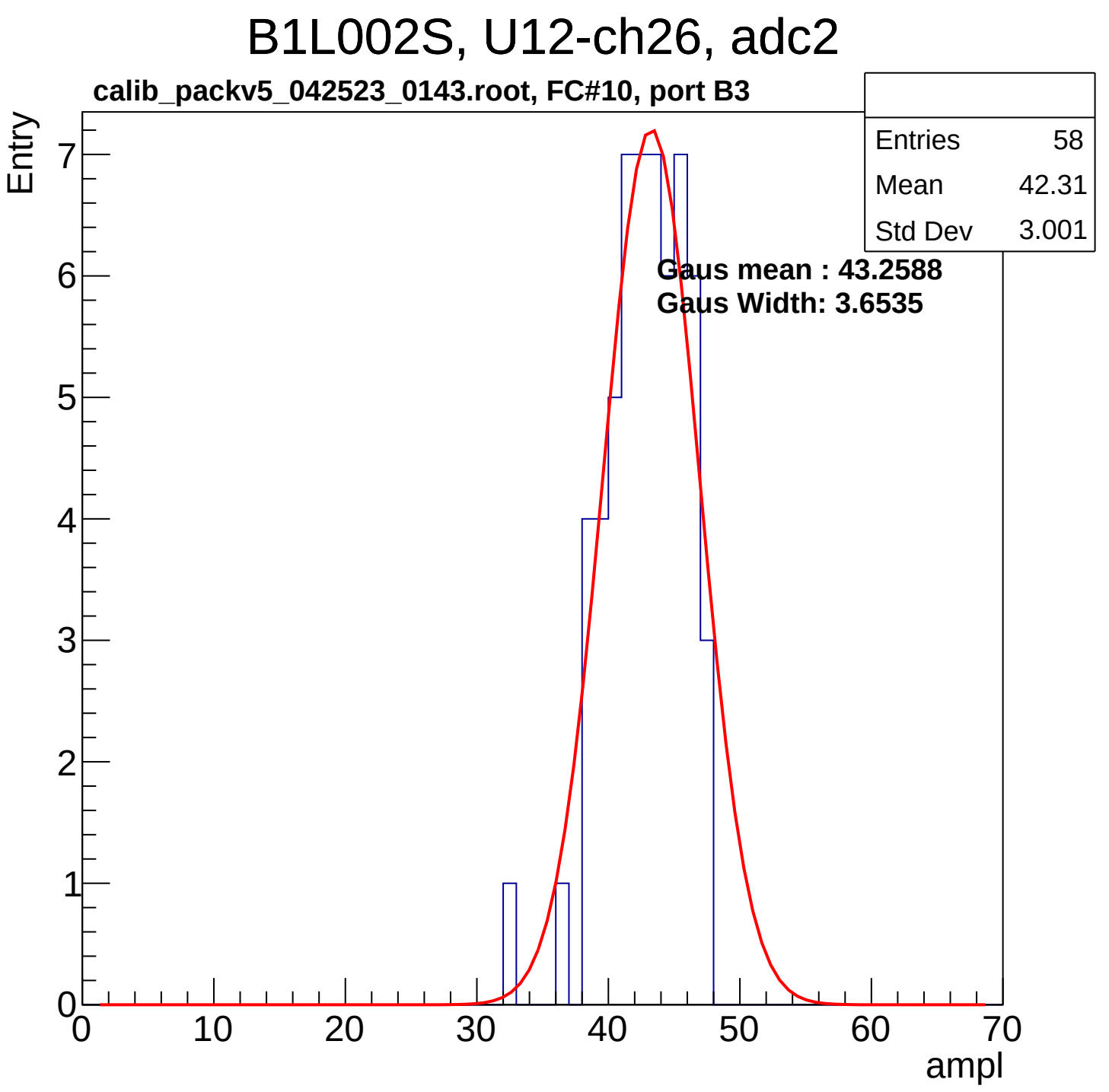
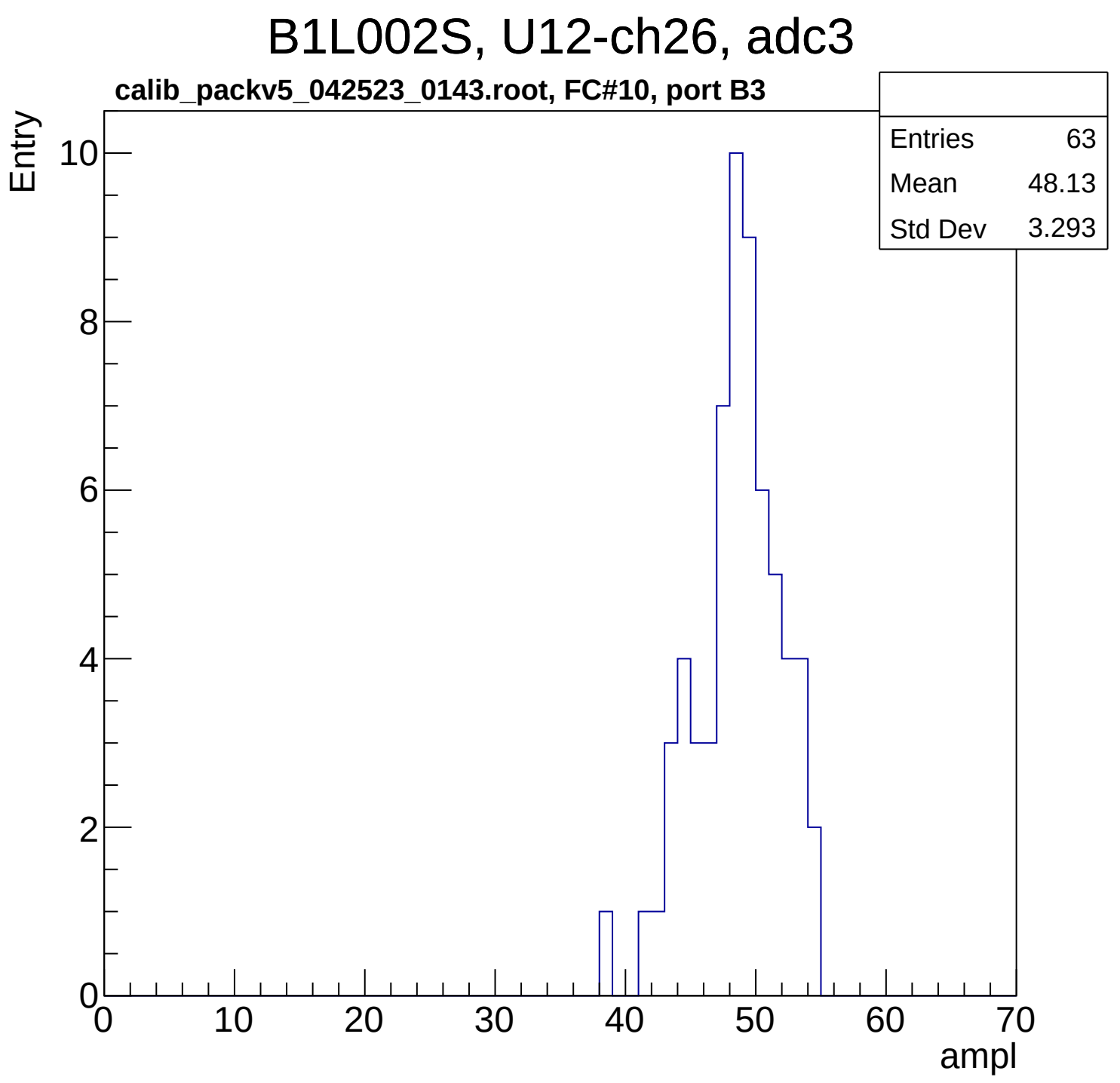

B1L002S, U12-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.31
Std Dev 3.001
Gaus mean : 43.2588
Gaus Width: 3.6535
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.13
Std Dev 3.293
Entry
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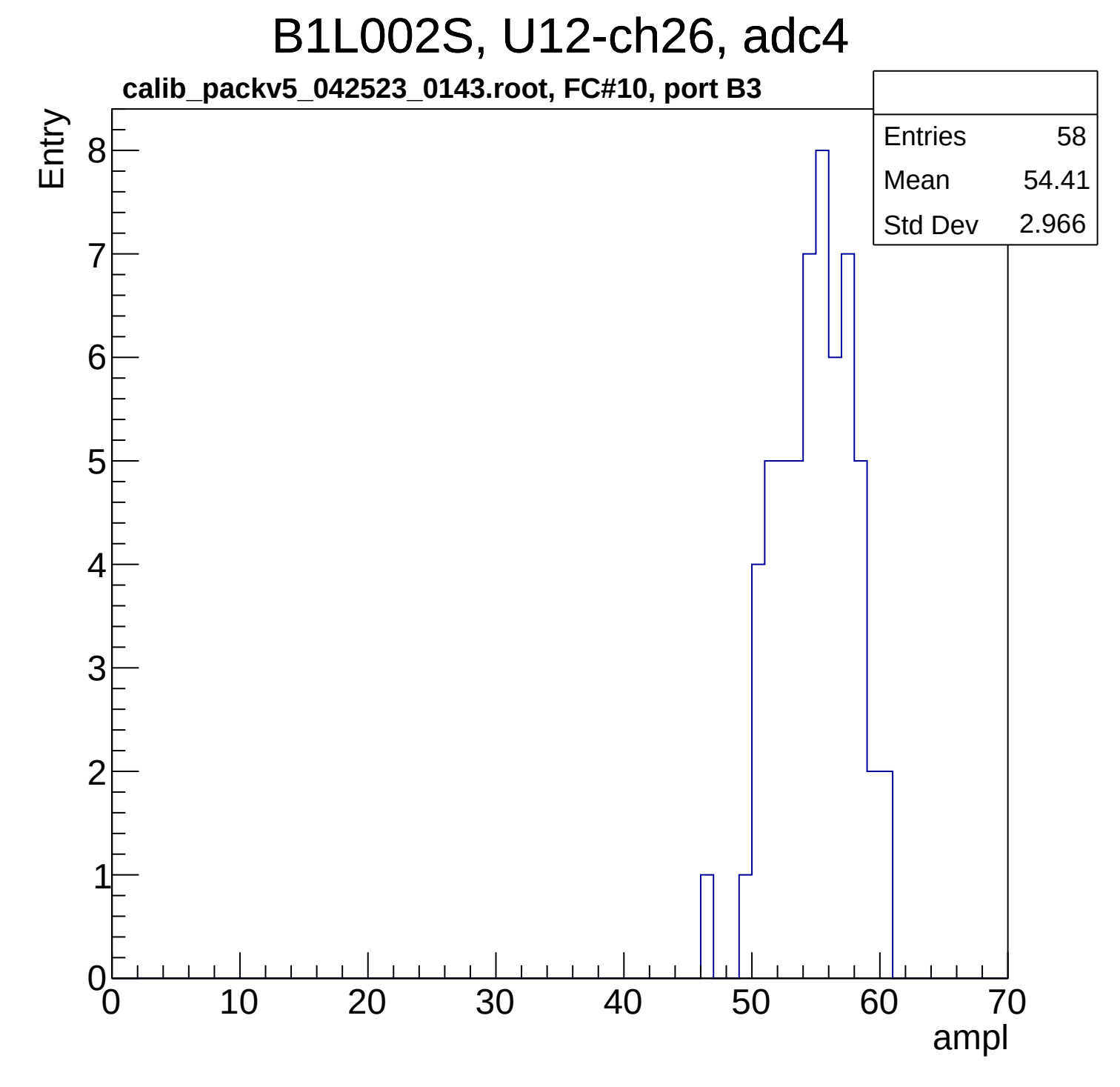
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.41
Std Dev 2.966
Entry
ampl
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1
2
3
4
5
6
7
8
0
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30
40
50
60
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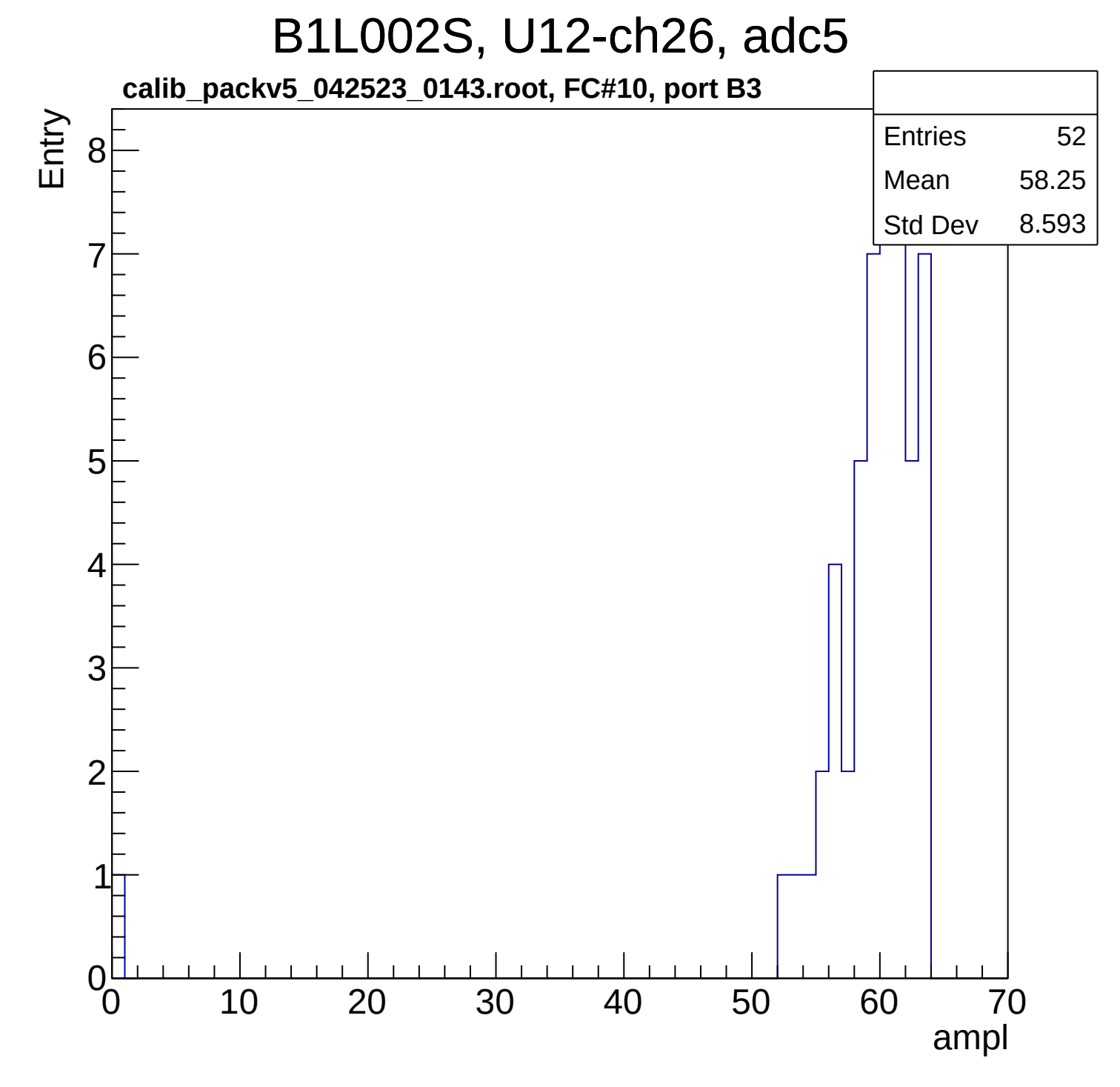
B1L002S, U12-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.25
Std Dev 8.593
Entry
ampl
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2
3
4
5
6
7
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0
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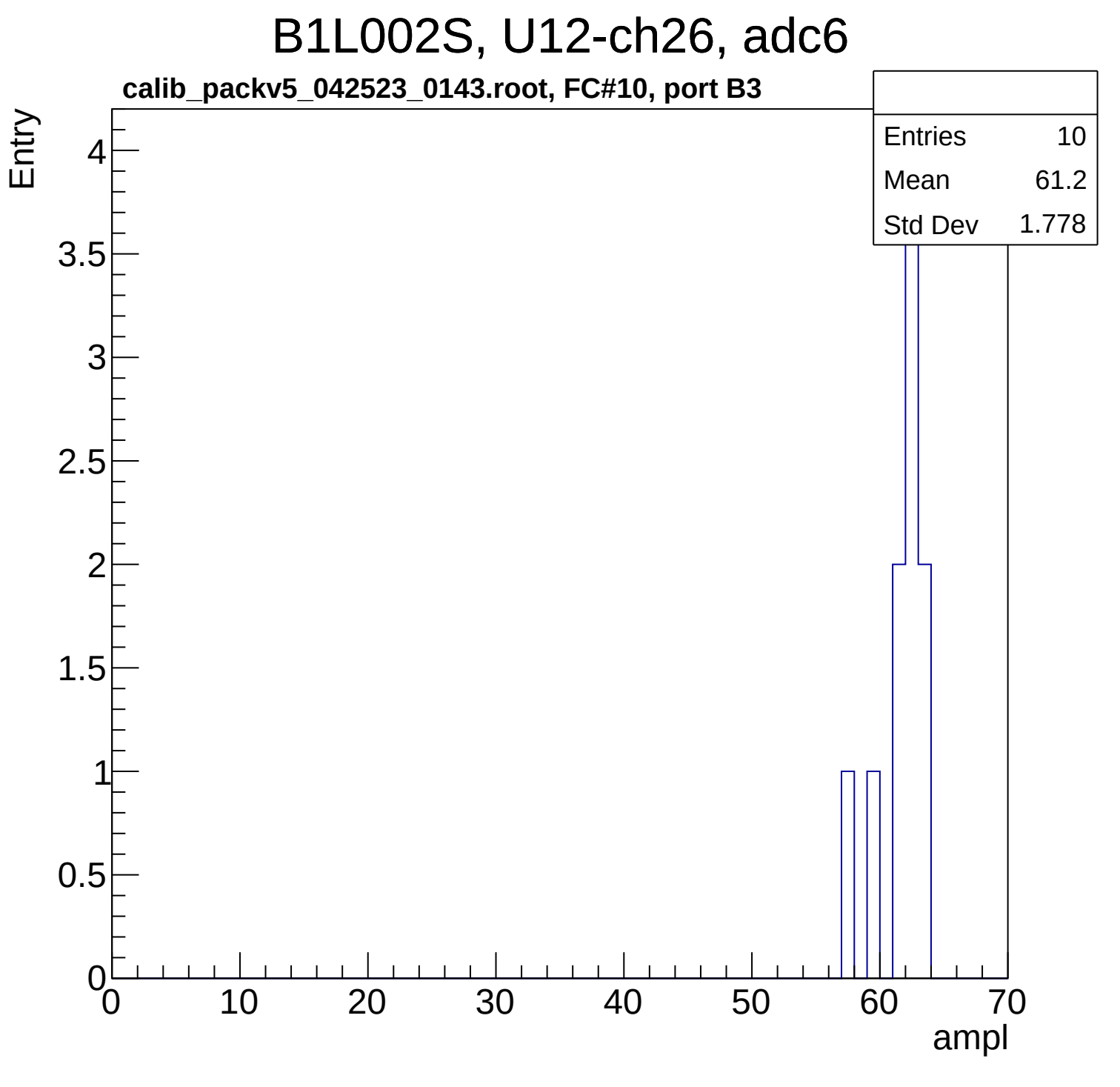
B1L002S, U12-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.2
Std Dev 1.778
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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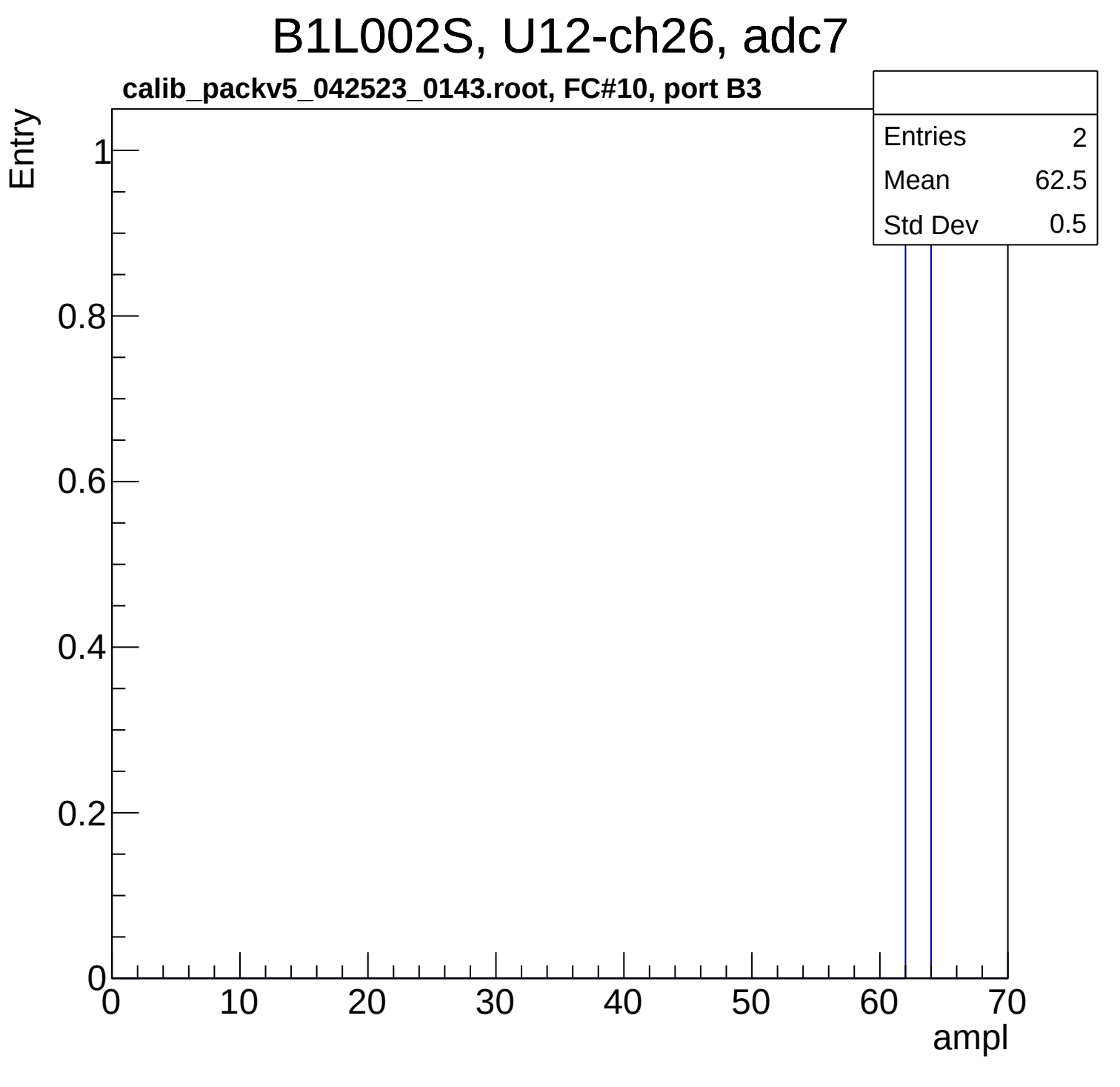
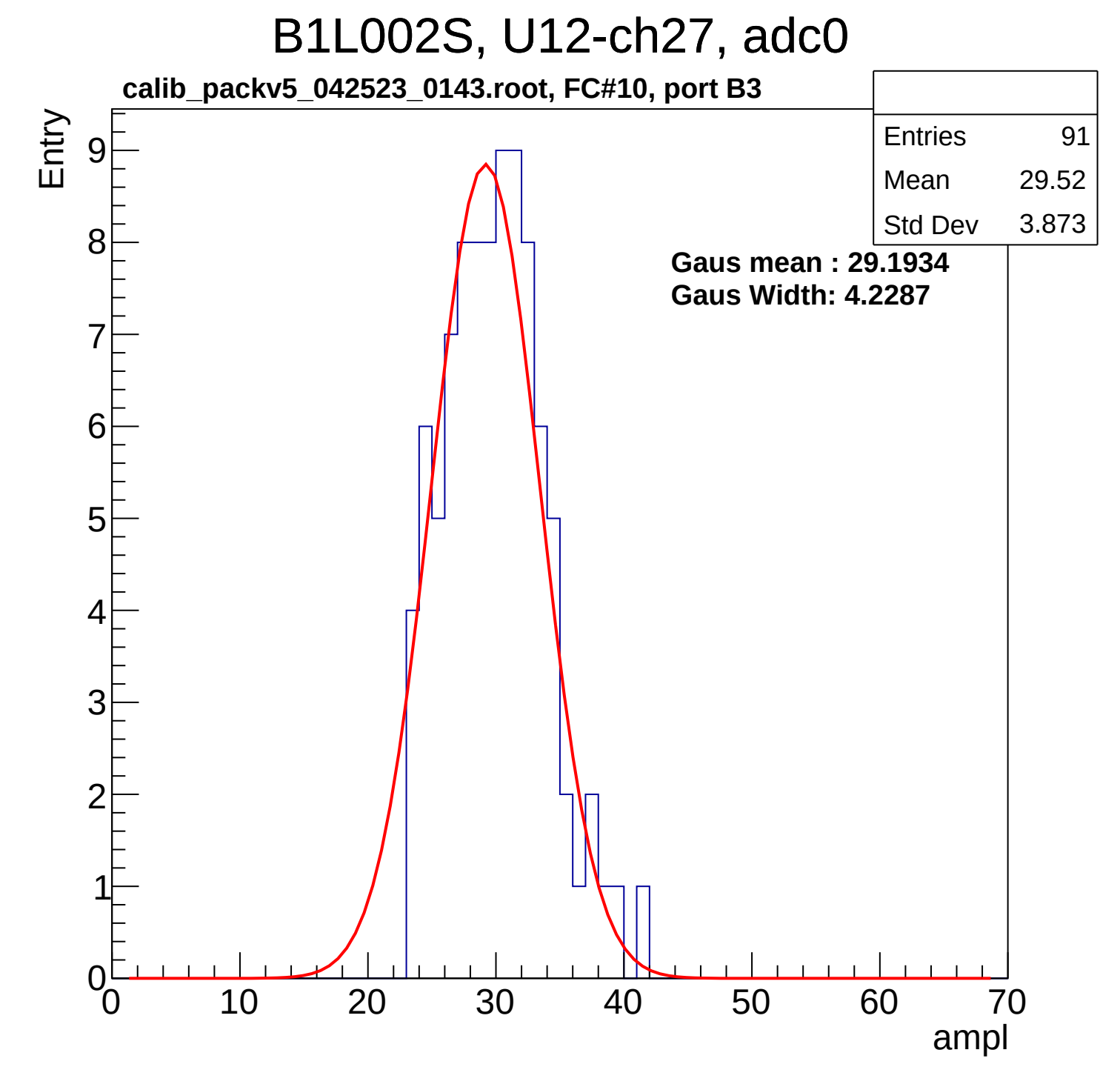
40
50
60
70


B1L002S, U12-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 91
Mean 29.52
Std Dev 3.873
Gaus mean : 29.1934
Gaus Width: 4.2287
Entry
ampl
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2
3
4
5
6
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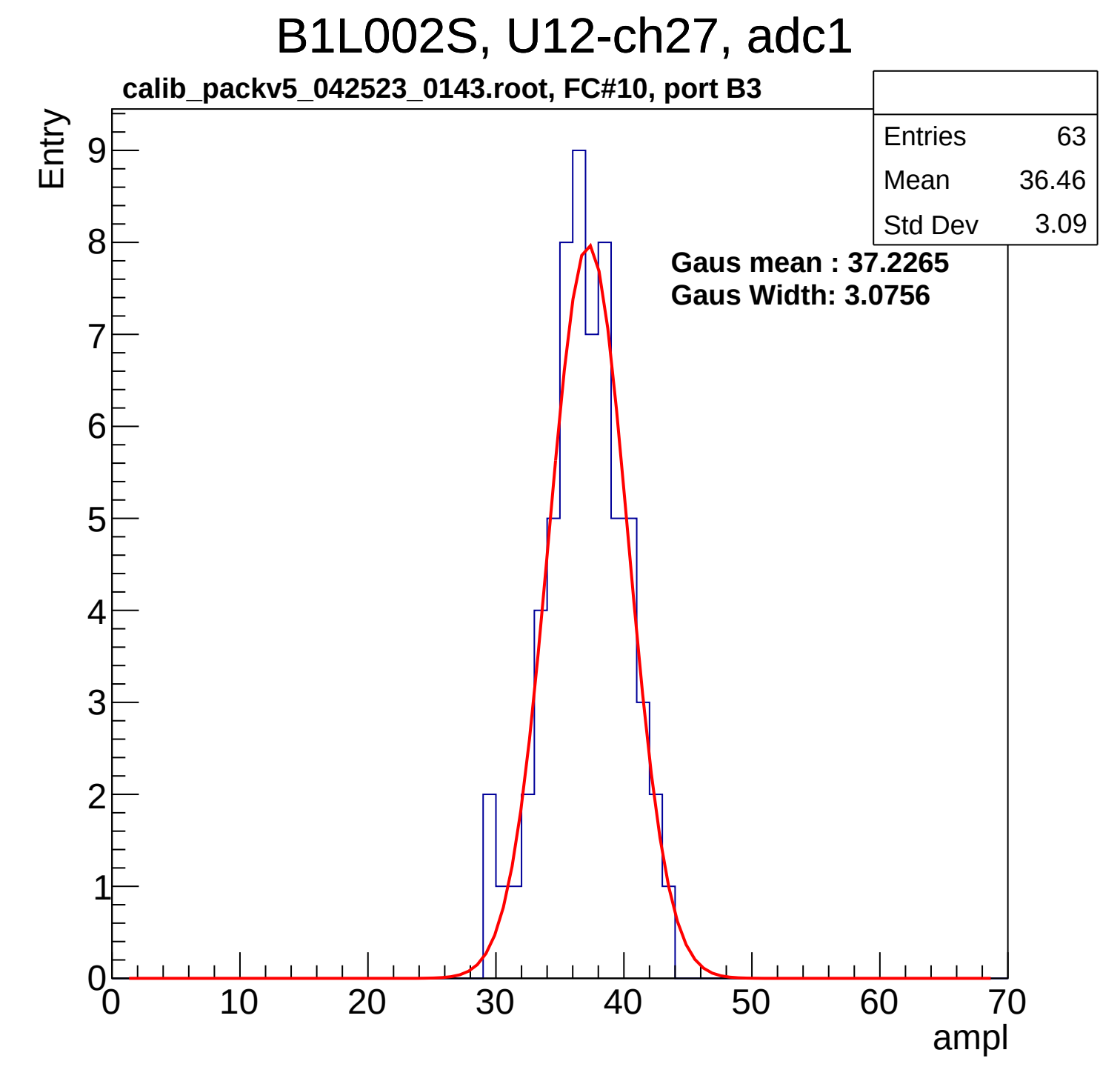
B1L002S, U12-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 36.46
Std Dev 3.09
Gaus mean : 37.2265
Gaus Width: 3.0756
Entry
ampl
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2
3
4
5
6
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0
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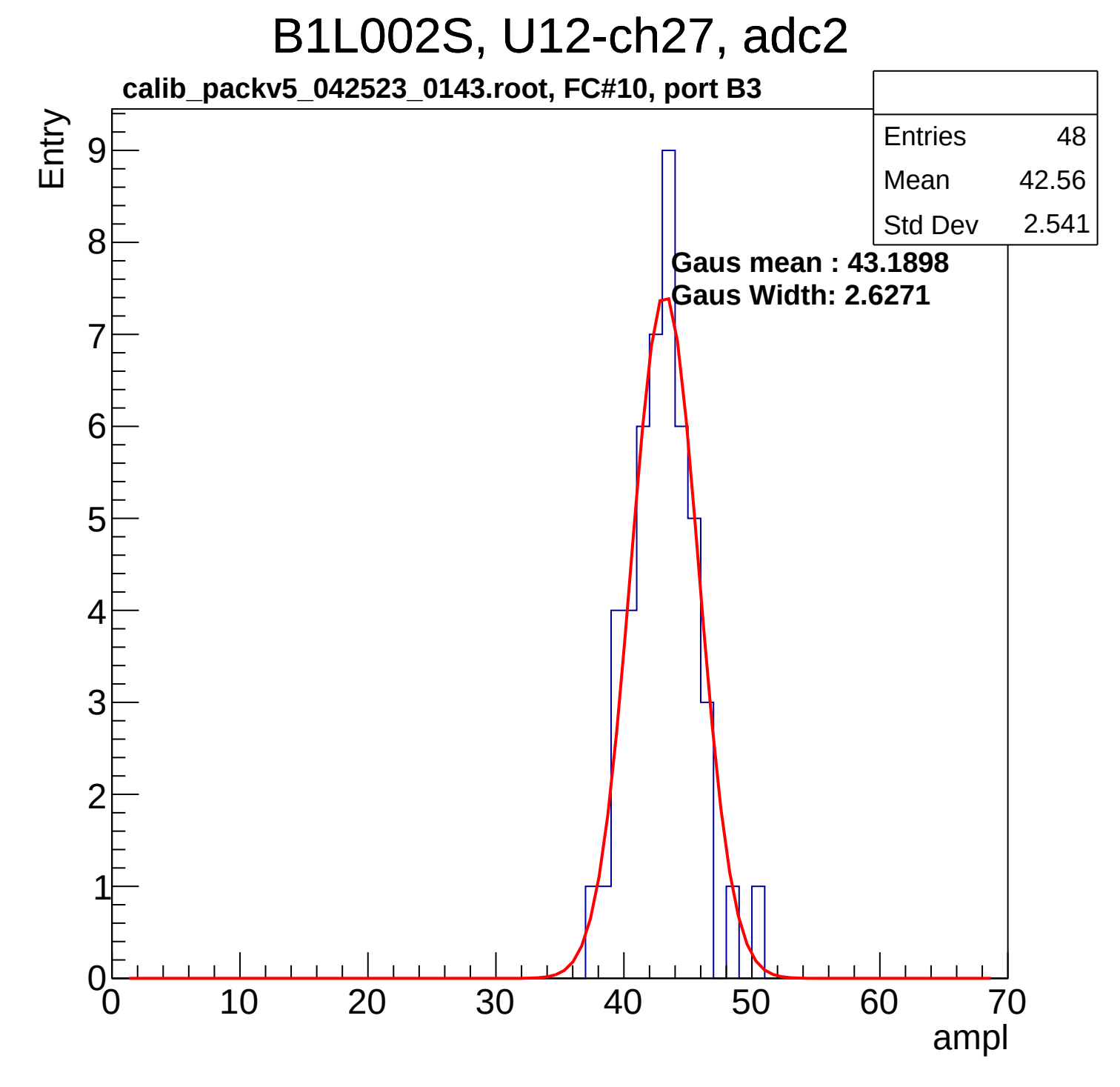
B1L002S, U12-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 42.56
Std Dev 2.541
Gaus mean : 43.1898
Gaus Width: 2.6271
Entry
ampl
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3
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6
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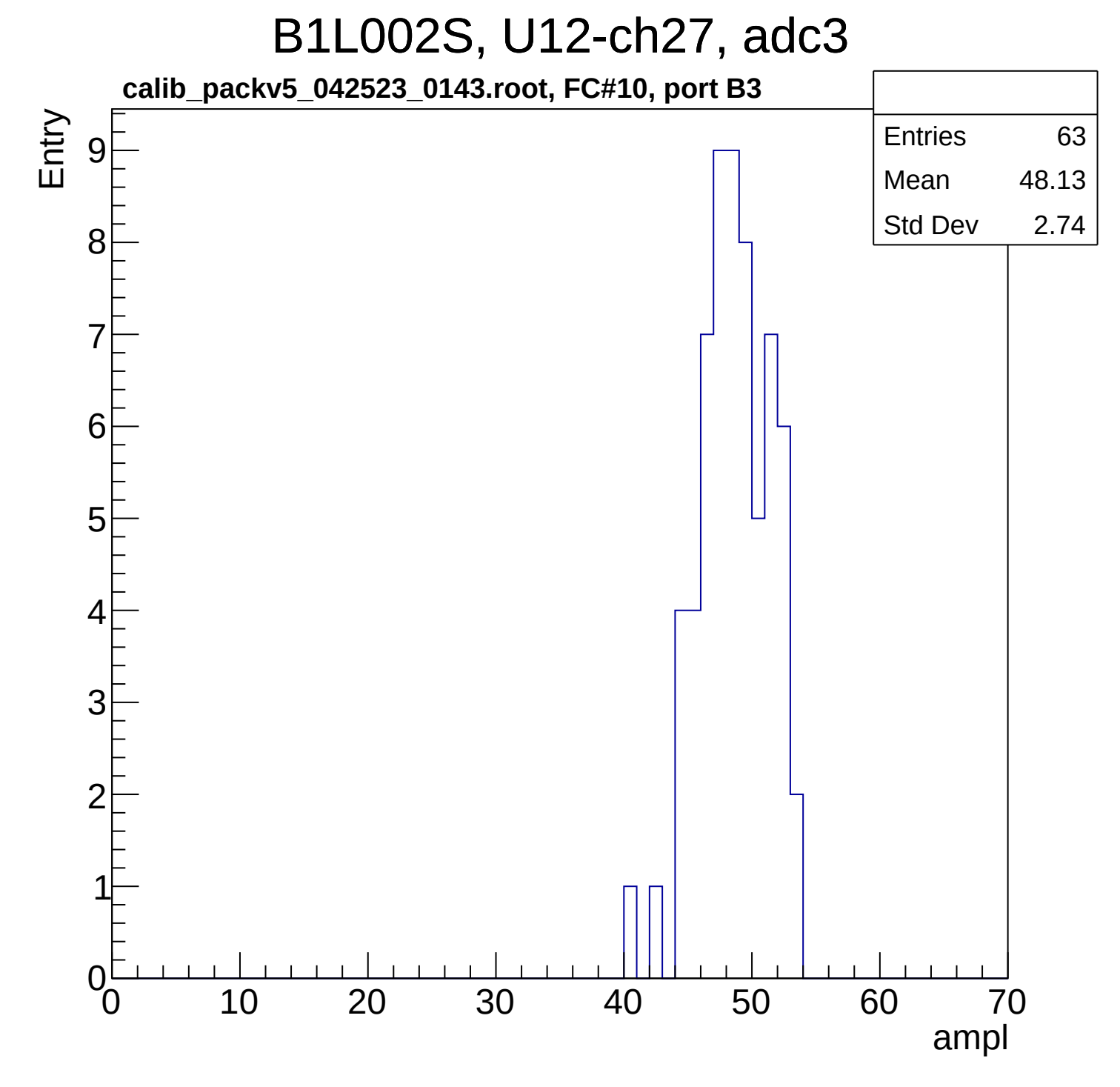
B1L002S, U12-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.13
Std Dev 2.74
Entry
ampl
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3
4
5
6
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0
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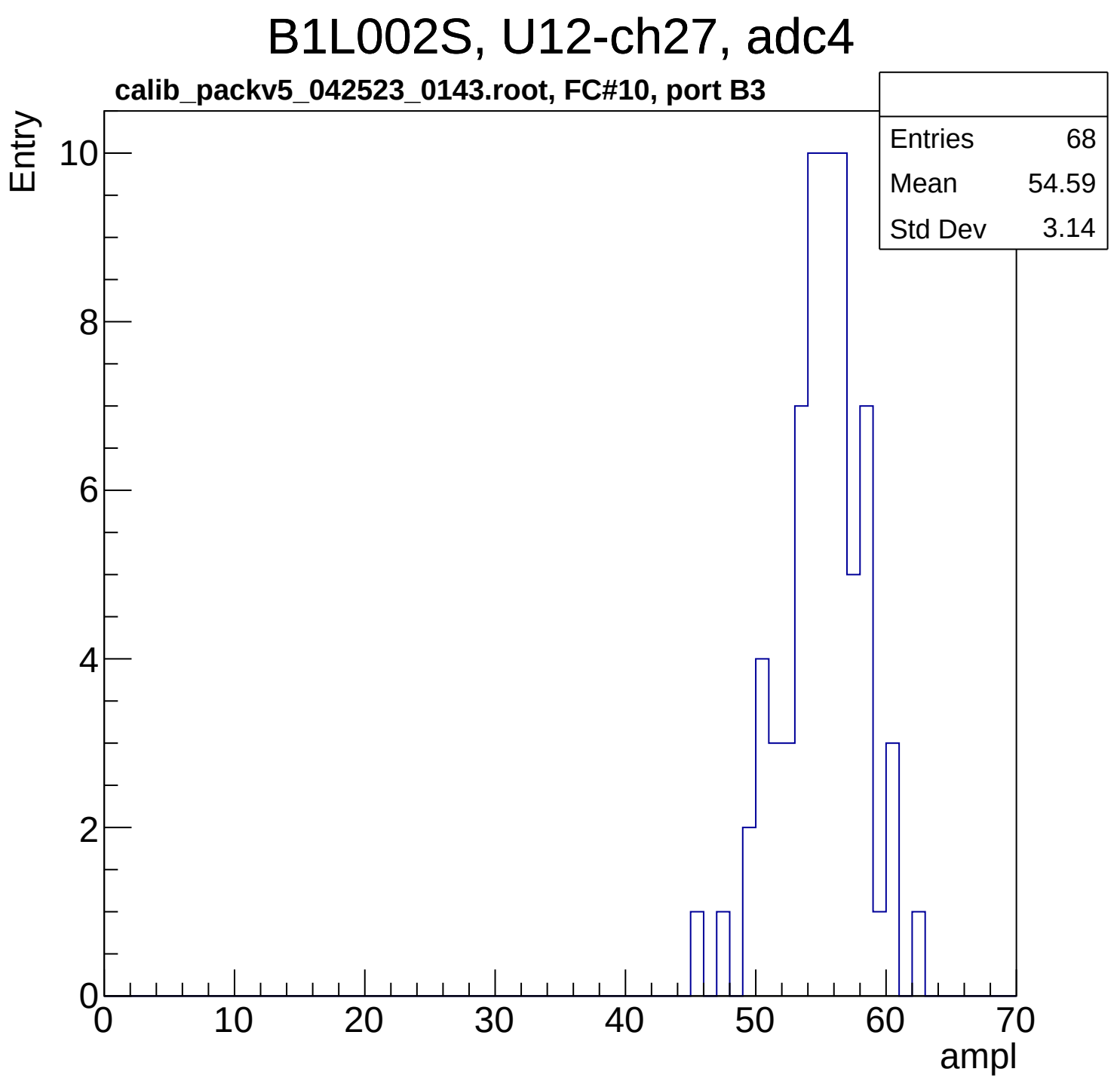
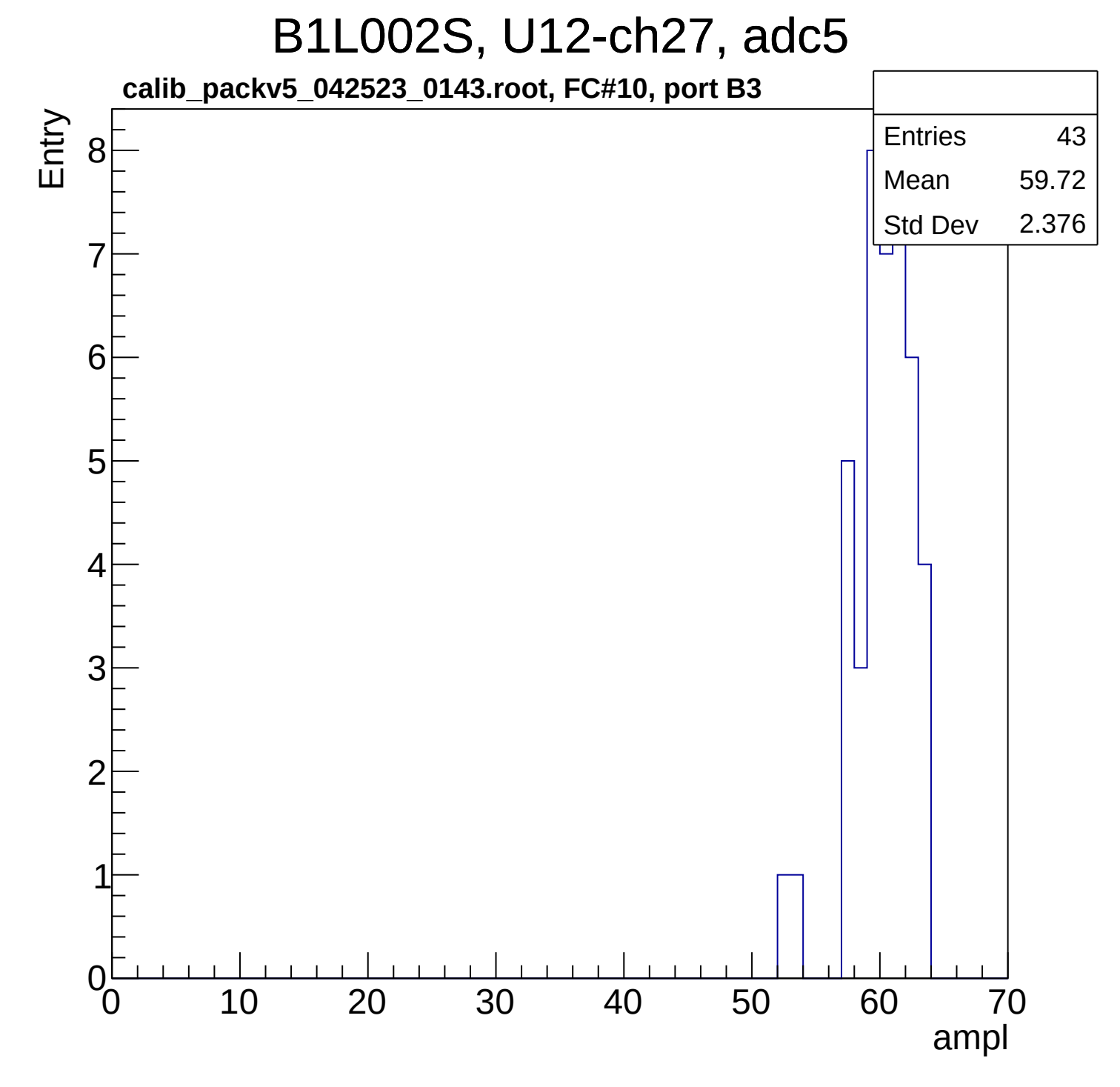

B1L002S, U12-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 54.59
Std Dev 3.14
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 59.72
Std Dev 2.376
Entry
ampl
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2
3
4
5
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7
8
0
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60
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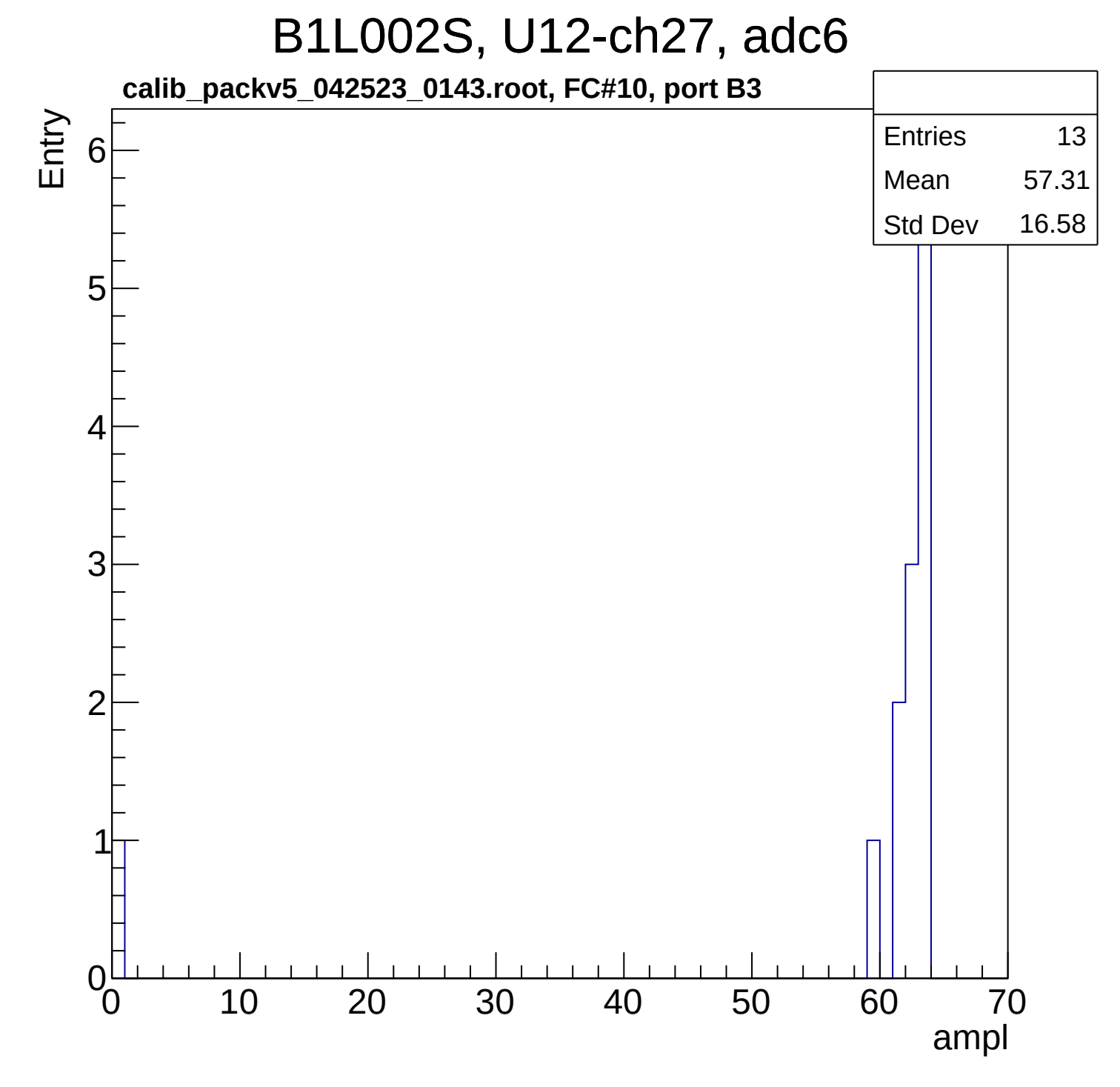
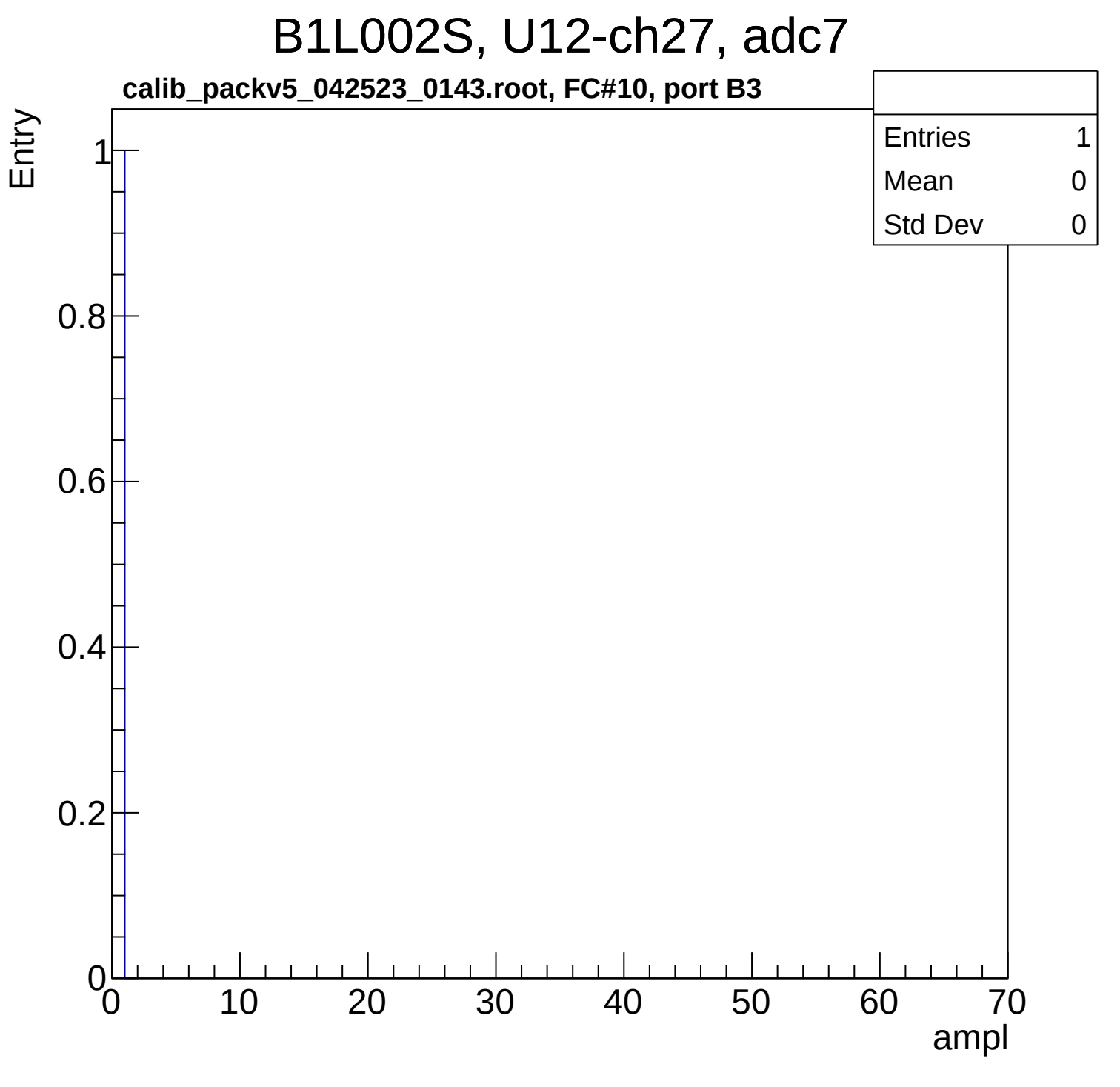

B1L002S, U12-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57.31
Std Dev 16.58
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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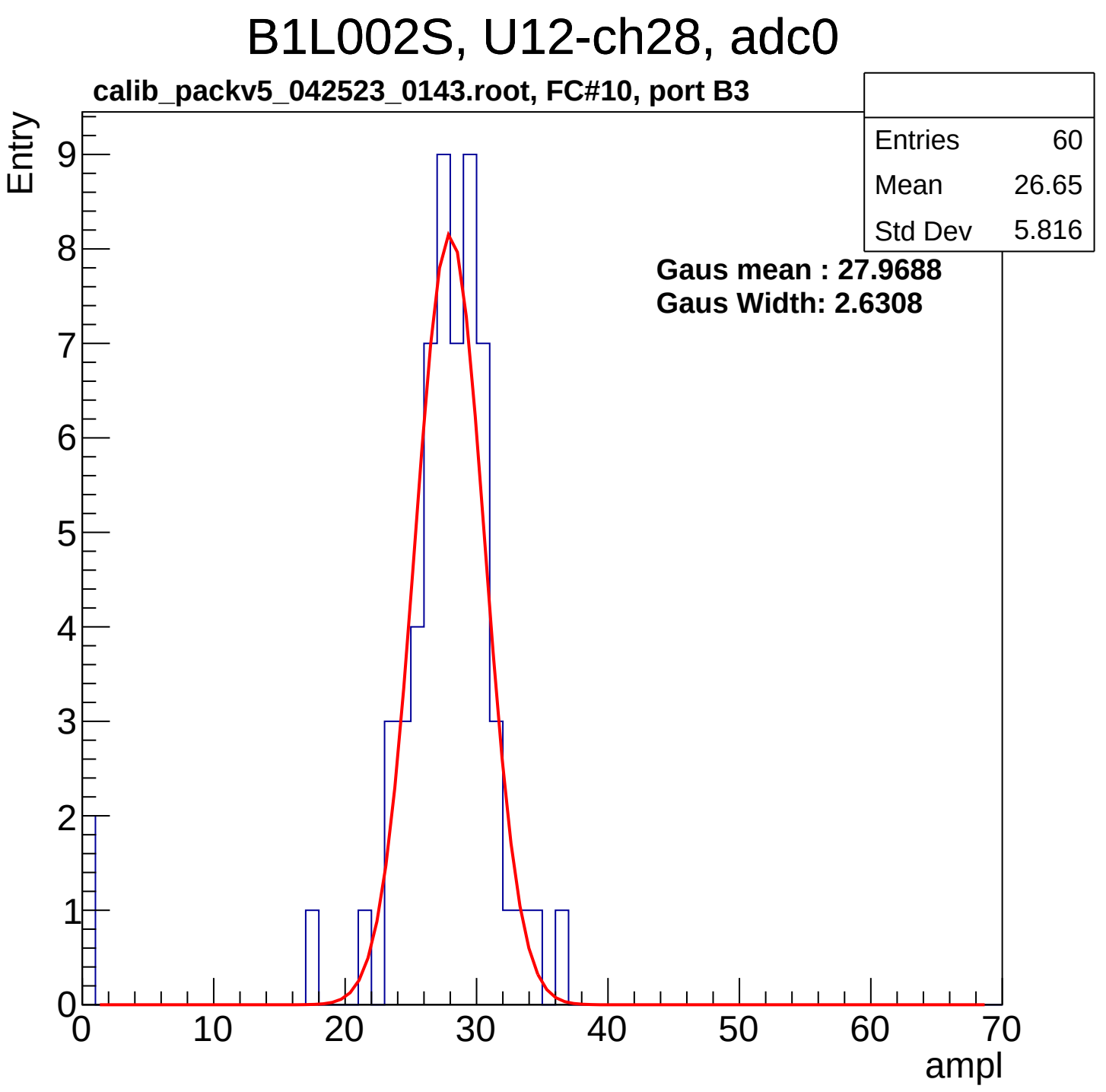
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 26.65
Std Dev 5.816
Gaus mean : 27.9688
Gaus Width: 2.6308
Entry
ampl
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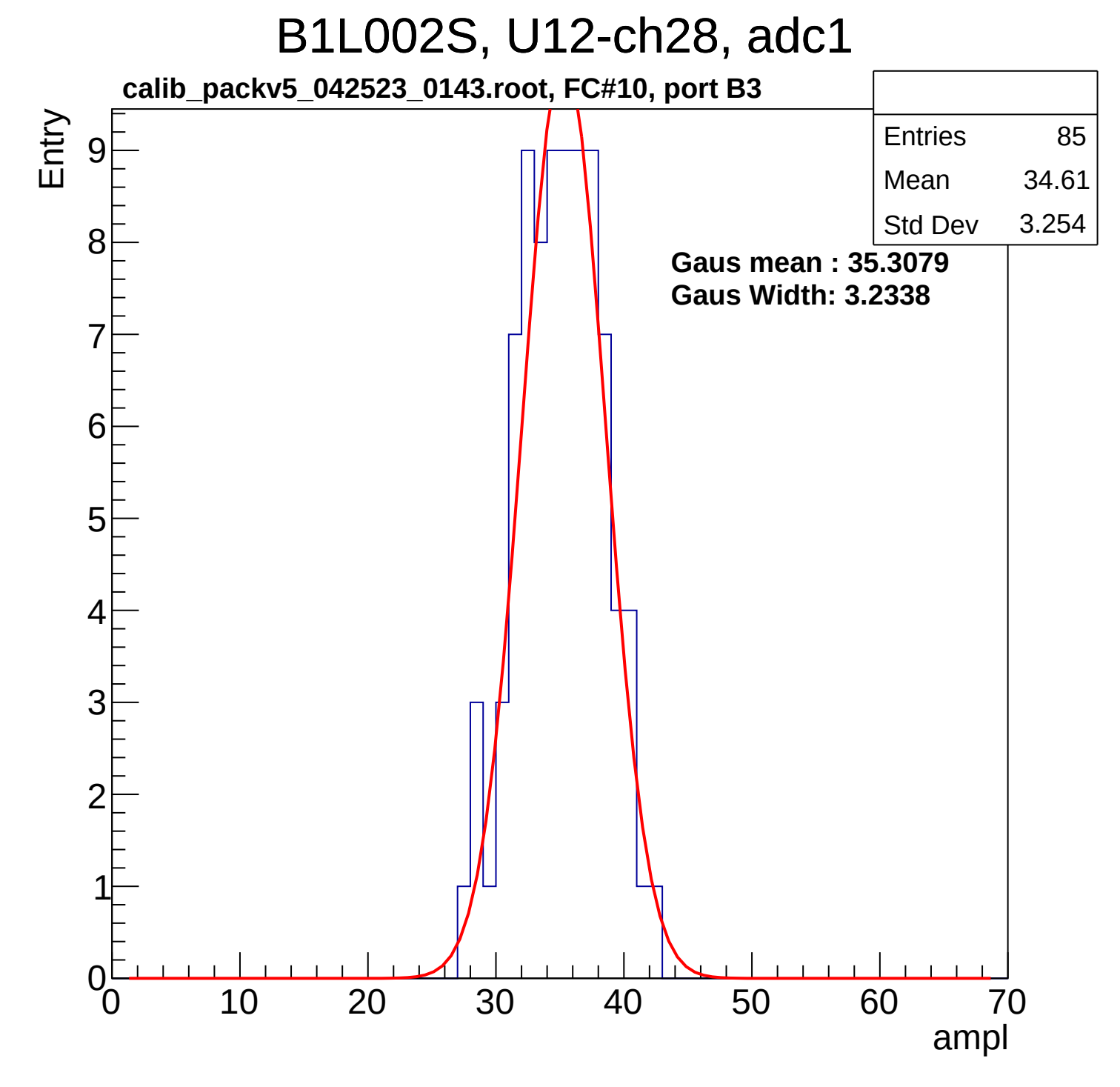
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 34.61
Std Dev 3.254
Gaus mean : 35.3079
Gaus Width: 3.2338
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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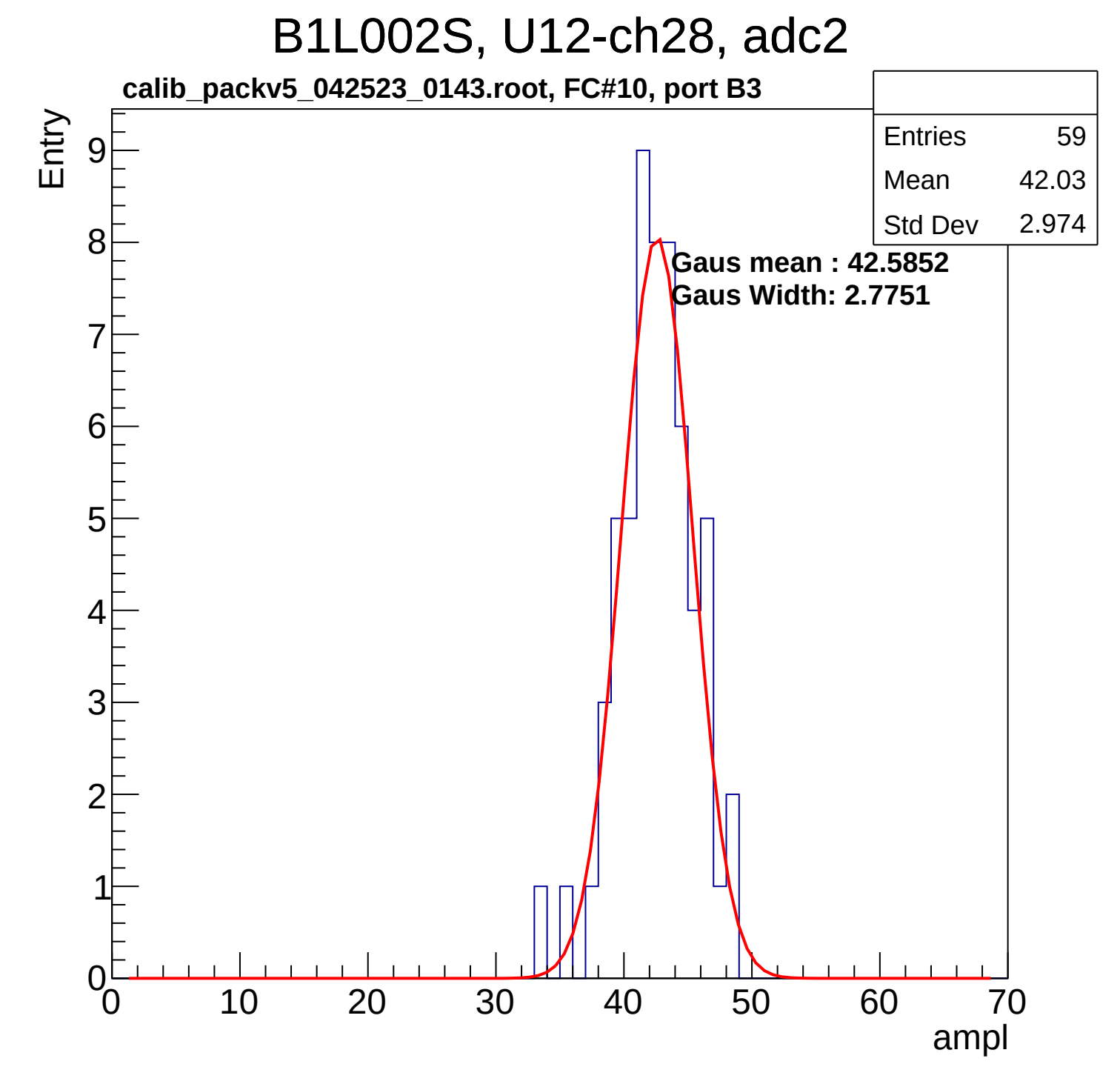
B1L002S, U12-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 42.03
Std Dev 2.974
Gaus mean : 42.5852
Gaus Width: 2.7751
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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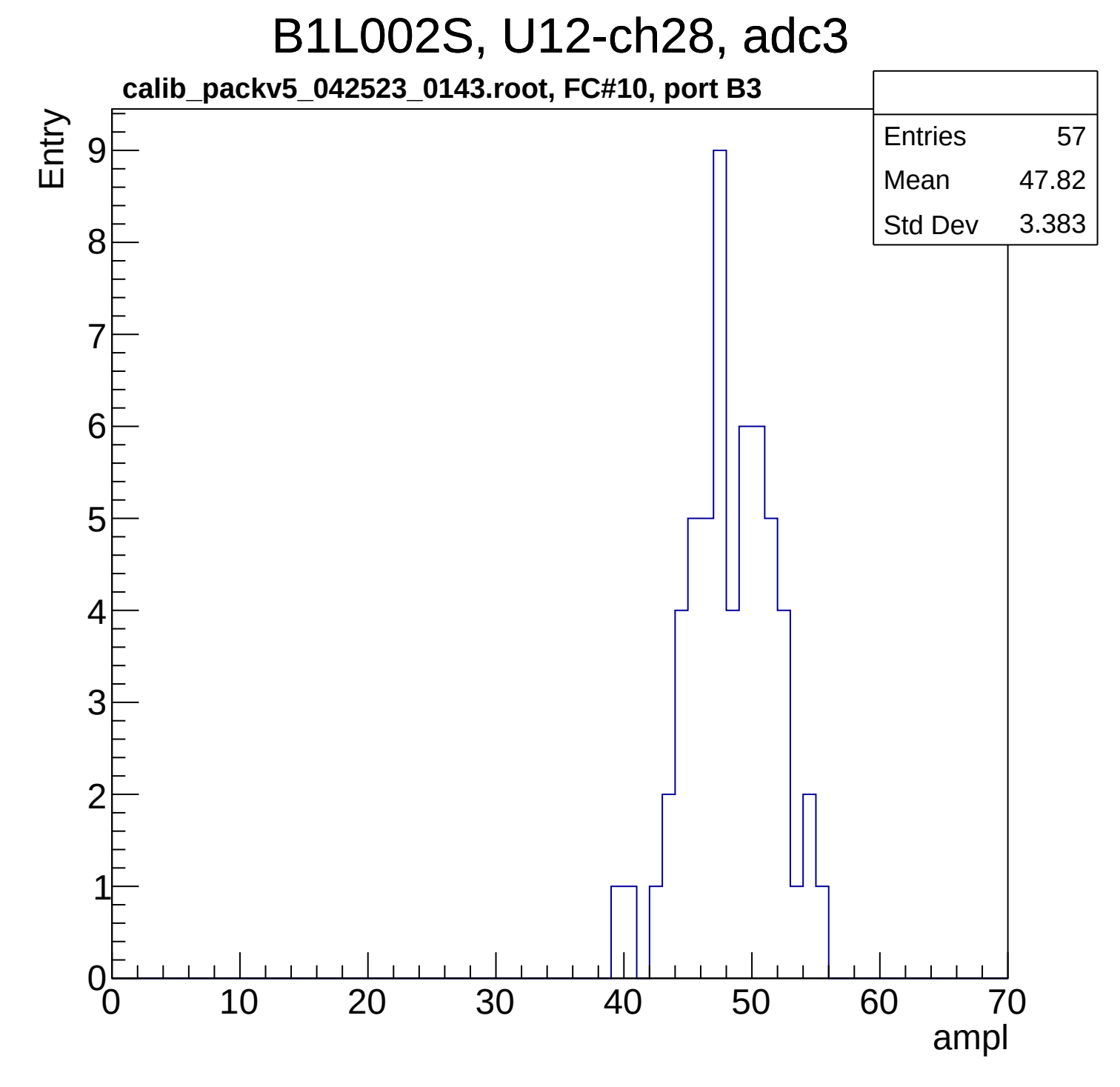
B1L002S, U12-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 47.82
Std Dev 3.383
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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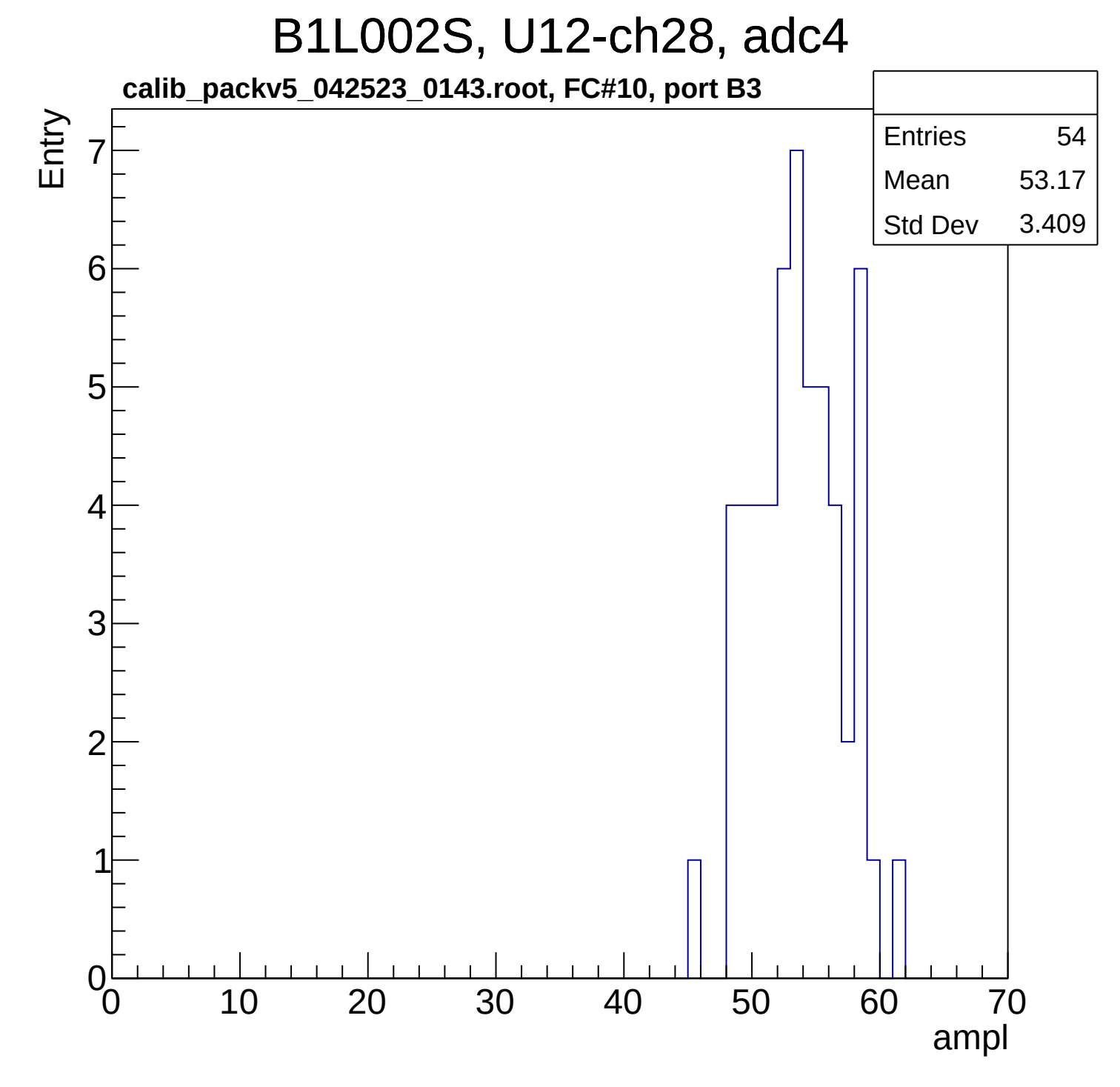
B1L002S, U12-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.17
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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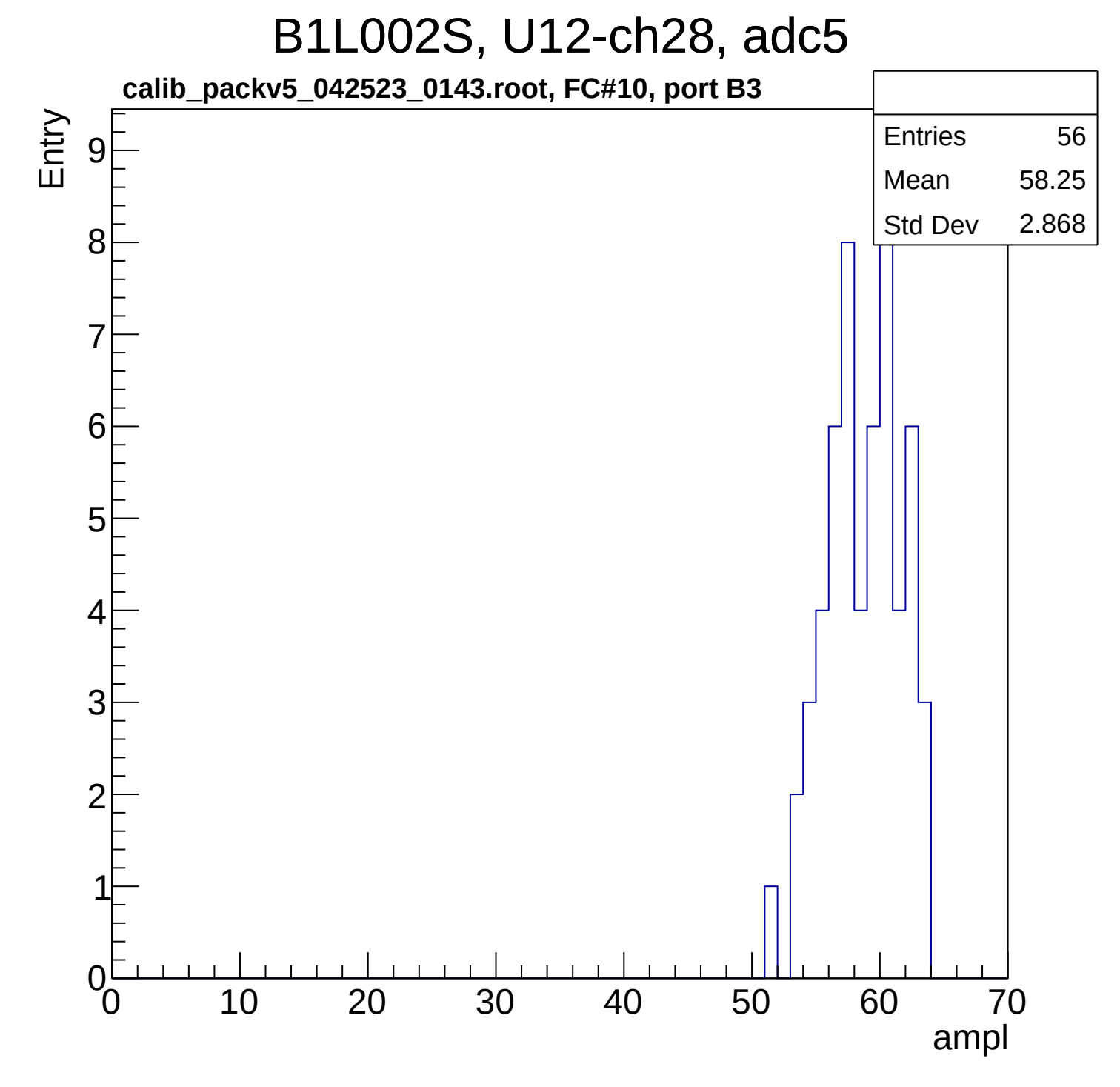
B1L002S, U12-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.25
Std Dev 2.868
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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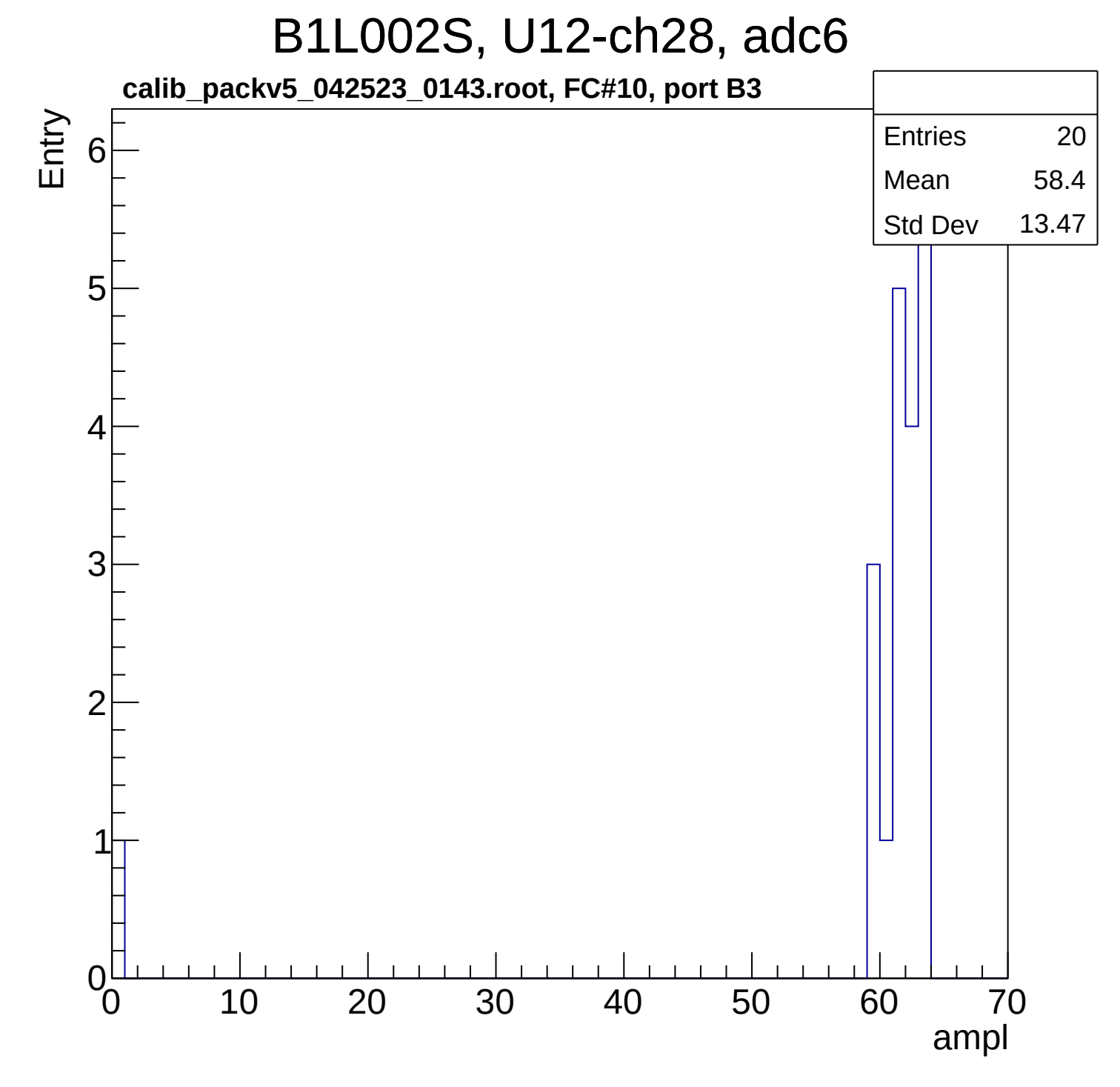
B1L002S, U12-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.4
Std Dev 13.47
Entry
0
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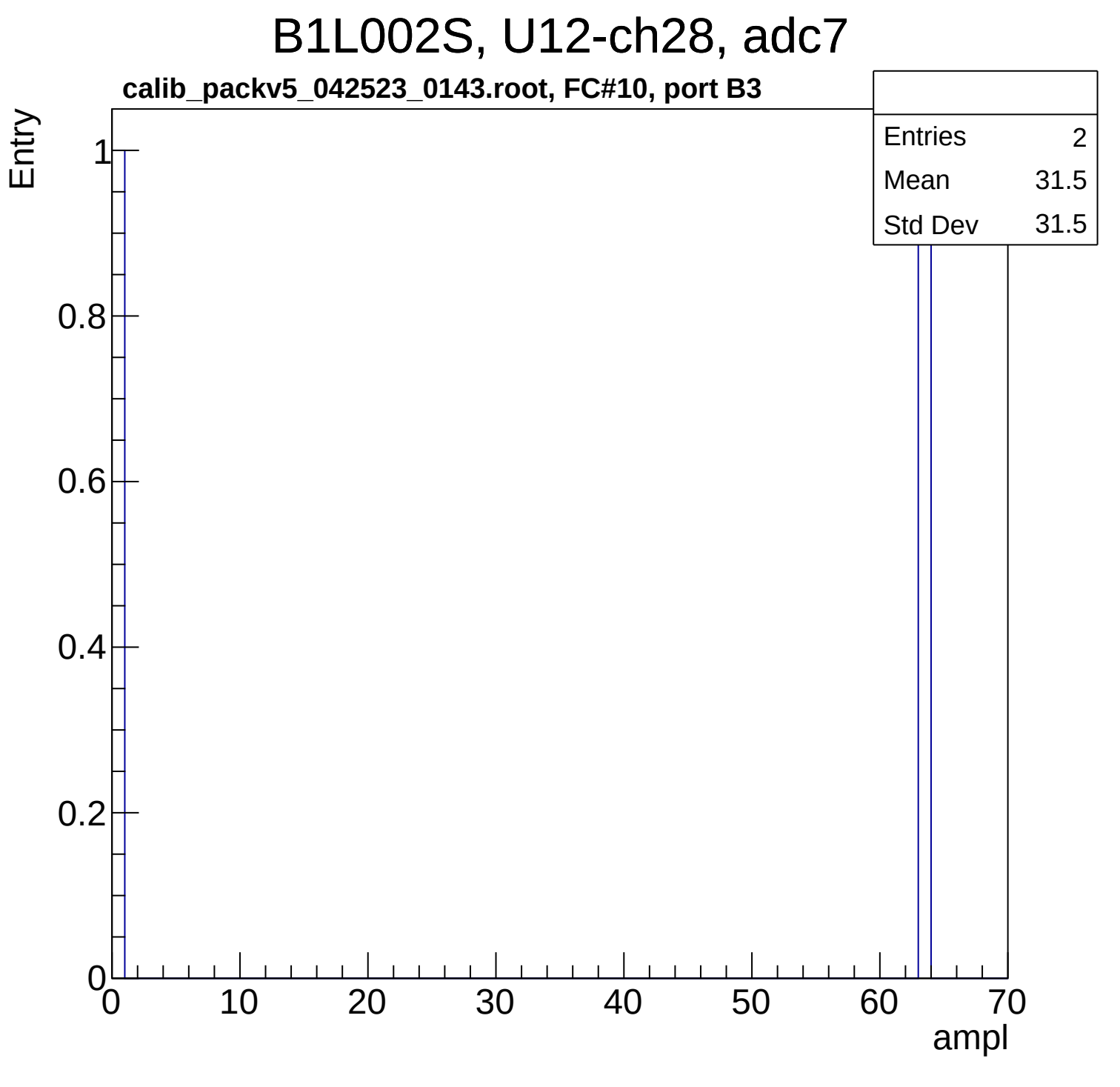
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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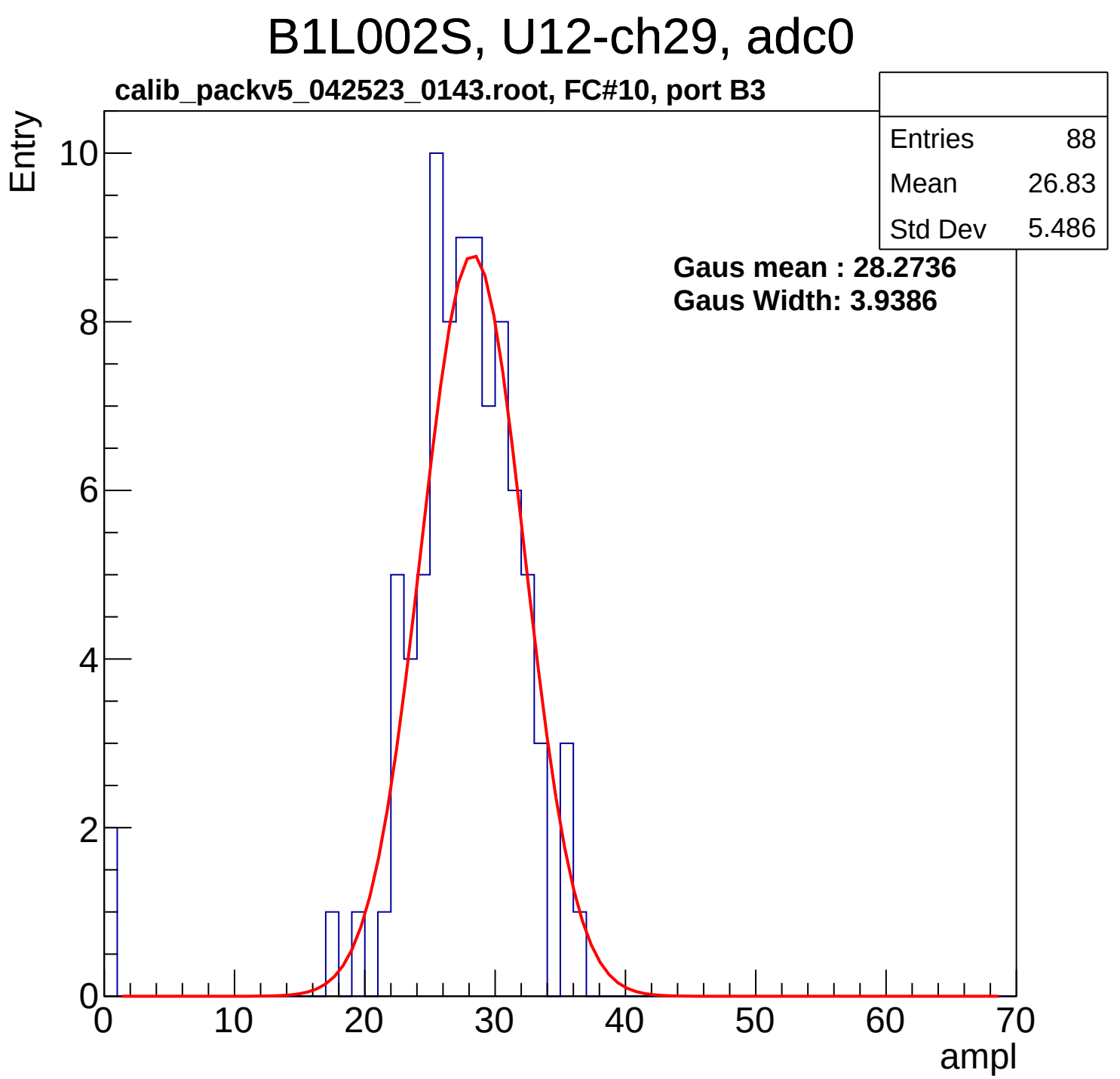
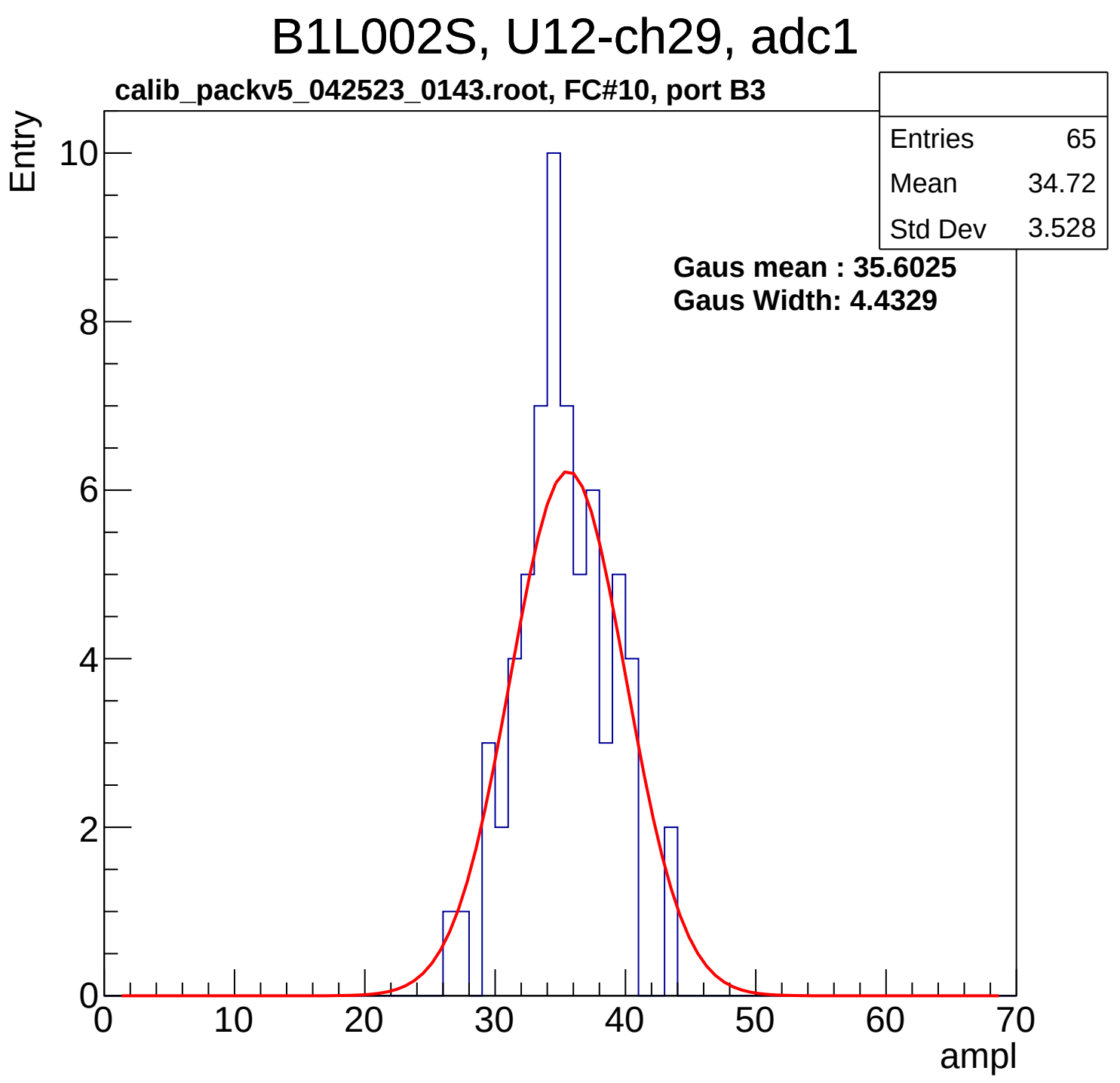
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 88
Mean 26.83
Std Dev 5.486
Gaus mean : 28.2736
Gaus Width: 3.9386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.72
Std Dev 3.528
Gaus mean : 35.6025
Gaus Width: 4.4329
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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60
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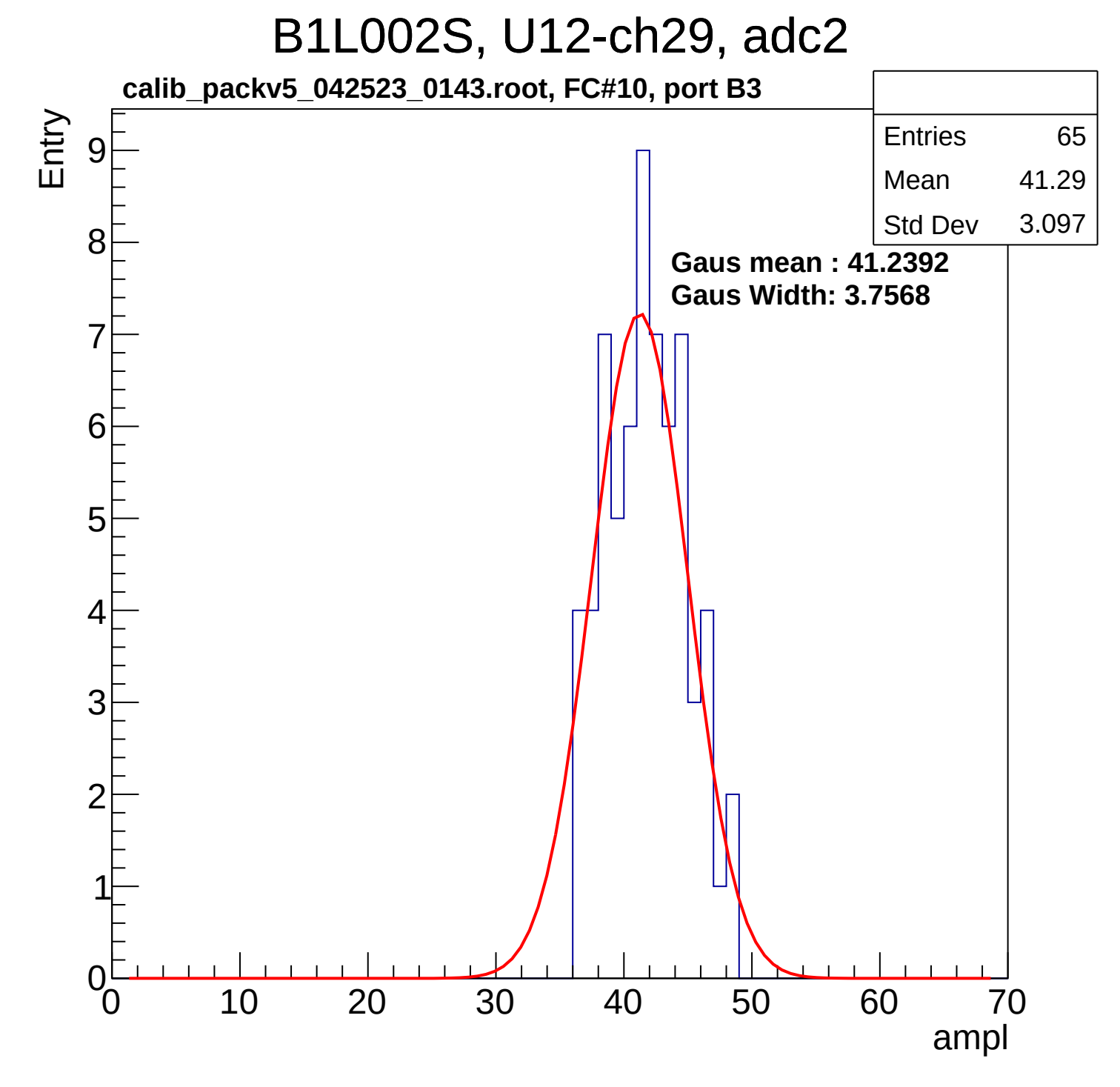
B1L002S, U12-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.29
Std Dev 3.097
Gaus mean : 41.2392
Gaus Width: 3.7568
Entry
ampl
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3
4
5
6
7
8
9
0
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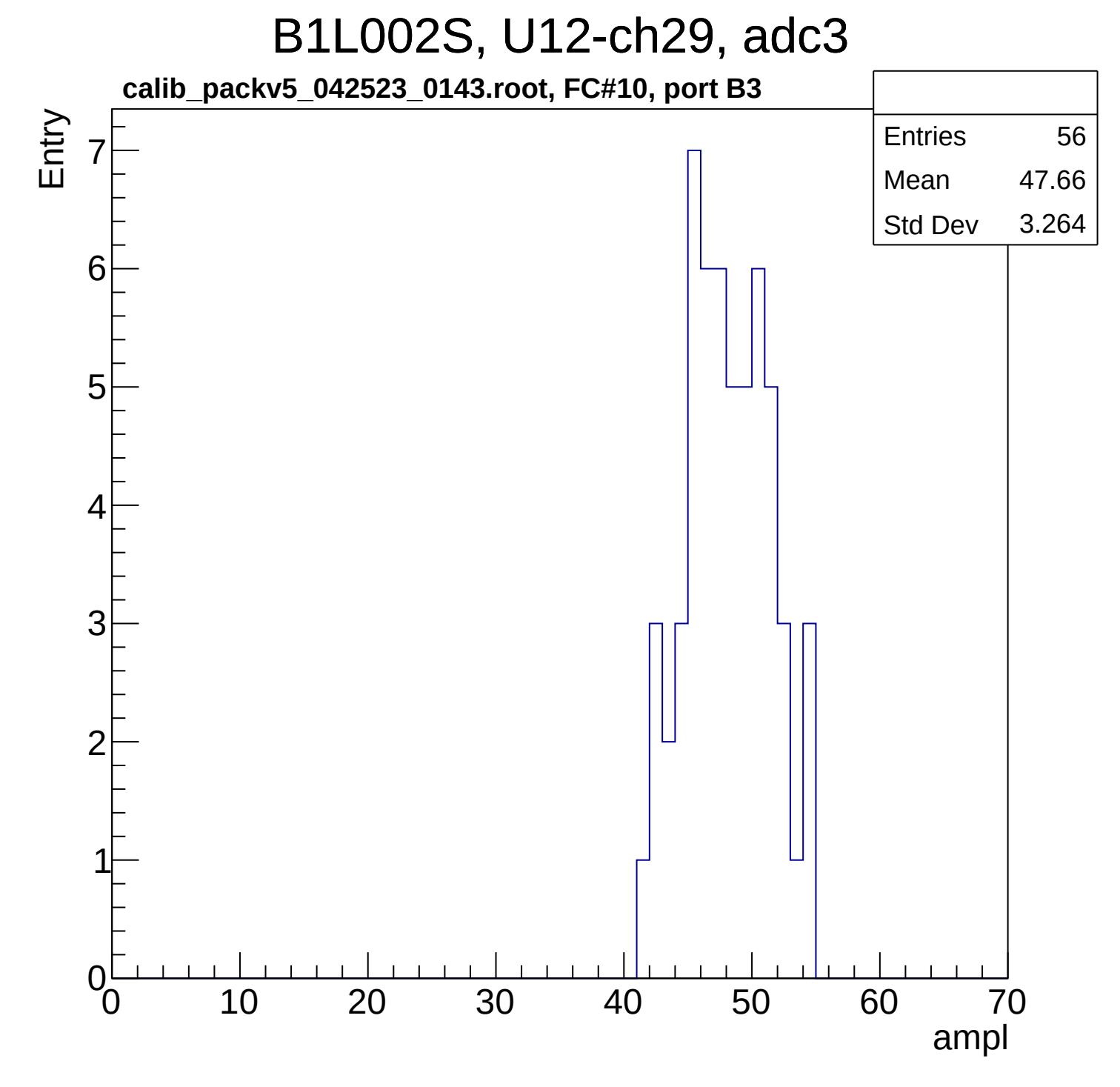
B1L002S, U12-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.66
Std Dev 3.264
Entry
ampl
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4
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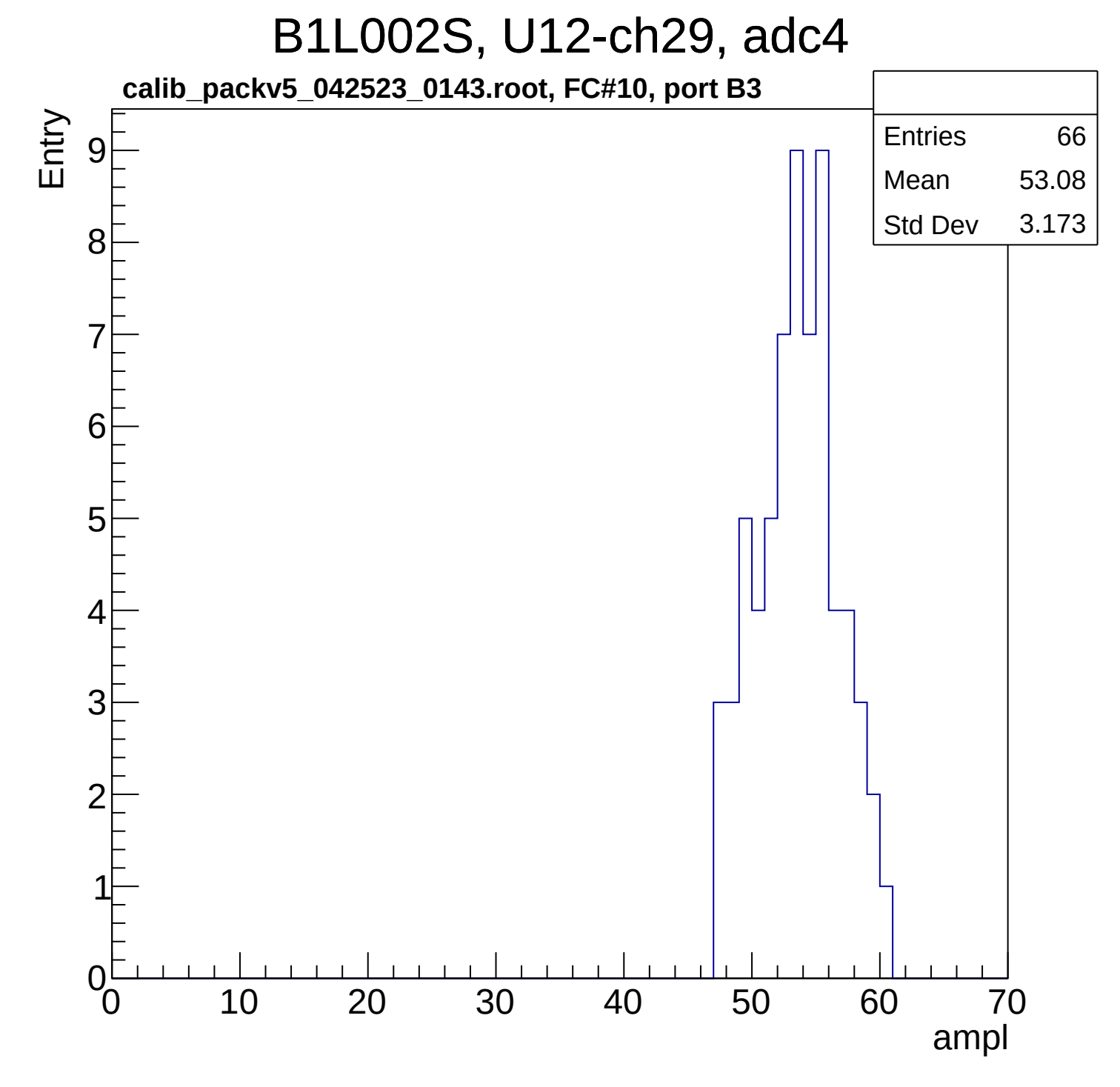
B1L002S, U12-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.08
Std Dev 3.173
Entry
ampl
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4
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6
7
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0
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60
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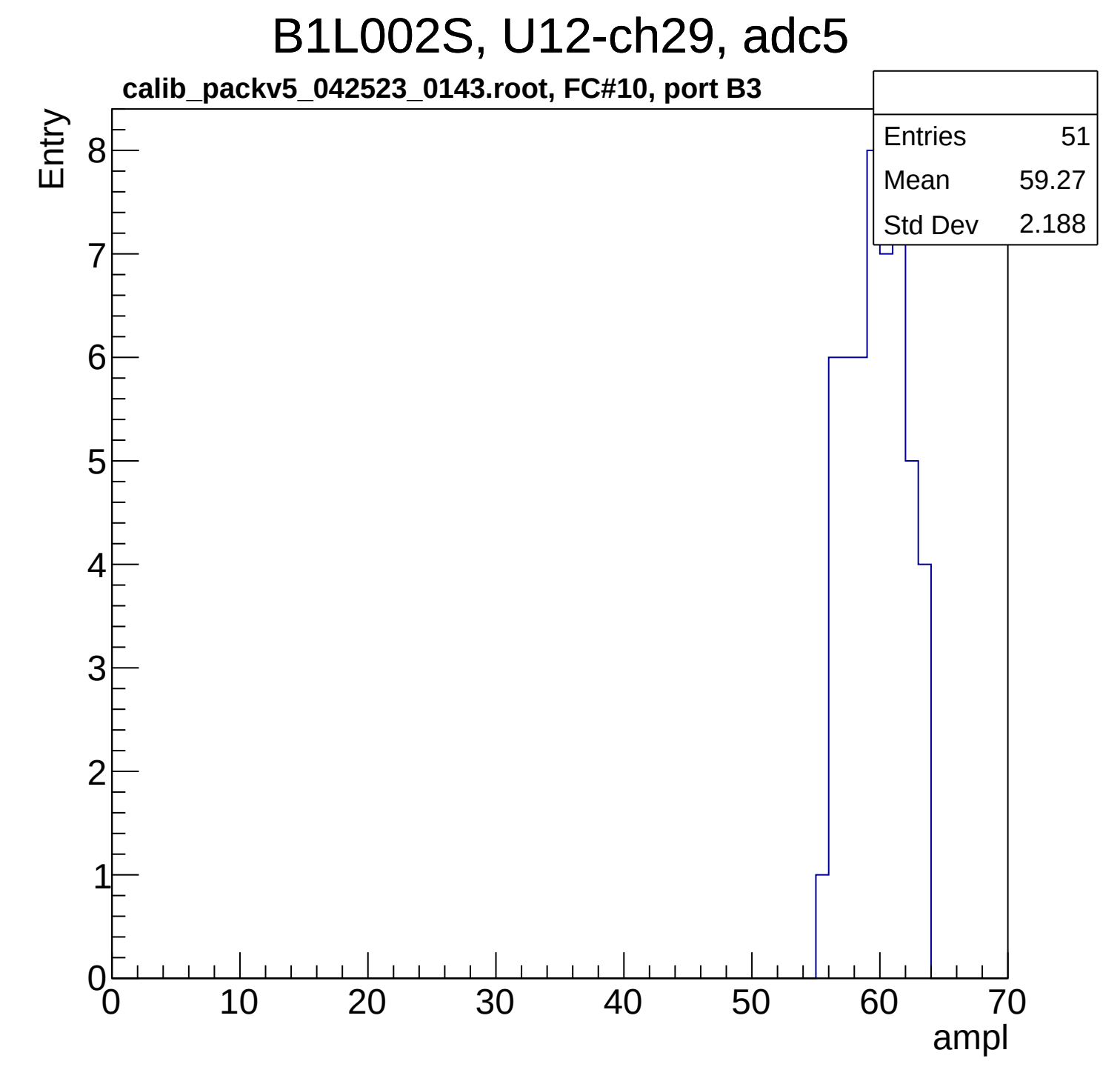
B1L002S, U12-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.27
Std Dev 2.188
Entry
ampl
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5
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0
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60
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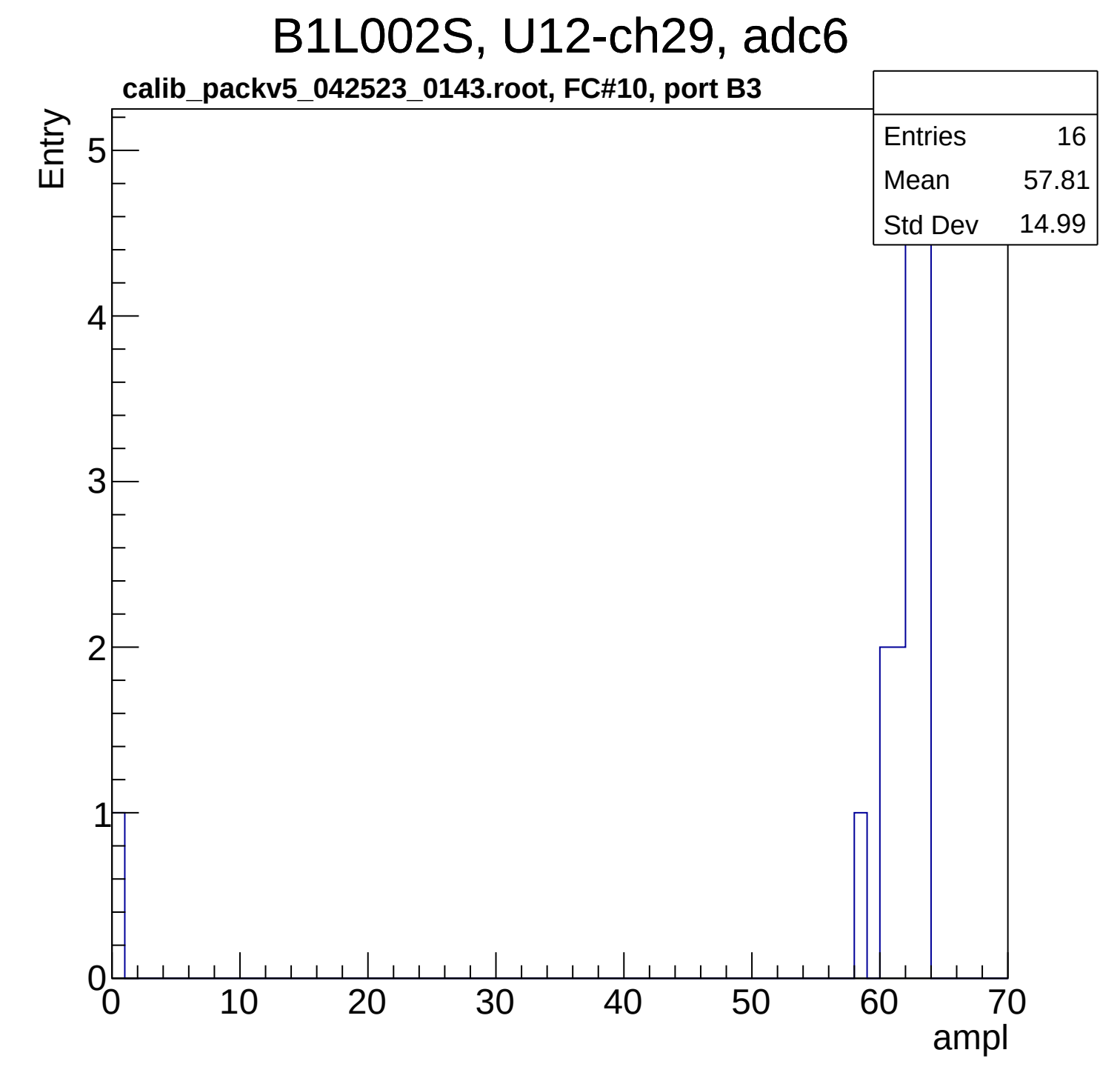
B1L002S, U12-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.81
Std Dev 14.99
Entry
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3
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5
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30
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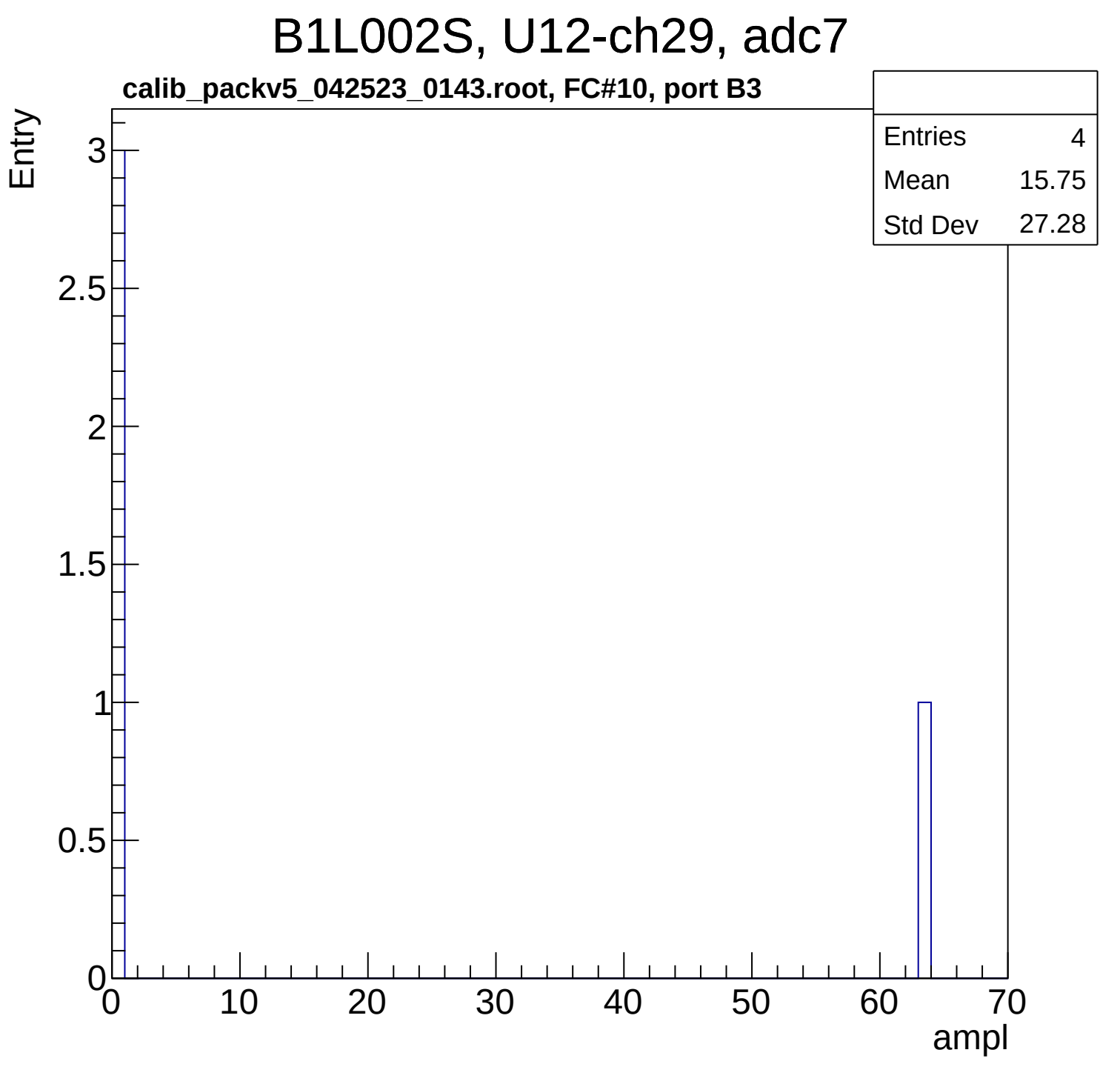
50
60
70
ampl


B1L002S, U12-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 15.75
Std Dev 27.28
Entry
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1.5
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2.5
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10
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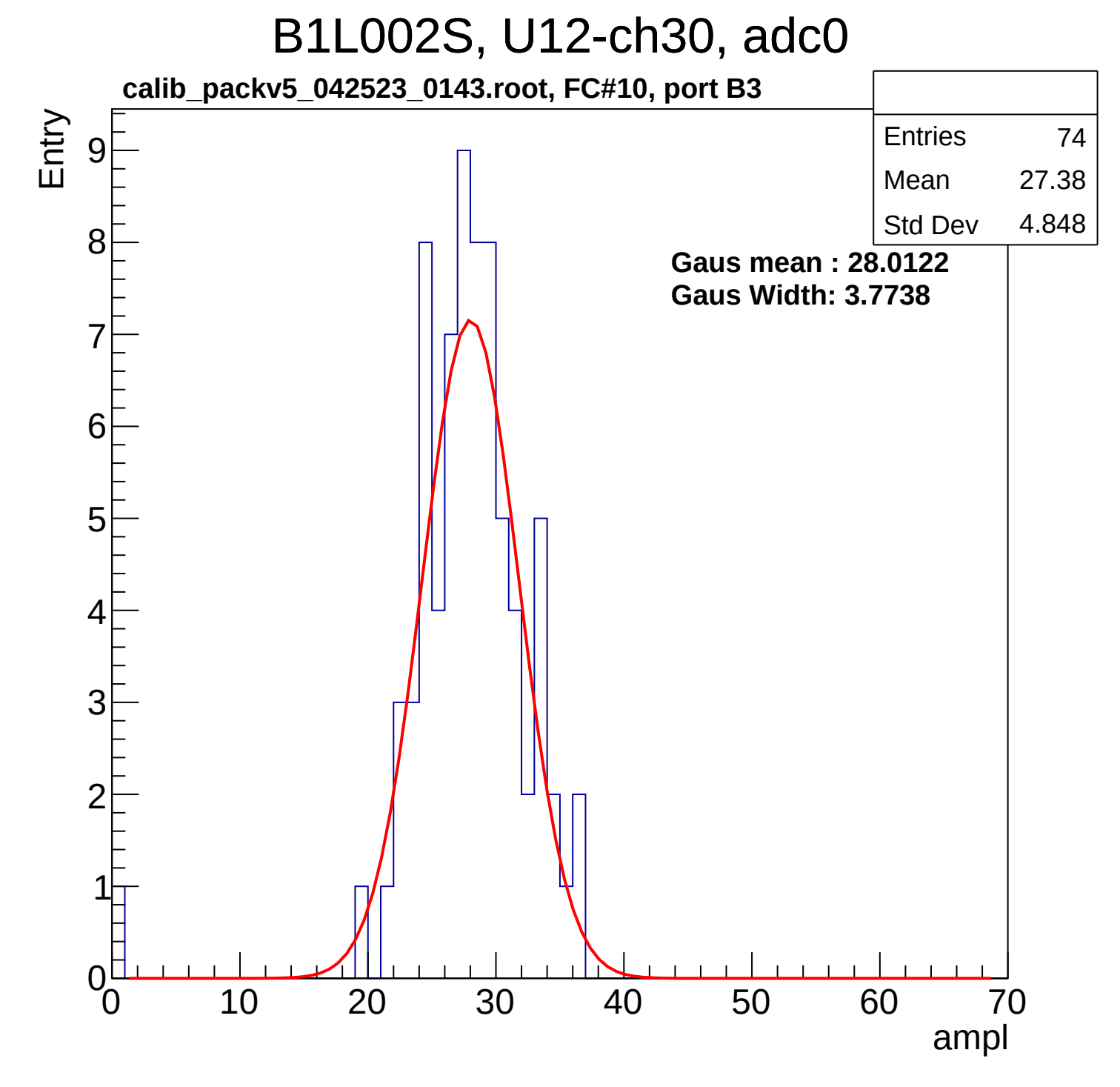
50
60
70
ampl


B1L002S, U12-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.38
Std Dev 4.848
Gaus mean : 28.0122
Gaus Width: 3.7738
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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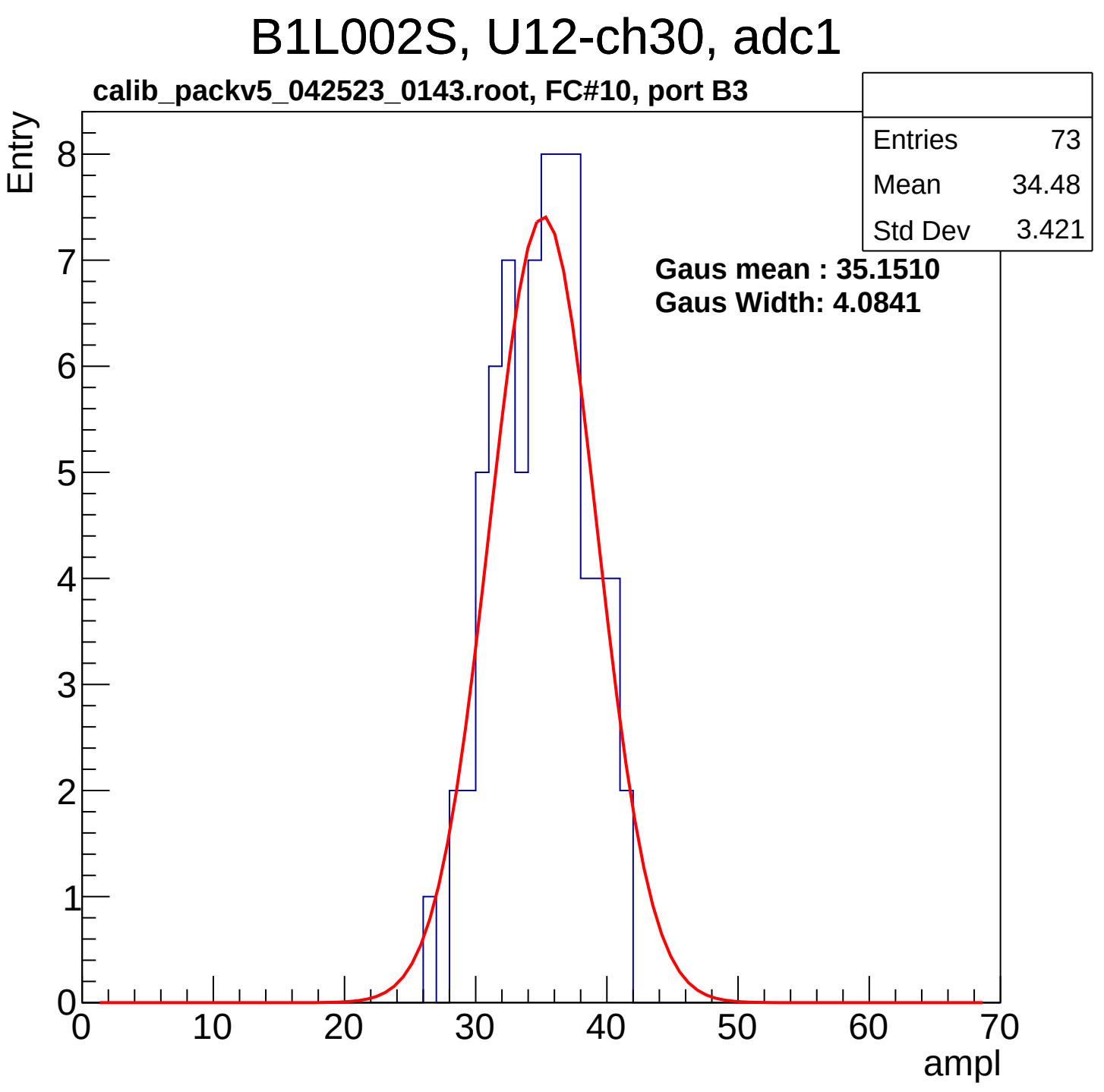
B1L002S, U12-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.48
Std Dev 3.421
Gaus mean : 35.1510
Gaus Width: 4.0841
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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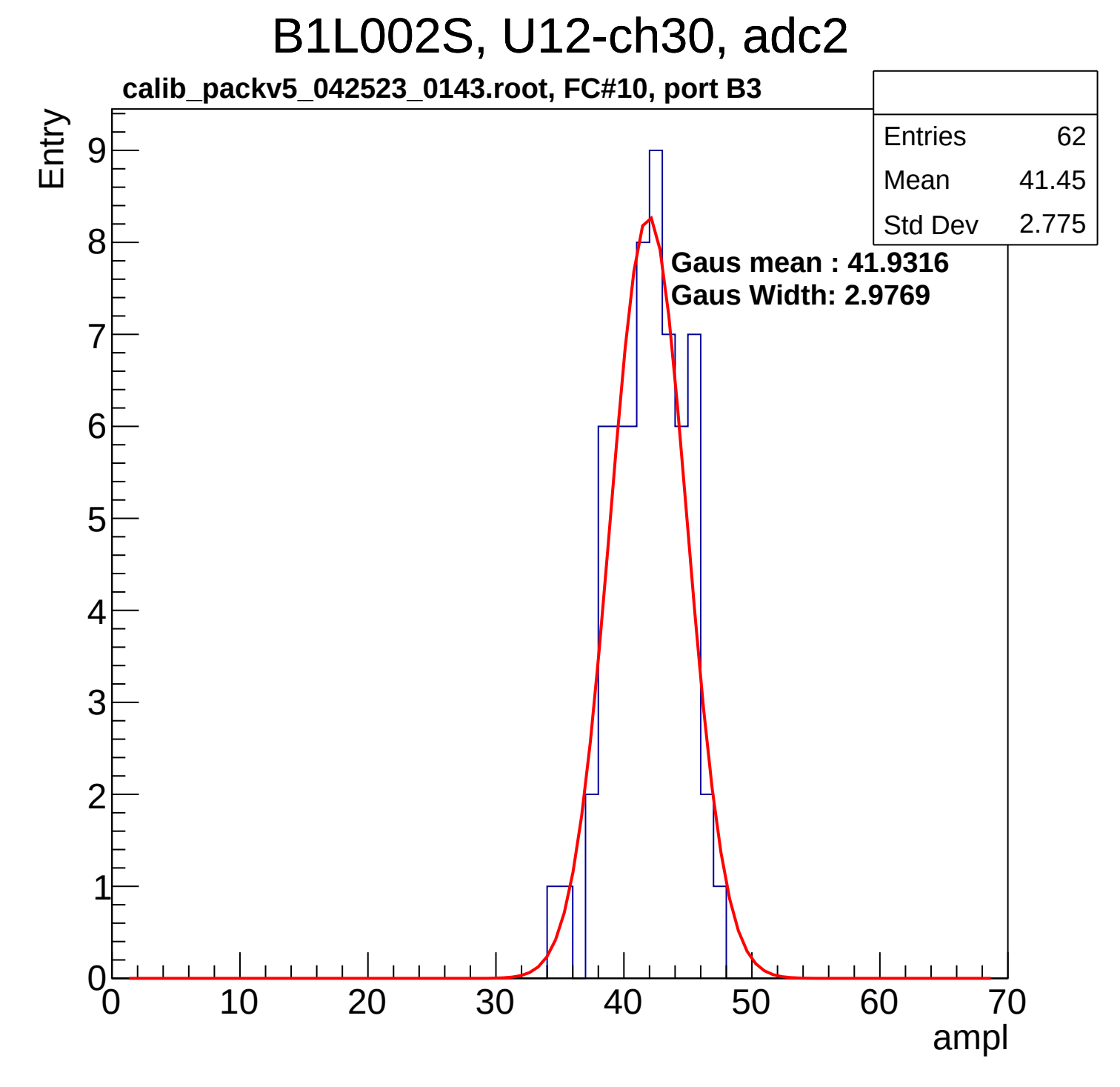
B1L002S, U12-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.45
Std Dev 2.775
Gaus mean : 41.9316
Gaus Width: 2.9769
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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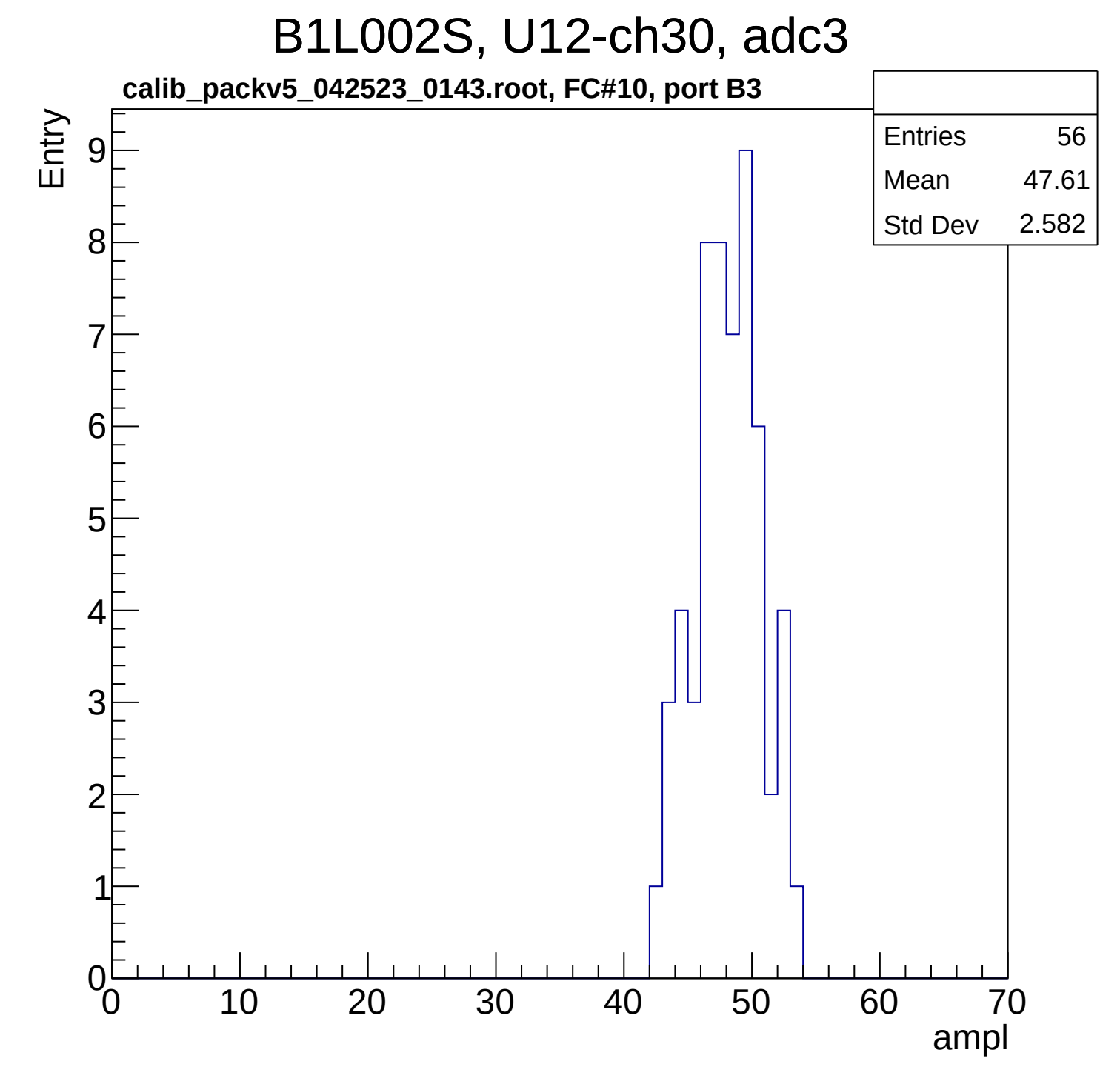
B1L002S, U12-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.61
Std Dev 2.582
Entry
ampl
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4
5
6
7
8
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0
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60
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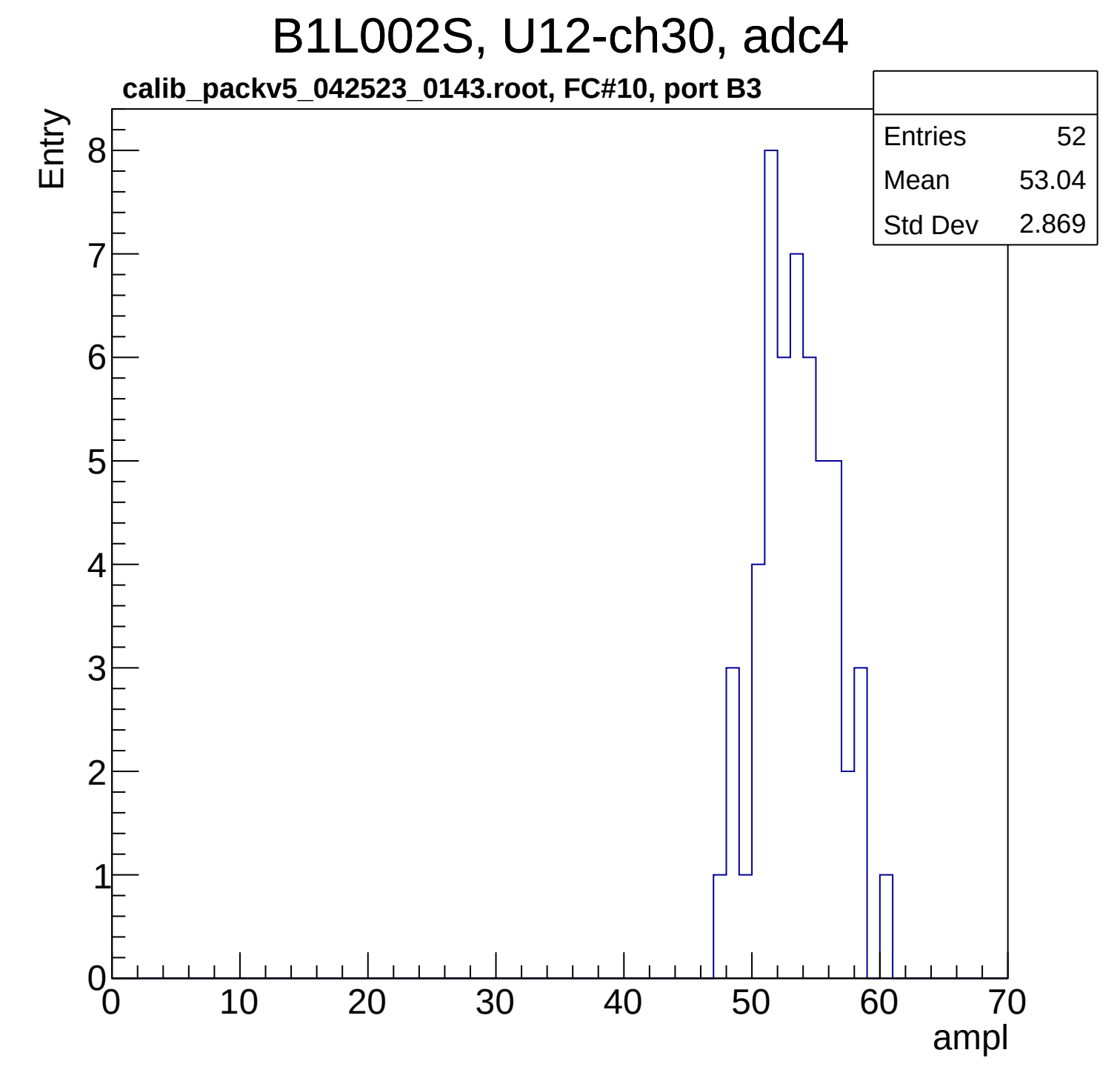
B1L002S, U12-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.04
Std Dev 2.869
Entry
ampl
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2
3
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5
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7
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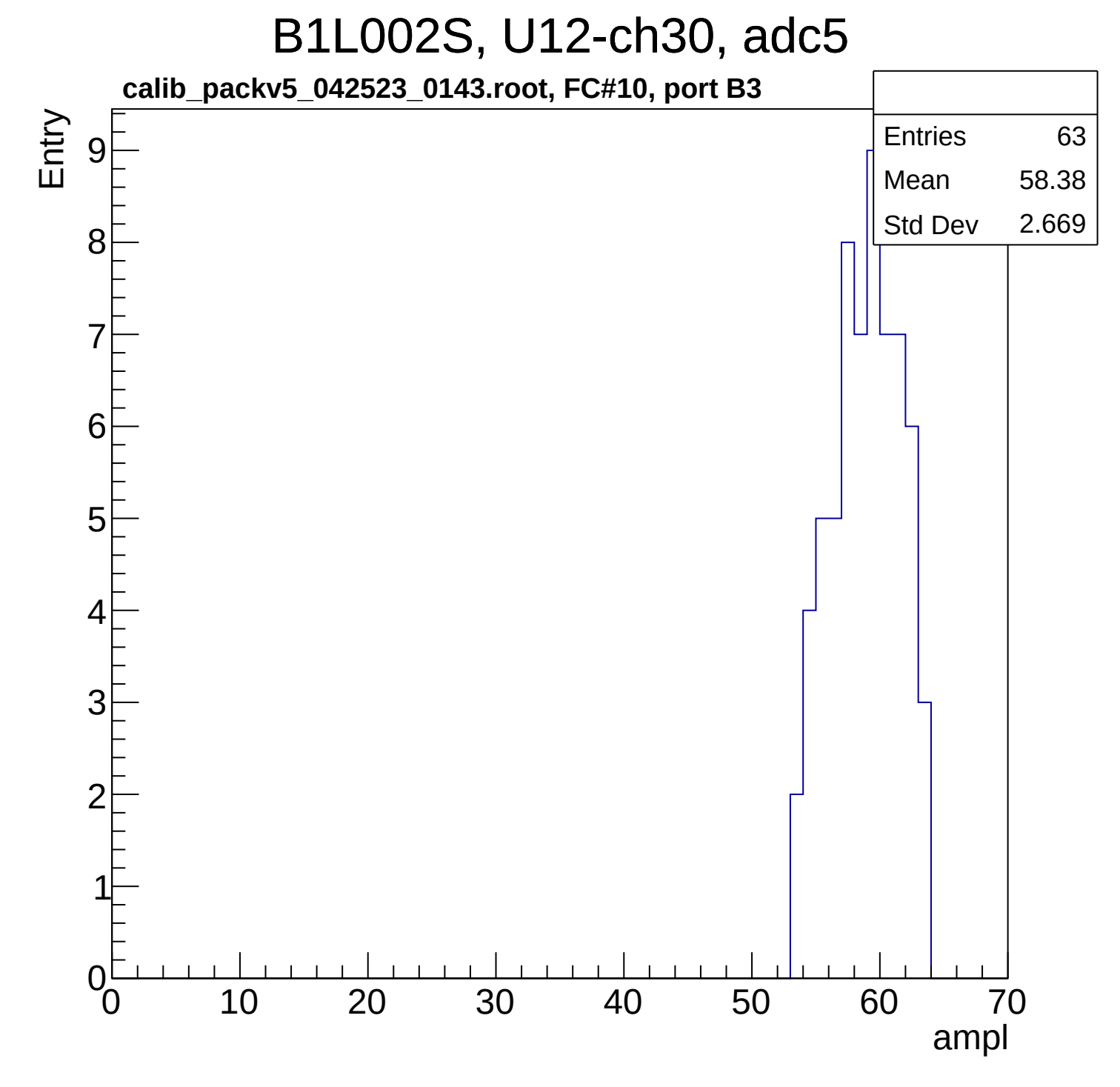
B1L002S, U12-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.38
Std Dev 2.669
Entry
ampl
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3
4
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7
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0
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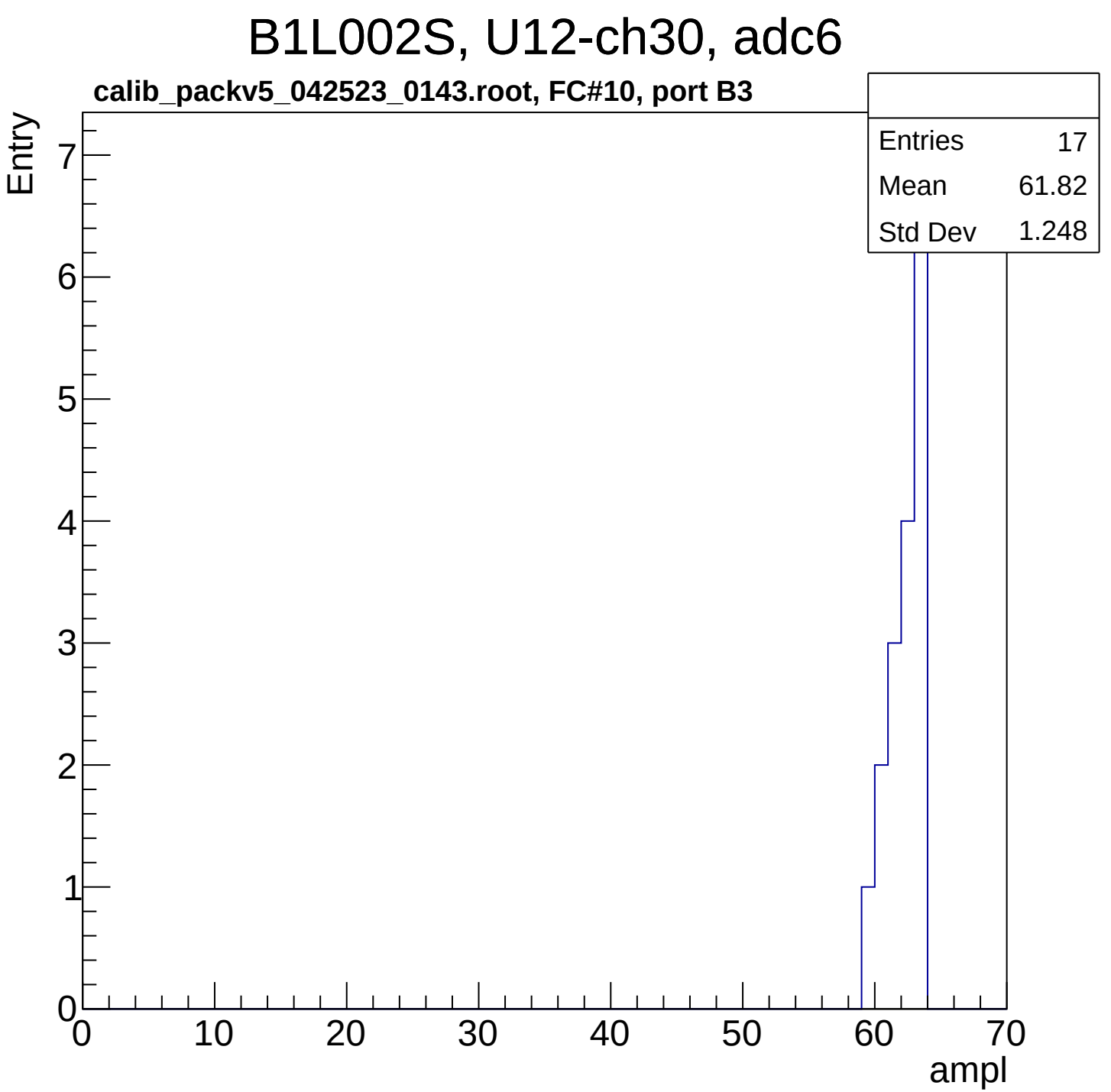
B1L002S, U12-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.82
Std Dev 1.248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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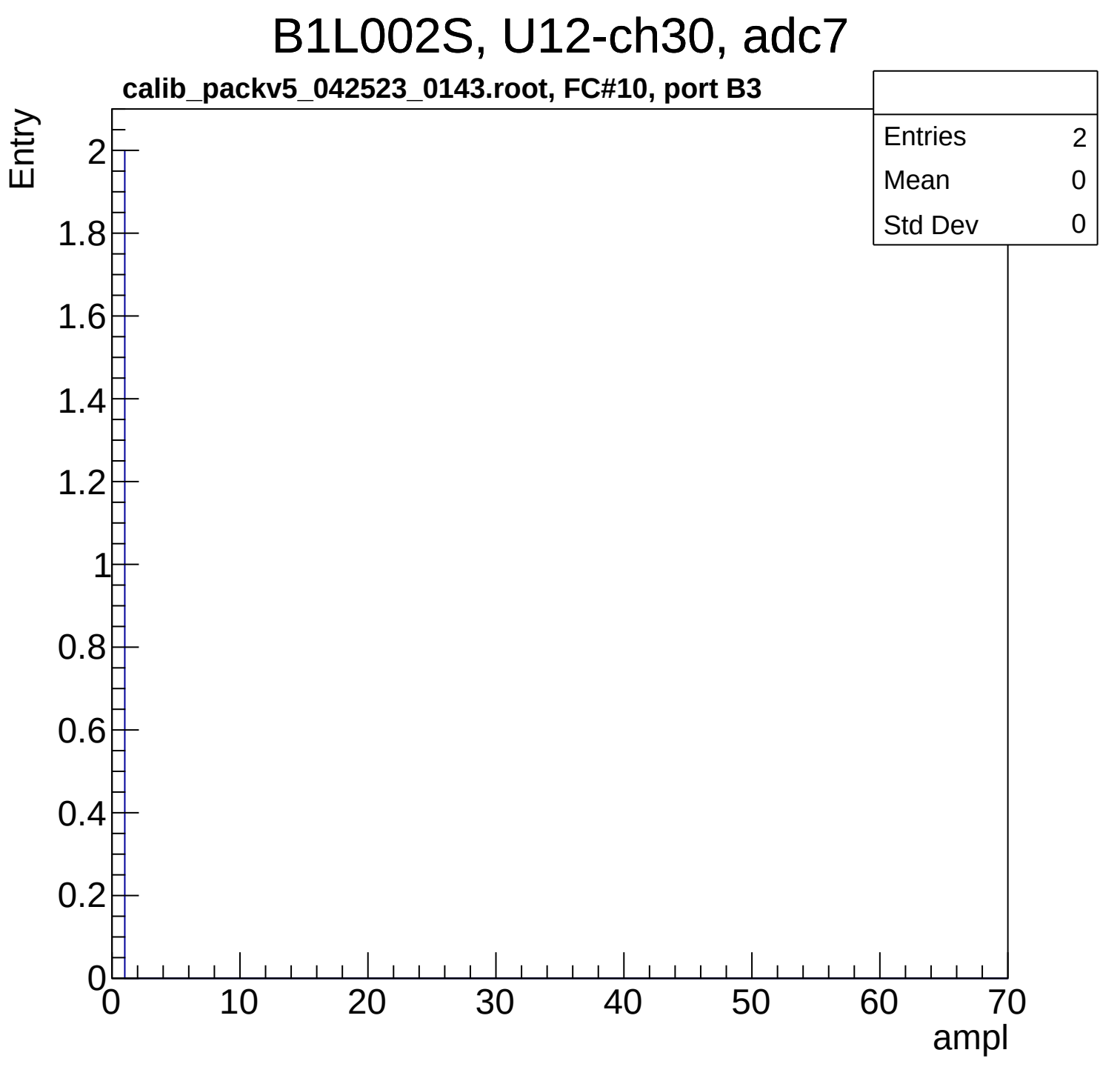
B1L002S, U12-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
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60
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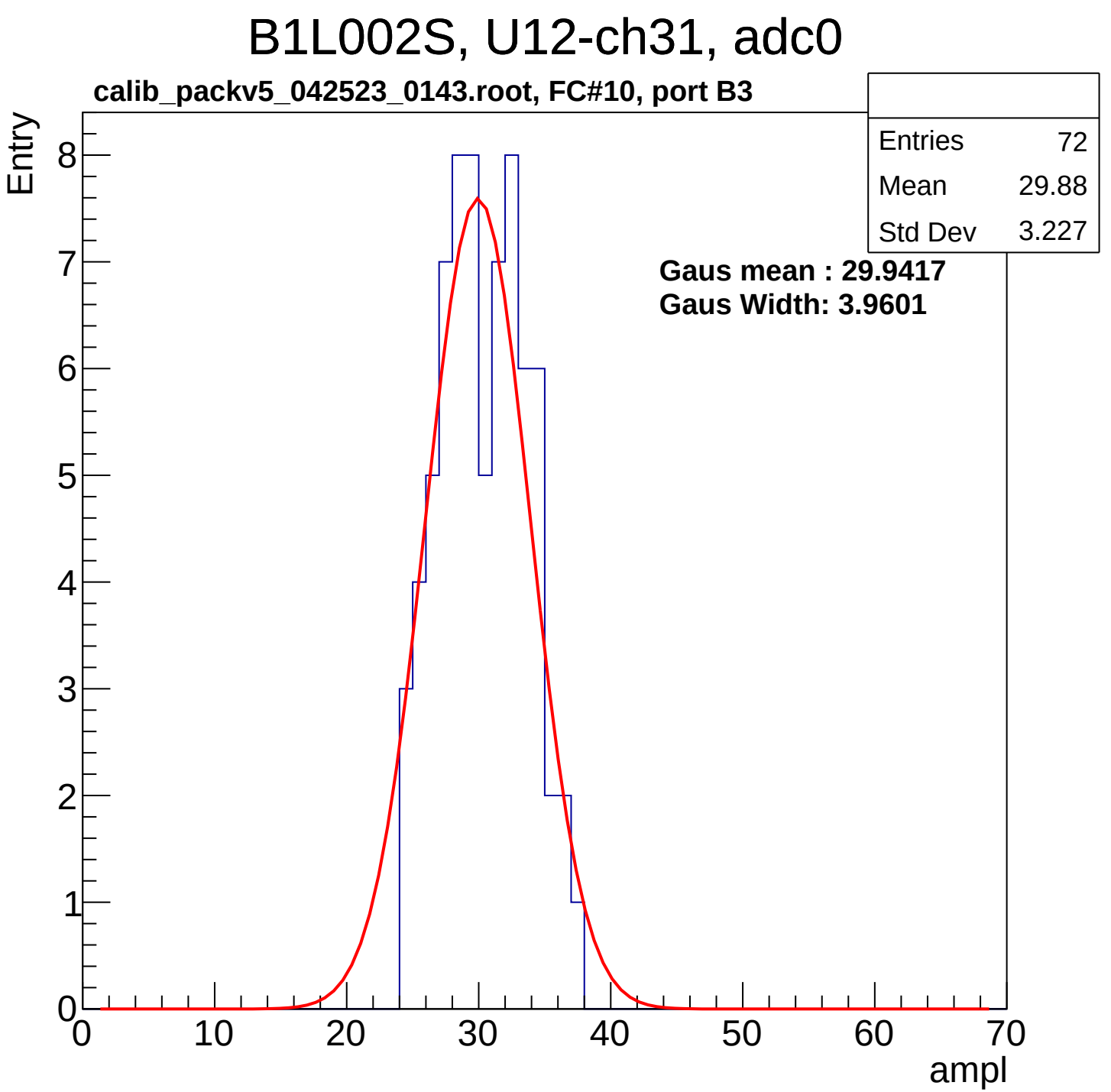
B1L002S, U12-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 29.88
Std Dev 3.227
Gaus mean : 29.9417
Gaus Width: 3.9601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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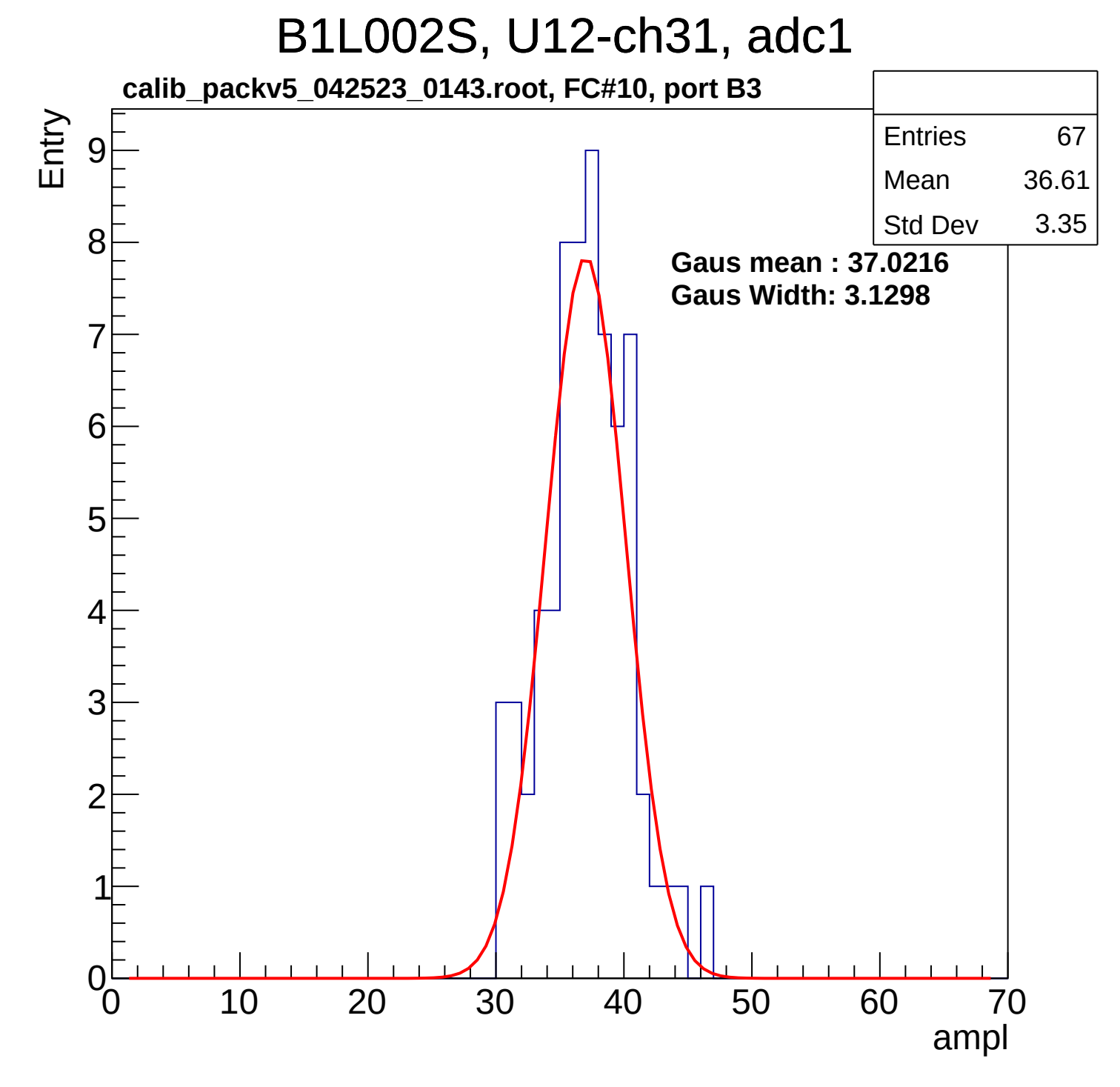
B1L002S, U12-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.61
Std Dev 3.35
Gaus mean : 37.0216
Gaus Width: 3.1298
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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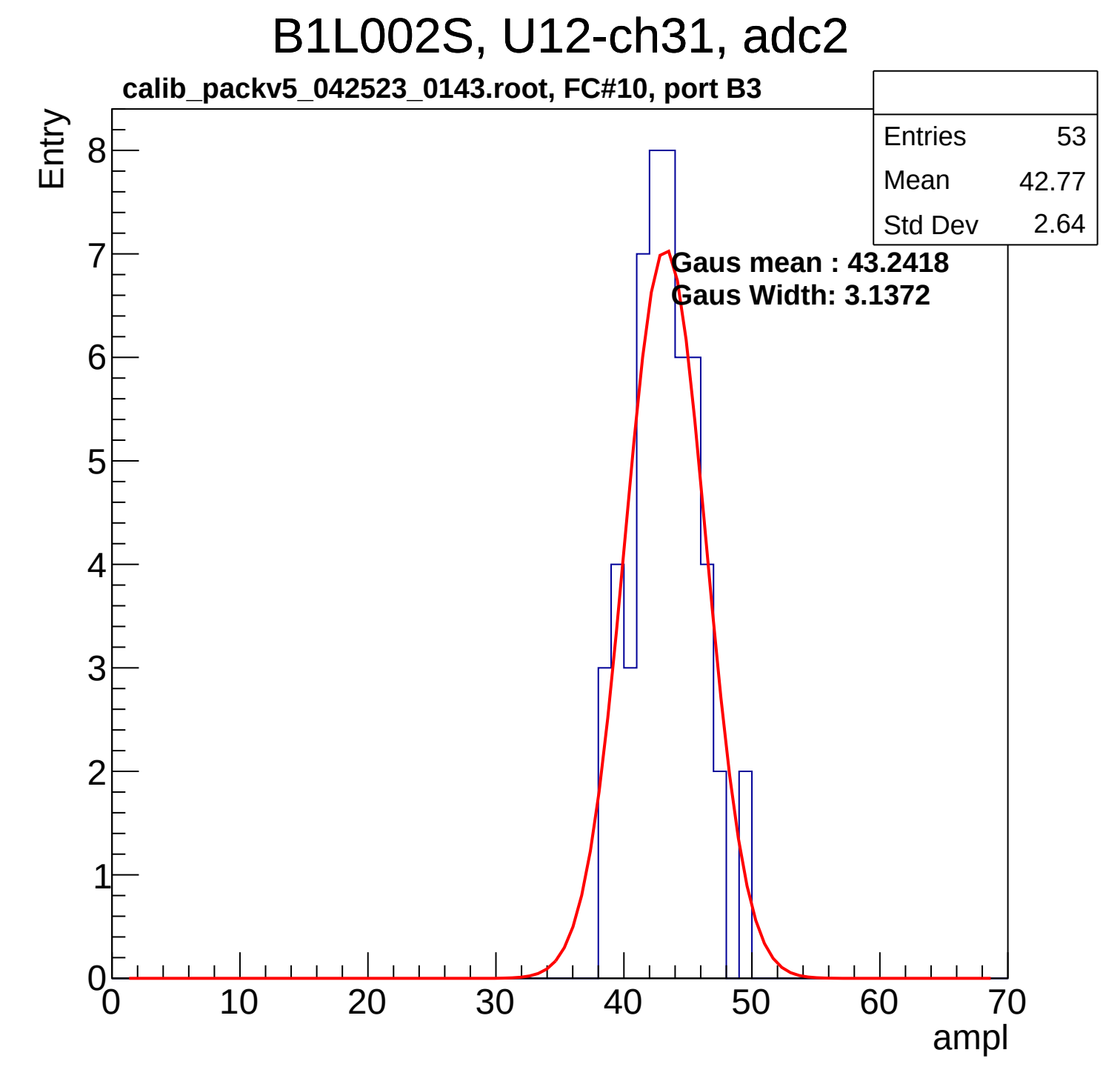
B1L002S, U12-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 42.77
Std Dev 2.64
Gaus mean : 43.2418
Gaus Width: 3.1372
Entry
ampl
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1
2
3
4
5
6
7
8
0
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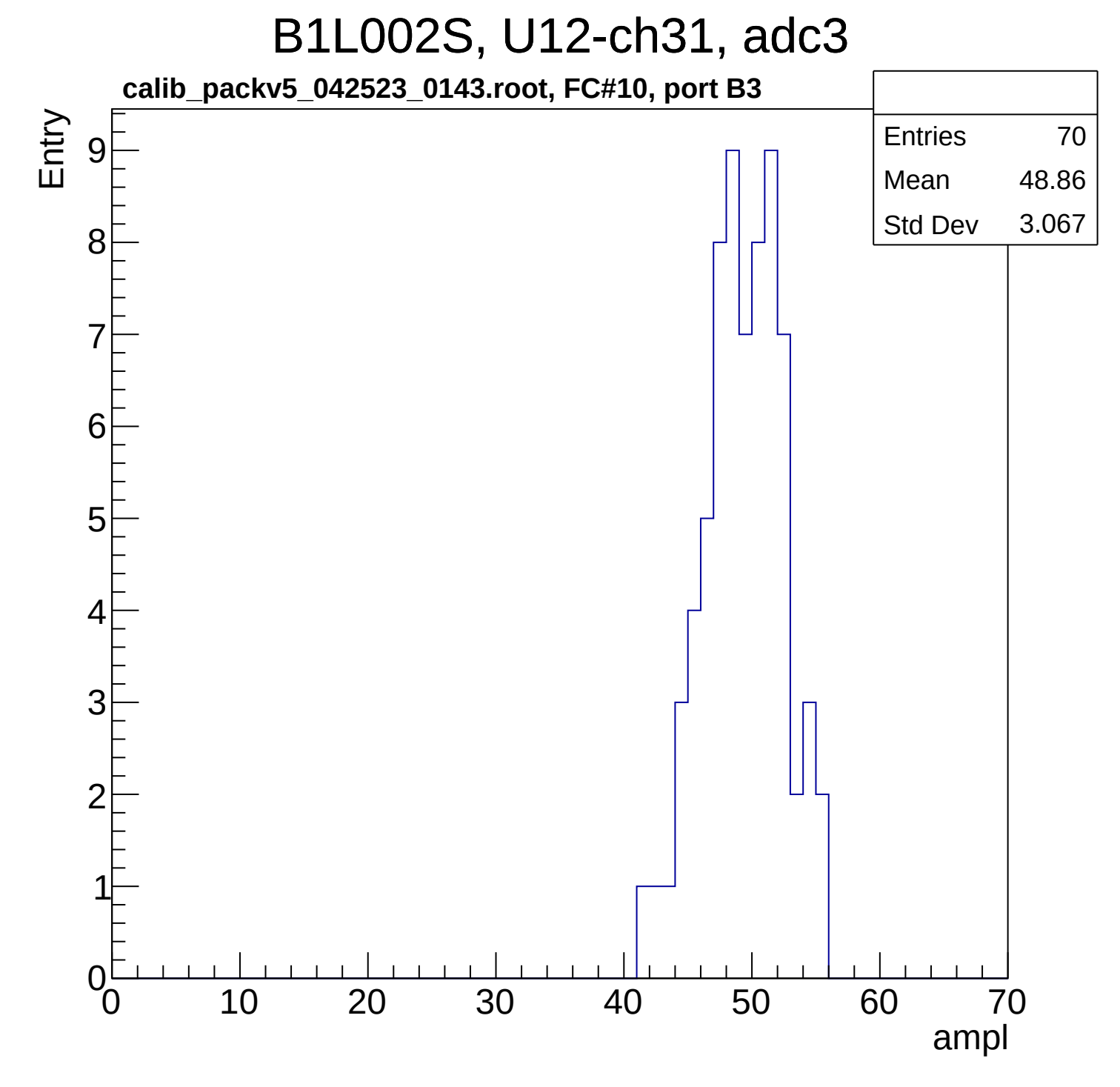
B1L002S, U12-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.86
Std Dev 3.067
Entry
ampl
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2
3
4
5
6
7
8
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0
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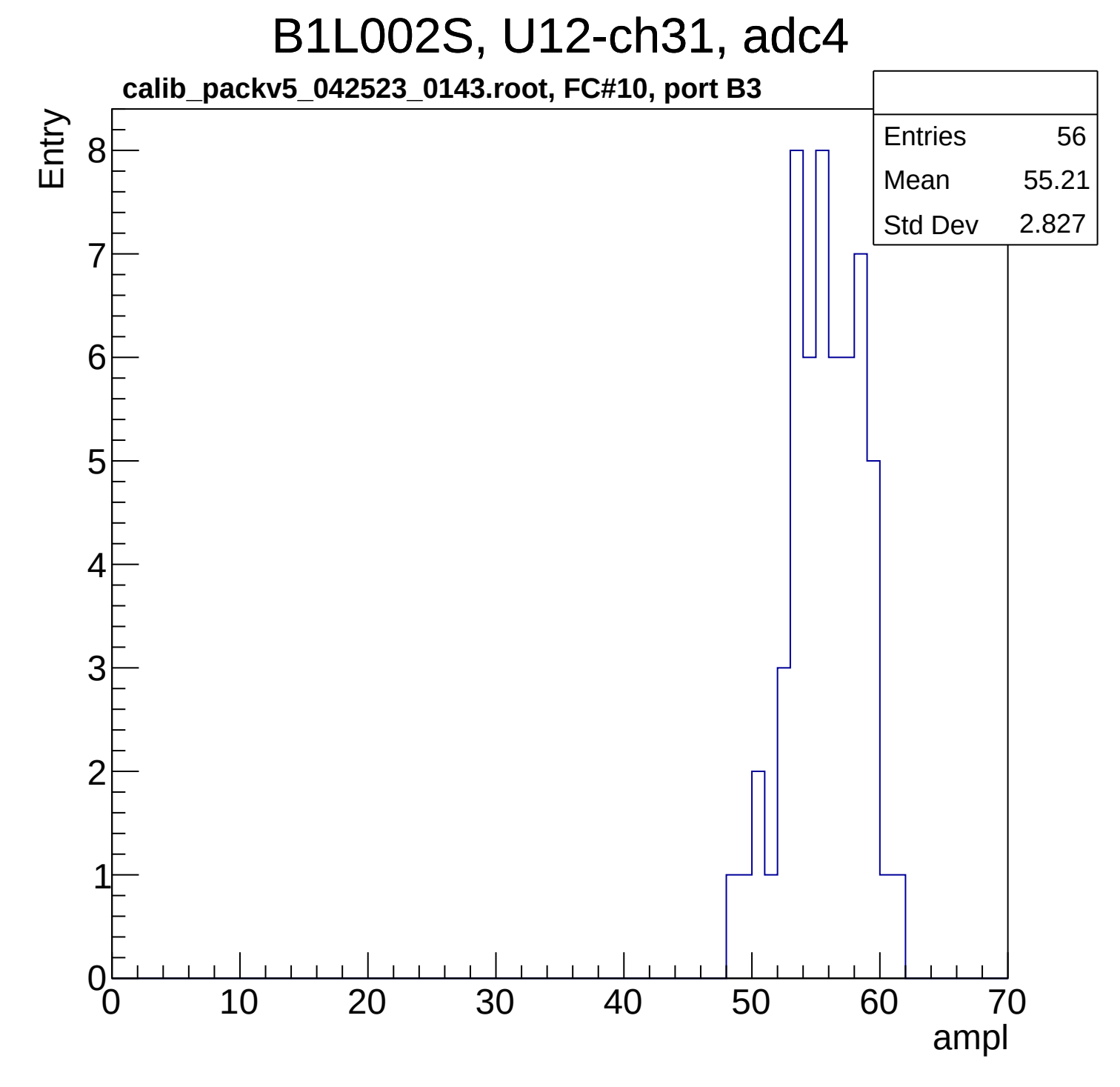
B1L002S, U12-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.21
Std Dev 2.827
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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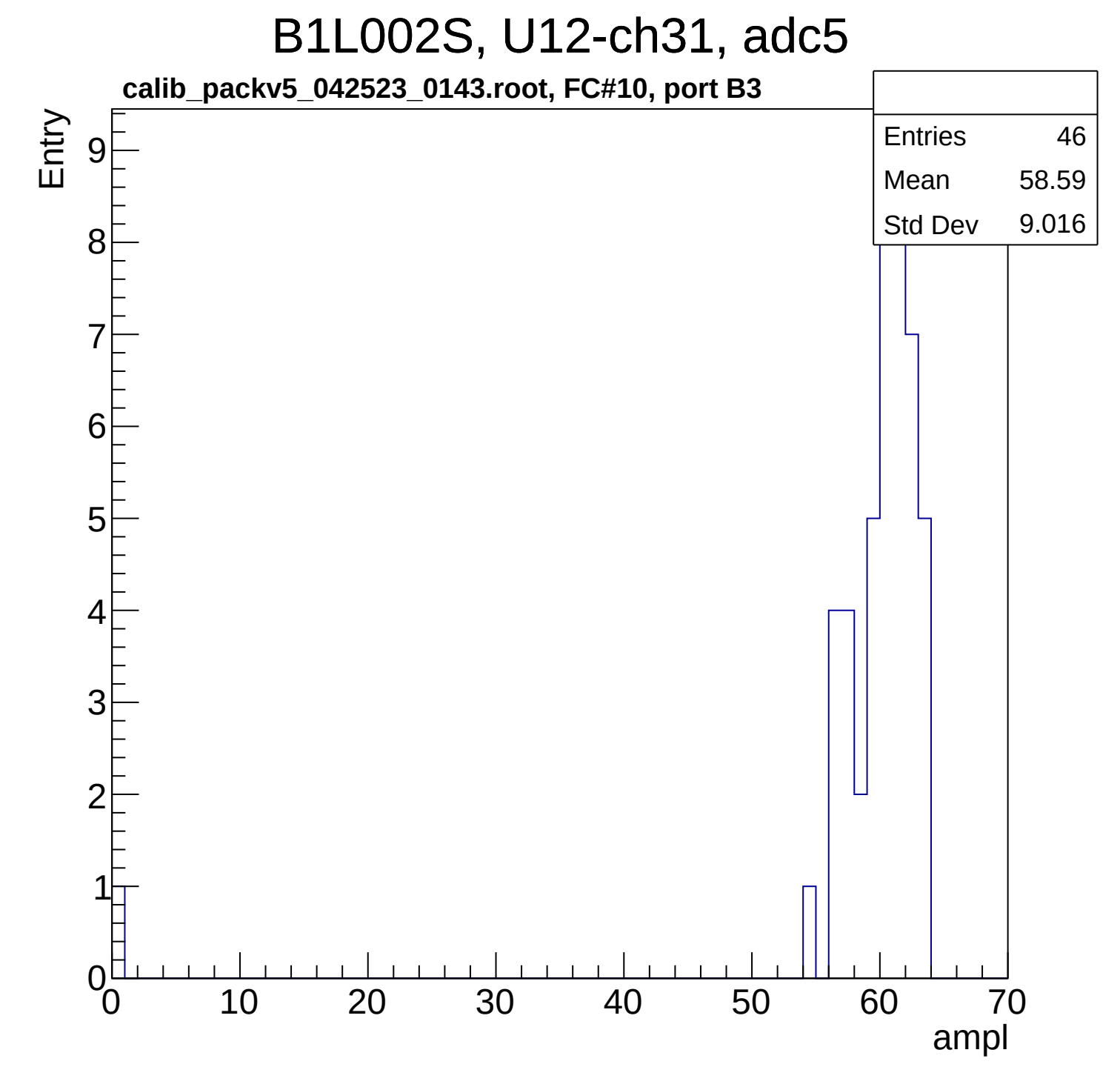
B1L002S, U12-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.59
Std Dev 9.016
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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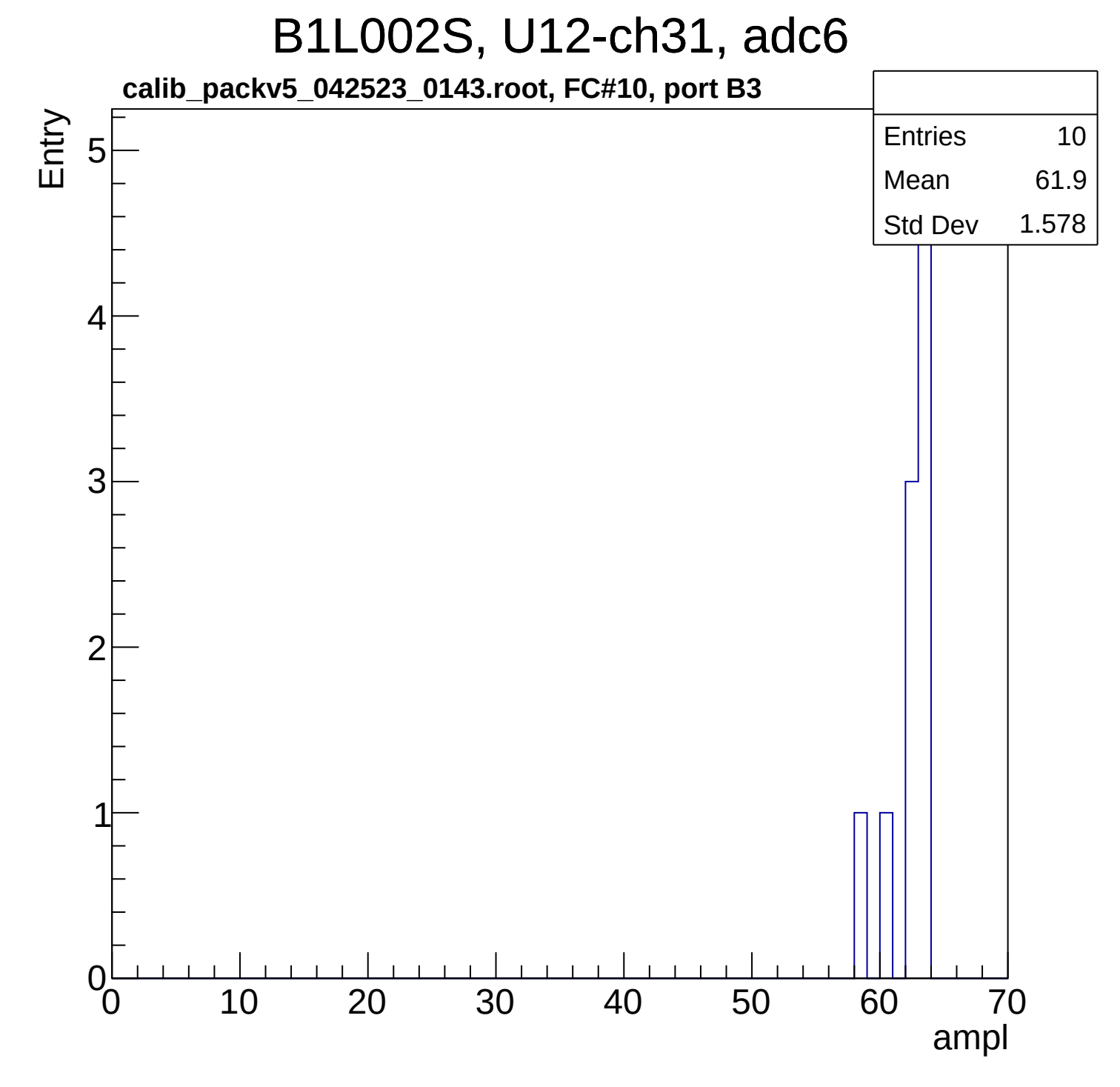
B1L002S, U12-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.9
Std Dev 1.578
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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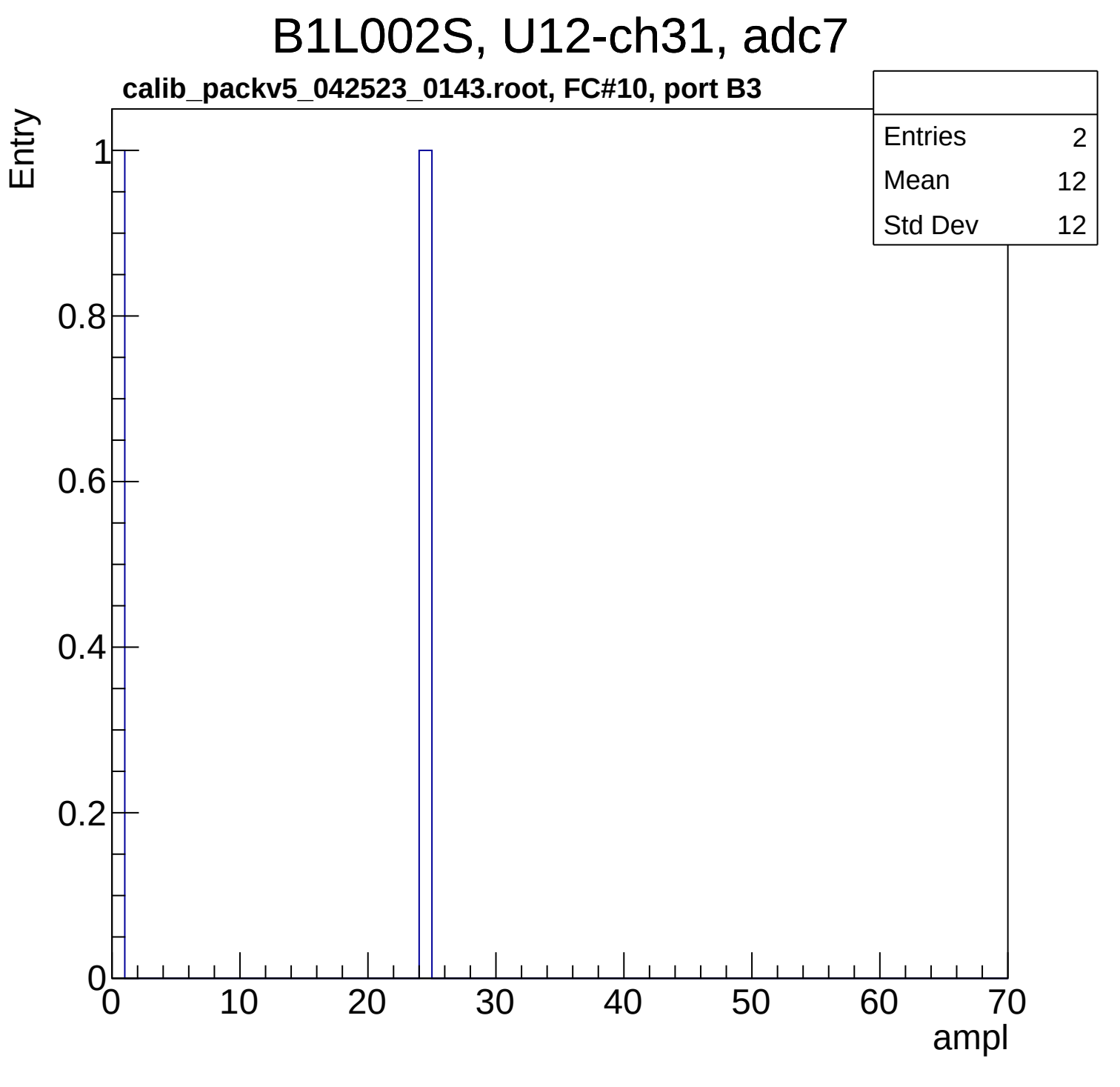
B1L002S, U12-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
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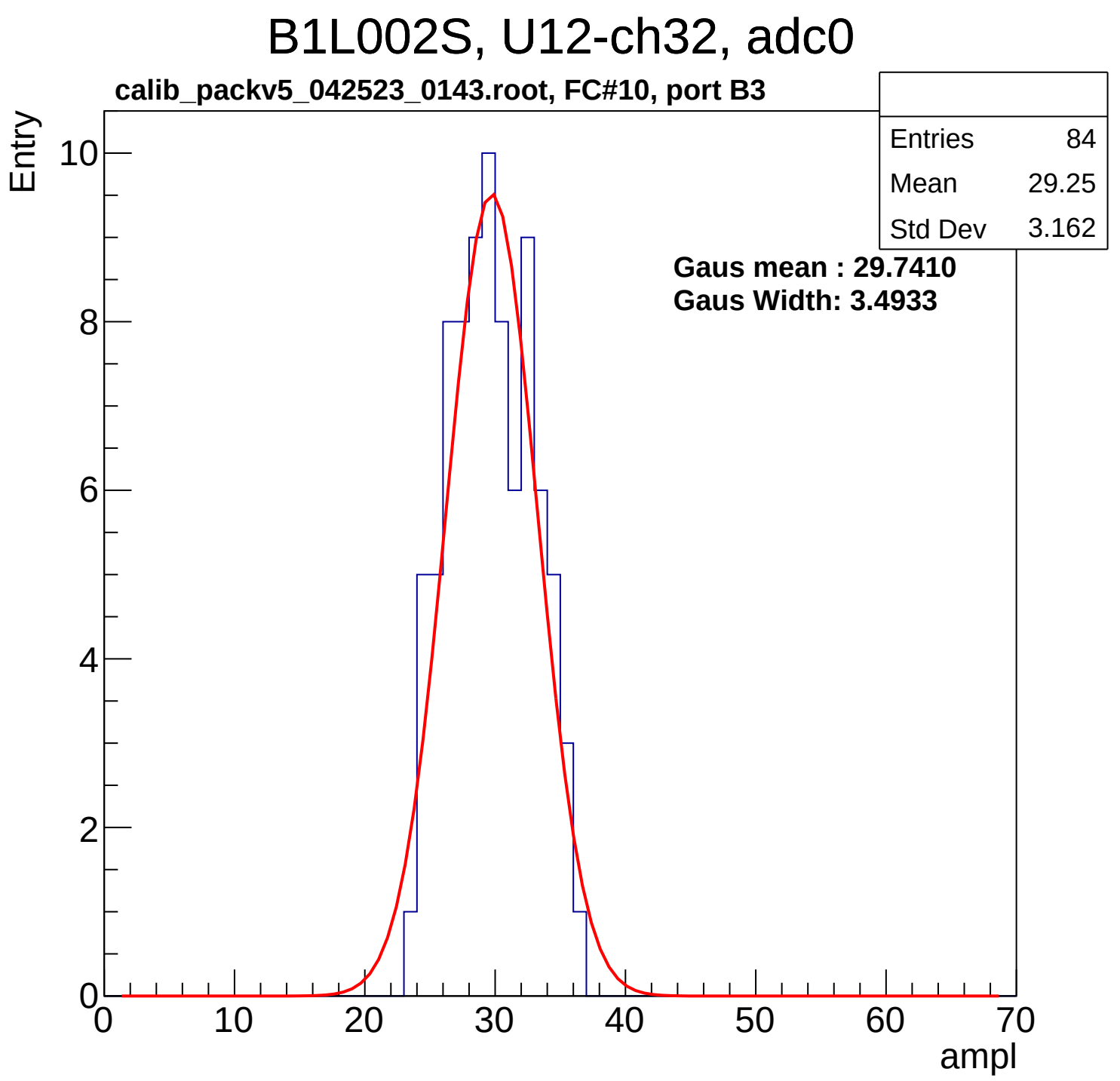
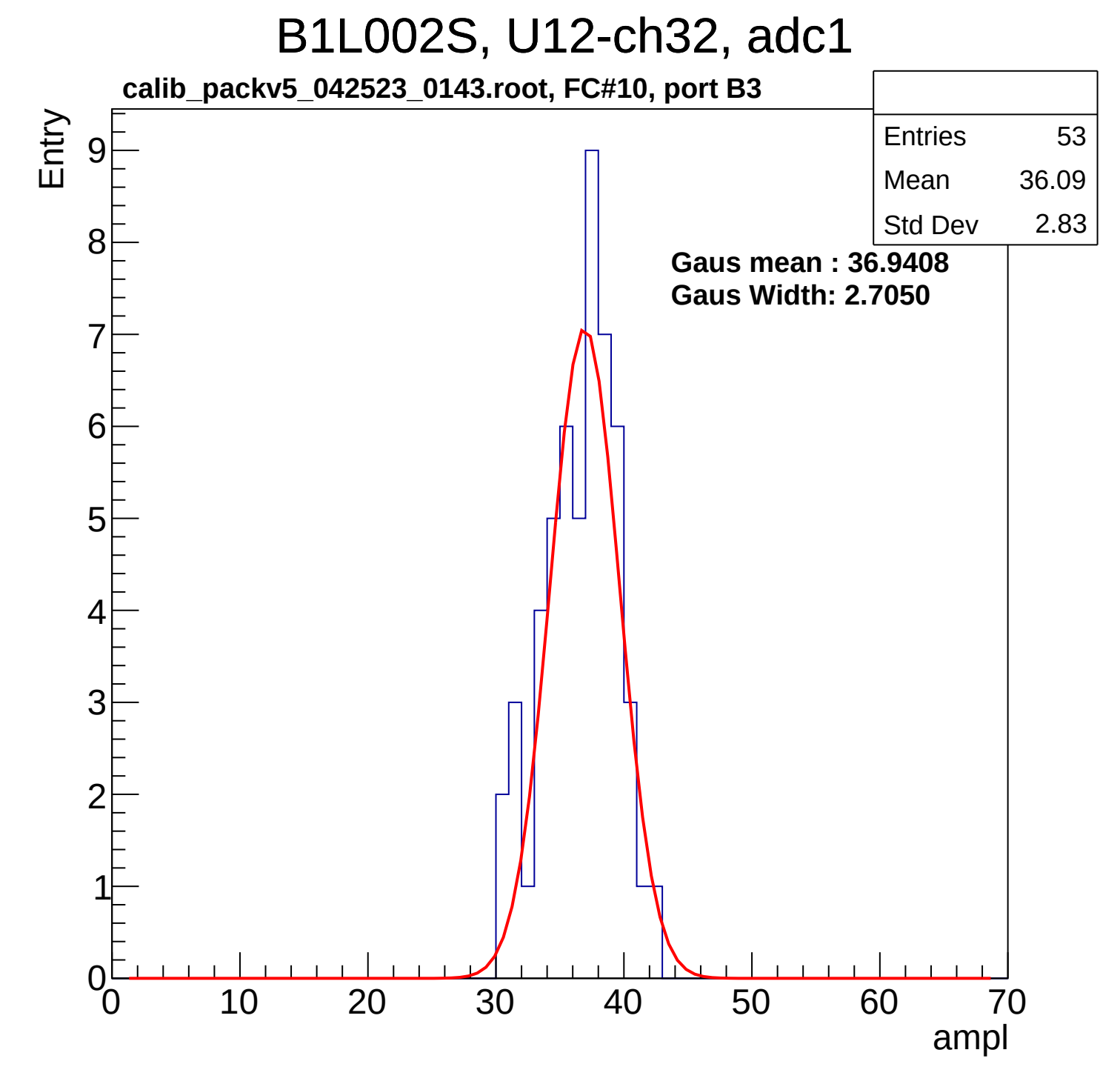
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 29.25
Std Dev 3.162
Gaus mean : 29.7410
Gaus Width: 3.4933
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 36.09
Std Dev 2.83
Gaus mean : 36.9408
Gaus Width: 2.7050
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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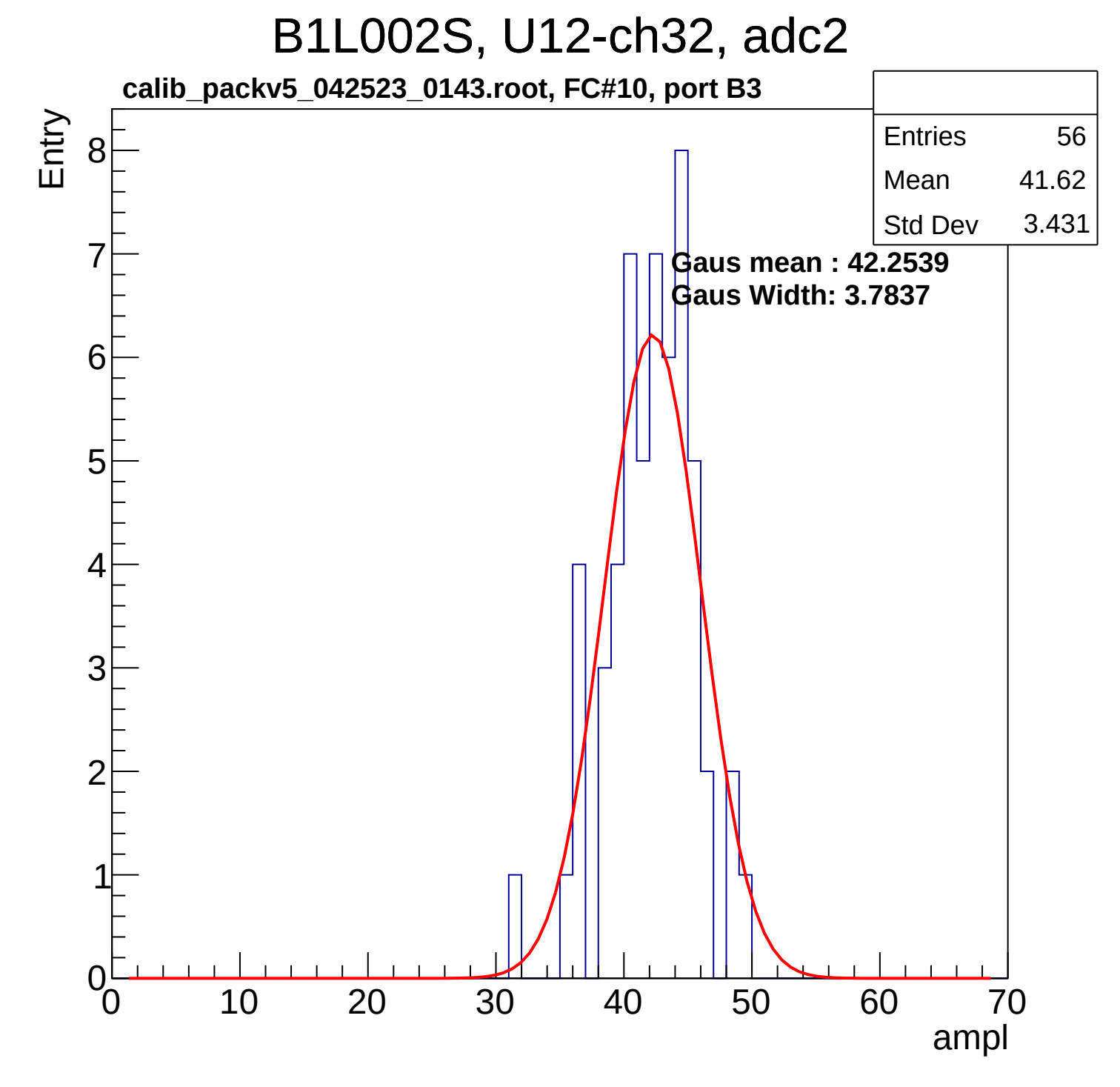
B1L002S, U12-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.62
Std Dev 3.431
Gaus mean : 42.2539
Gaus Width: 3.7837
Entry
ampl
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1
2
3
4
5
6
7
8
0
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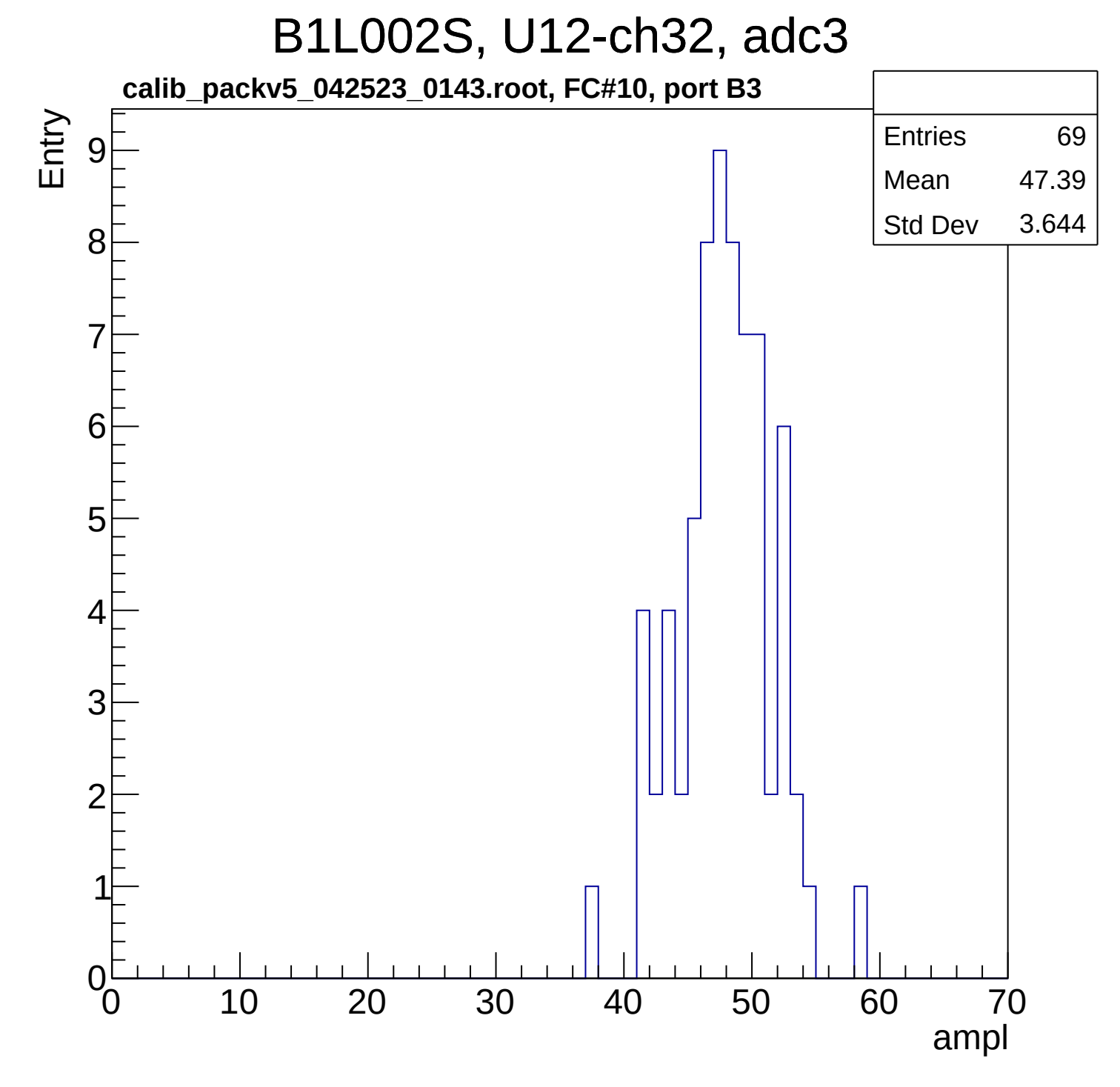
B1L002S, U12-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.39
Std Dev 3.644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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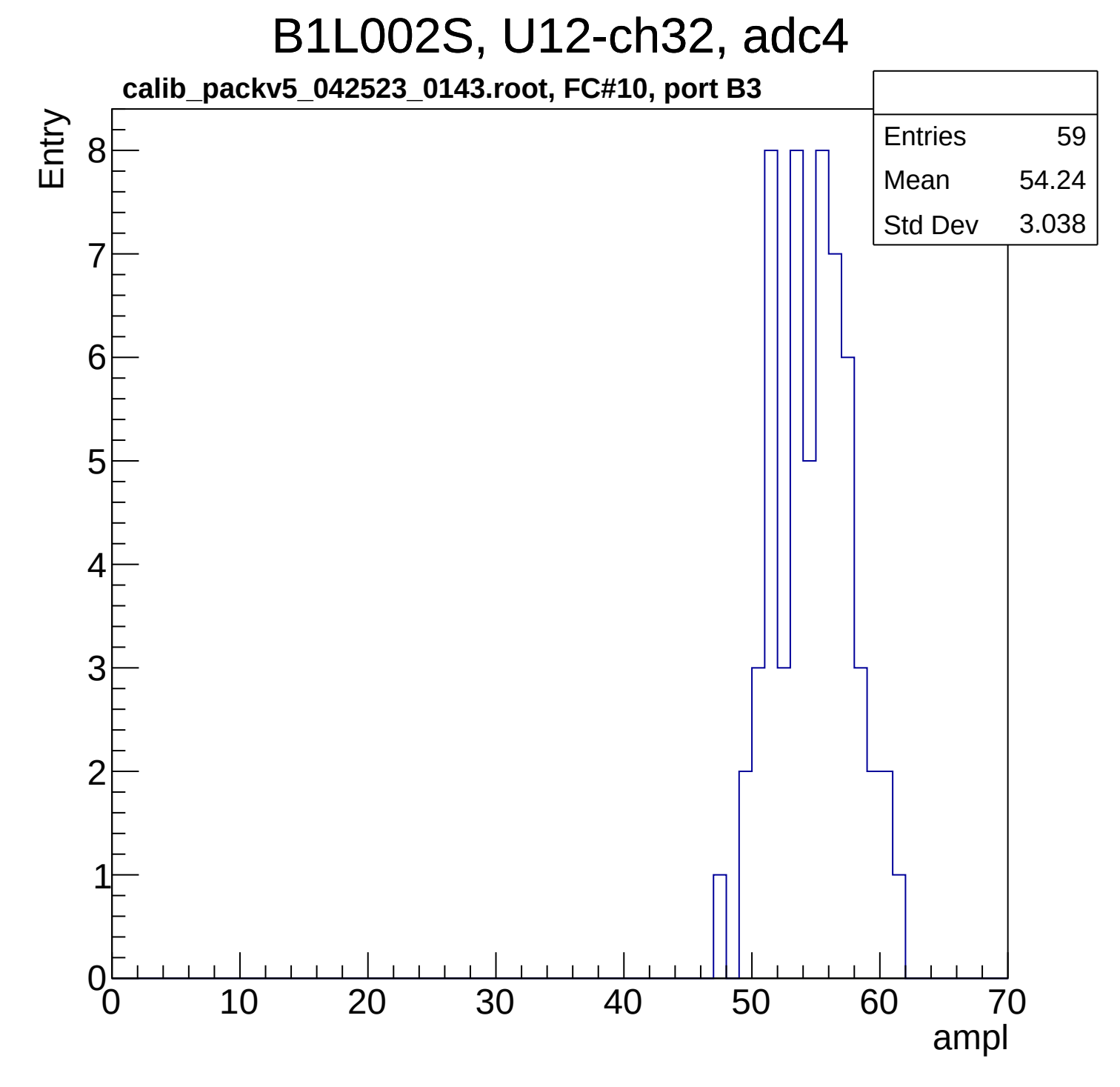
B1L002S, U12-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.24
Std Dev 3.038
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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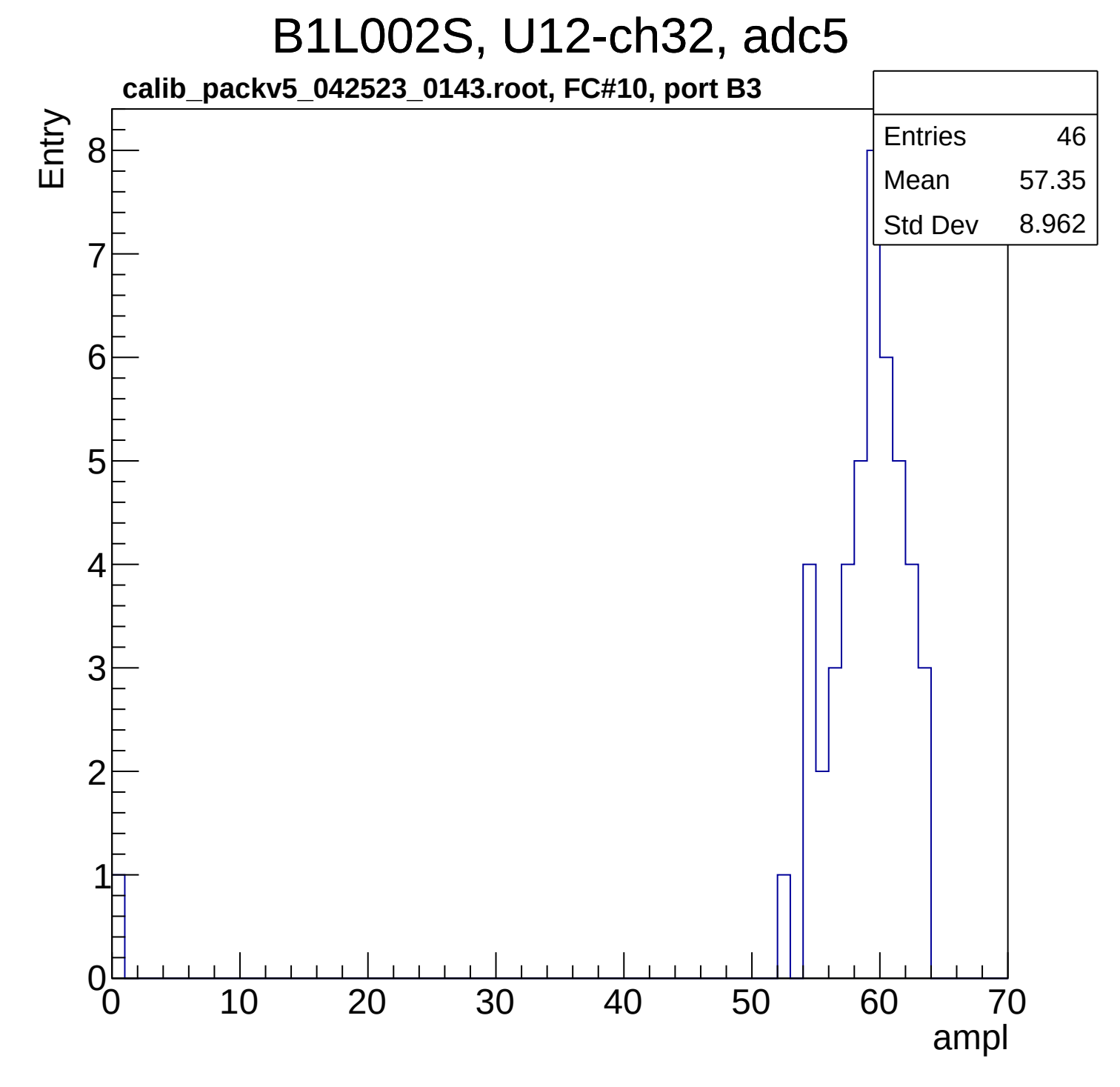
B1L002S, U12-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.35
Std Dev 8.962
Entry
ampl
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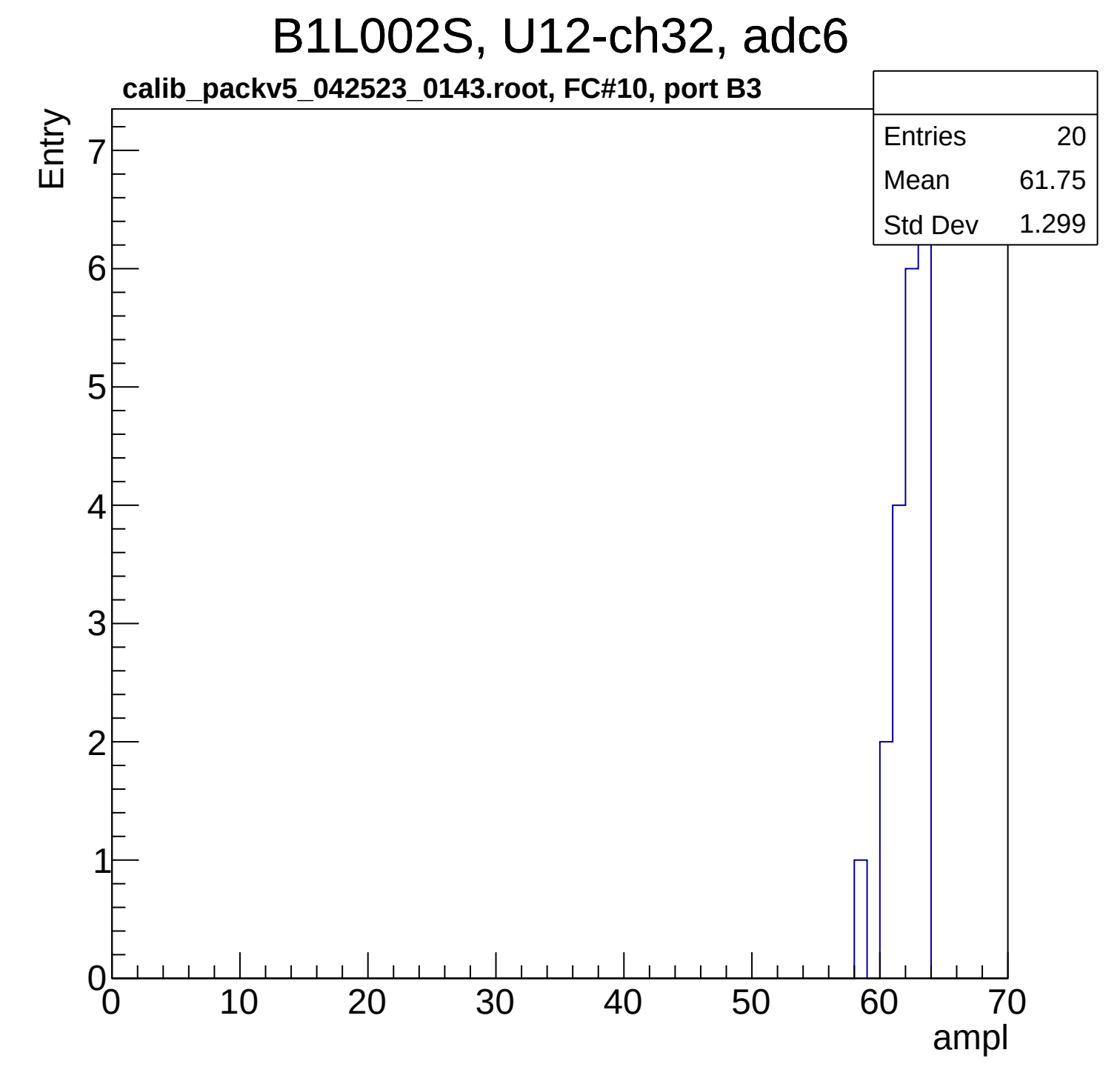
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.75
Std Dev 1.299
Entry
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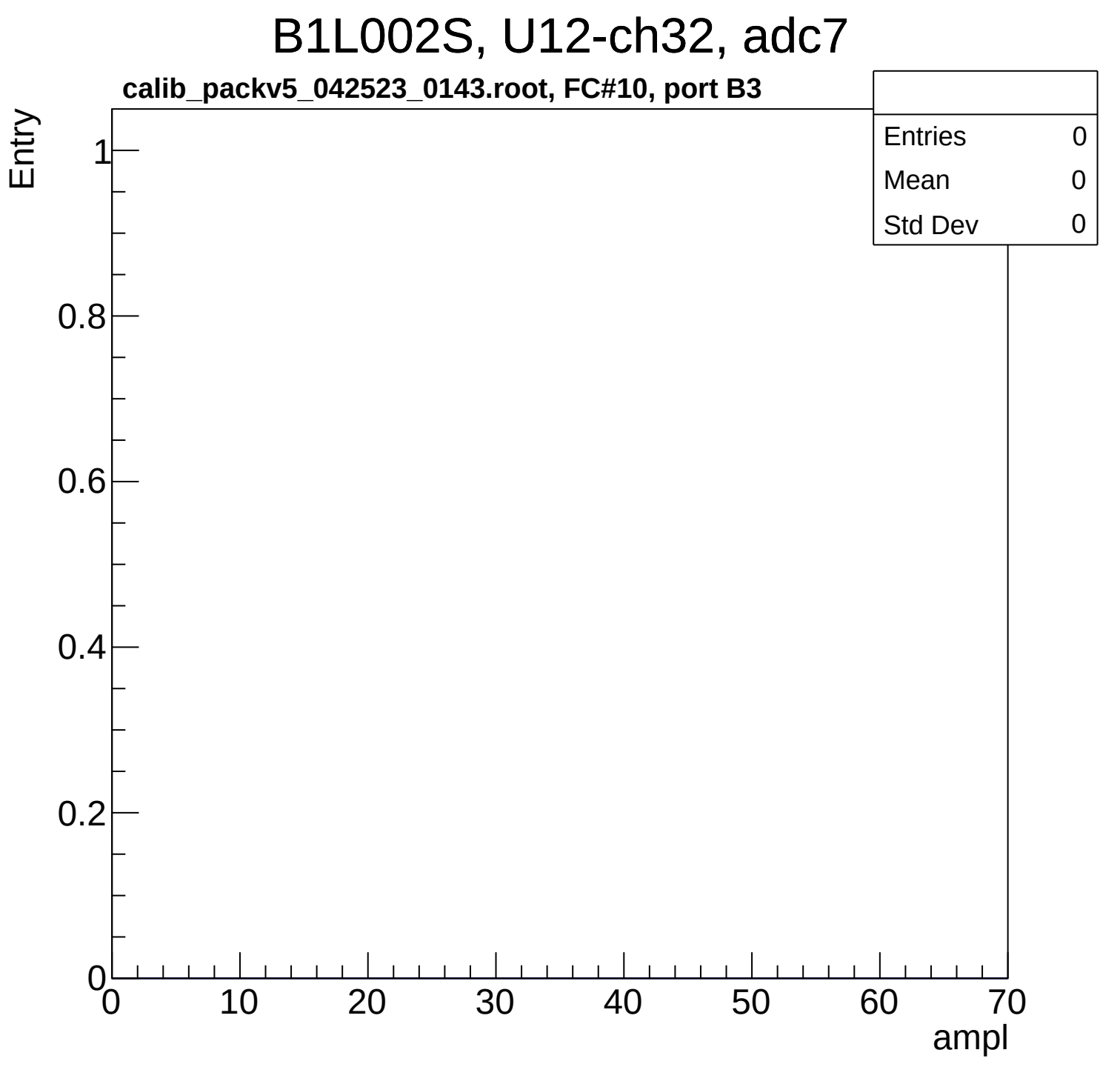
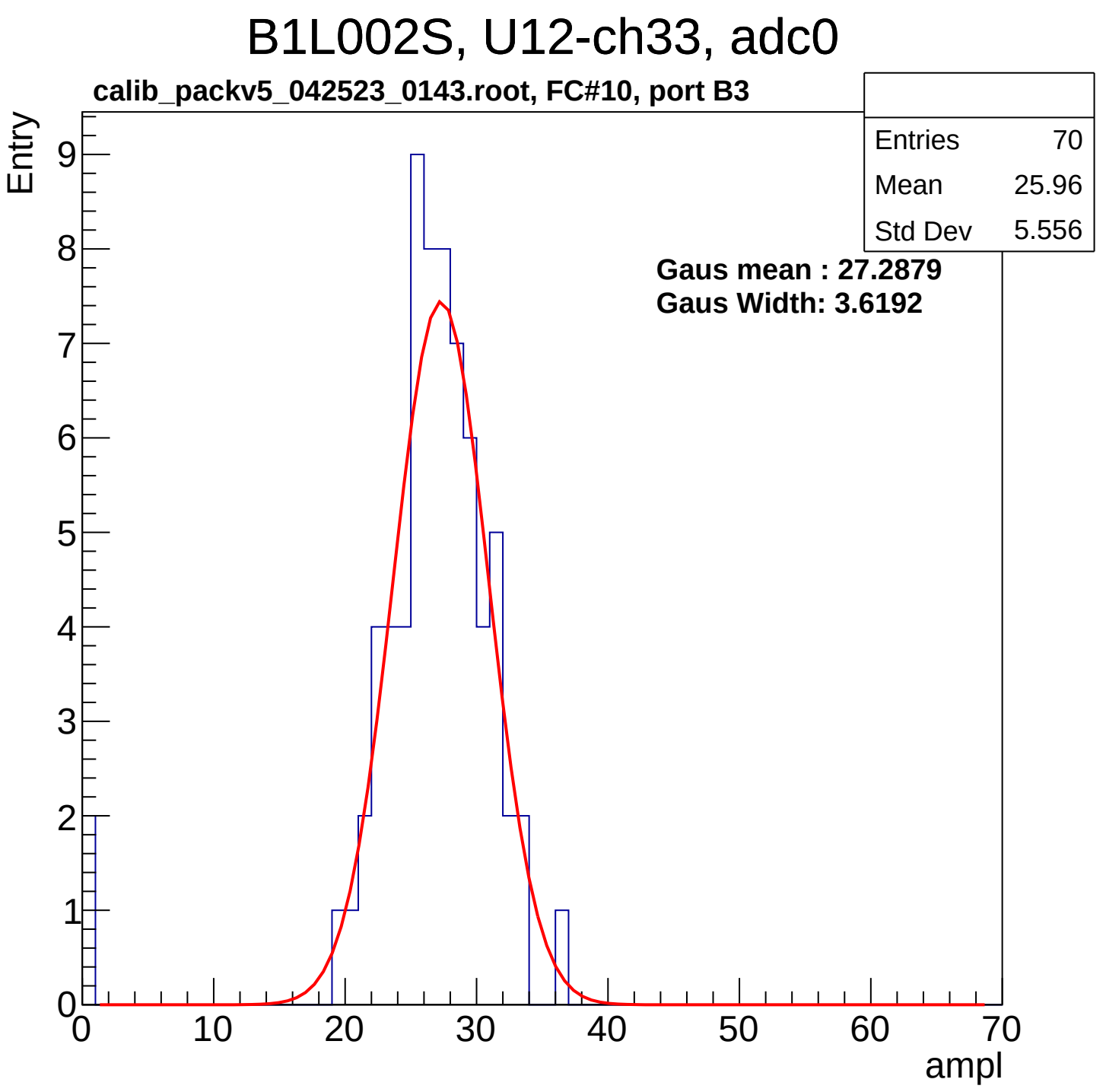
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 25.96
Std Dev 5.556
Gaus mean : 27.2879
Gaus Width: 3.6192
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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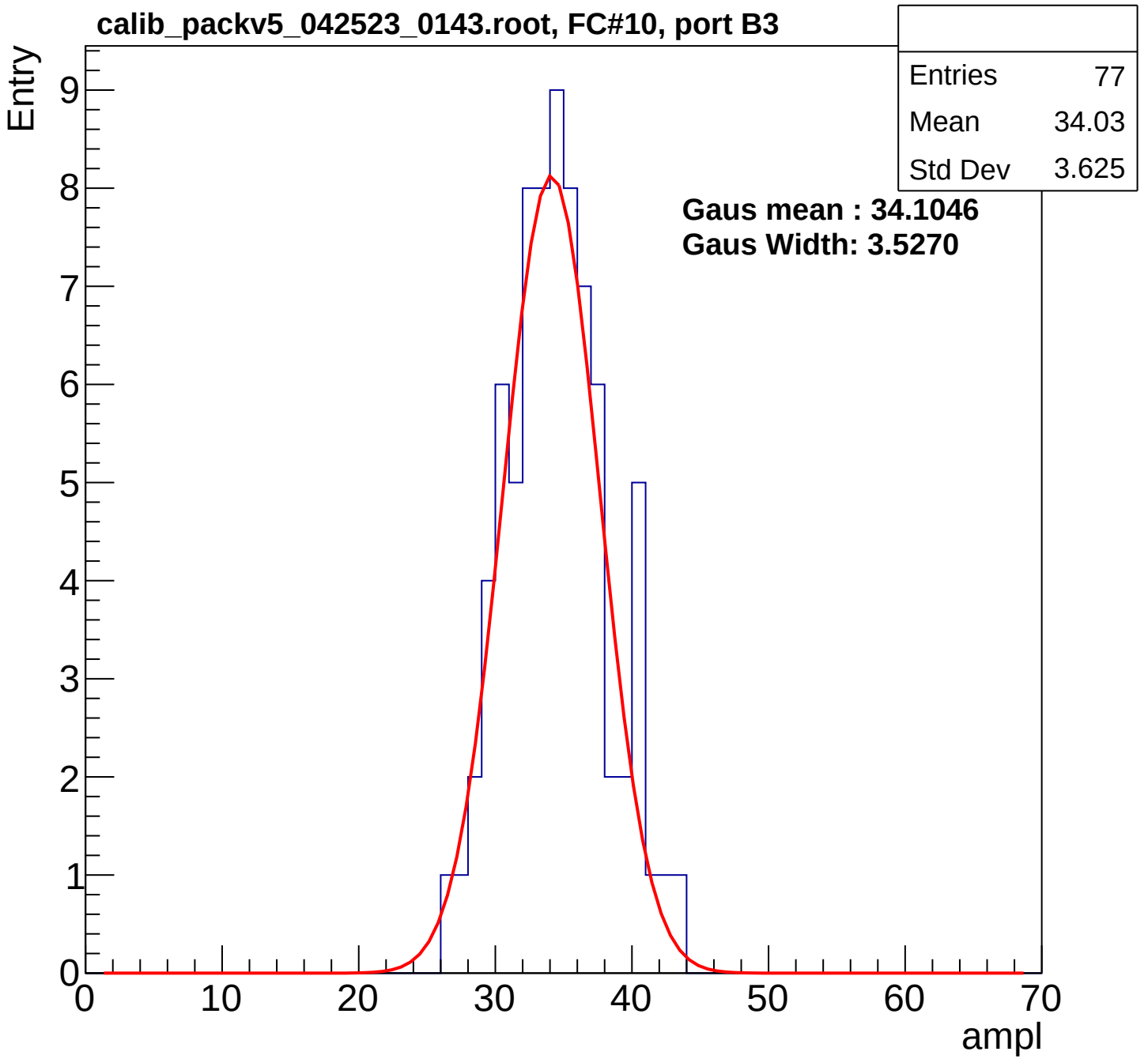
B1L002S, U12-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.03
Std Dev 3.625
Gaus mean : 34.1046
Gaus Width: 3.5270
Entry
ampl
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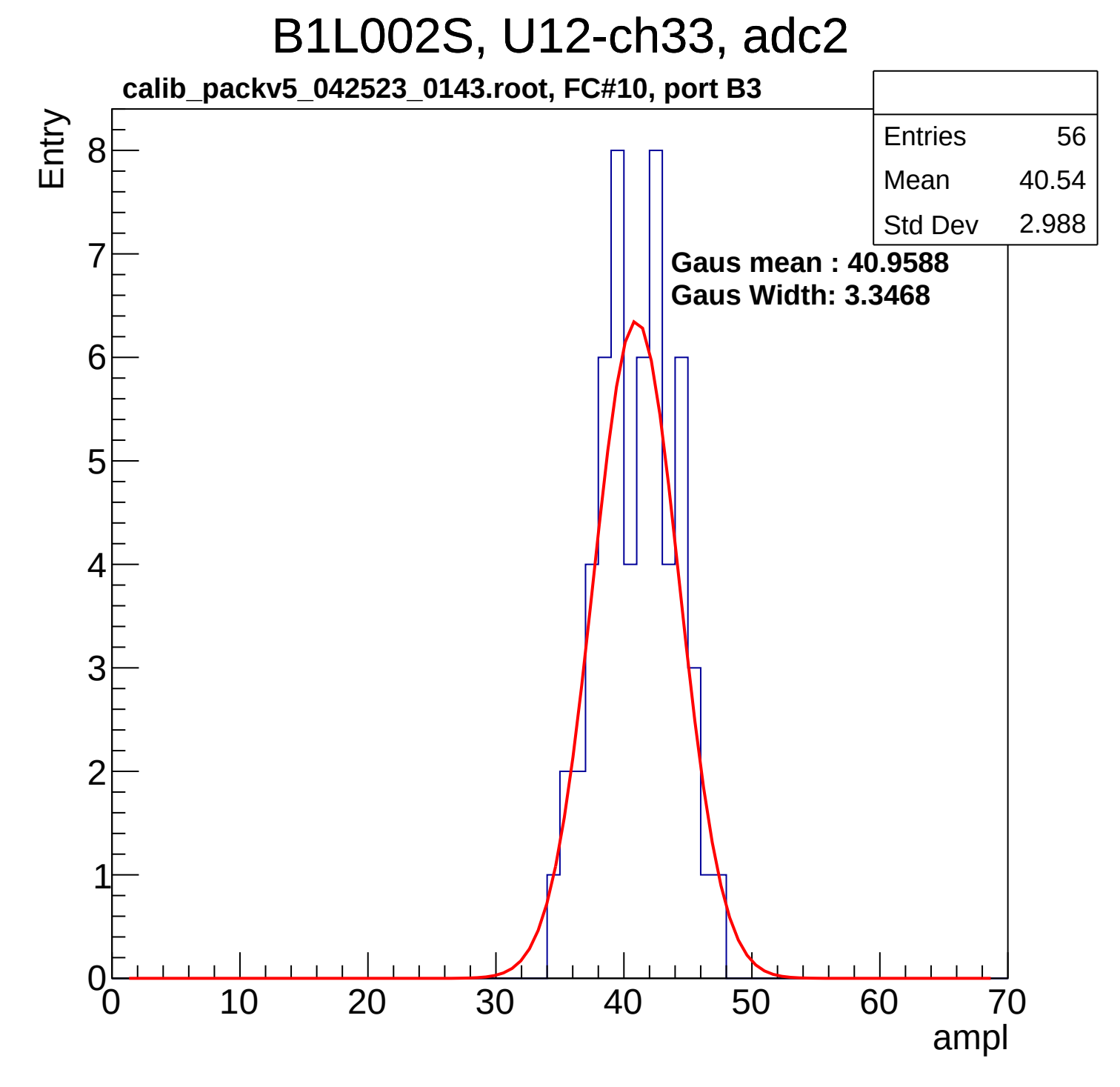
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.54
Std Dev 2.988
Gaus mean : 40.9588
Gaus Width: 3.3468
Entry
ampl
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4
5
6
7
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0
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20
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40
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60
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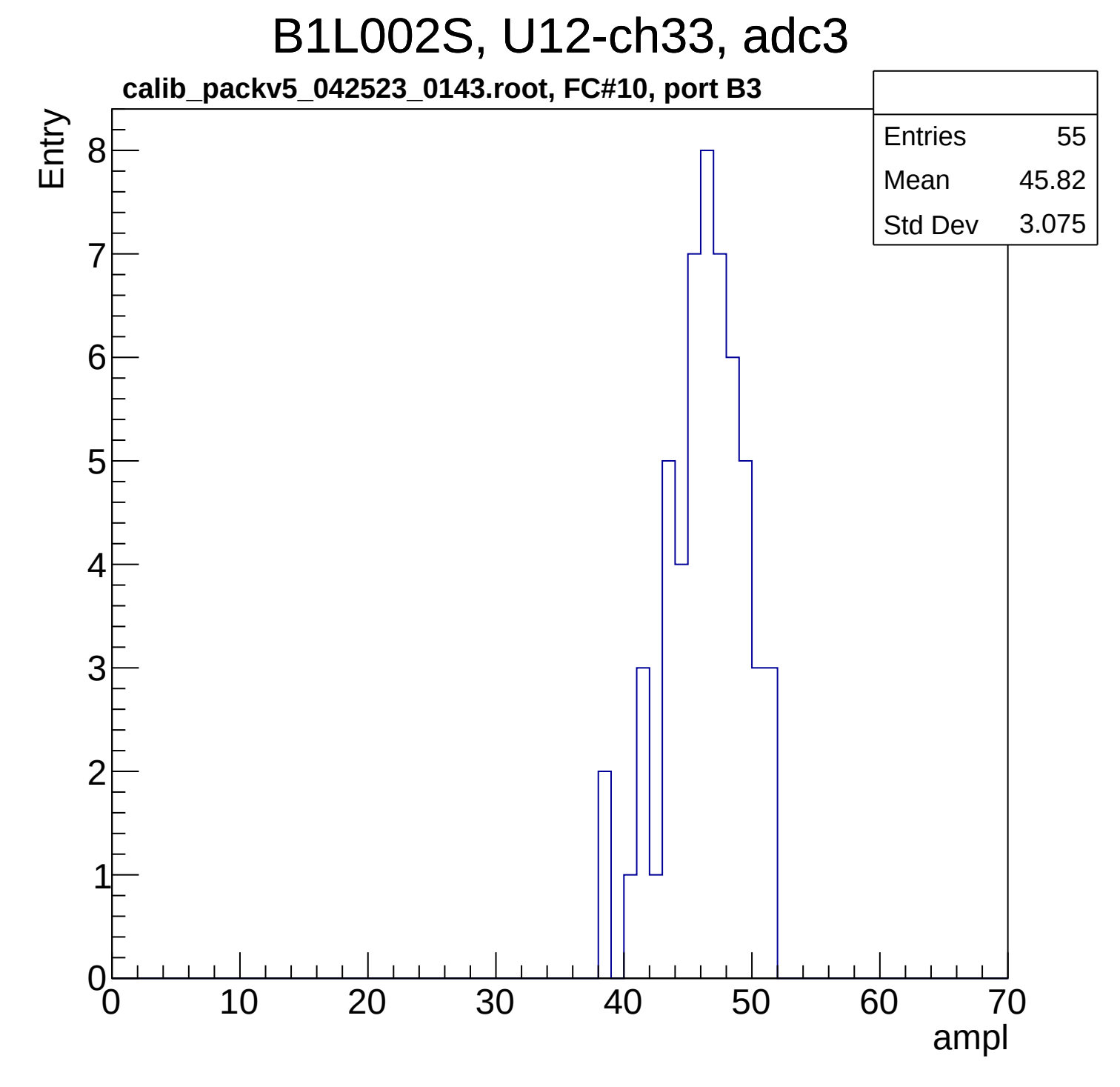
B1L002S, U12-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 45.82
Std Dev 3.075
Entry
ampl
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2
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4
5
6
7
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0
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20
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40
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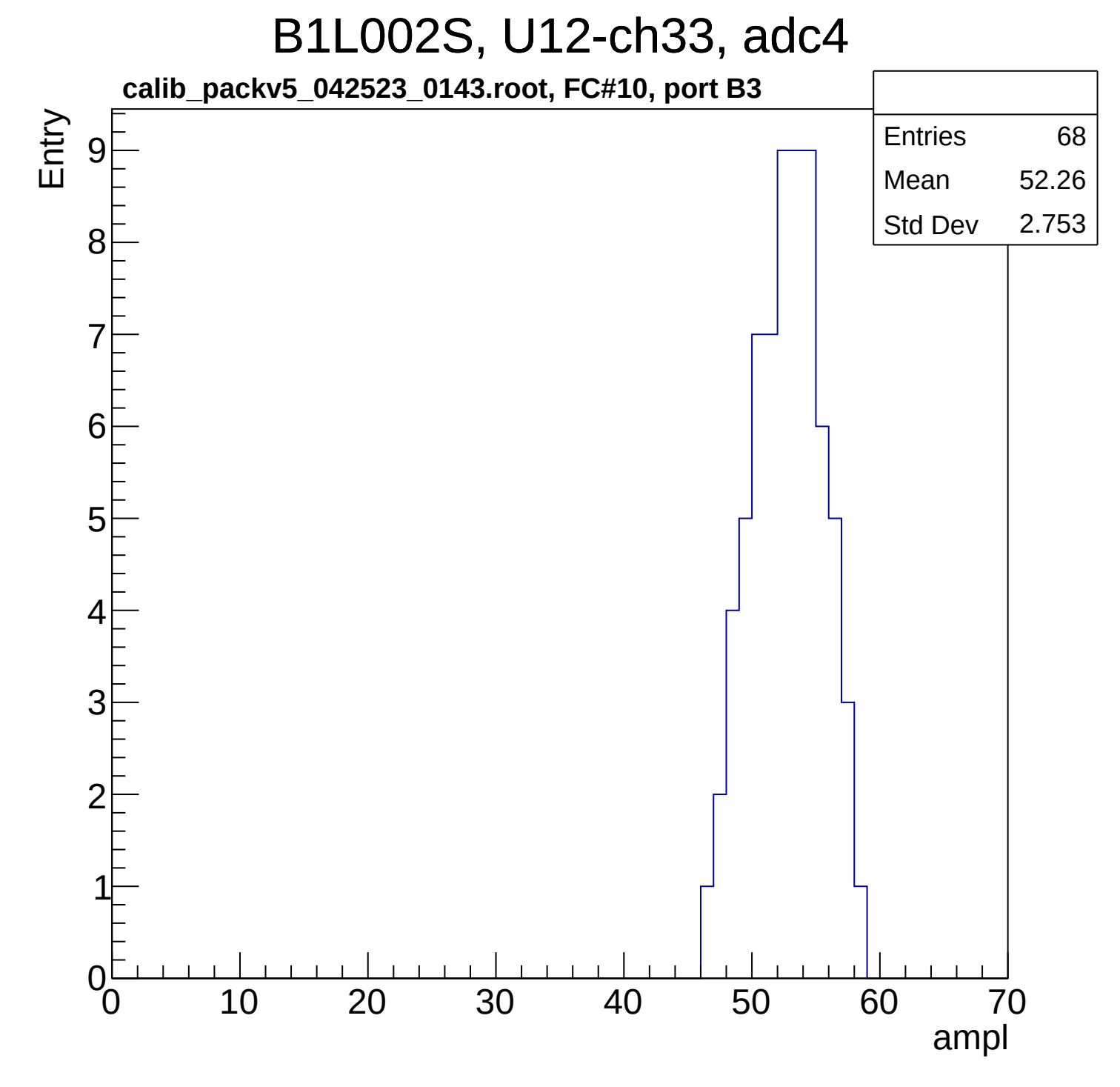
B1L002S, U12-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52.26
Std Dev 2.753
Entry
ampl
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3
4
5
6
7
8
9
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20
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40
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60
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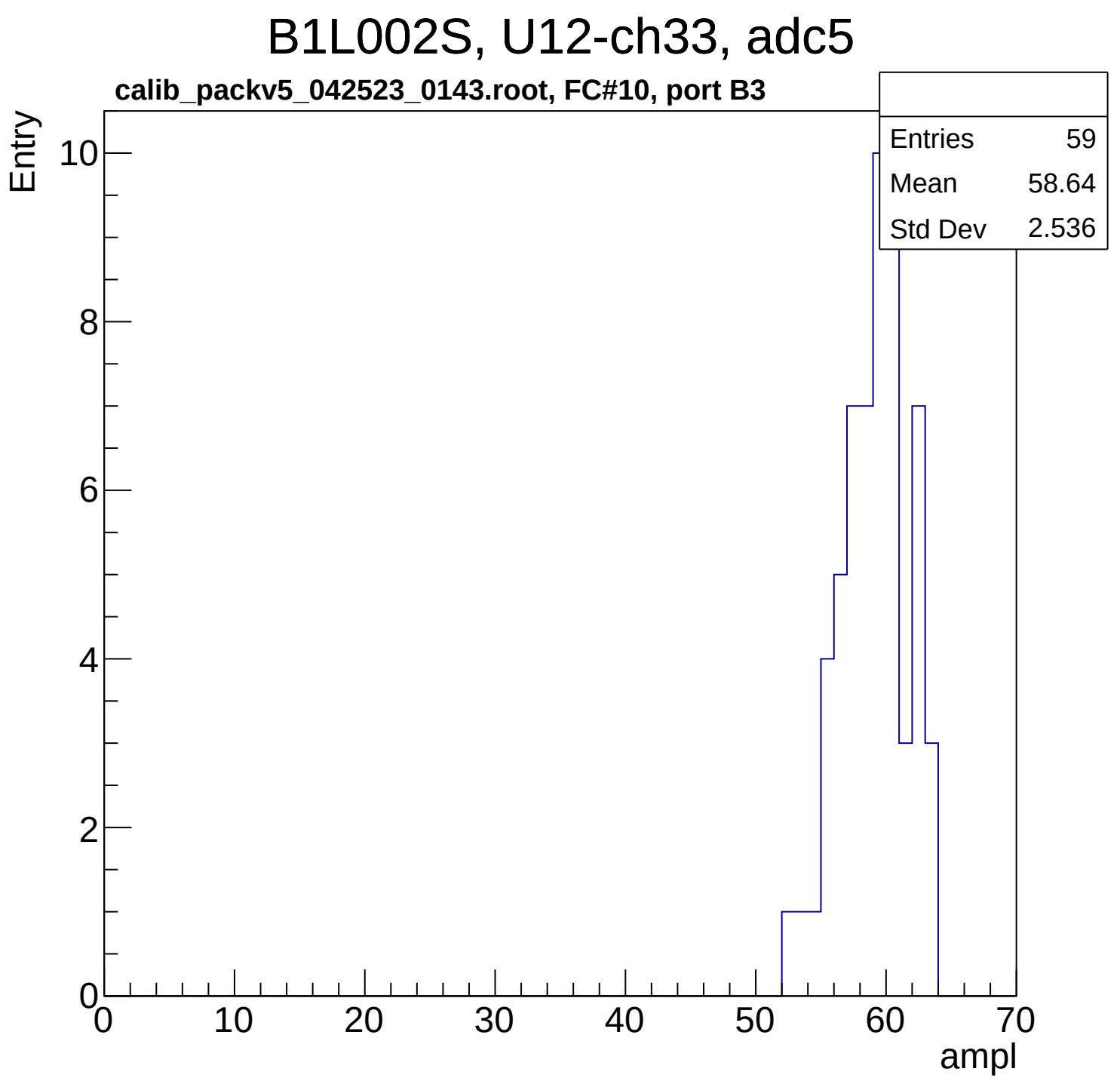
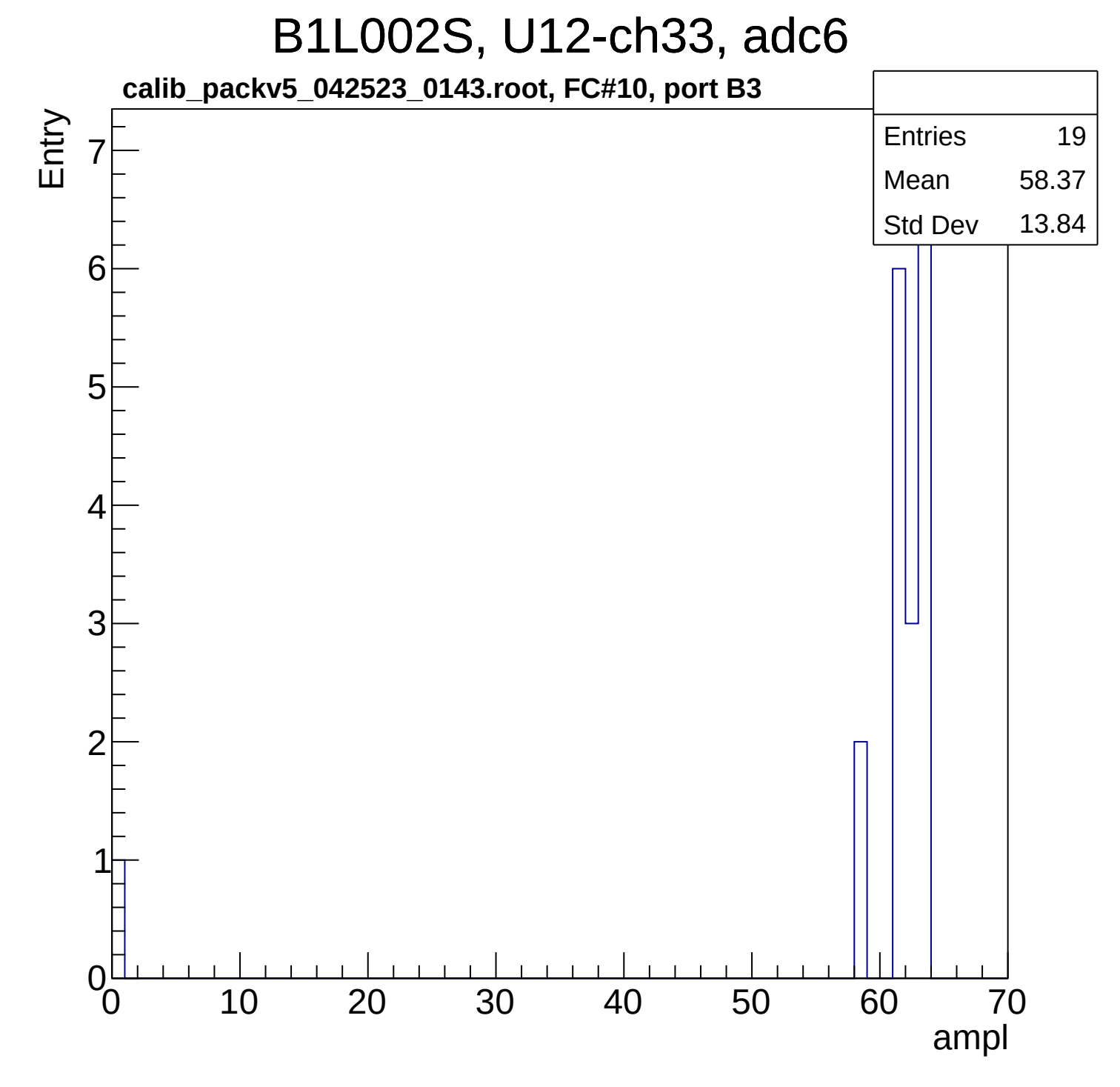

B1L002S, U12-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.64
Std Dev 2.536
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.37
Std Dev 13.84
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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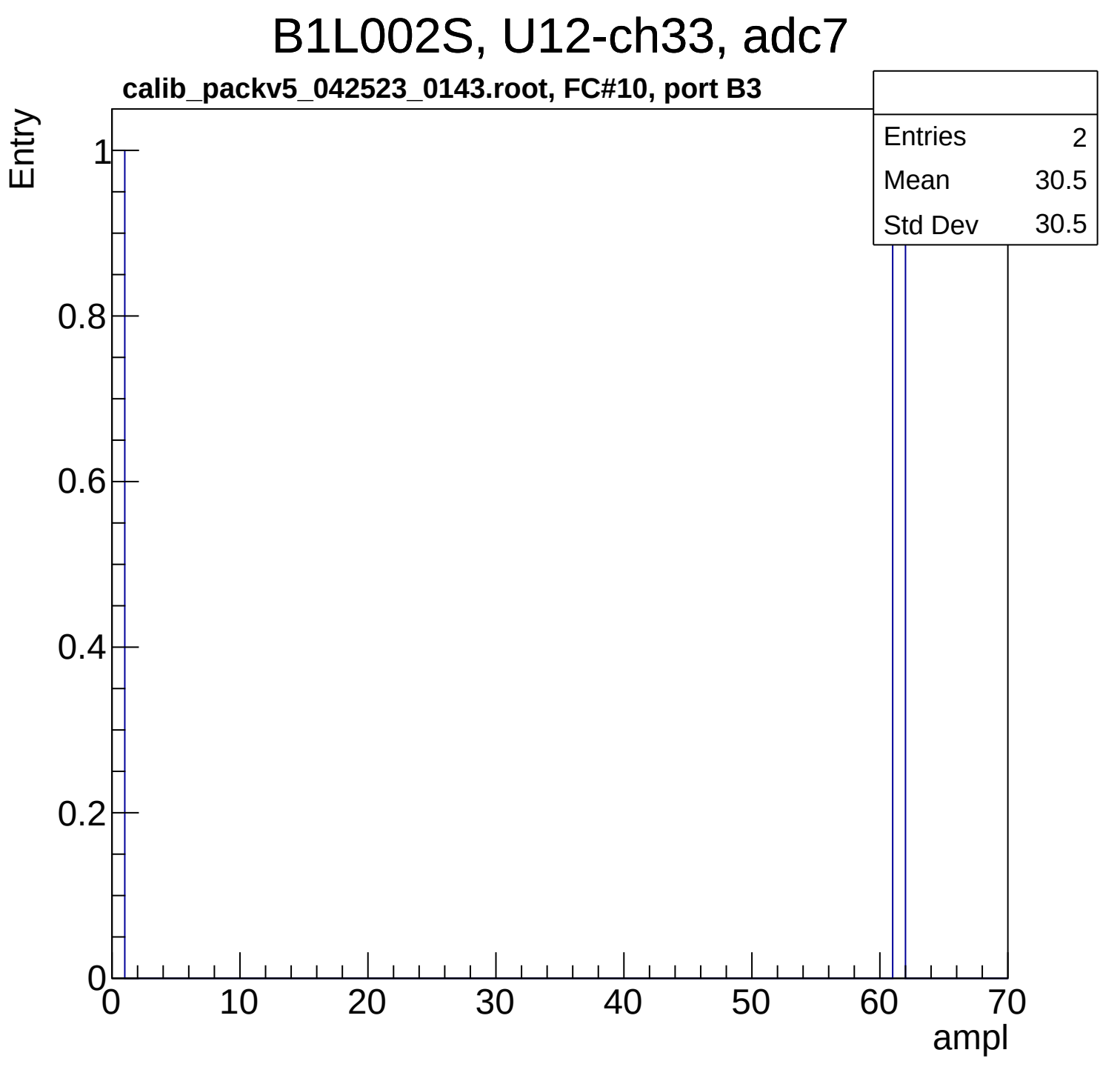
B1L002S, U12-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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0.6
0.4
0.2
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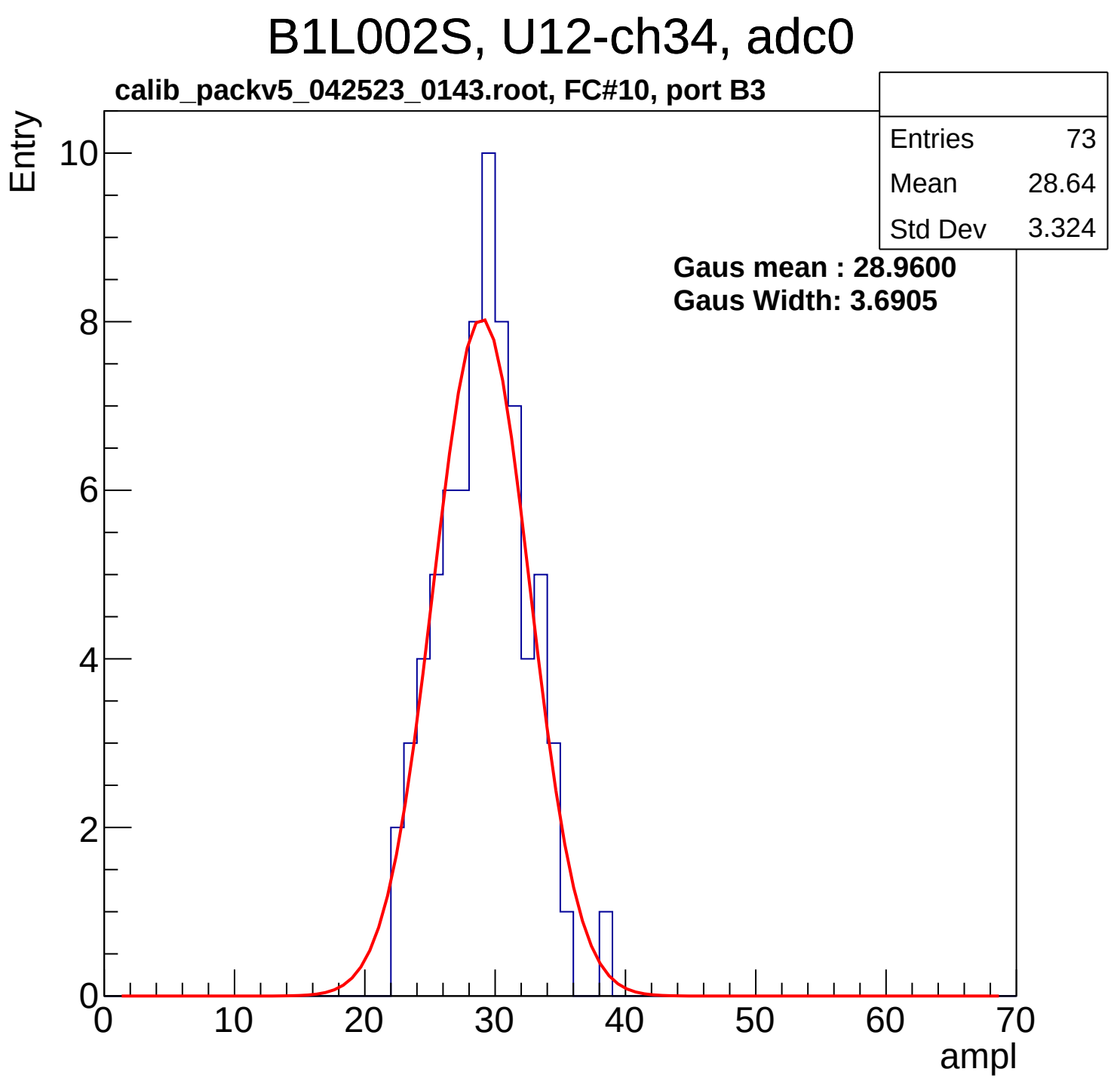
50
60
70
ampl


B1L002S, U12-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.64
Std Dev 3.324
Gaus mean : 28.9600
Gaus Width: 3.6905
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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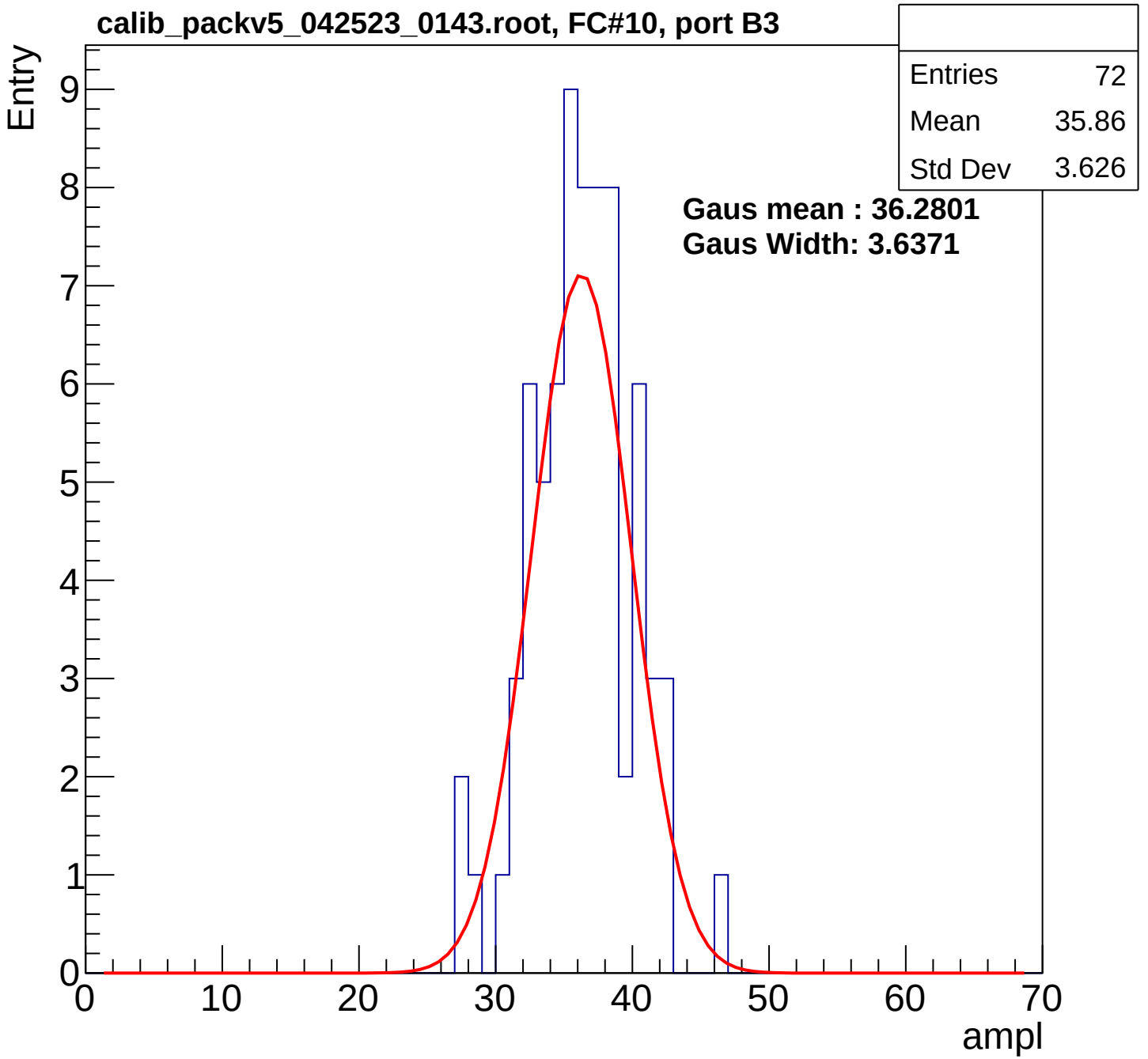
B1L002S, U12-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.86
Std Dev 3.626
Gaus mean : 36.2801
Gaus Width: 3.6371
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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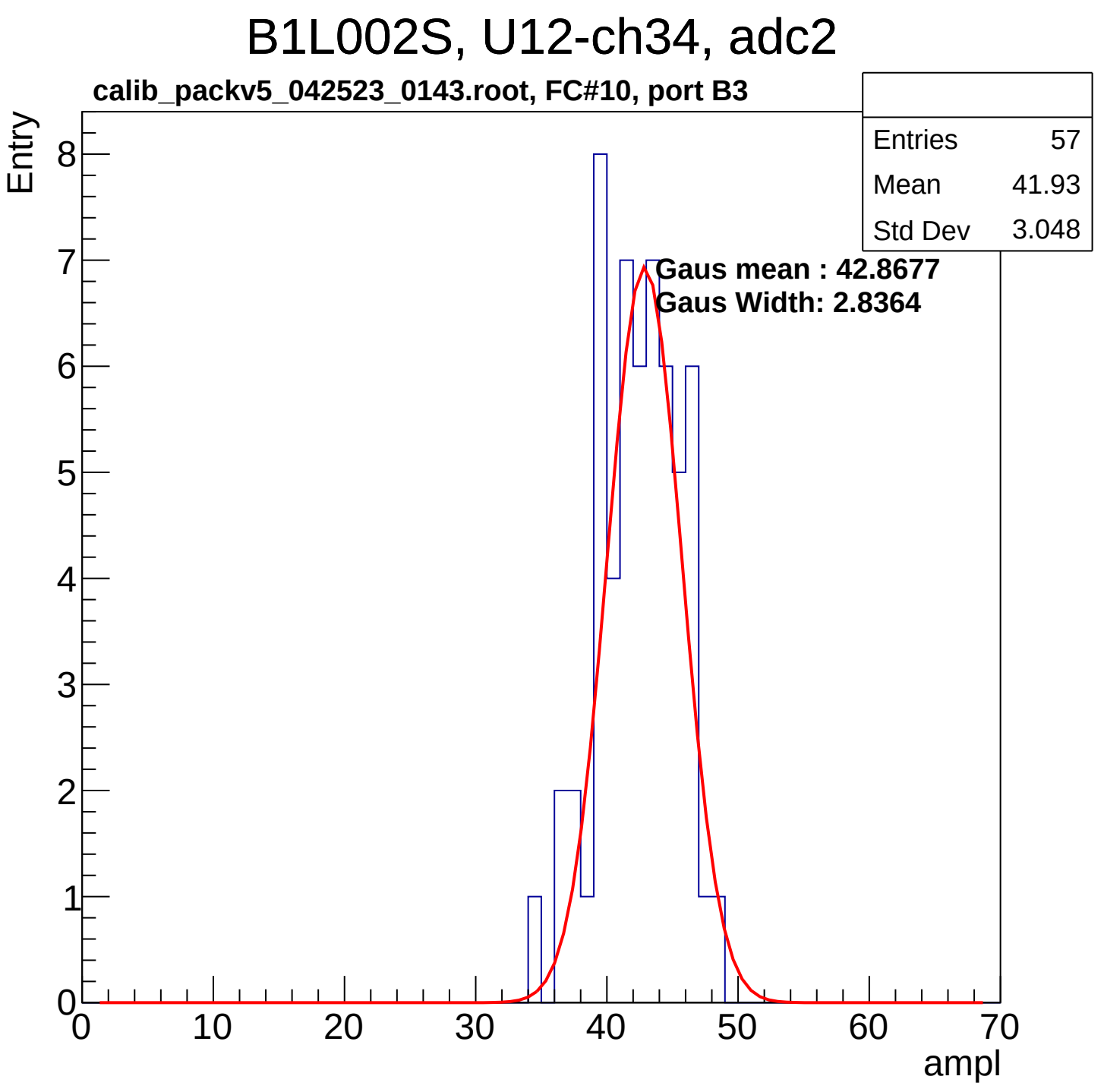
B1L002S, U12-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.93
Std Dev 3.048
Gaus mean : 42.8677
Gaus Width: 2.8364
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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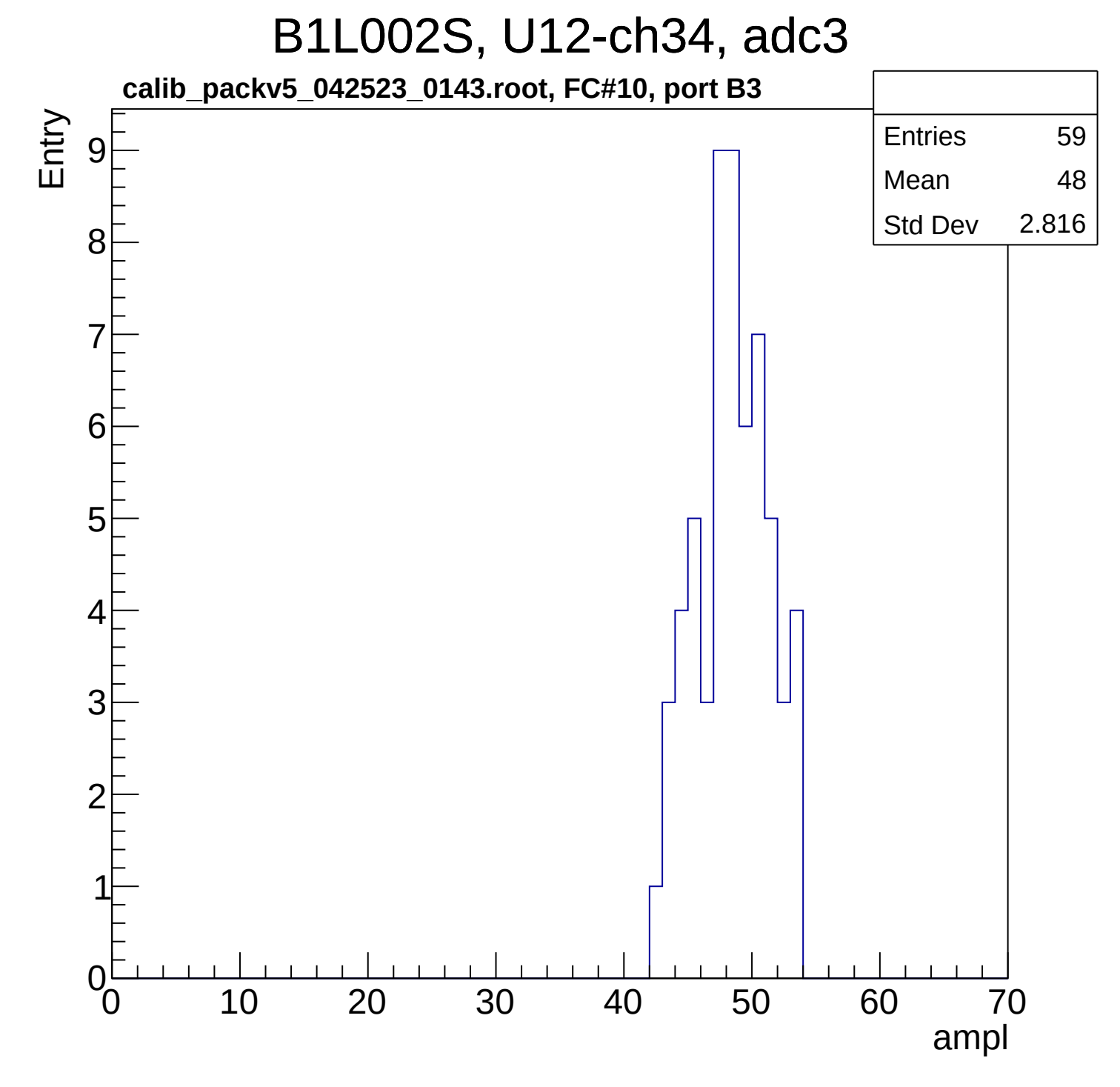
B1L002S, U12-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 48
Std Dev 2.816
Entry
ampl
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3
4
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6
7
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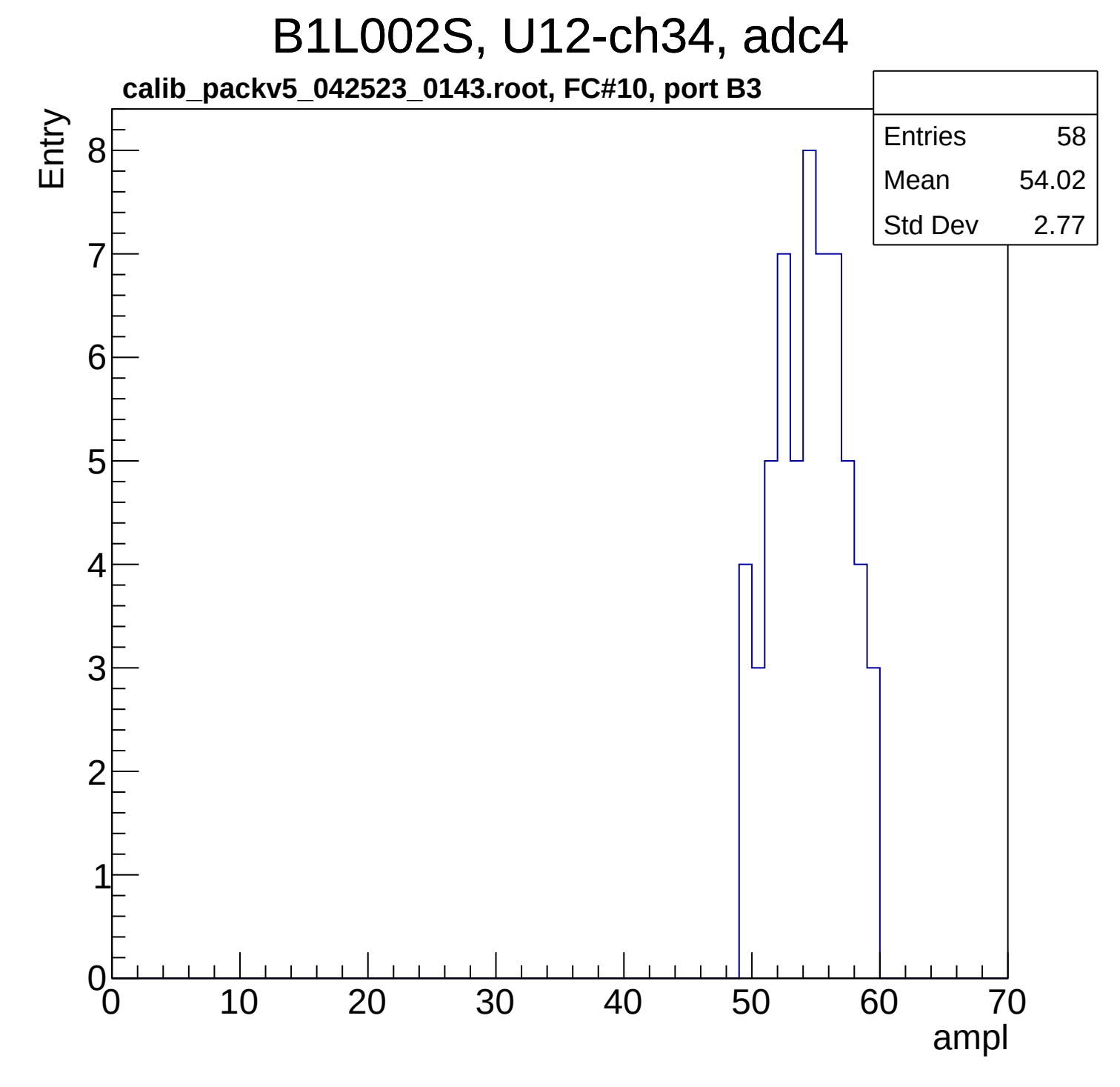
B1L002S, U12-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.02
Std Dev 2.77
Entry
ampl
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2
3
4
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7
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40
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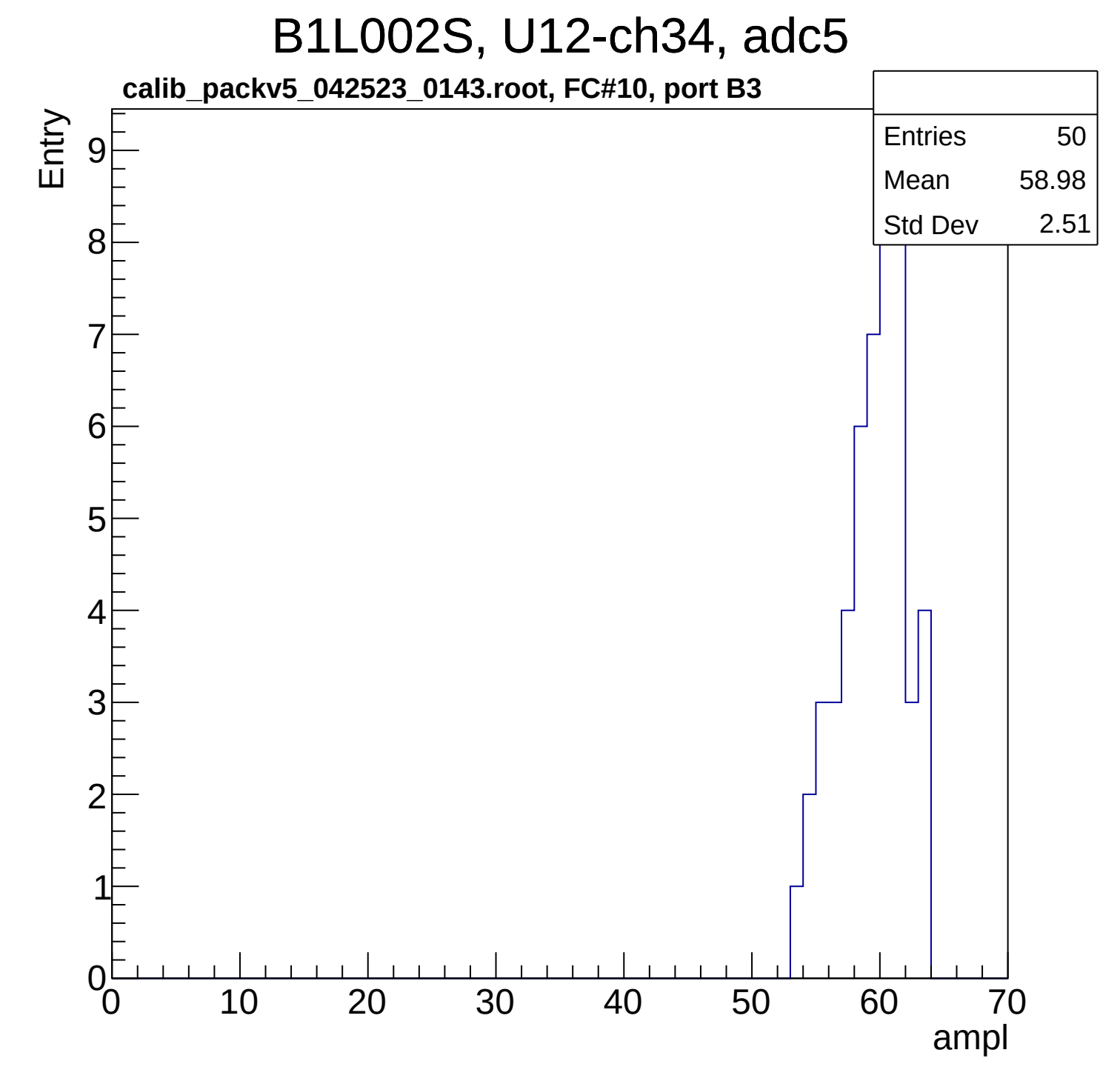
B1L002S, U12-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.98
Std Dev 2.51
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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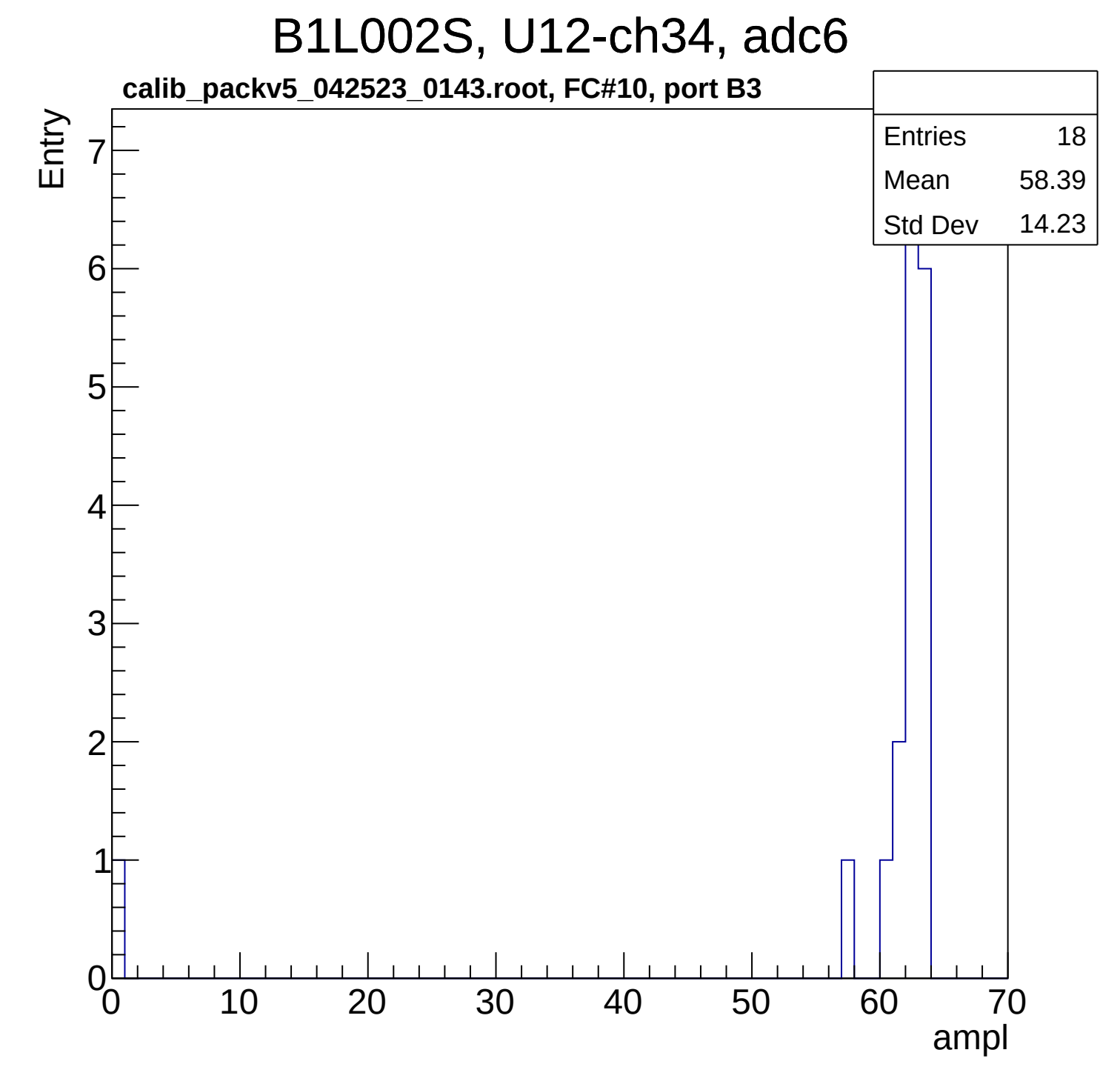
B1L002S, U12-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.39
Std Dev 14.23
Entry
ampl
0
1
2
3
4
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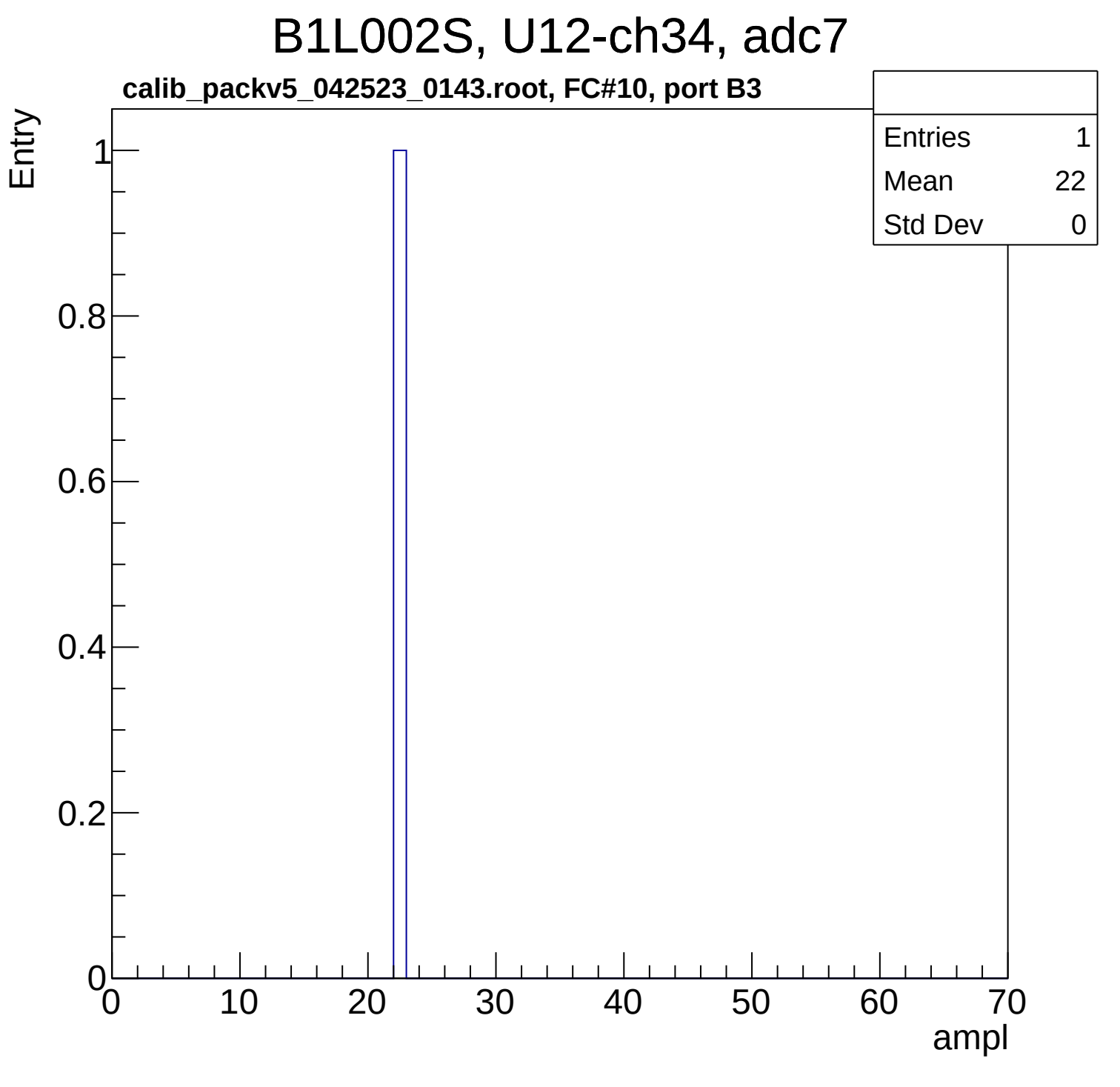
B1L002S, U12-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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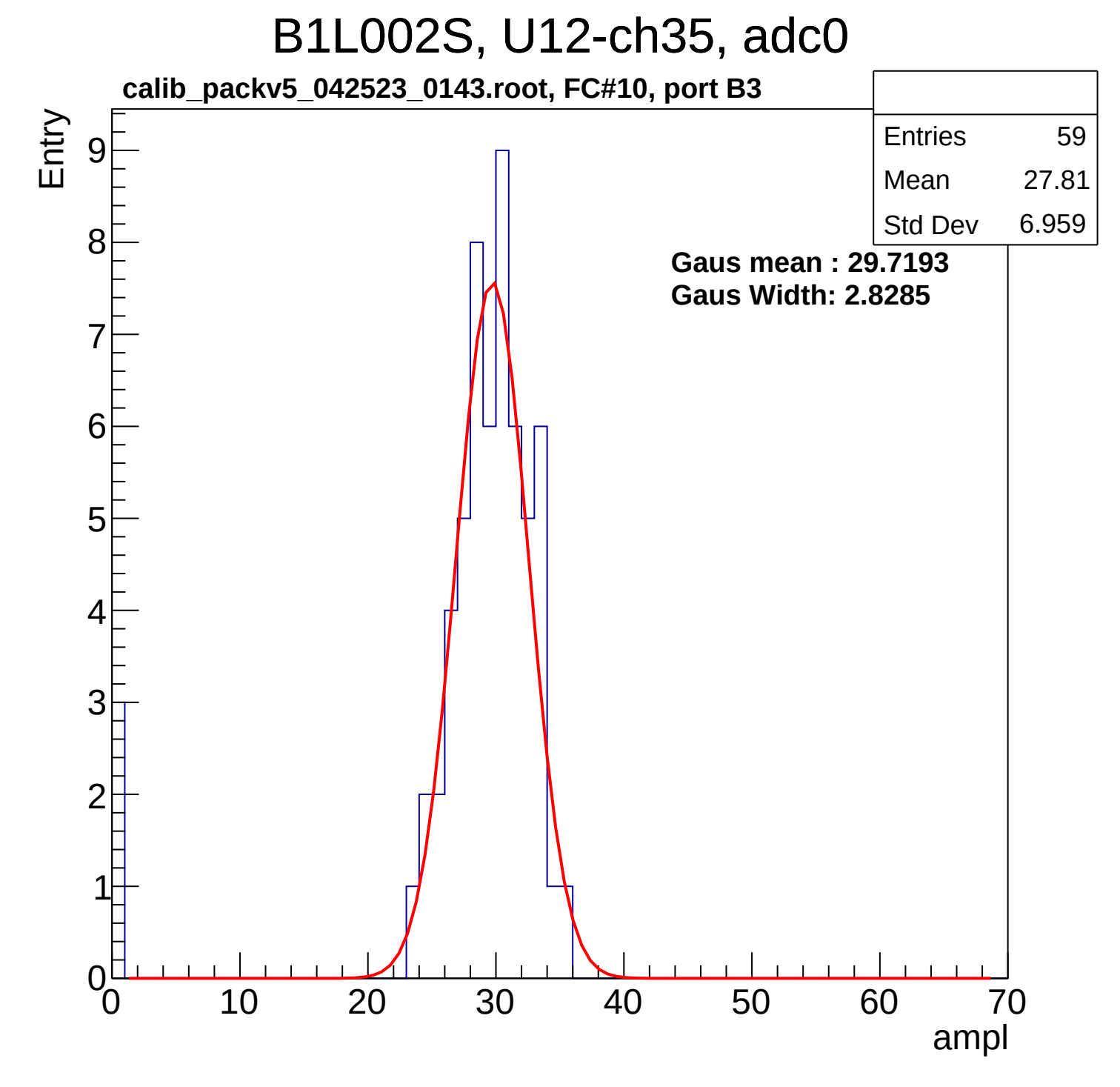
60
70
ampl


B1L002S, U12-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.81
Std Dev 6.959
Gaus mean : 29.7193
Gaus Width: 2.8285
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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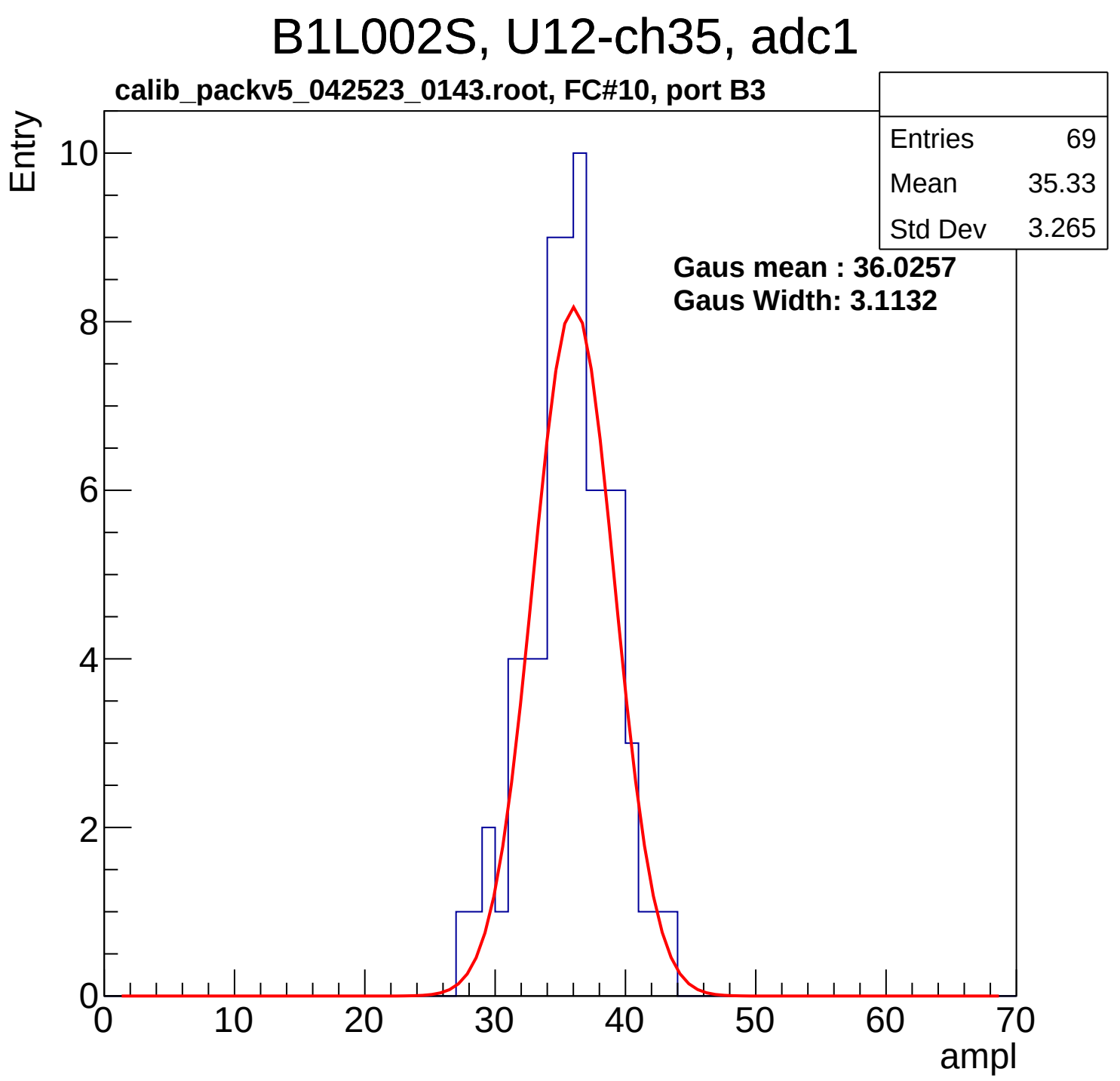
B1L002S, U12-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.33
Std Dev 3.265
Gaus mean : 36.0257
Gaus Width: 3.1132
Entry
ampl
0
2
4
6
8
10
0
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60
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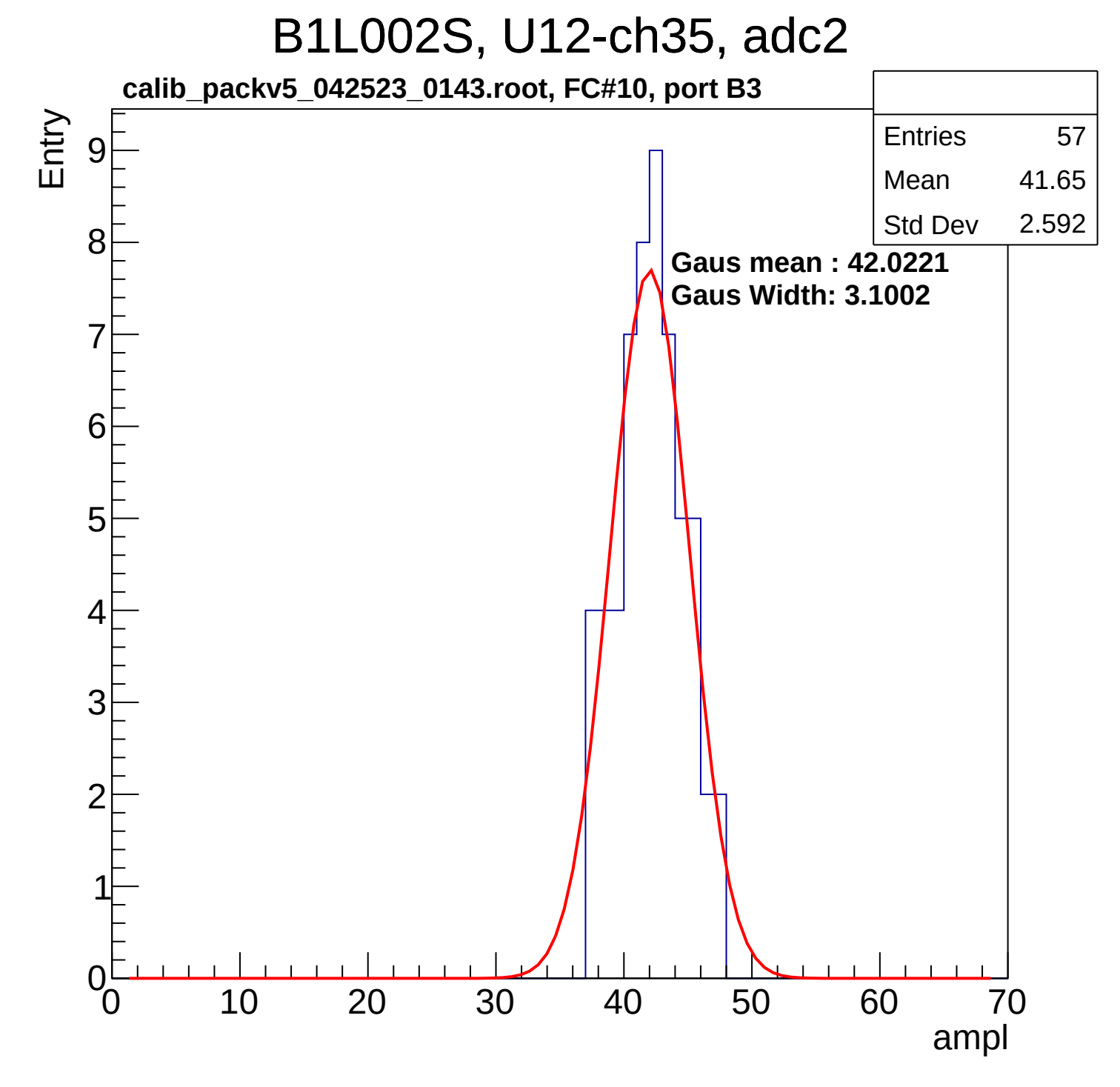
B1L002S, U12-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.65
Std Dev 2.592
Gaus mean : 42.0221
Gaus Width: 3.1002
Entry
ampl
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2
3
4
5
6
7
8
9
0
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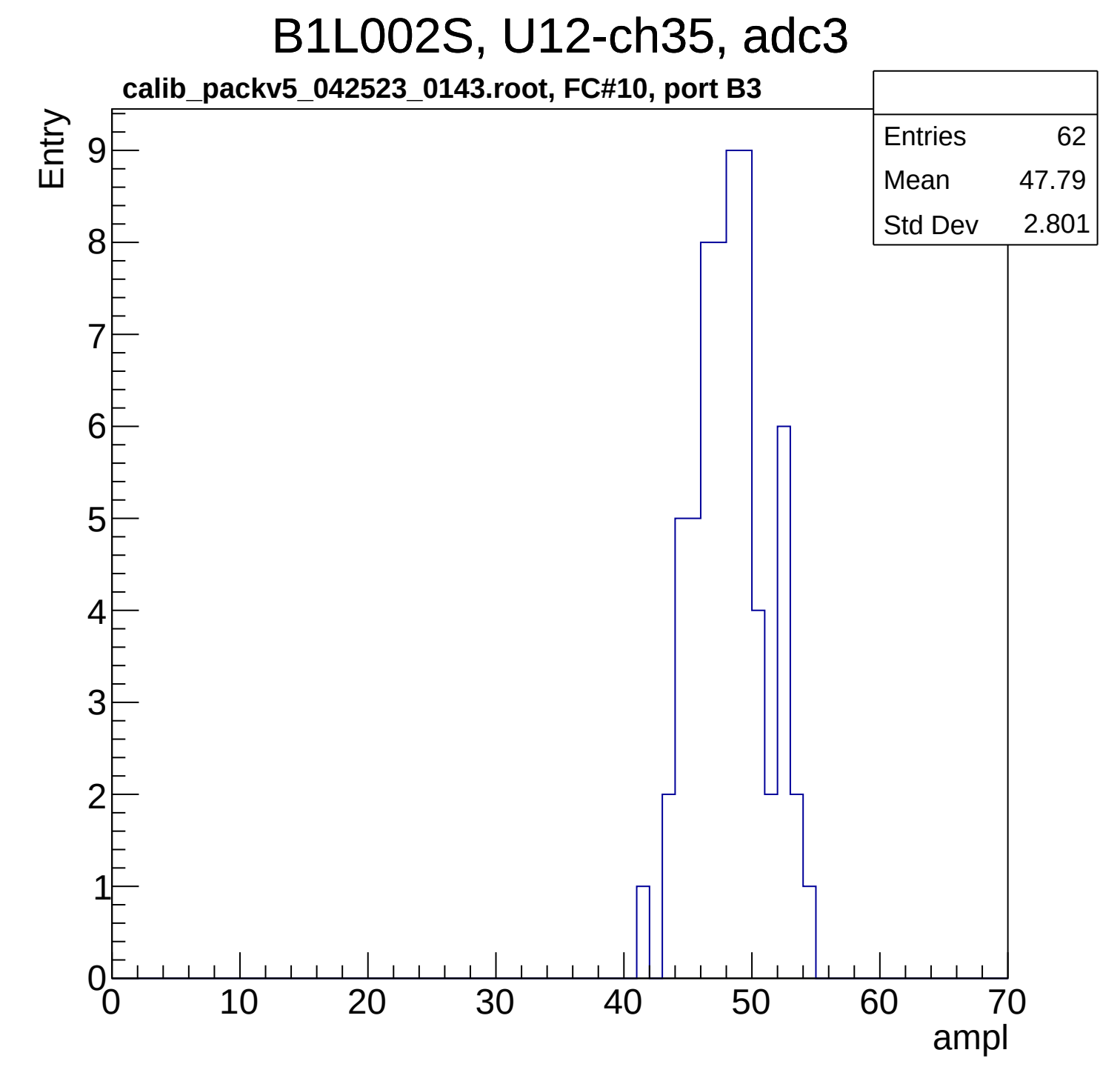
B1L002S, U12-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.79
Std Dev 2.801
Entry
ampl
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2
3
4
5
6
7
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0
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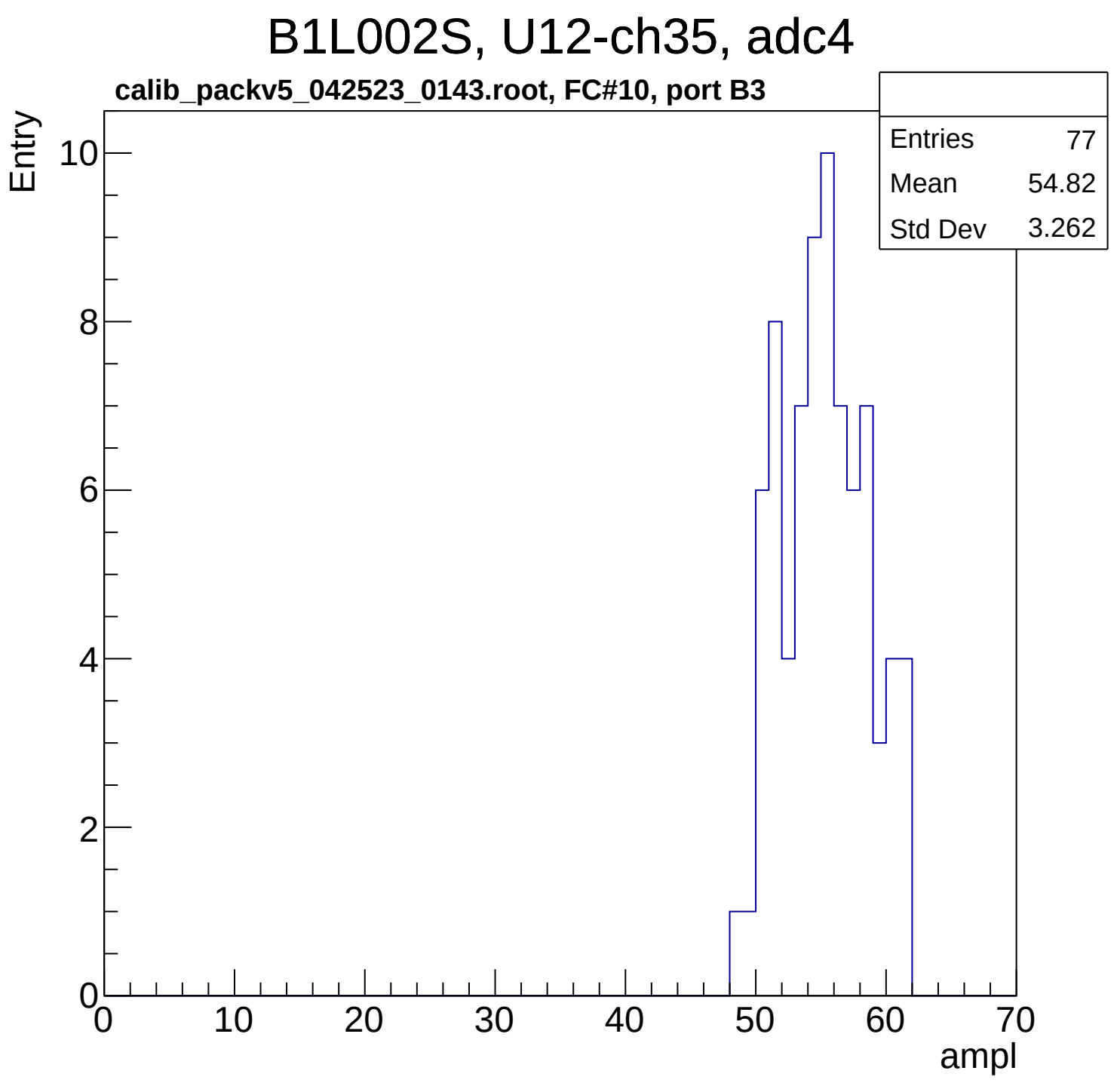
B1L002S, U12-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 54.82
Std Dev 3.262
Entry
0
2
4
6
8
10
0
10
20
30
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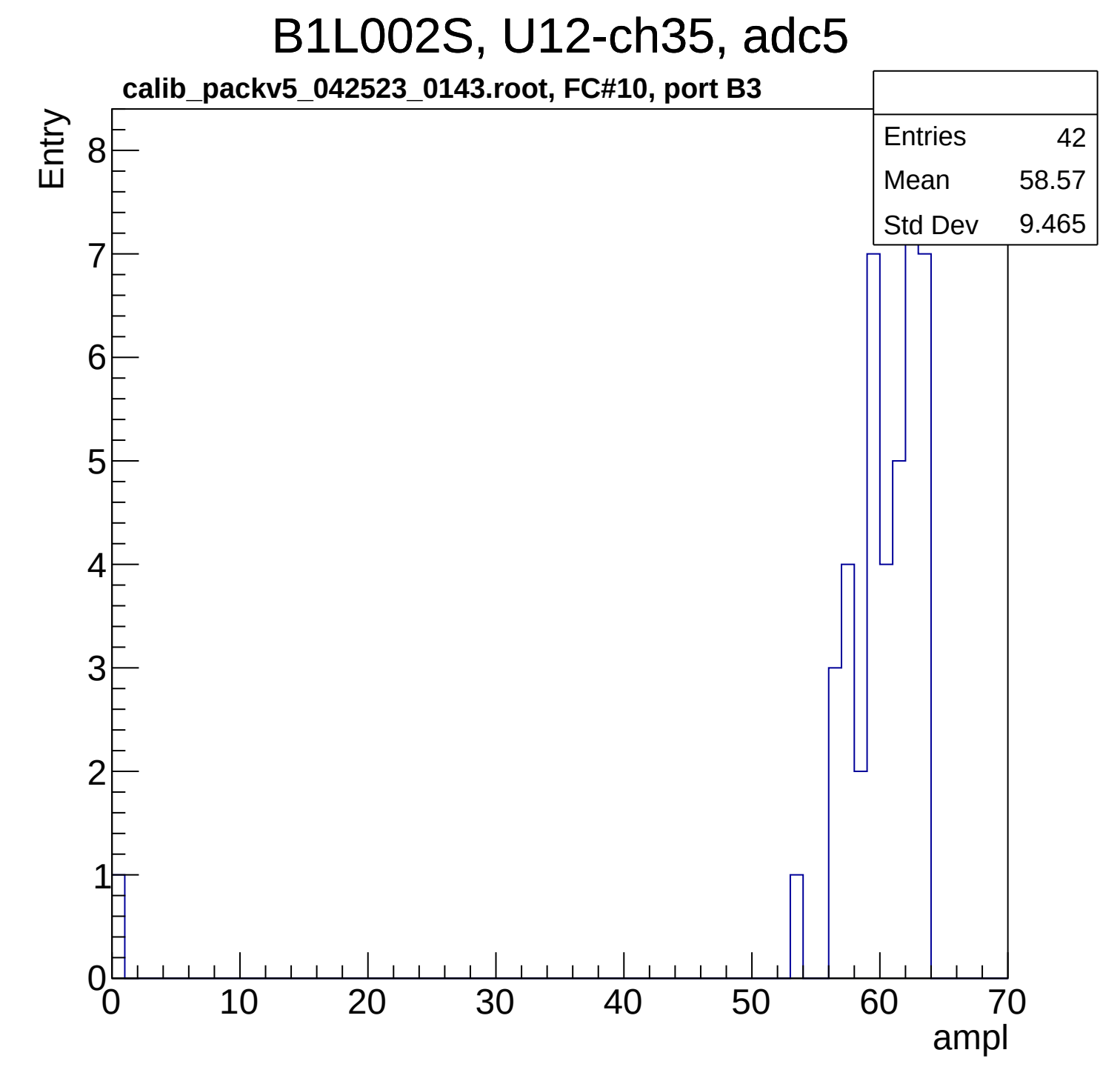
50
60
70
ampl


B1L002S, U12-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.57
Std Dev 9.465
Entry
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1
2
3
4
5
6
7
8
0
10
20
30
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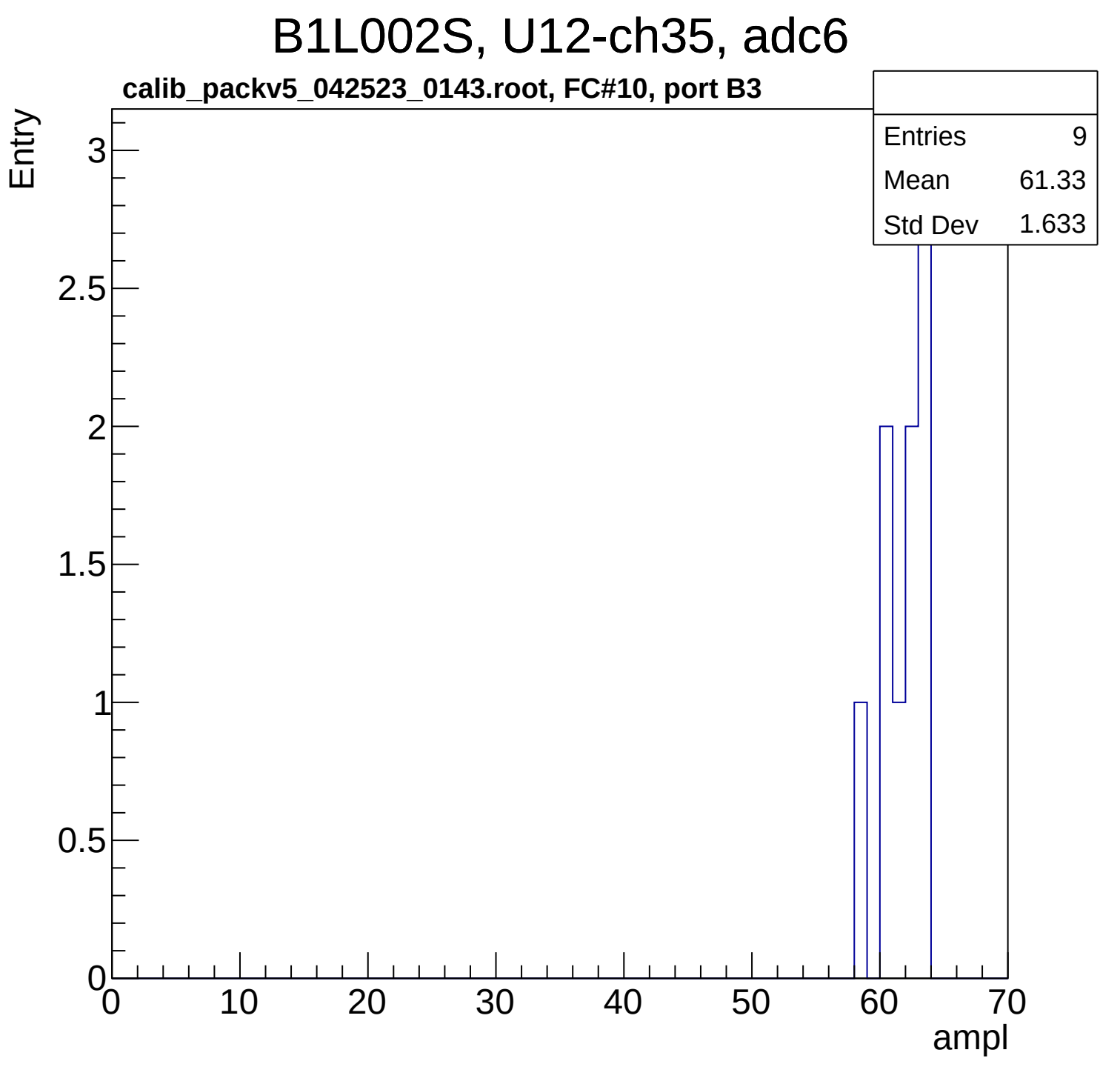
50
60
70
ampl


B1L002S, U12-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 61.33
Std Dev 1.633
Entry
ampl
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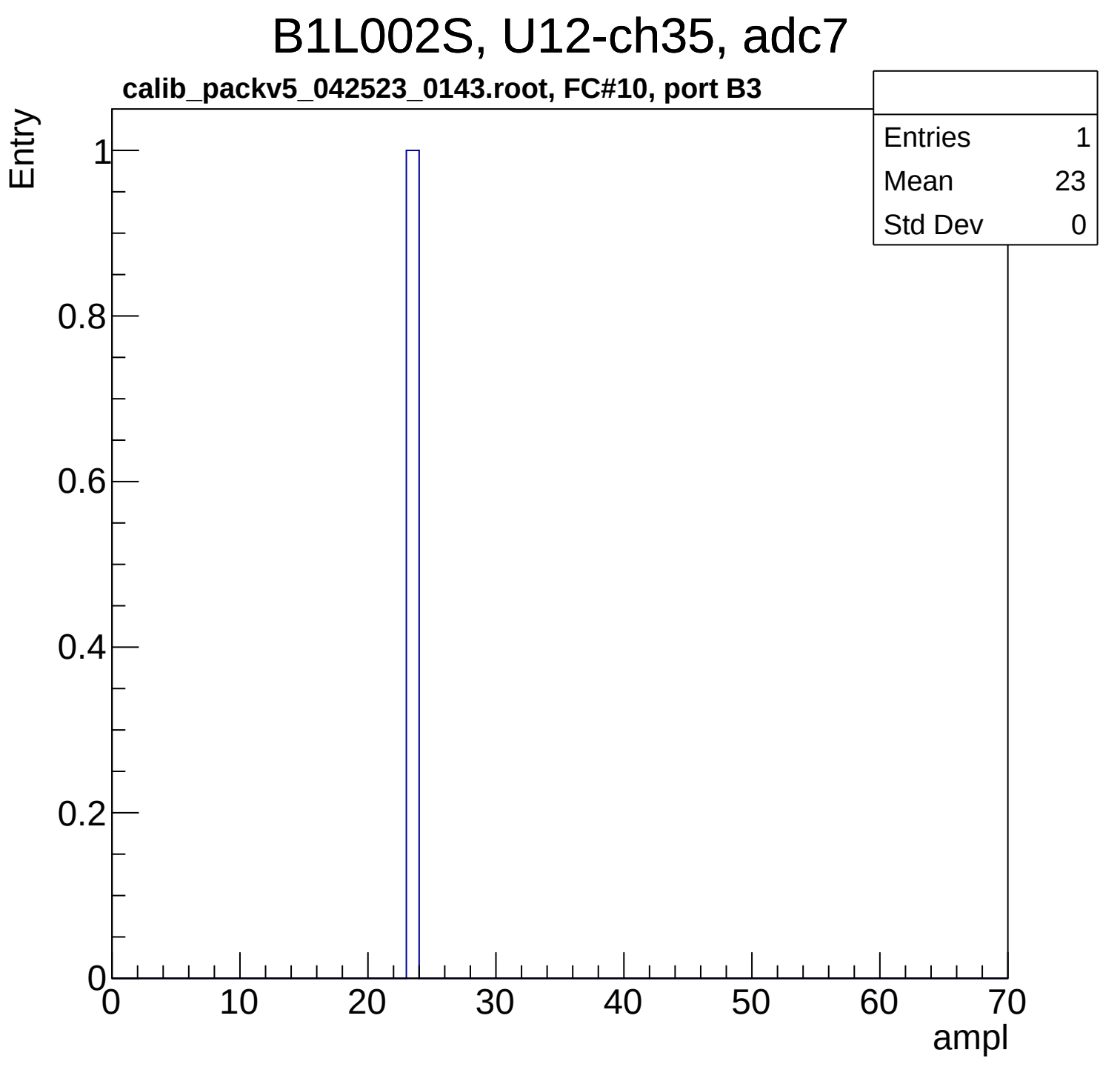
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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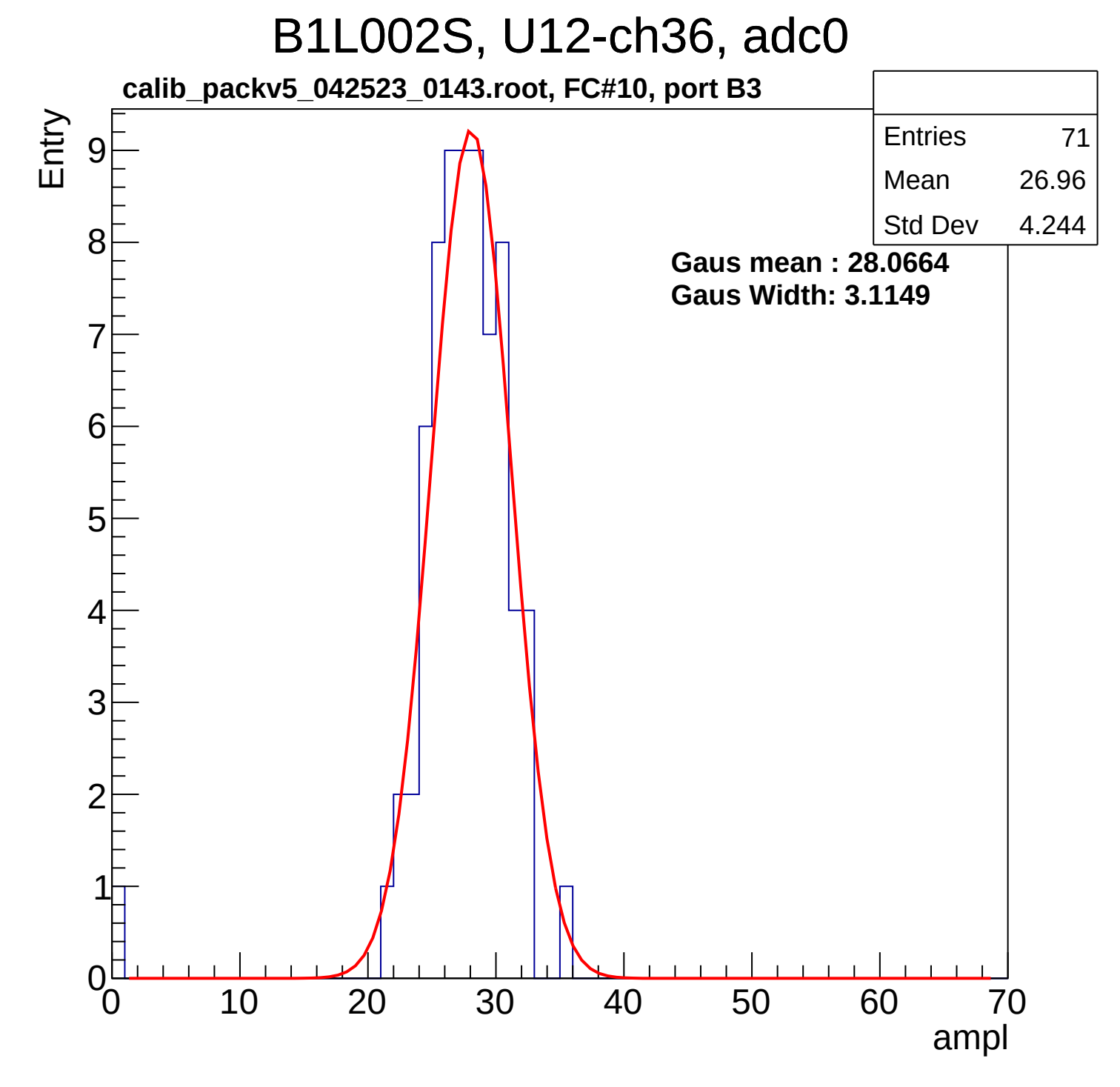
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 26.96
Std Dev 4.244
Gaus mean : 28.0664
Gaus Width: 3.1149
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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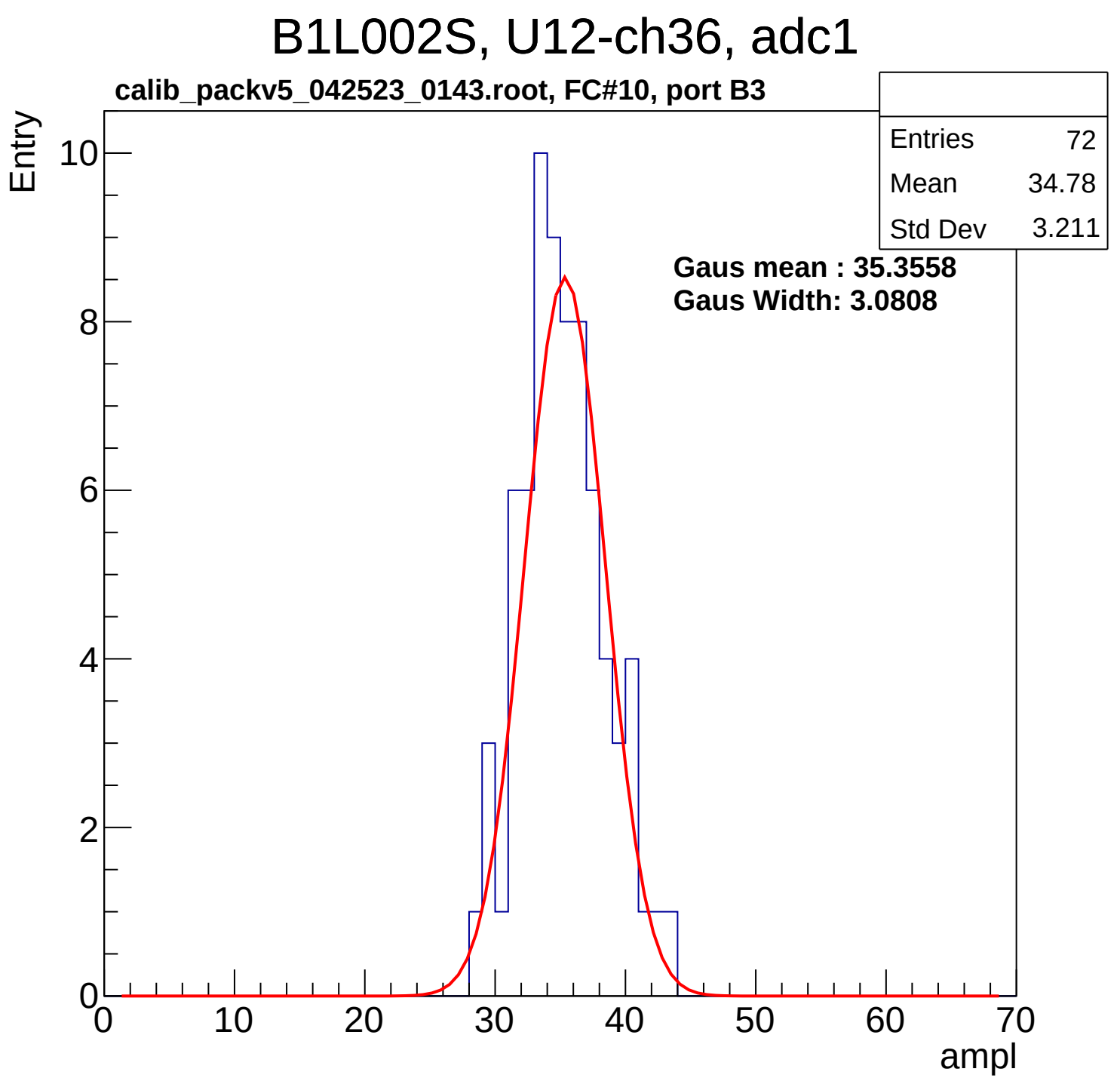
B1L002S, U12-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.78
Std Dev 3.211
Gaus mean : 35.3558
Gaus Width: 3.0808
Entry
ampl
0
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4
6
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0
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40
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60
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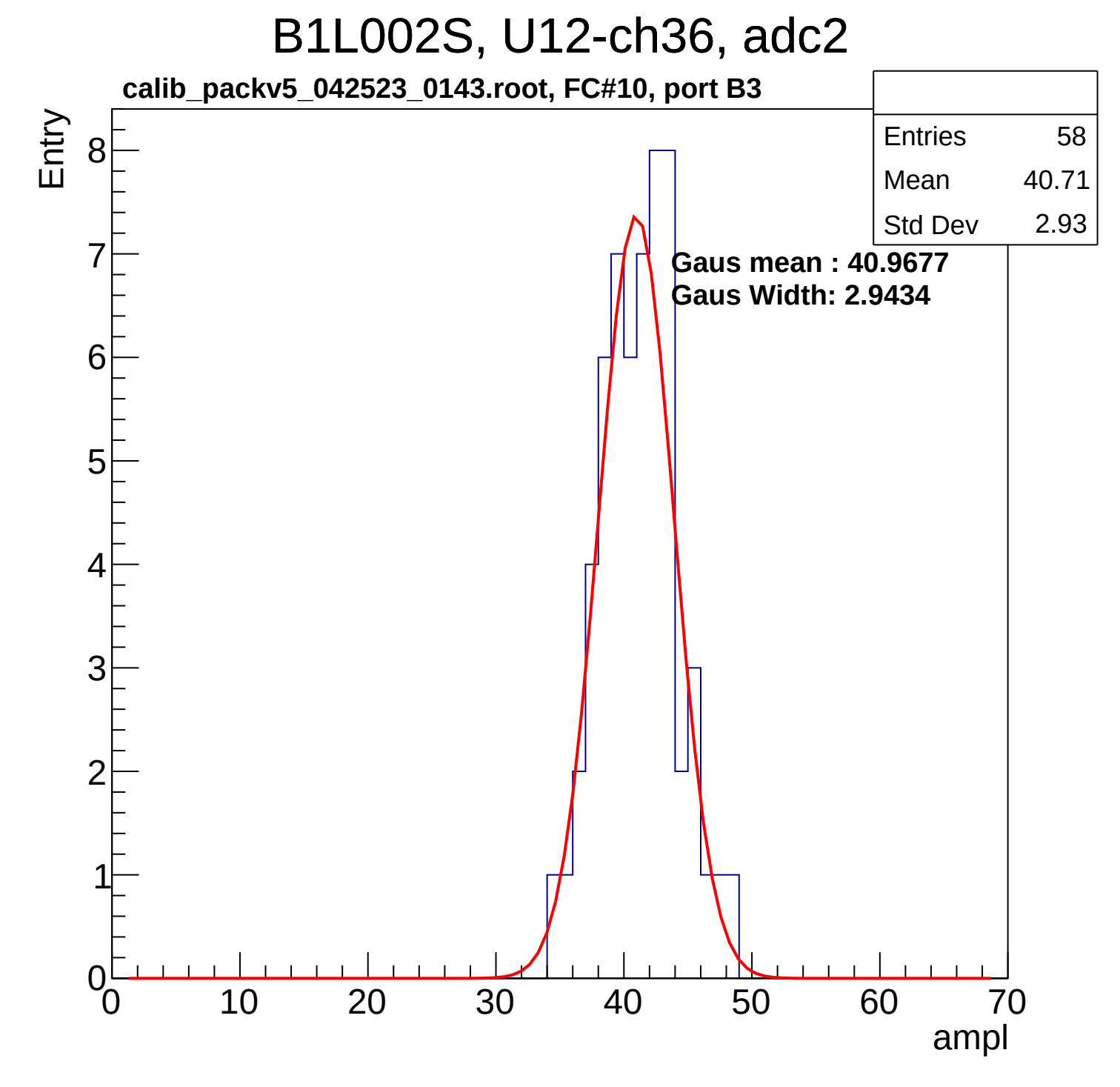
B1L002S, U12-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.71
Std Dev 2.93
Gaus mean : 40.9677
Gaus Width: 2.9434
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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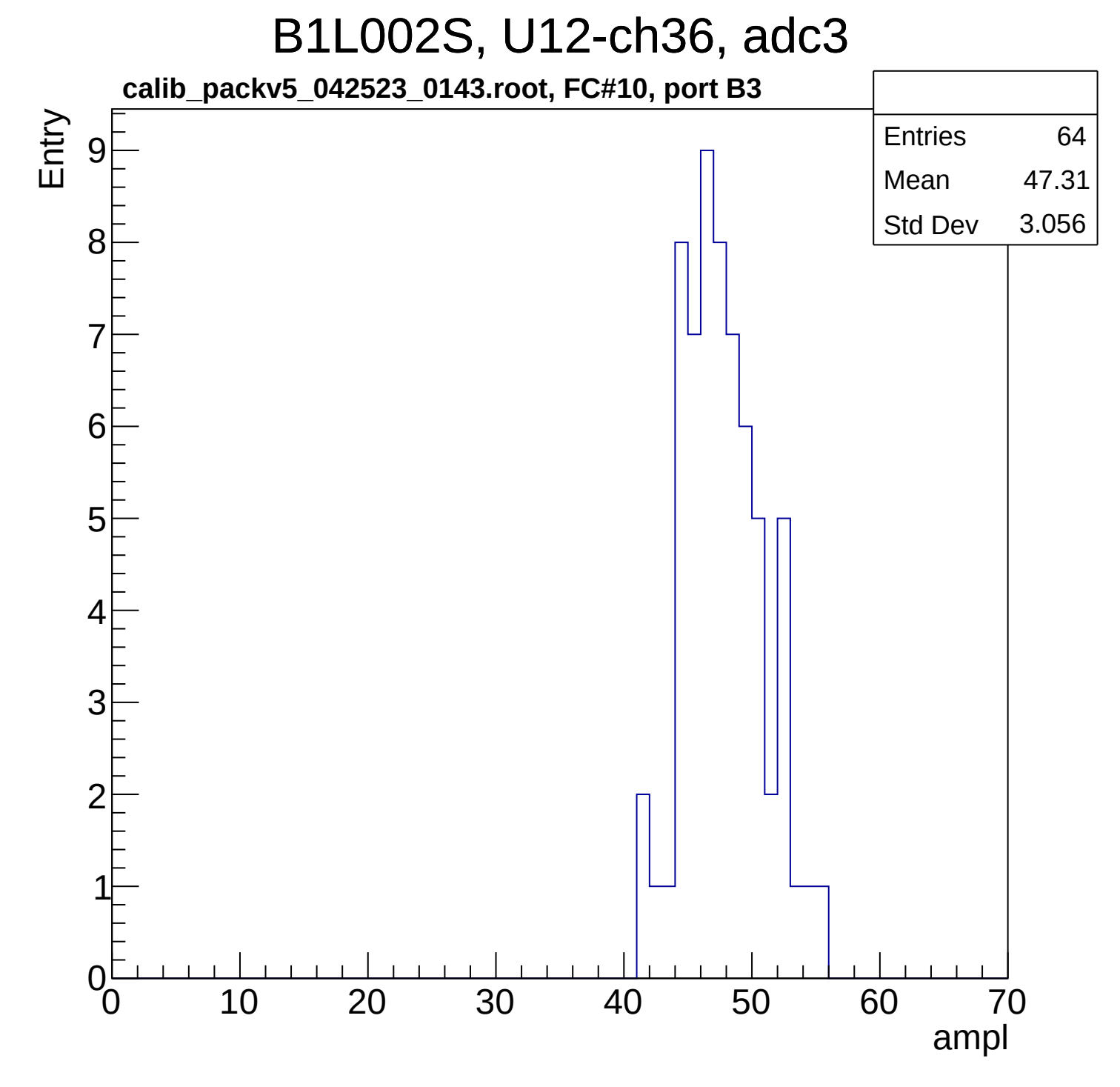
B1L002S, U12-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.31
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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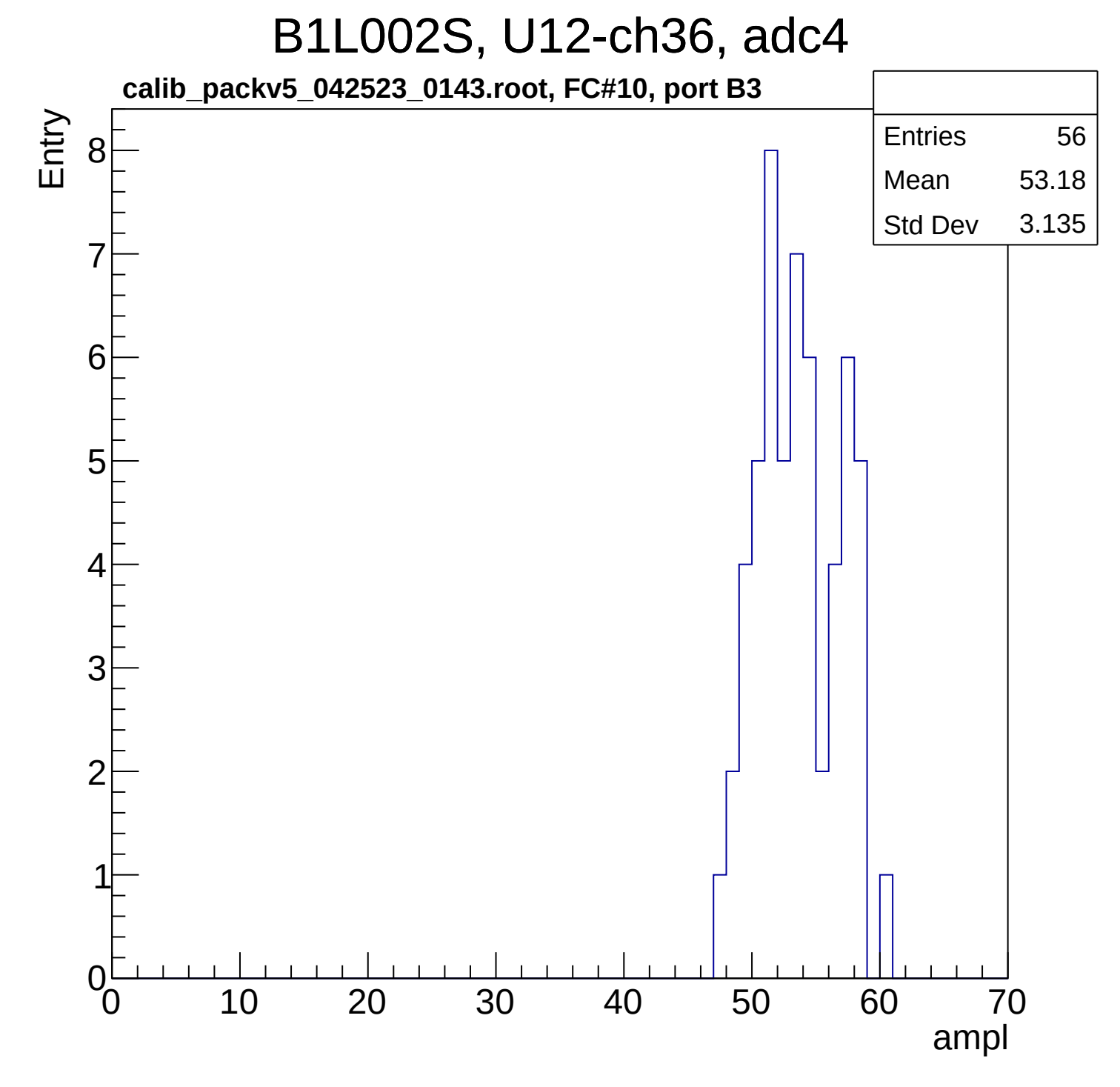
B1L002S, U12-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.18
Std Dev 3.135
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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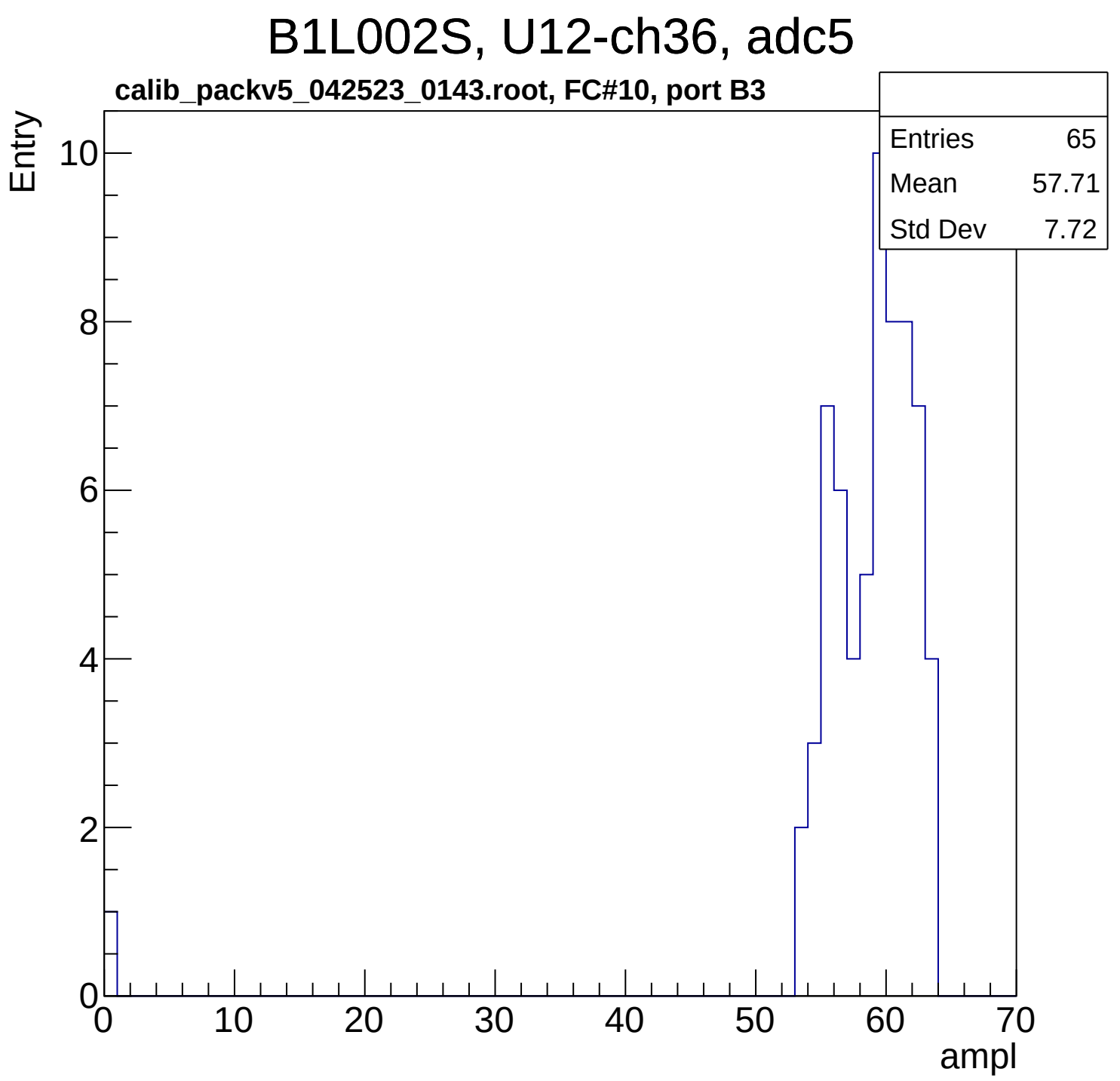
B1L002S, U12-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.71
Std Dev 7.72
Entry
ampl
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2
4
6
8
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0
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60
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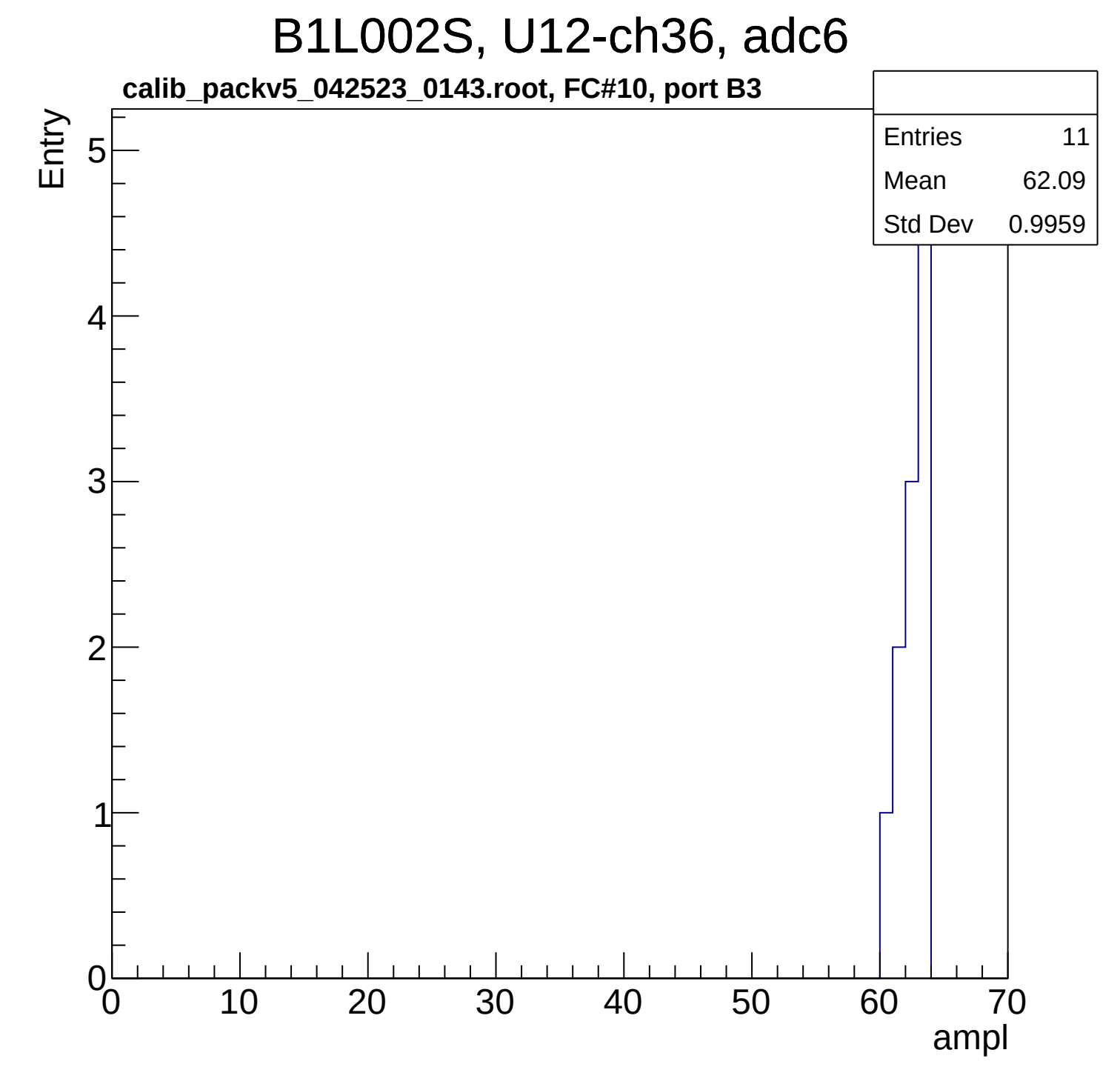
B1L002S, U12-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.09
Std Dev 0.9959
Entry
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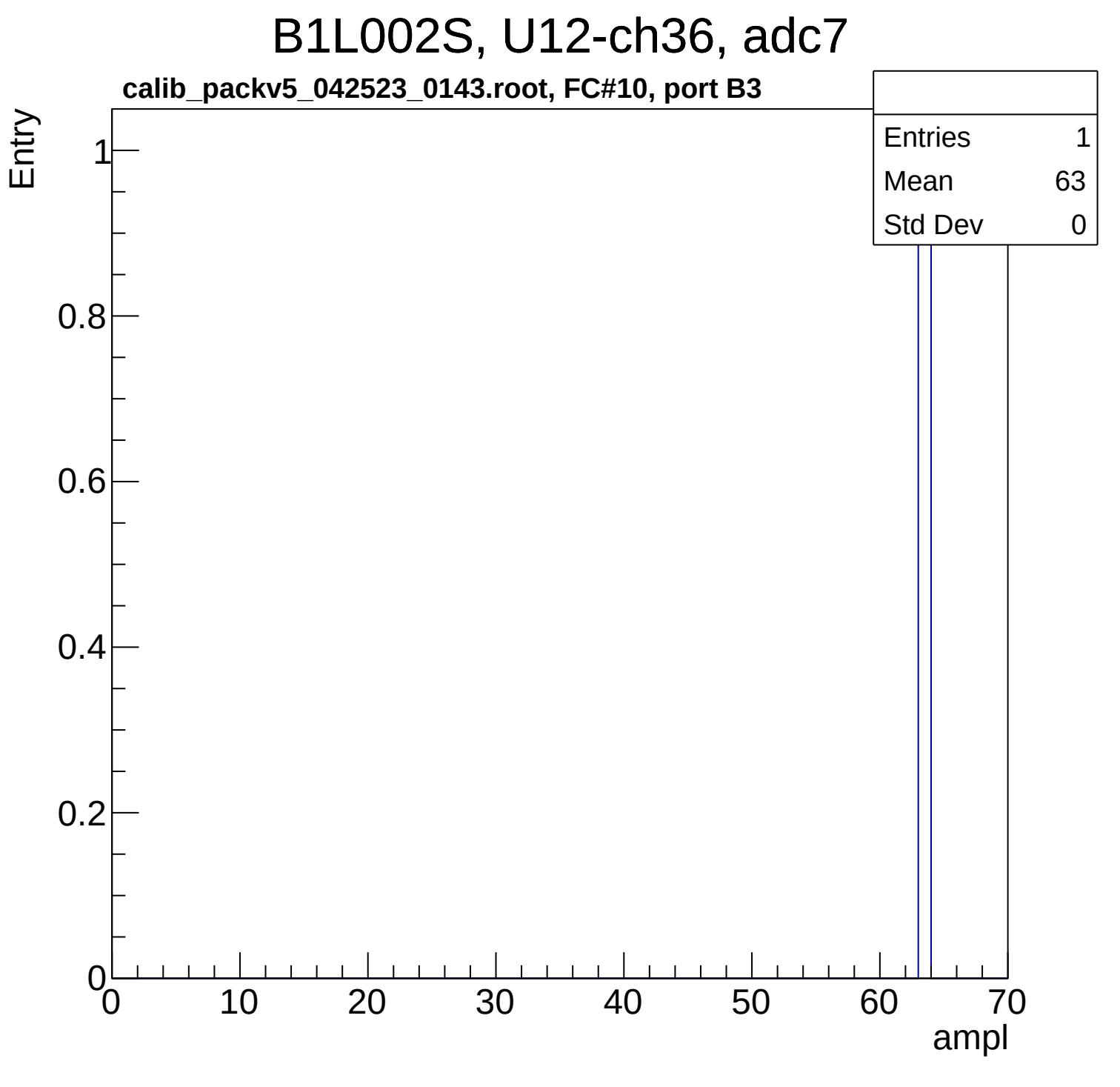
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
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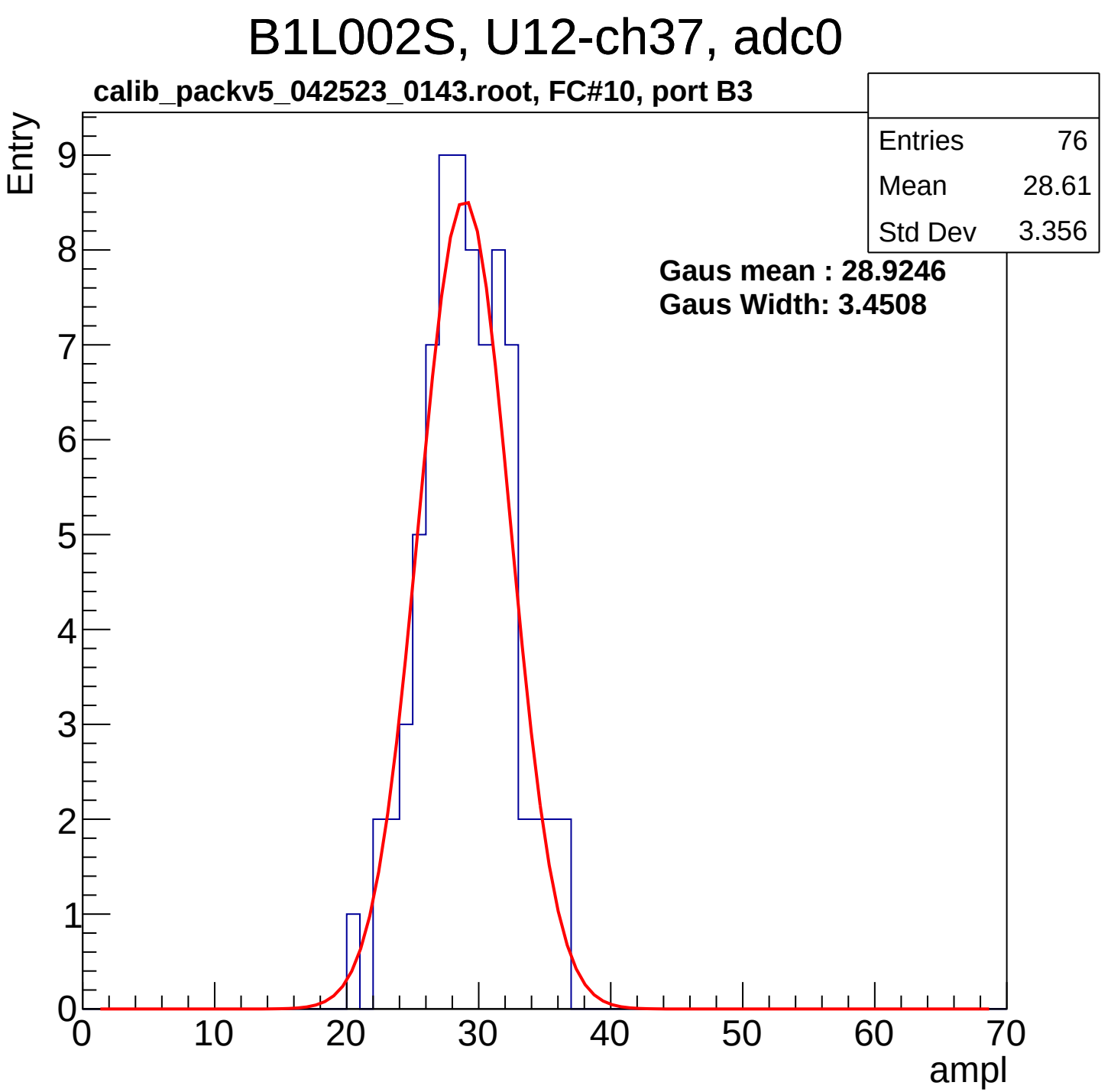
50
60
70
ampl


B1L002S, U12-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.61
Std Dev 3.356
Gaus mean : 28.9246
Gaus Width: 3.4508
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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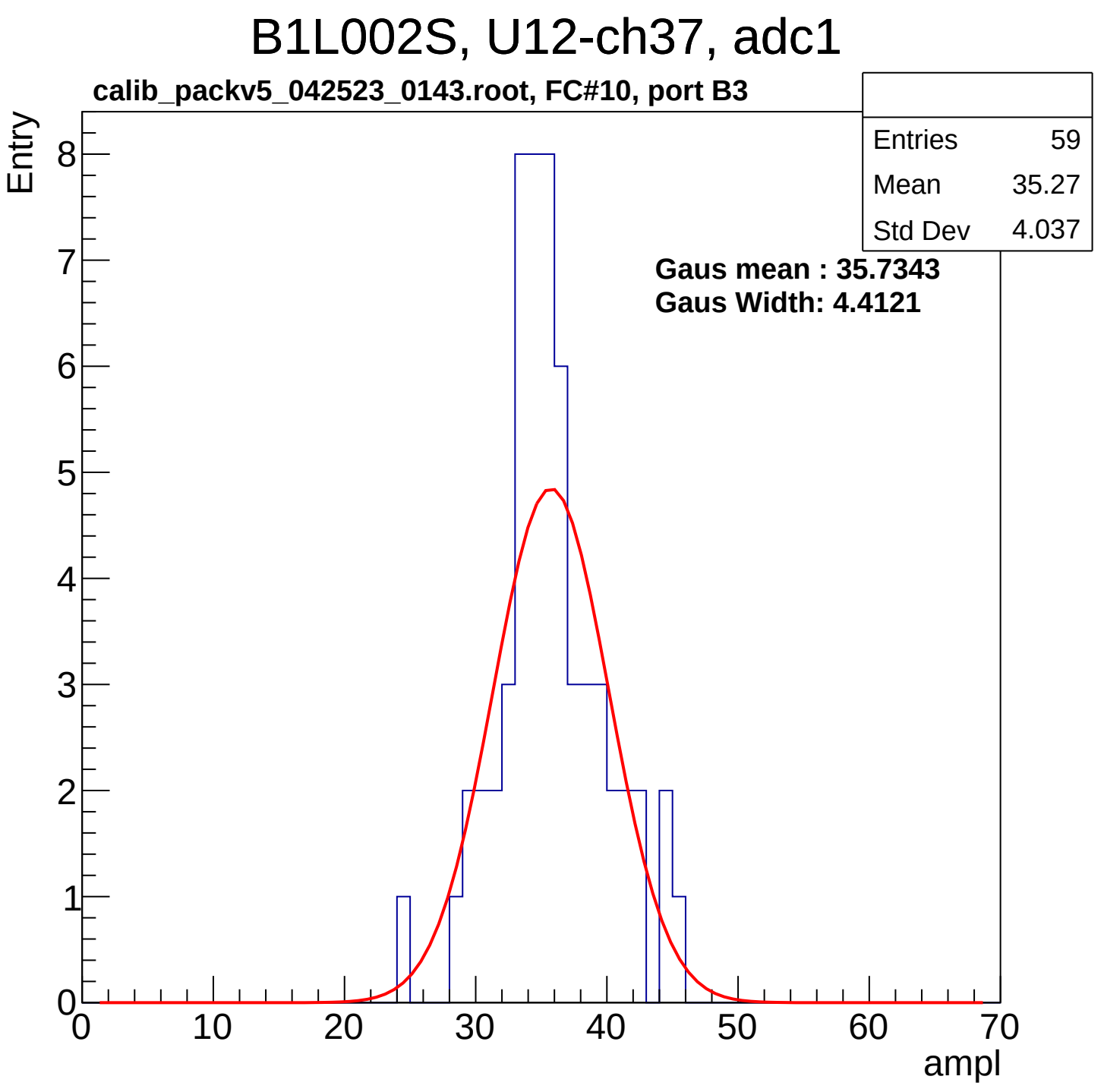
B1L002S, U12-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.27
Std Dev 4.037
Gaus mean : 35.7343
Gaus Width: 4.4121
Entry
ampl
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2
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4
5
6
7
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0
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60
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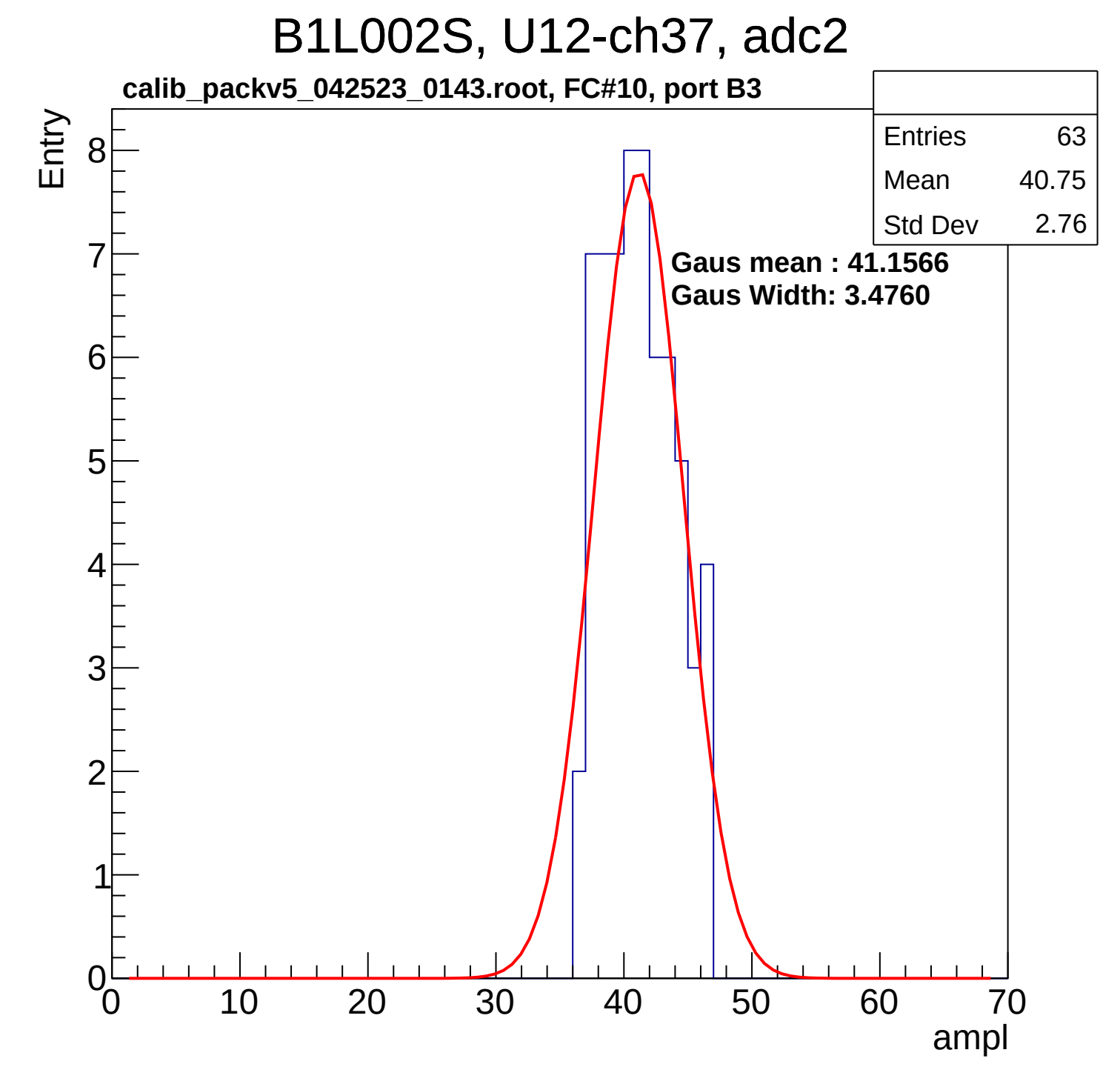
B1L002S, U12-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.75
Std Dev 2.76
Gaus mean : 41.1566
Gaus Width: 3.4760
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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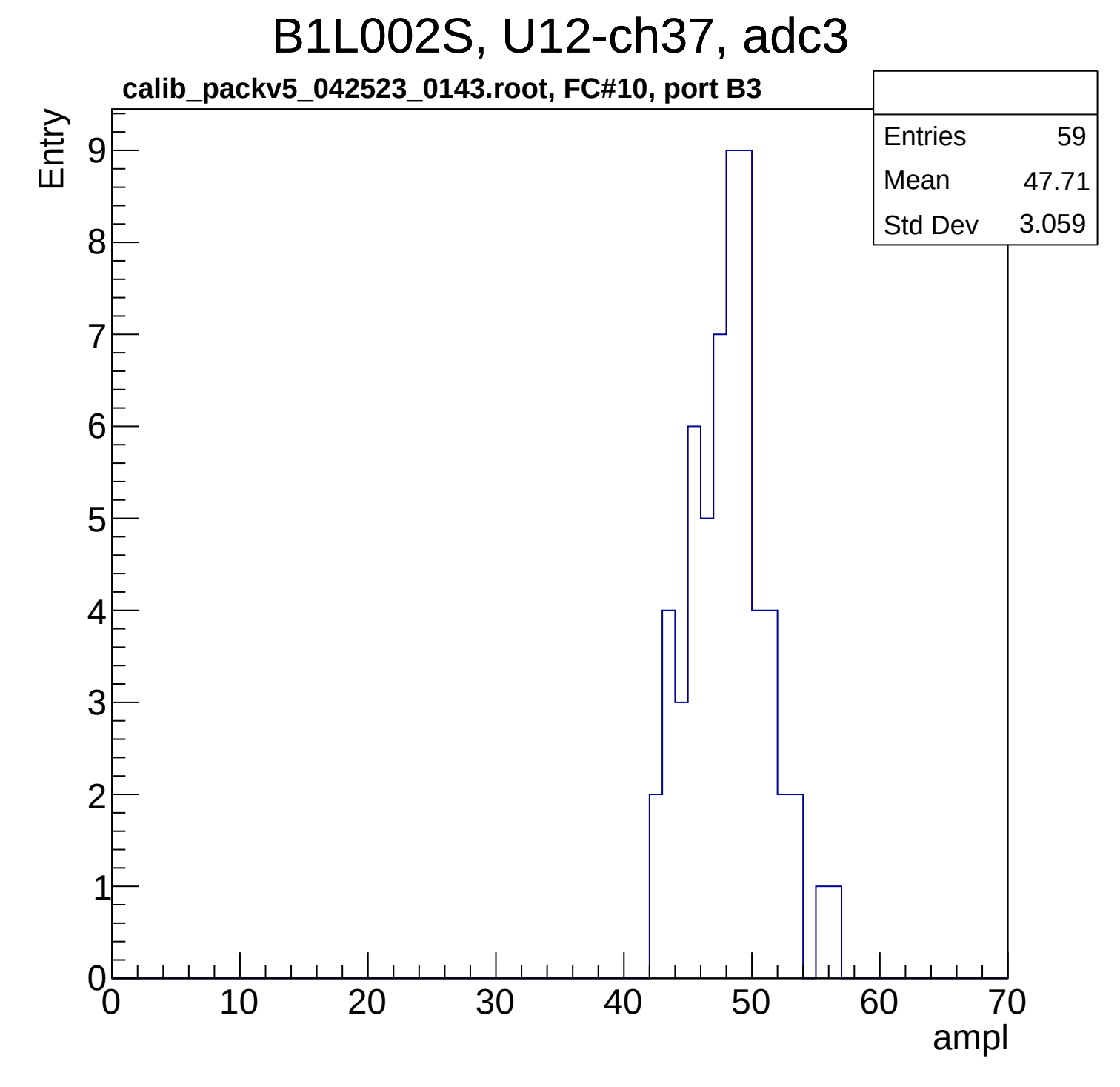
B1L002S, U12-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.71
Std Dev 3.059
Entry
ampl
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4
5
6
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0
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60
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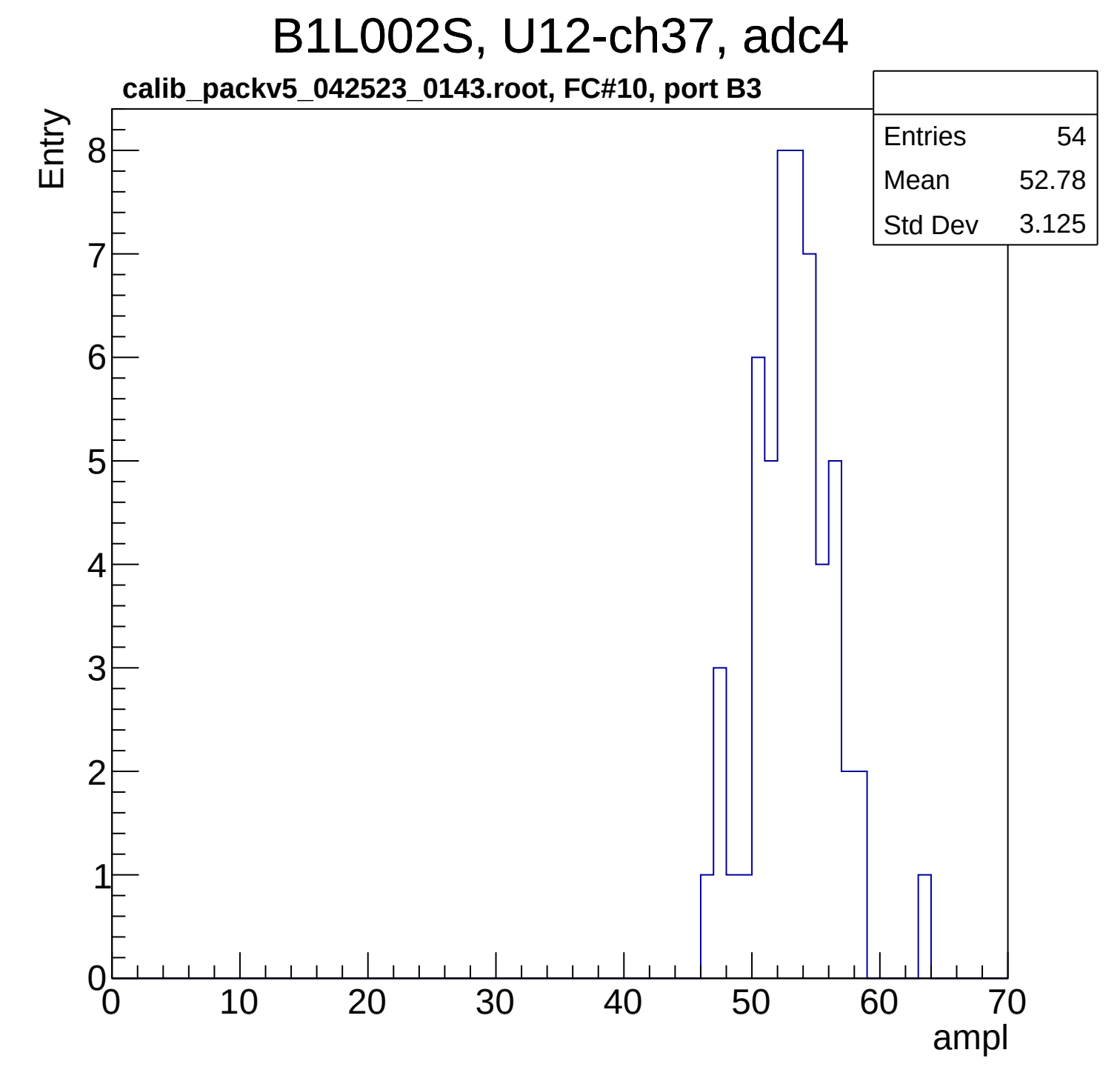
B1L002S, U12-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.78
Std Dev 3.125
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
50
60
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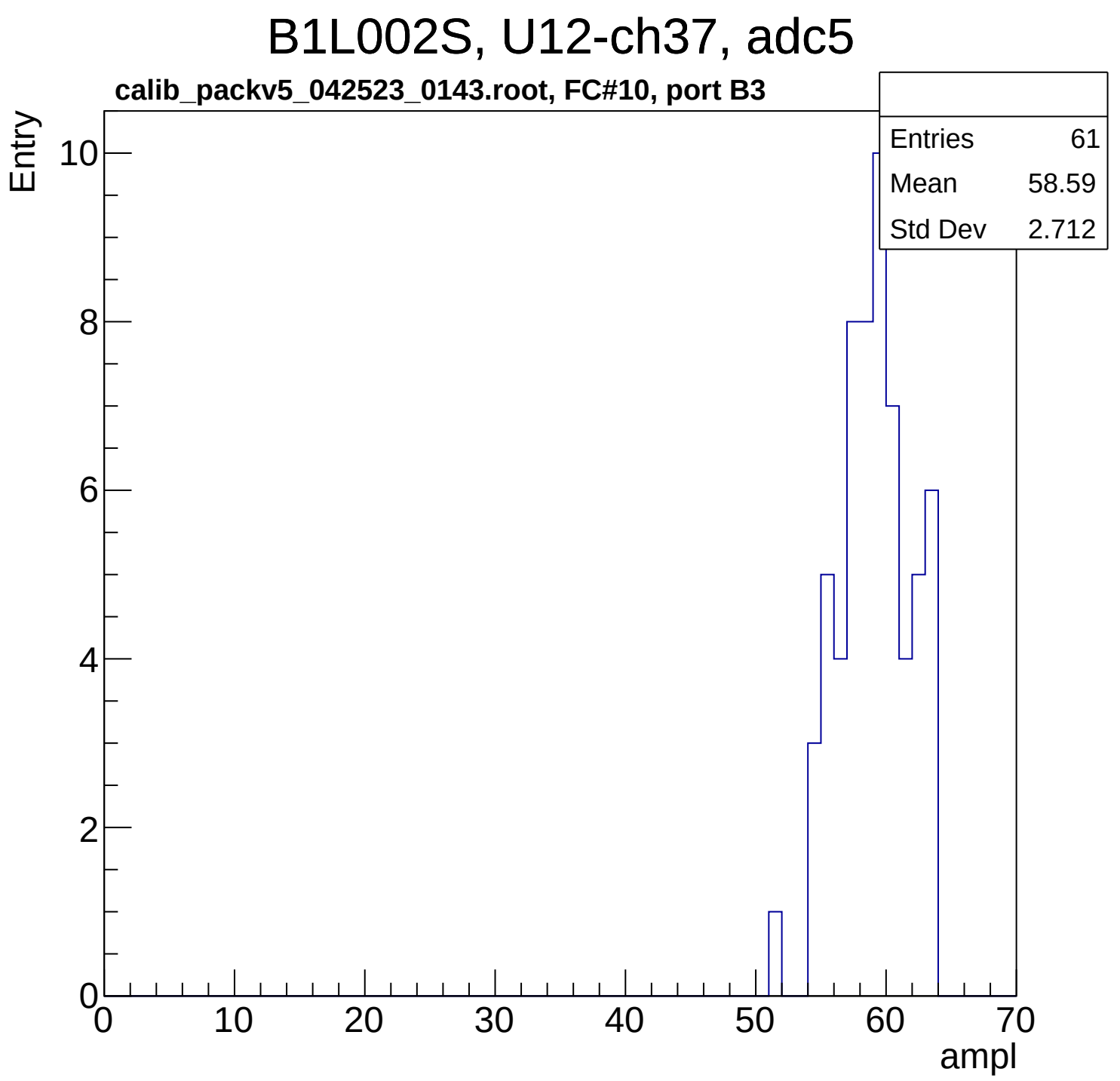
B1L002S, U12-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.59
Std Dev 2.712
Entry
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2
4
6
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10
0
10
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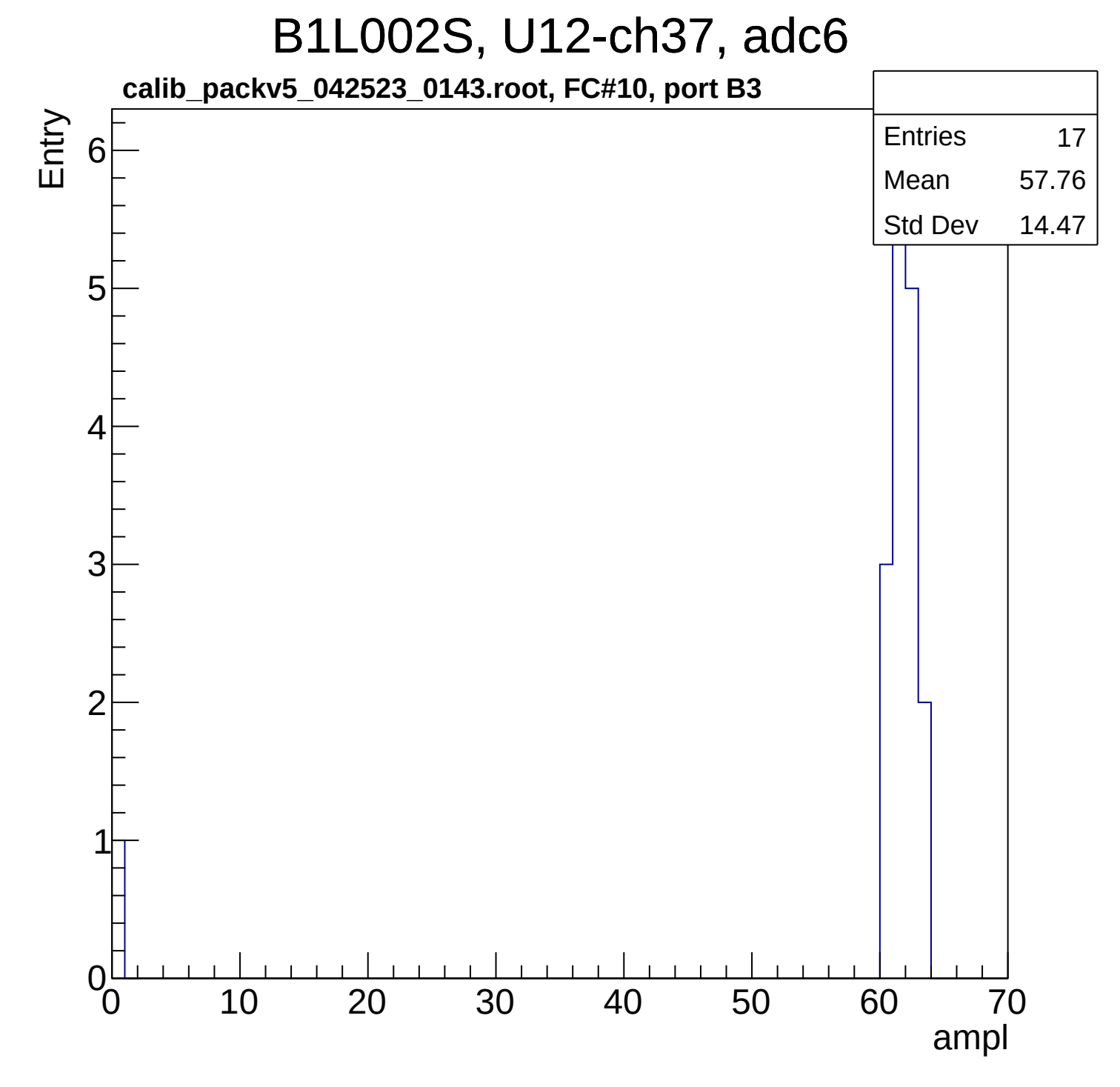
30
40
50
60
70
ampl


B1L002S, U12-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.76
Std Dev 14.47
Entry
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2
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4
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6
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10
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30
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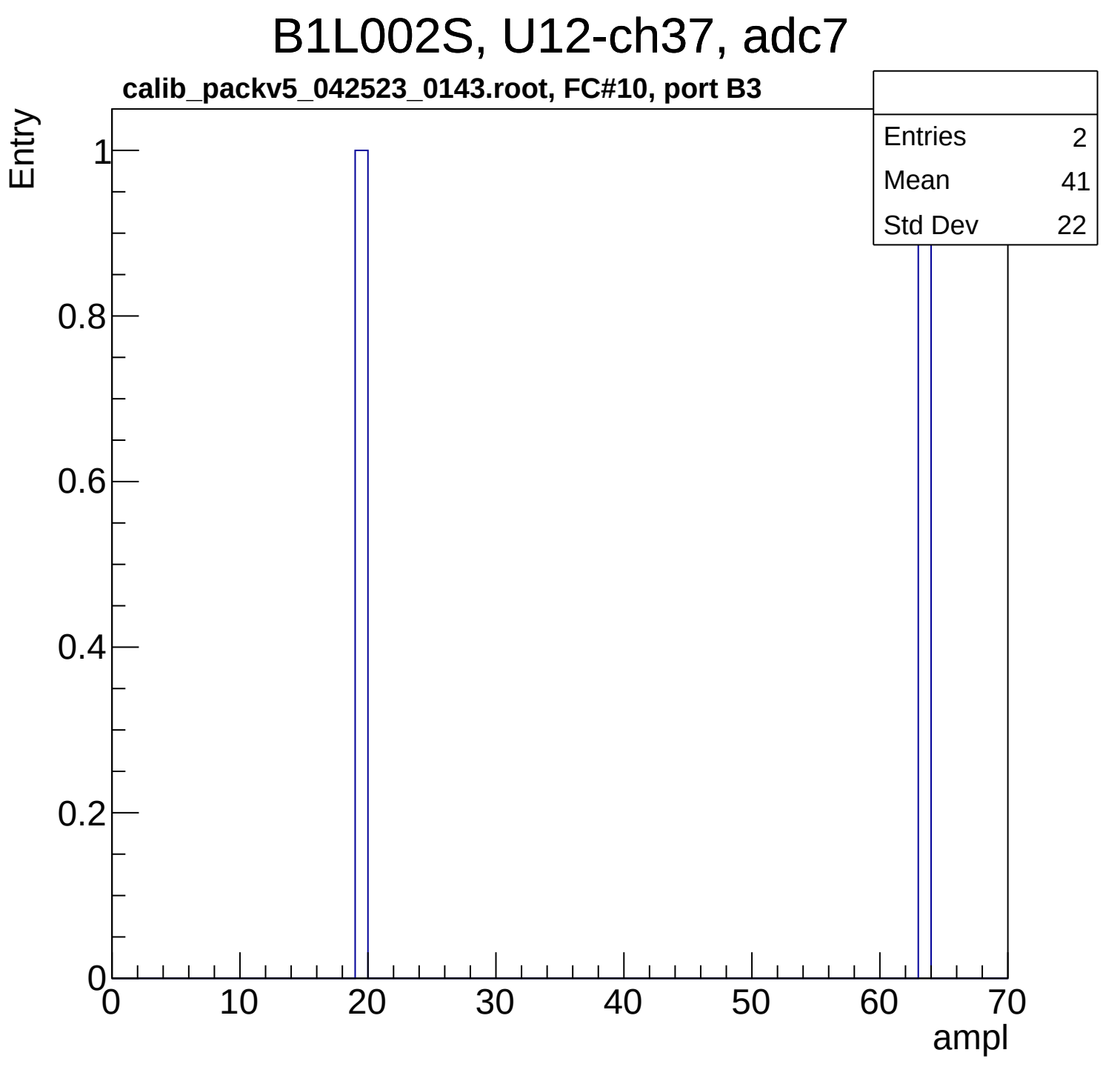
50
60
70
ampl


B1L002S, U12-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 22
Entry
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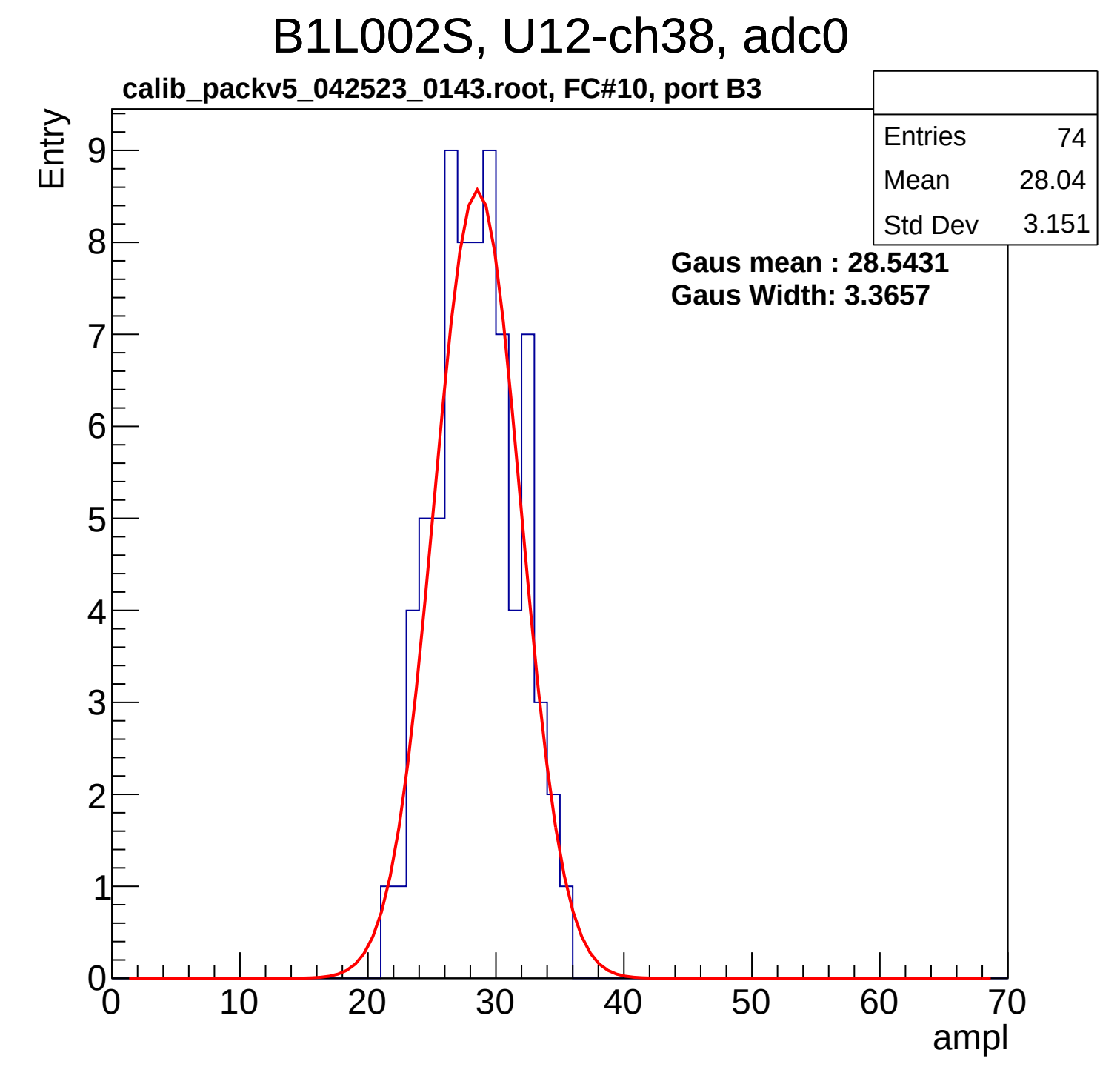
40
50
60
70
ampl


B1L002S, U12-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.04
Std Dev 3.151
Gaus mean : 28.5431
Gaus Width: 3.3657
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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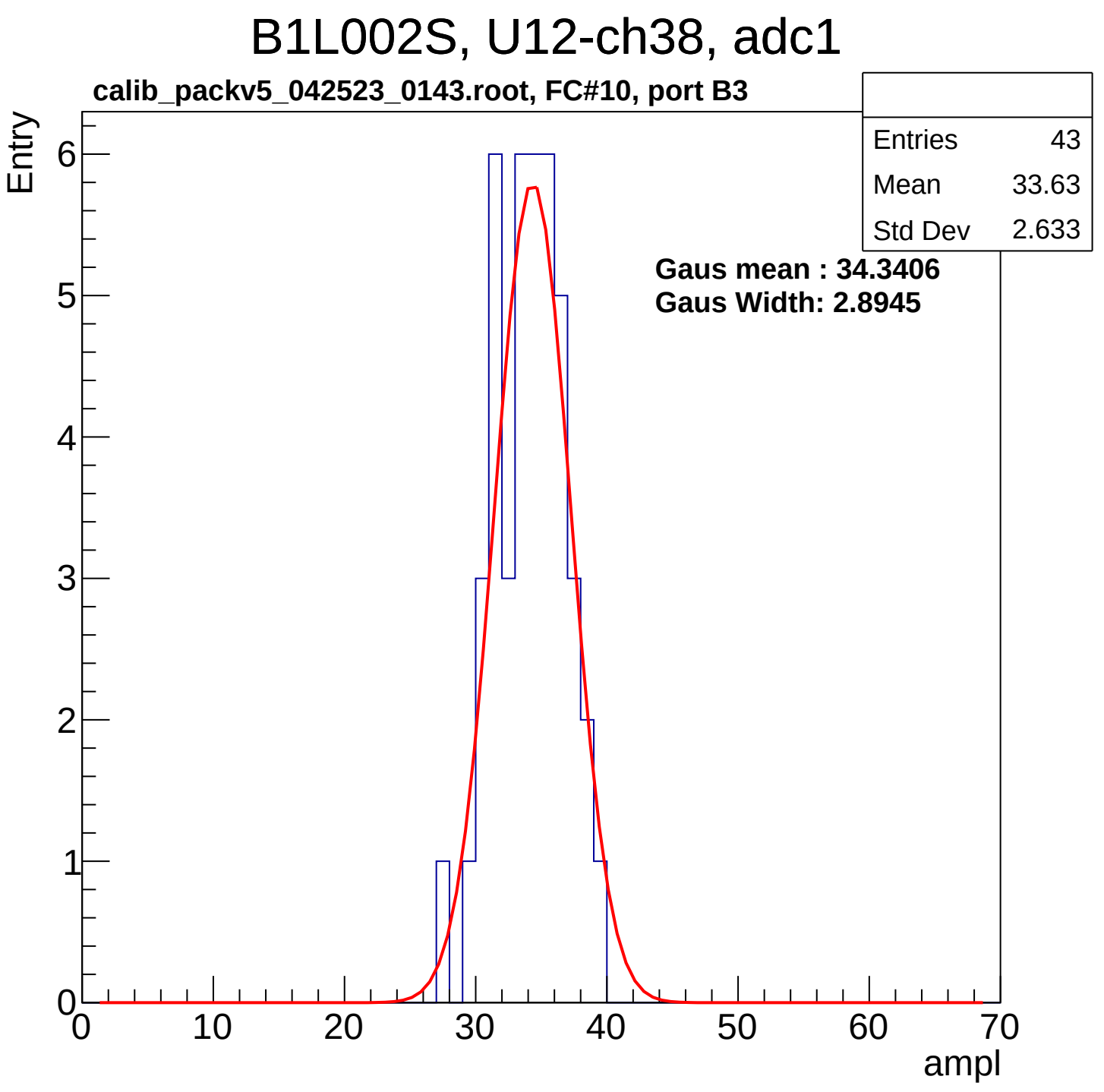
B1L002S, U12-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 33.63
Std Dev 2.633
Gaus mean : 34.3406
Gaus Width: 2.8945
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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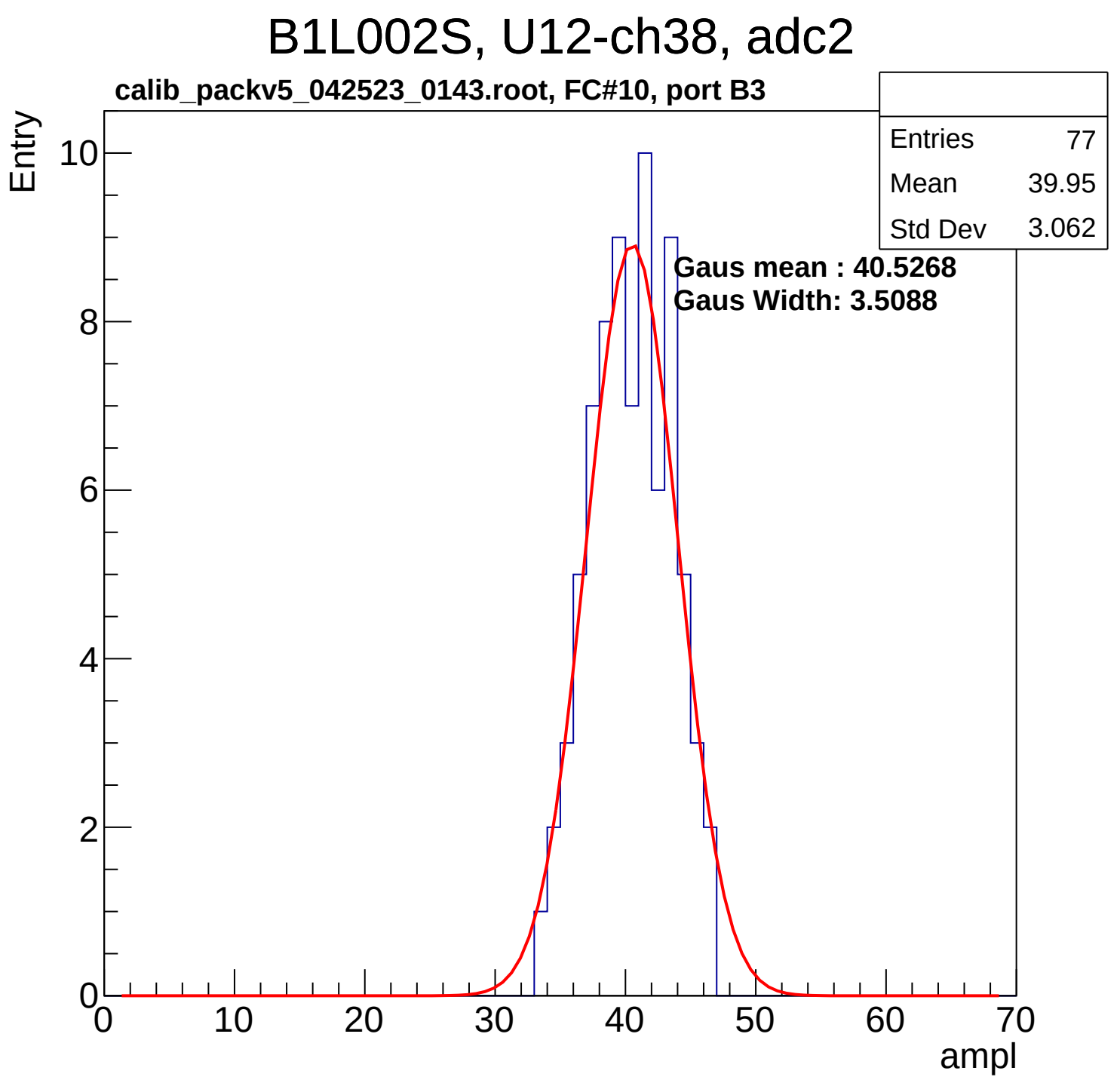
B1L002S, U12-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 39.95
Std Dev 3.062
Gaus mean : 40.5268
Gaus Width: 3.5088
Entry
ampl
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4
6
8
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60
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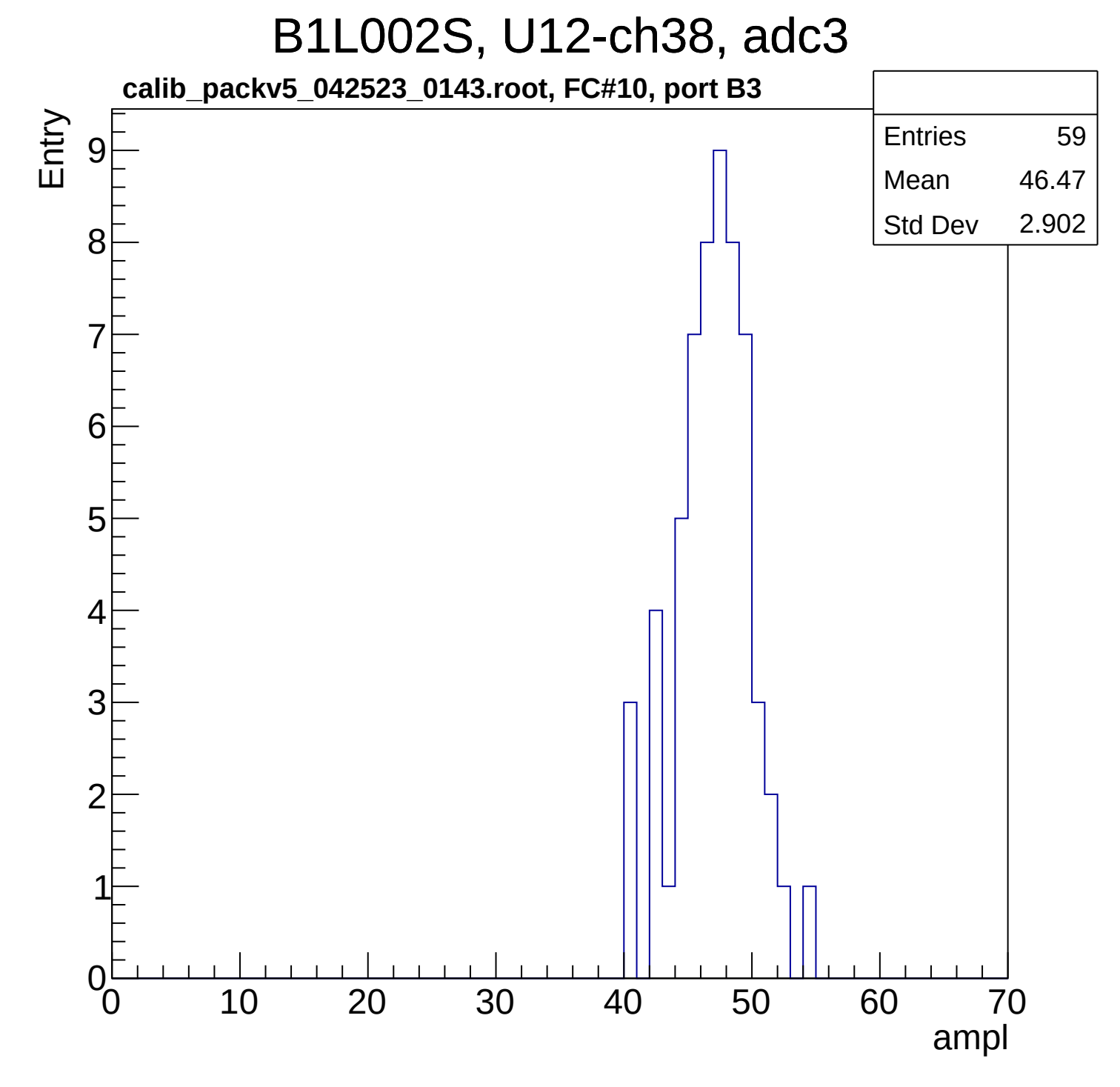
B1L002S, U12-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.47
Std Dev 2.902
Entry
ampl
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3
4
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6
7
8
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0
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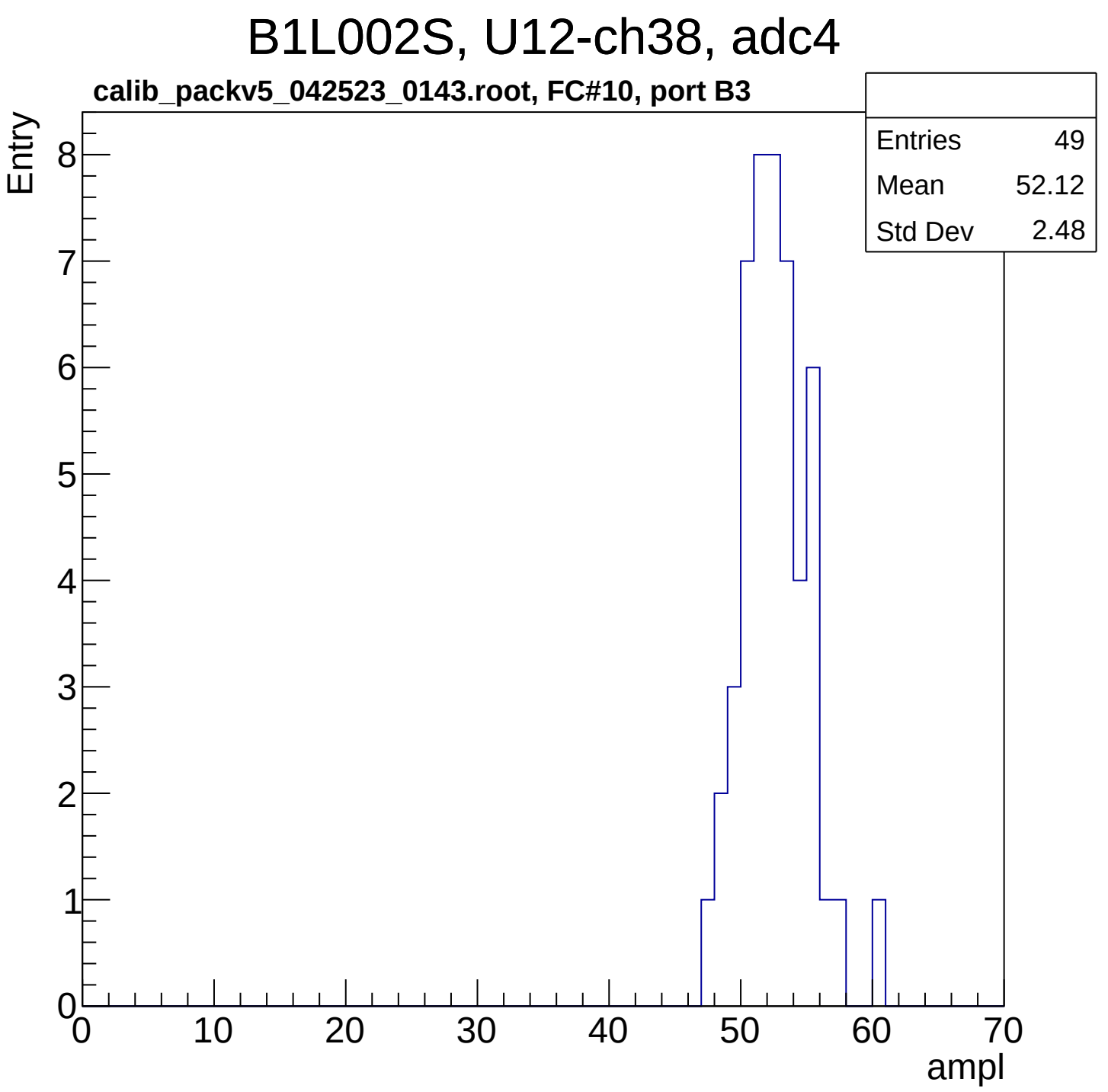
B1L002S, U12-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.12
Std Dev 2.48
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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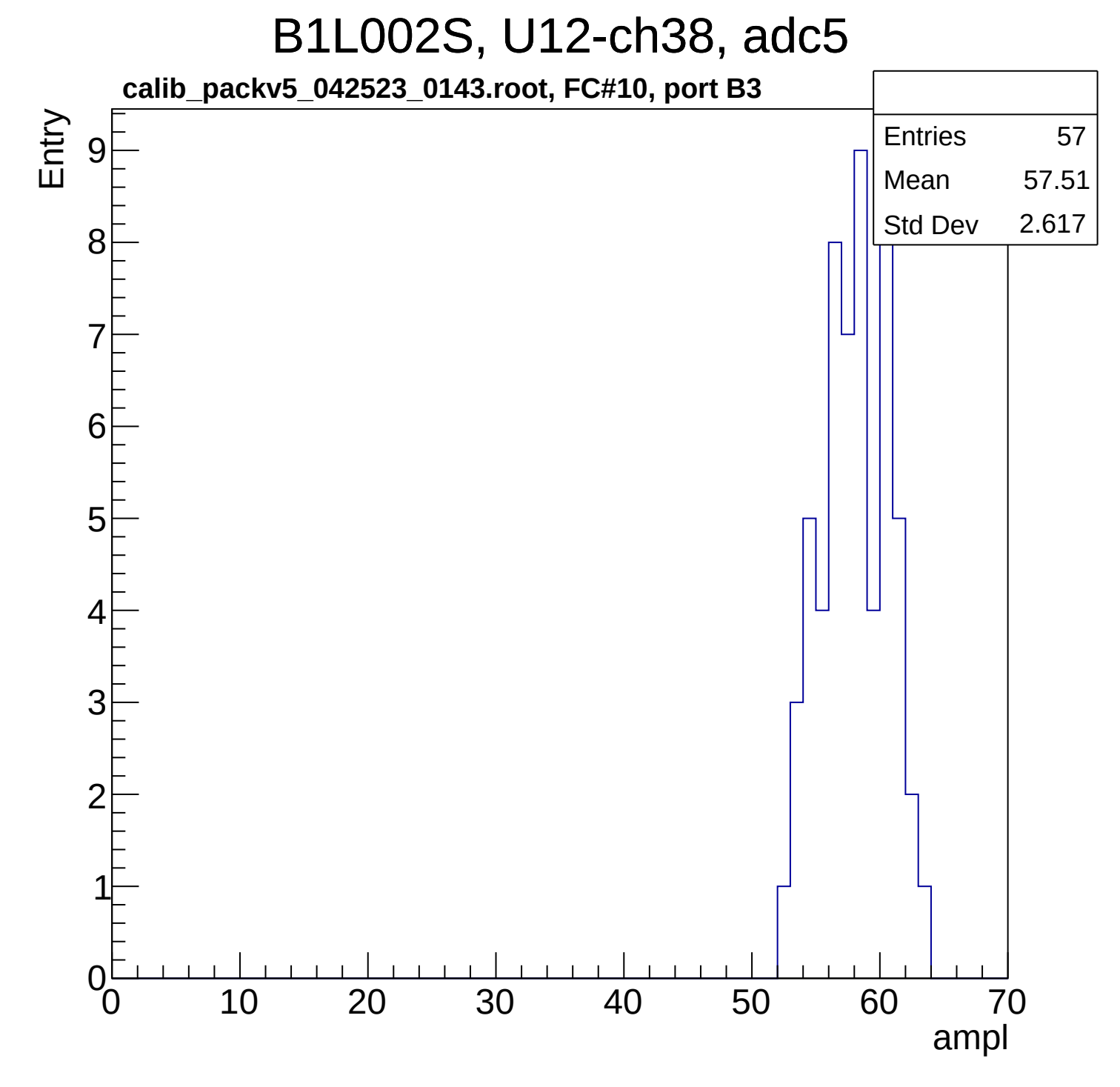
B1L002S, U12-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.51
Std Dev 2.617
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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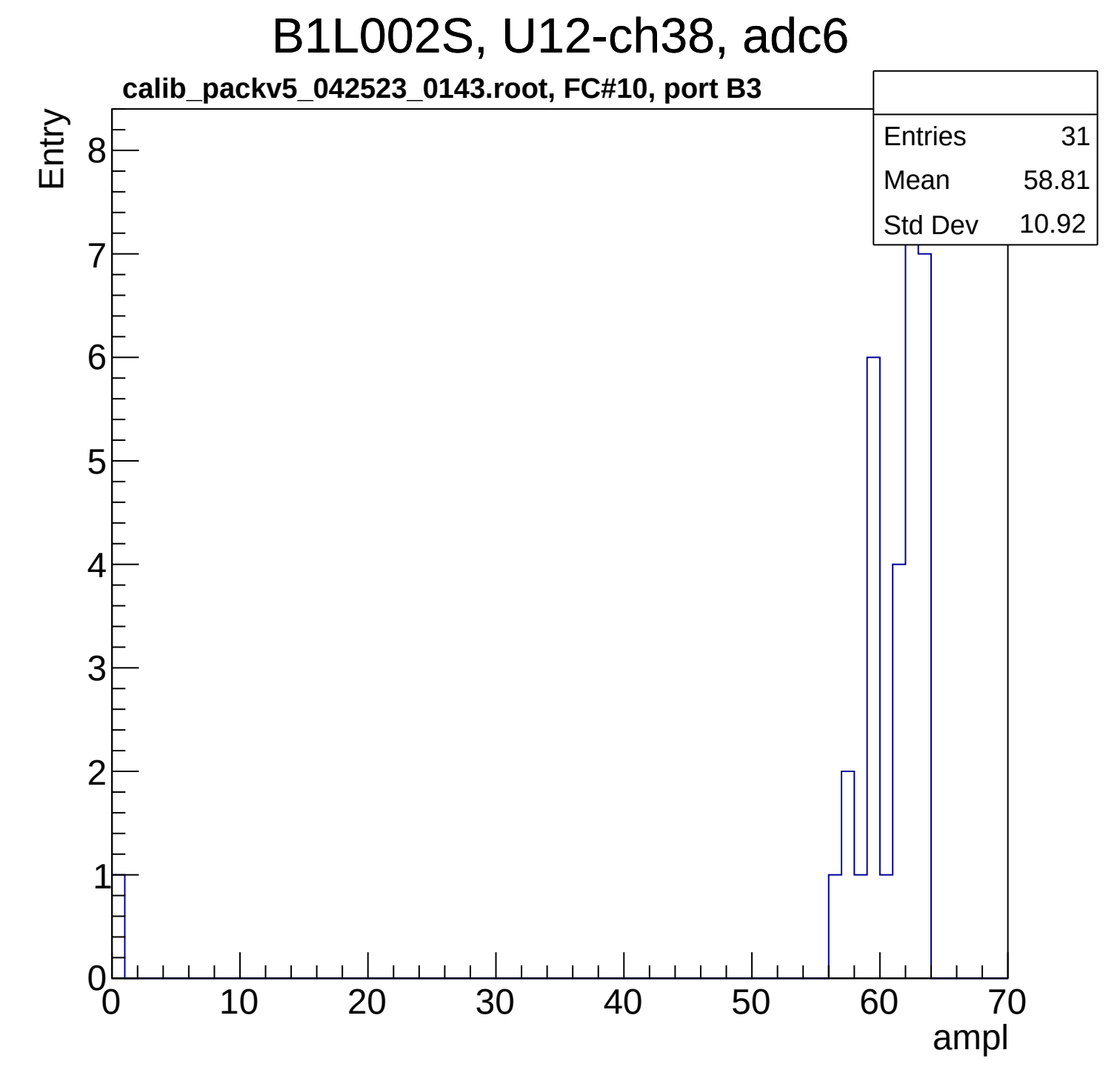
B1L002S, U12-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.81
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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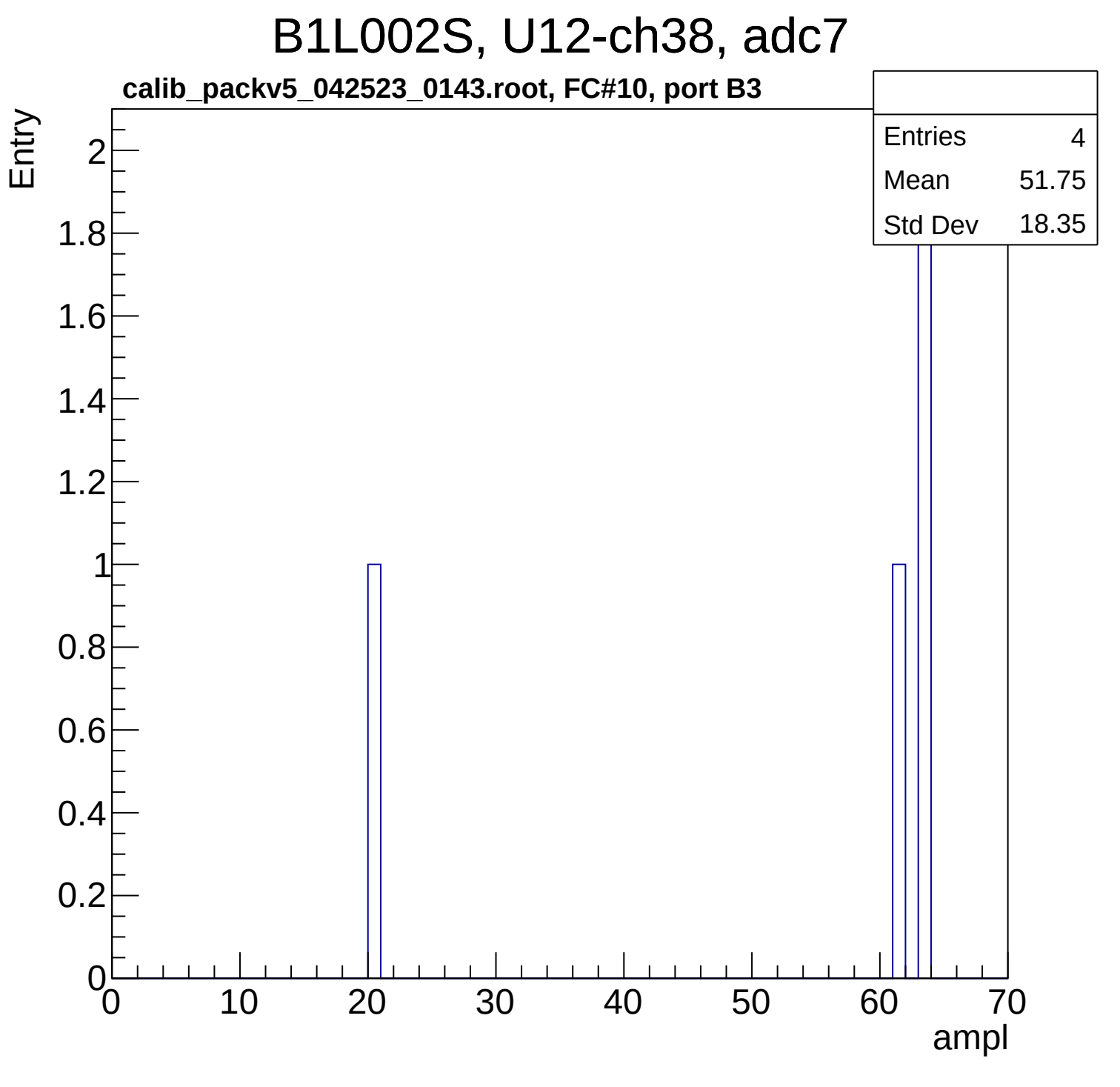
B1L002S, U12-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.75
Std Dev 18.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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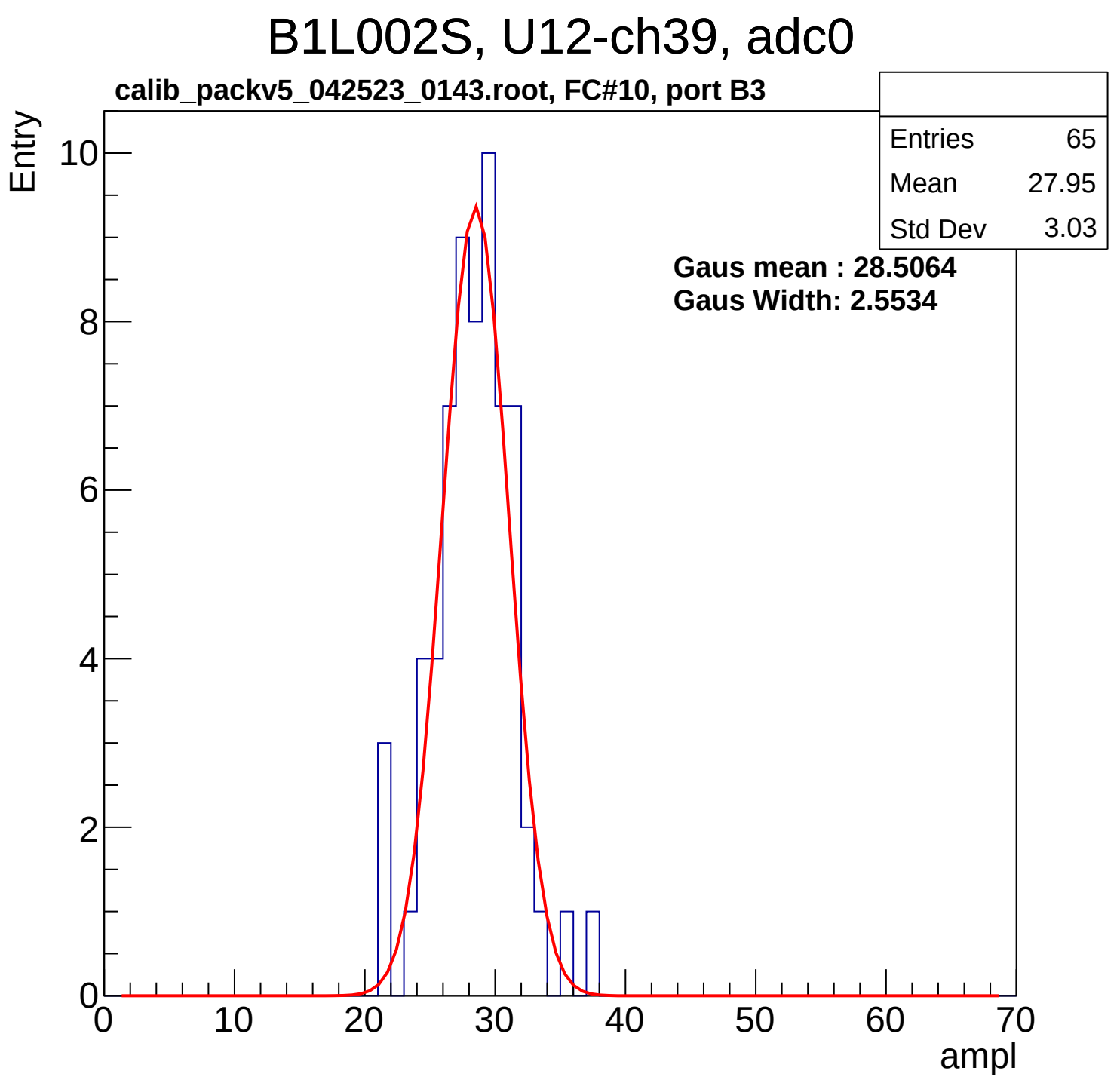
B1L002S, U12-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.95
Std Dev 3.03
Gaus mean : 28.5064
Gaus Width: 2.5534
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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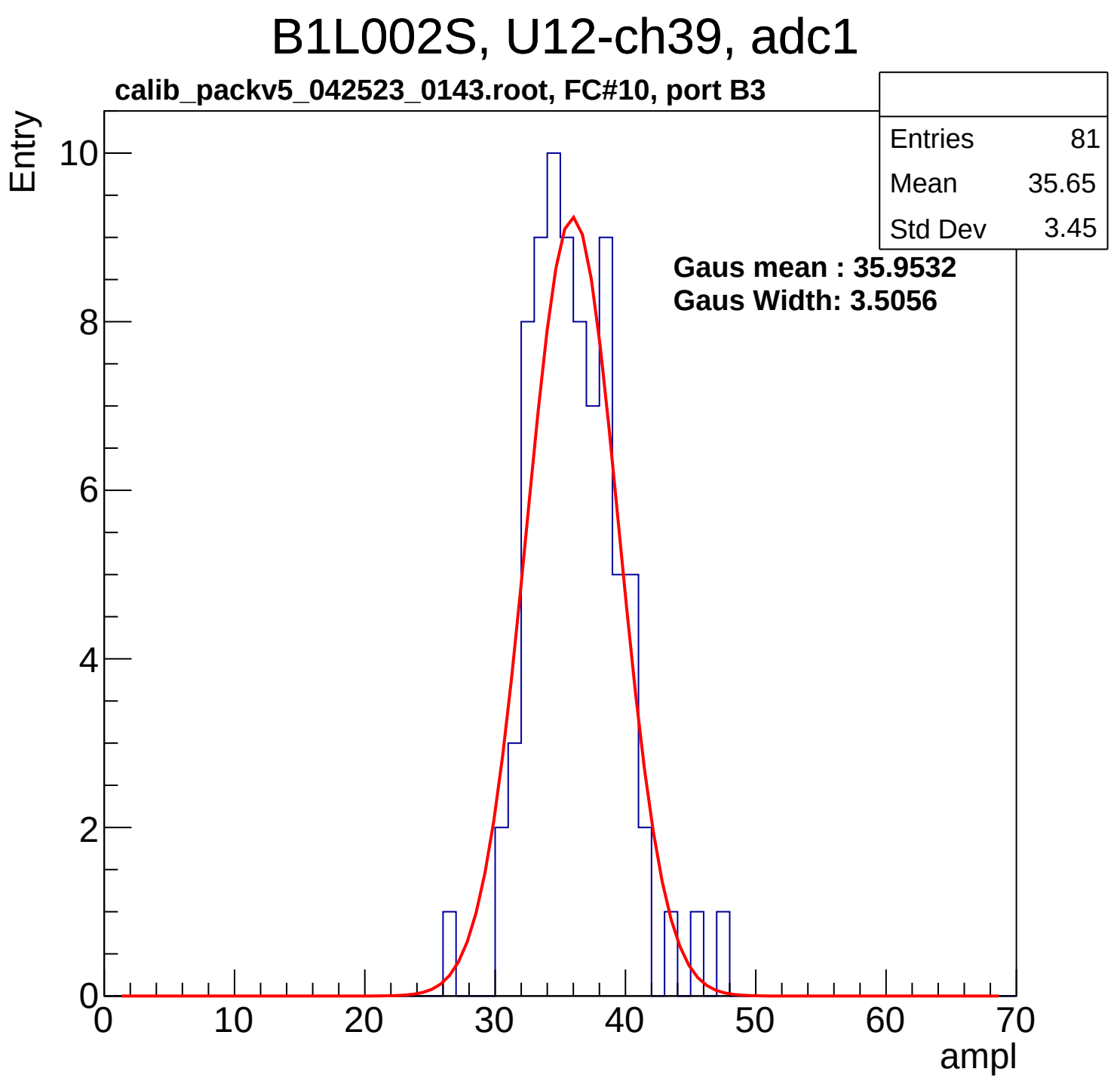
B1L002S, U12-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.65
Std Dev 3.45
Gaus mean : 35.9532
Gaus Width: 3.5056
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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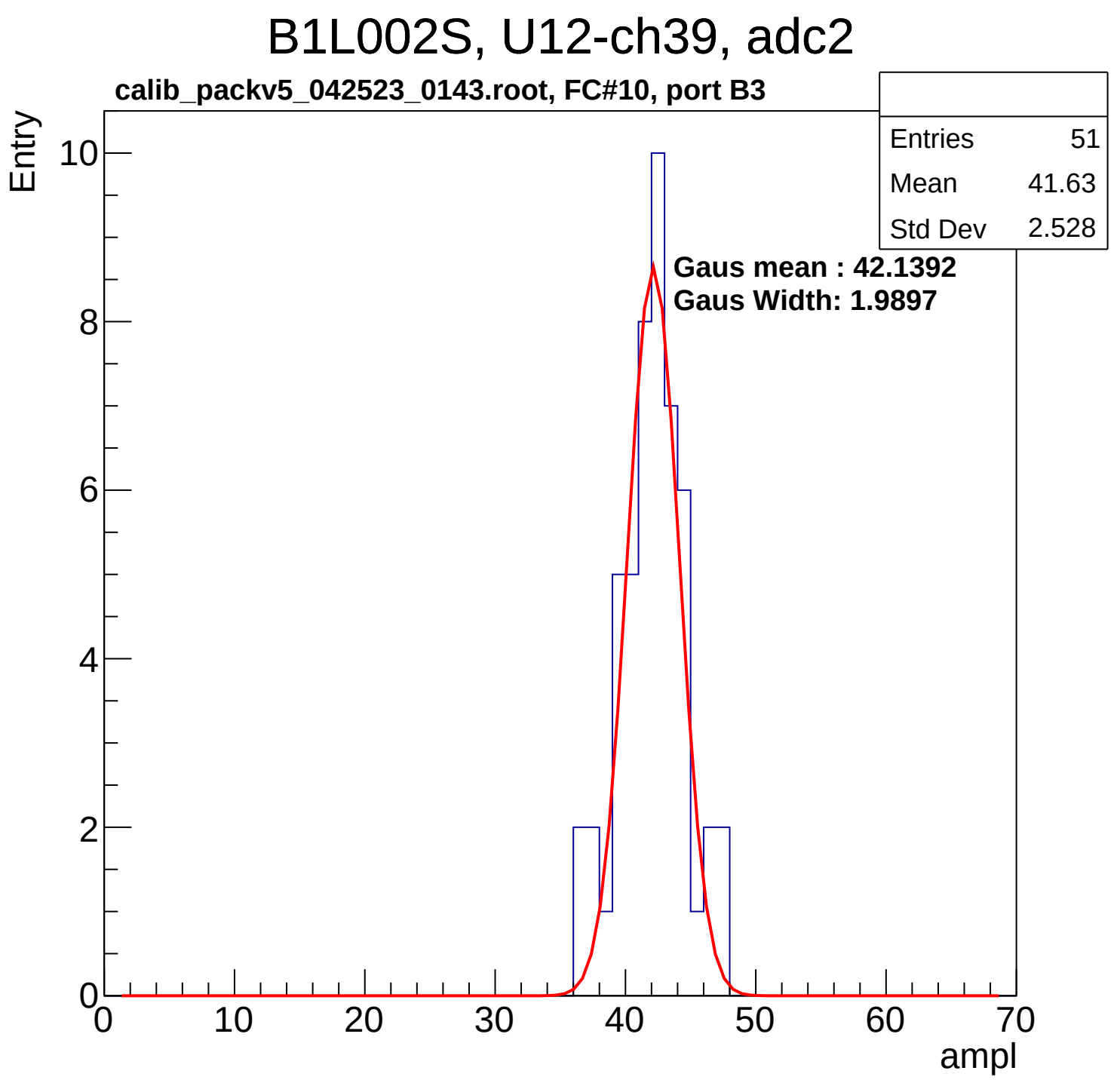
B1L002S, U12-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 41.63
Std Dev 2.528
Gaus mean : 42.1392
Gaus Width: 1.9897
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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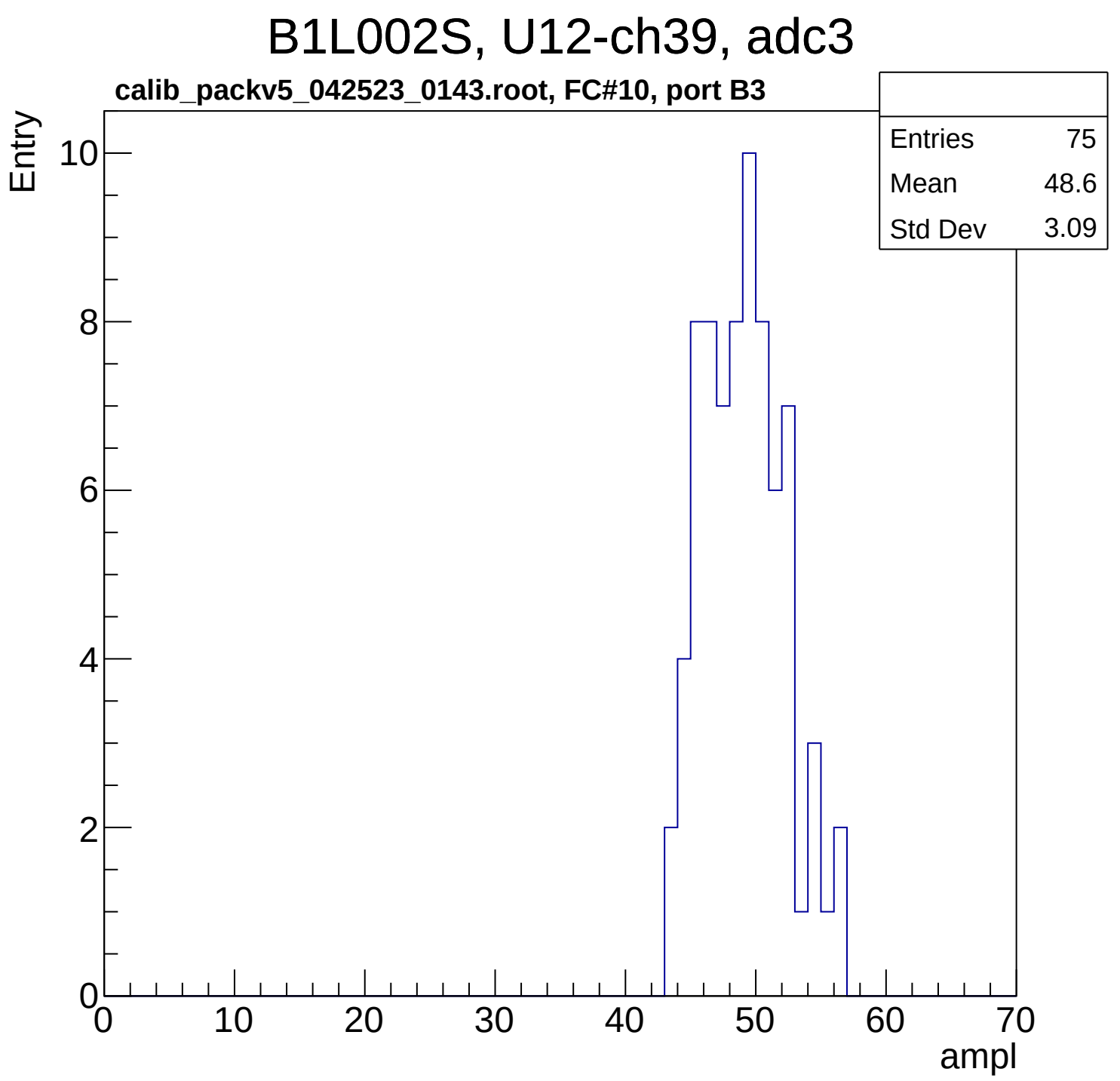
B1L002S, U12-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 48.6
Std Dev 3.09
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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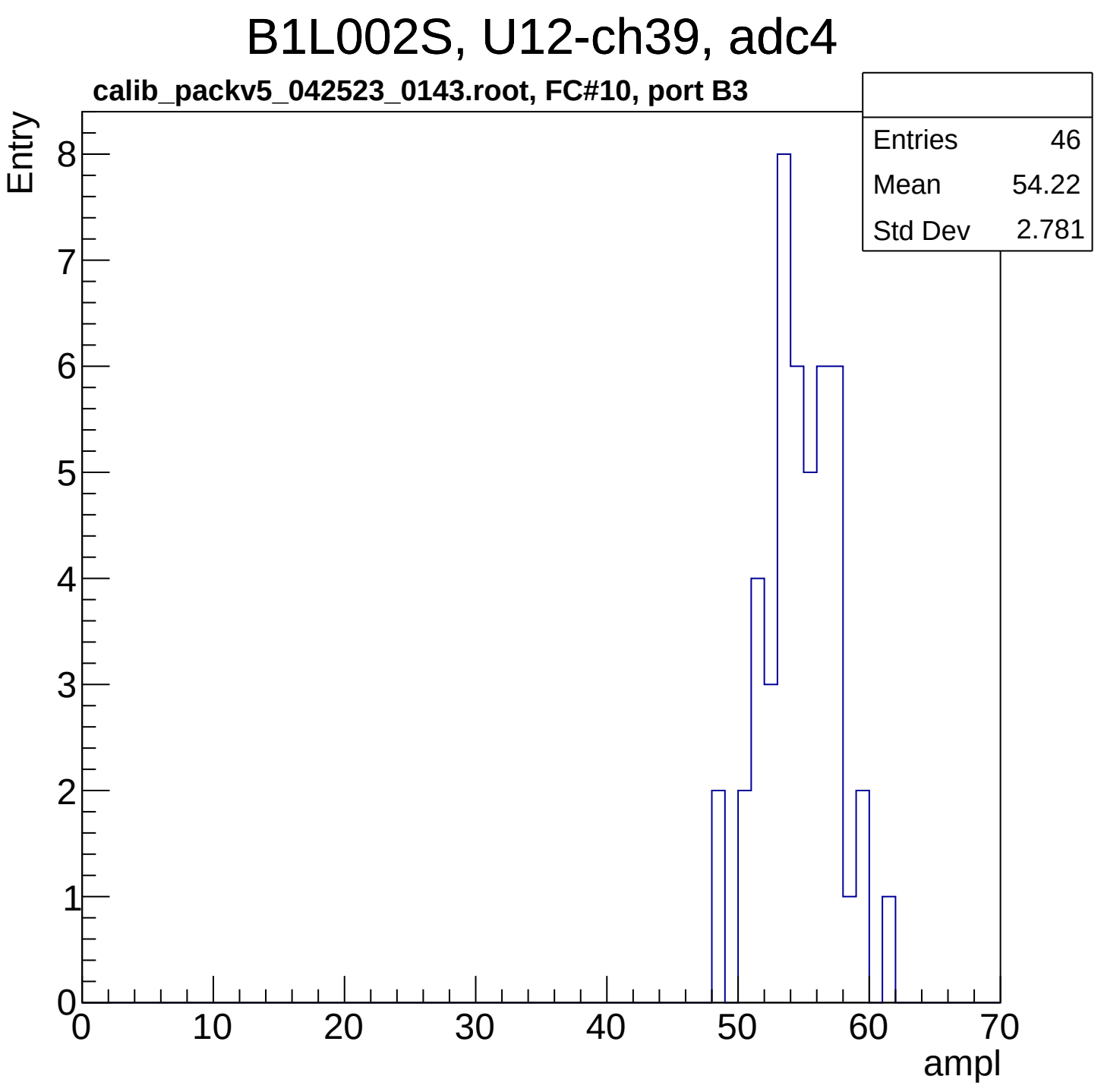
B1L002S, U12-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 54.22
Std Dev 2.781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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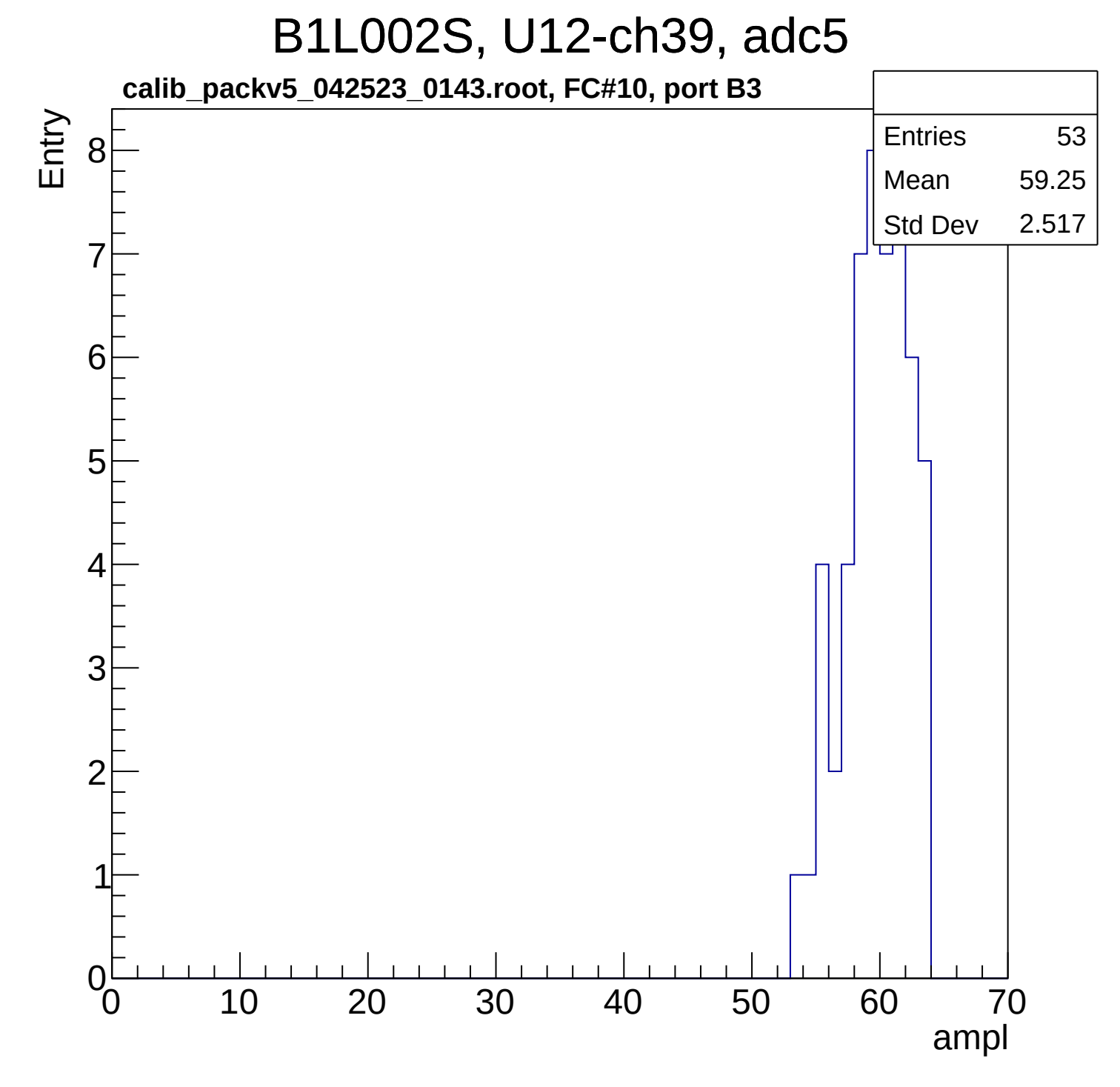
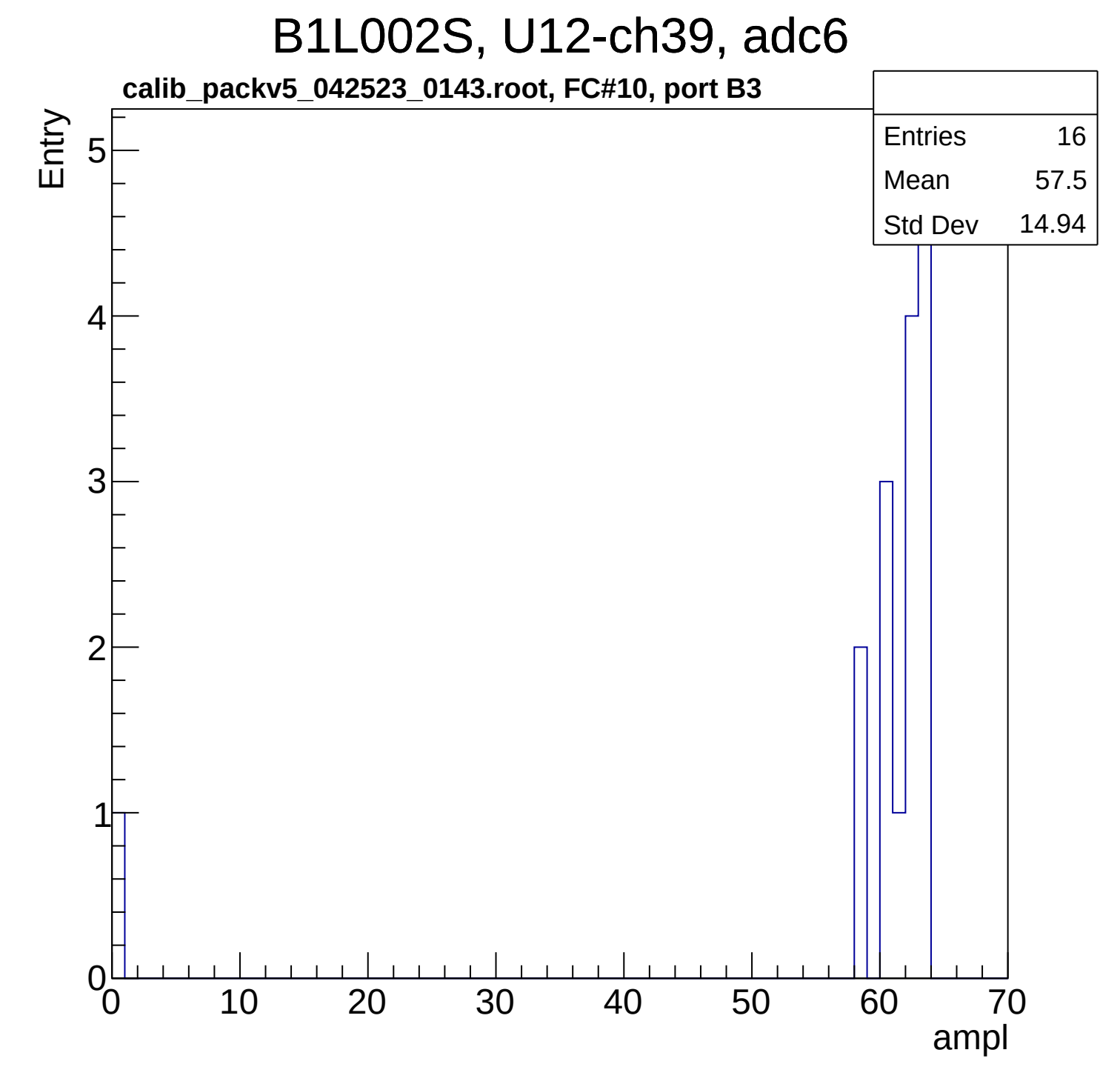
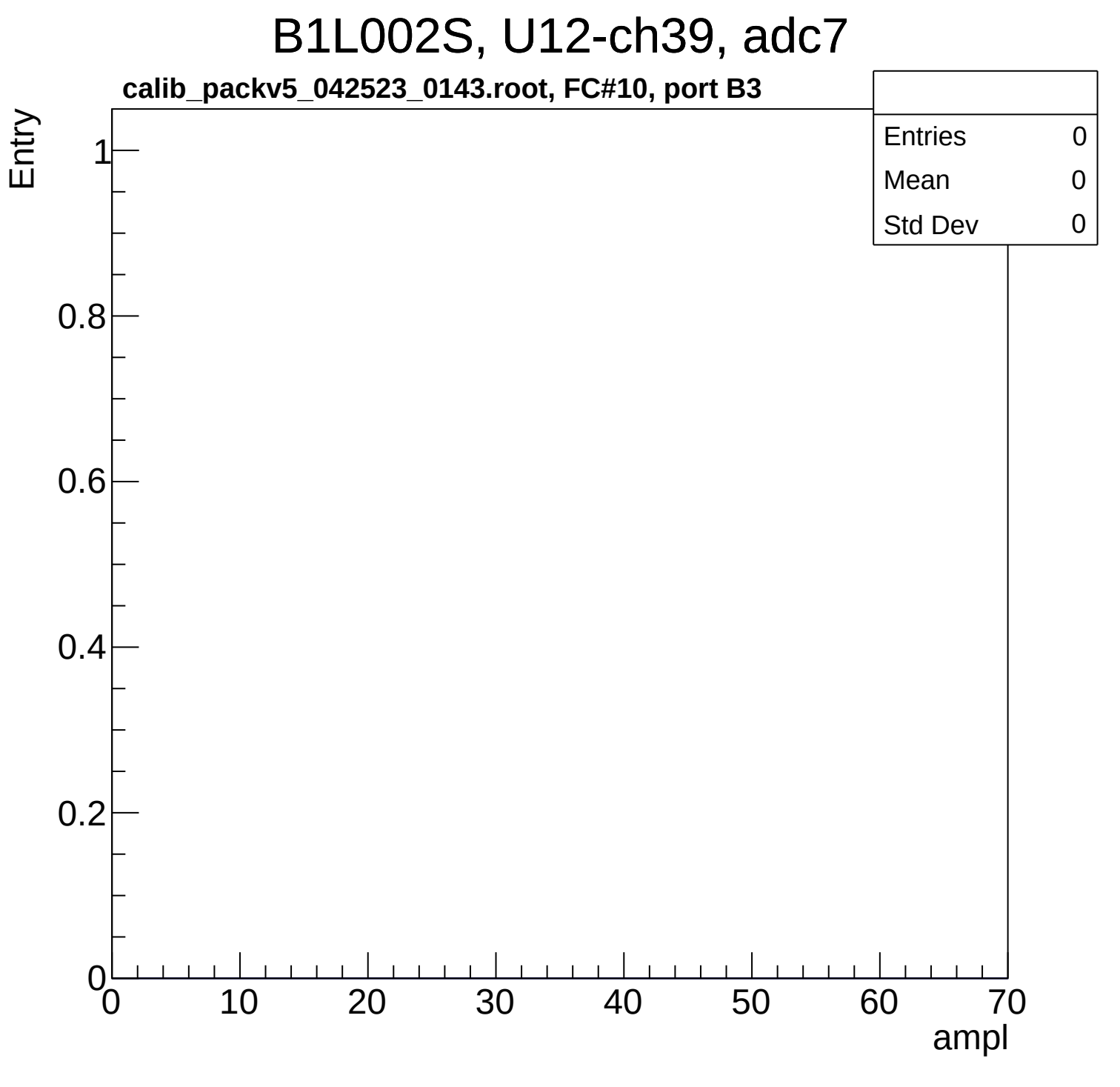
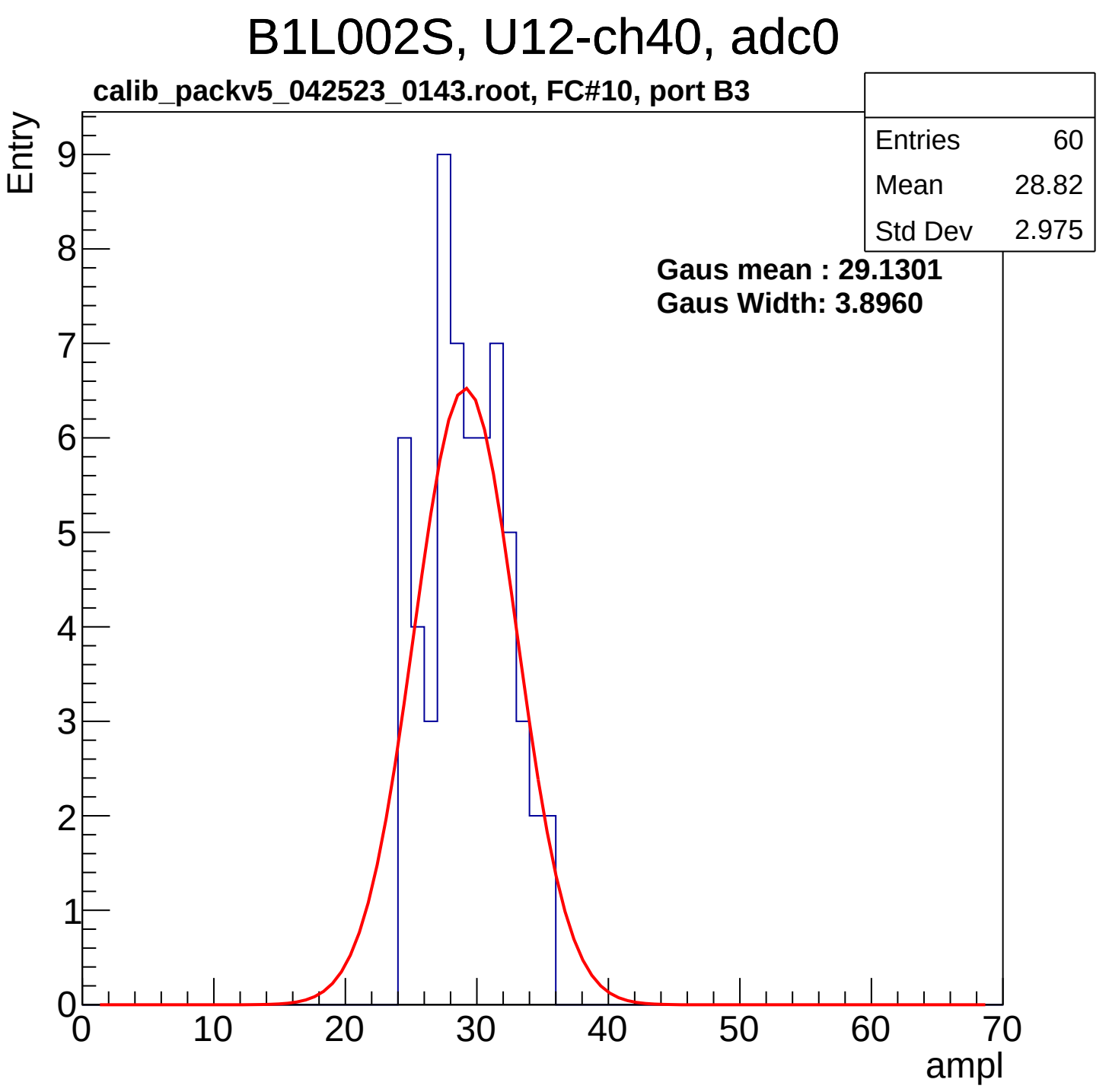

B1L002S, U12-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.25
Std Dev 2.517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.5
Std Dev 14.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.82
Std Dev 2.975
Gaus mean : 29.1301
Gaus Width: 3.8960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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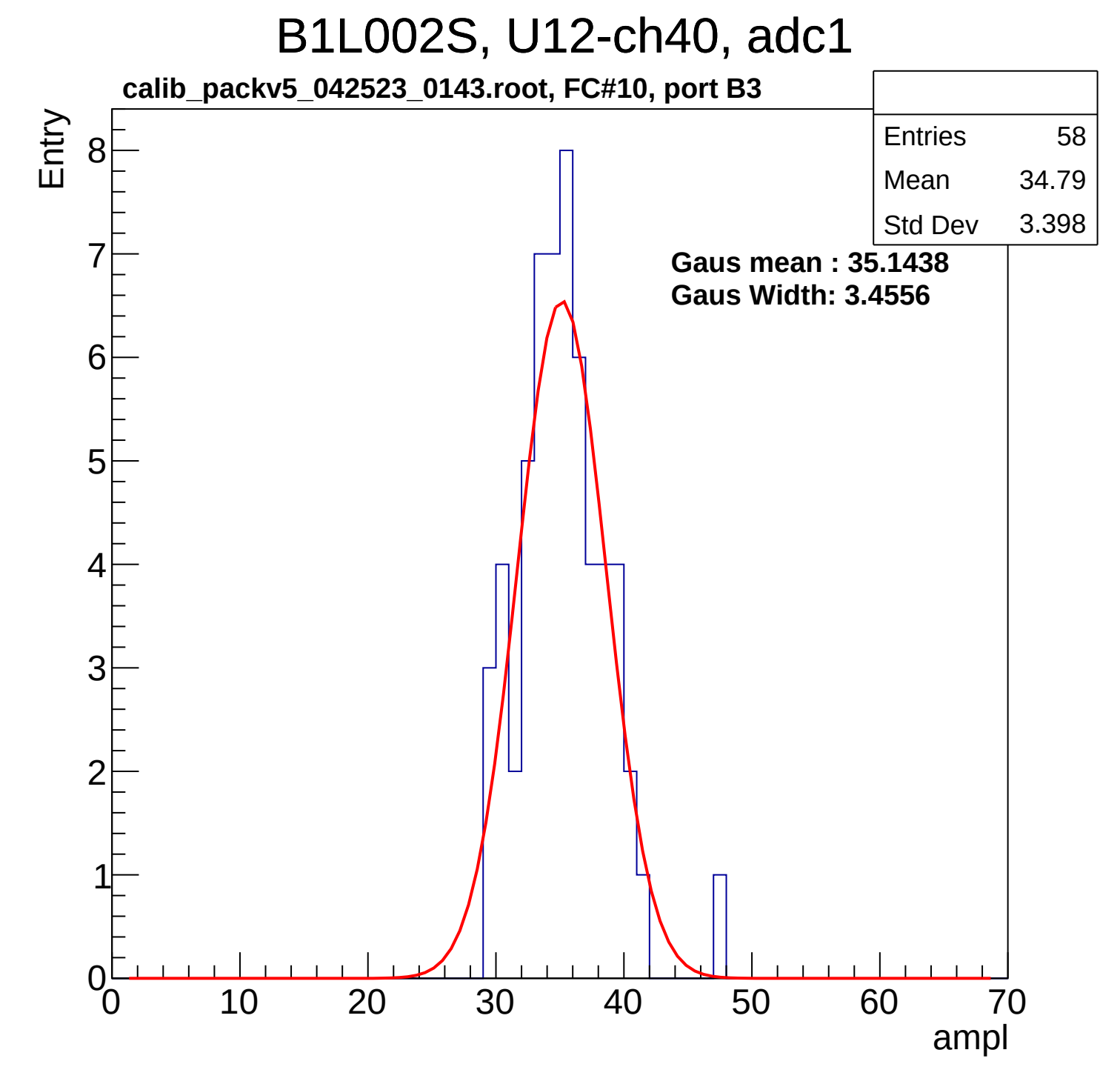
B1L002S, U12-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.79
Std Dev 3.398
Gaus mean : 35.1438
Gaus Width: 3.4556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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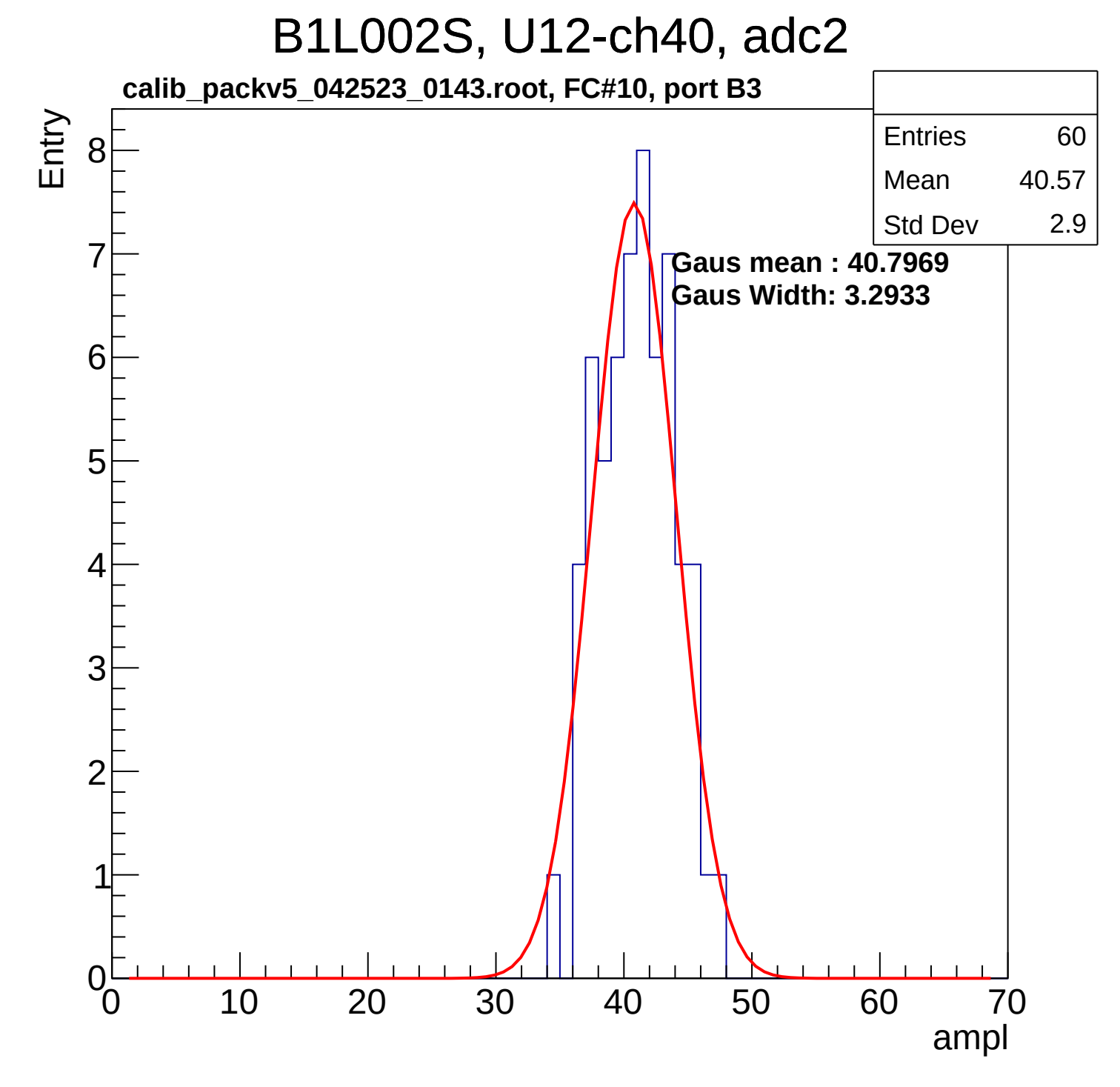
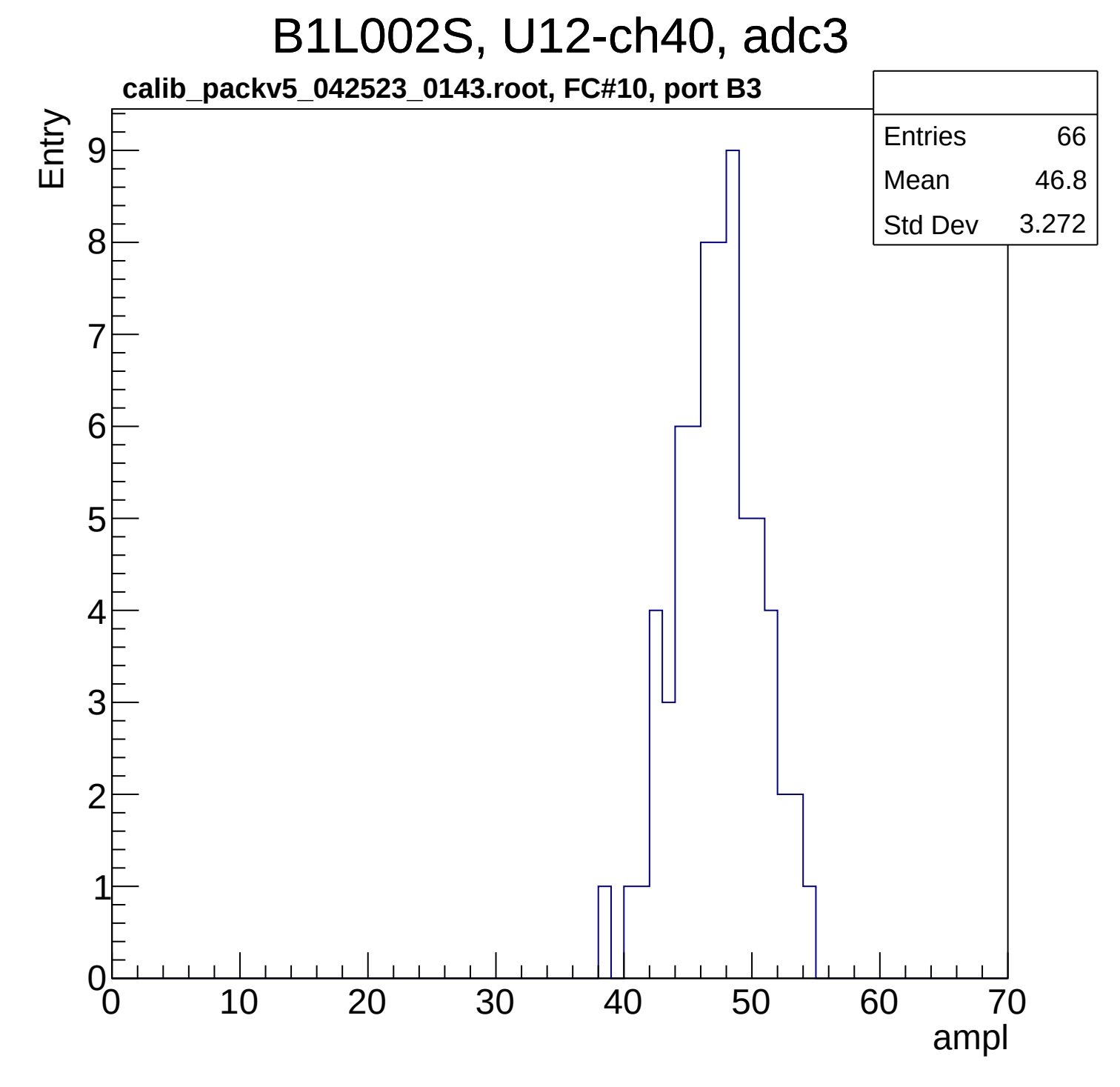
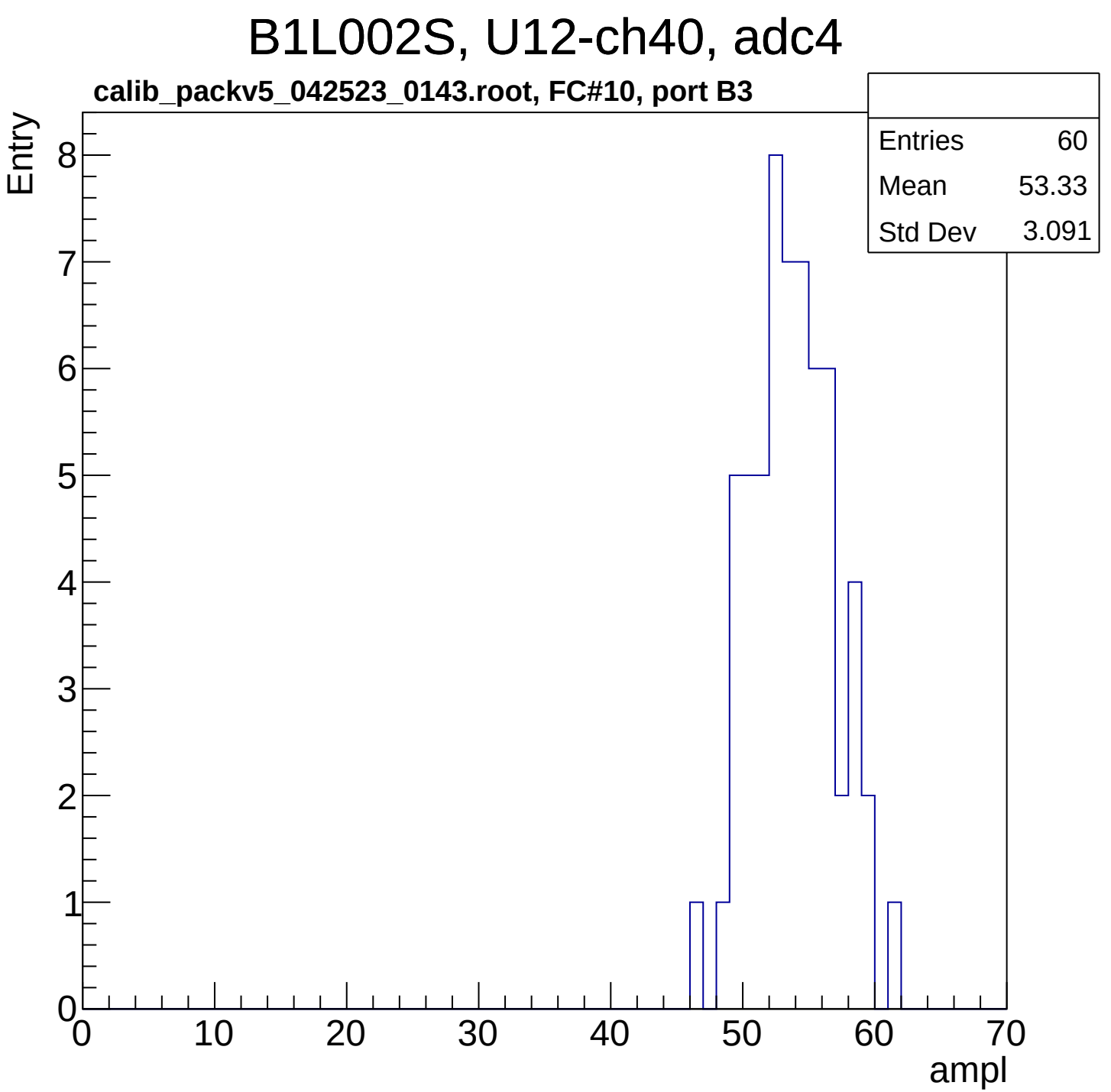
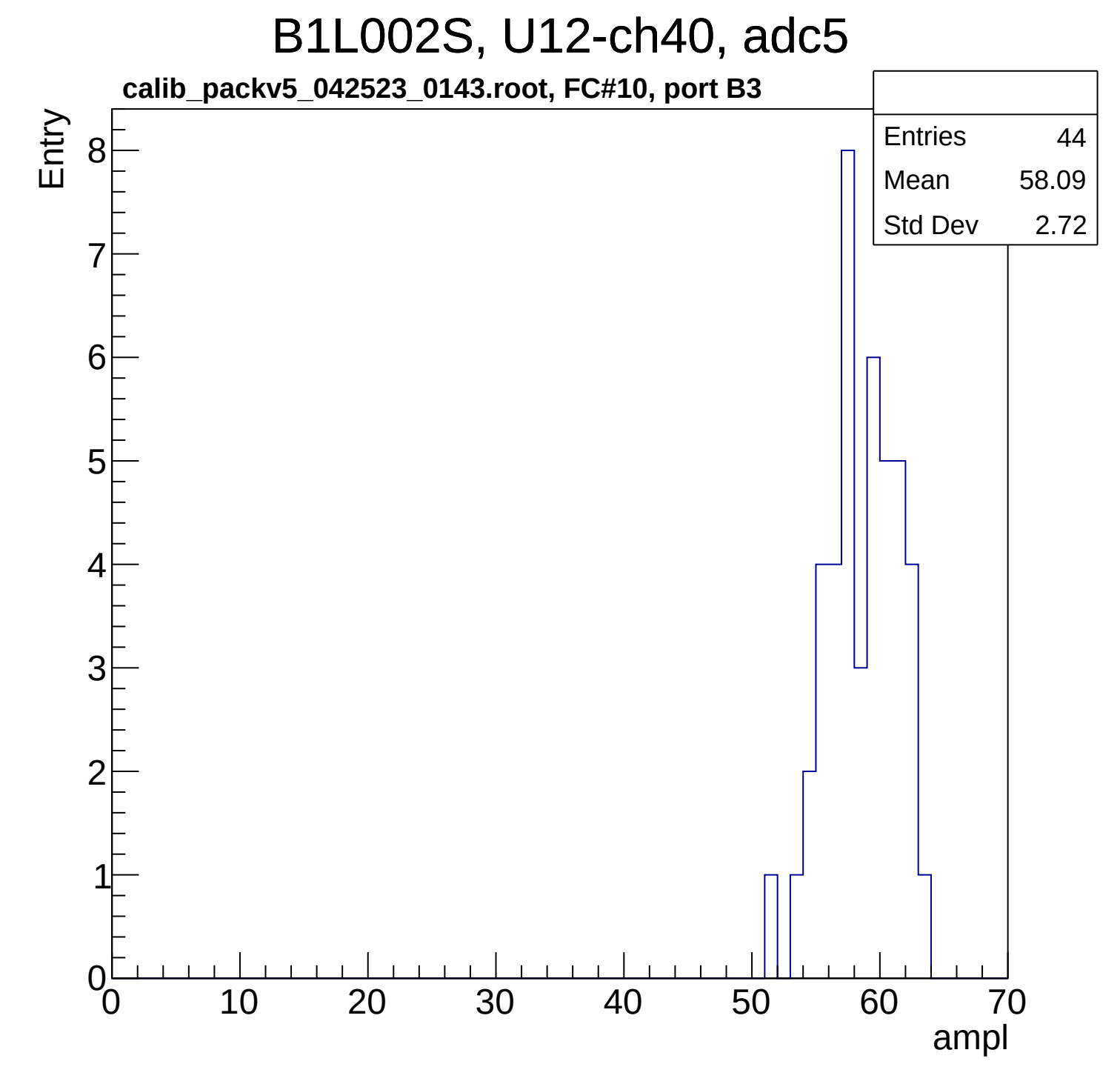

B1L002S, U12-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.57
Std Dev 2.9
Gaus mean : 40.7969
Gaus Width: 3.2933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.8
Std Dev 3.272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.33
Std Dev 3.091
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.09
Std Dev 2.72
Entry
ampl
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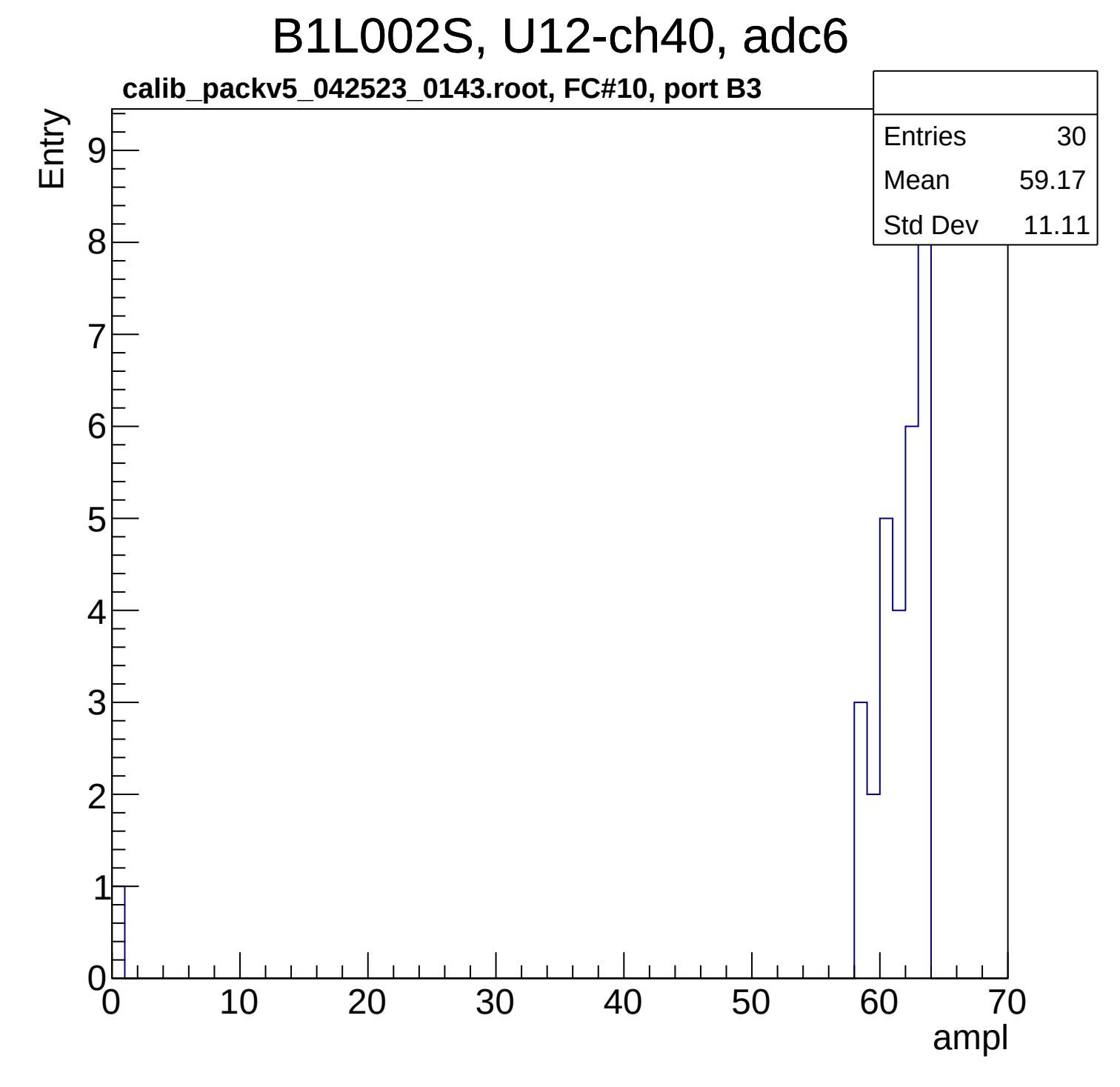
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 59.17
Std Dev 11.11
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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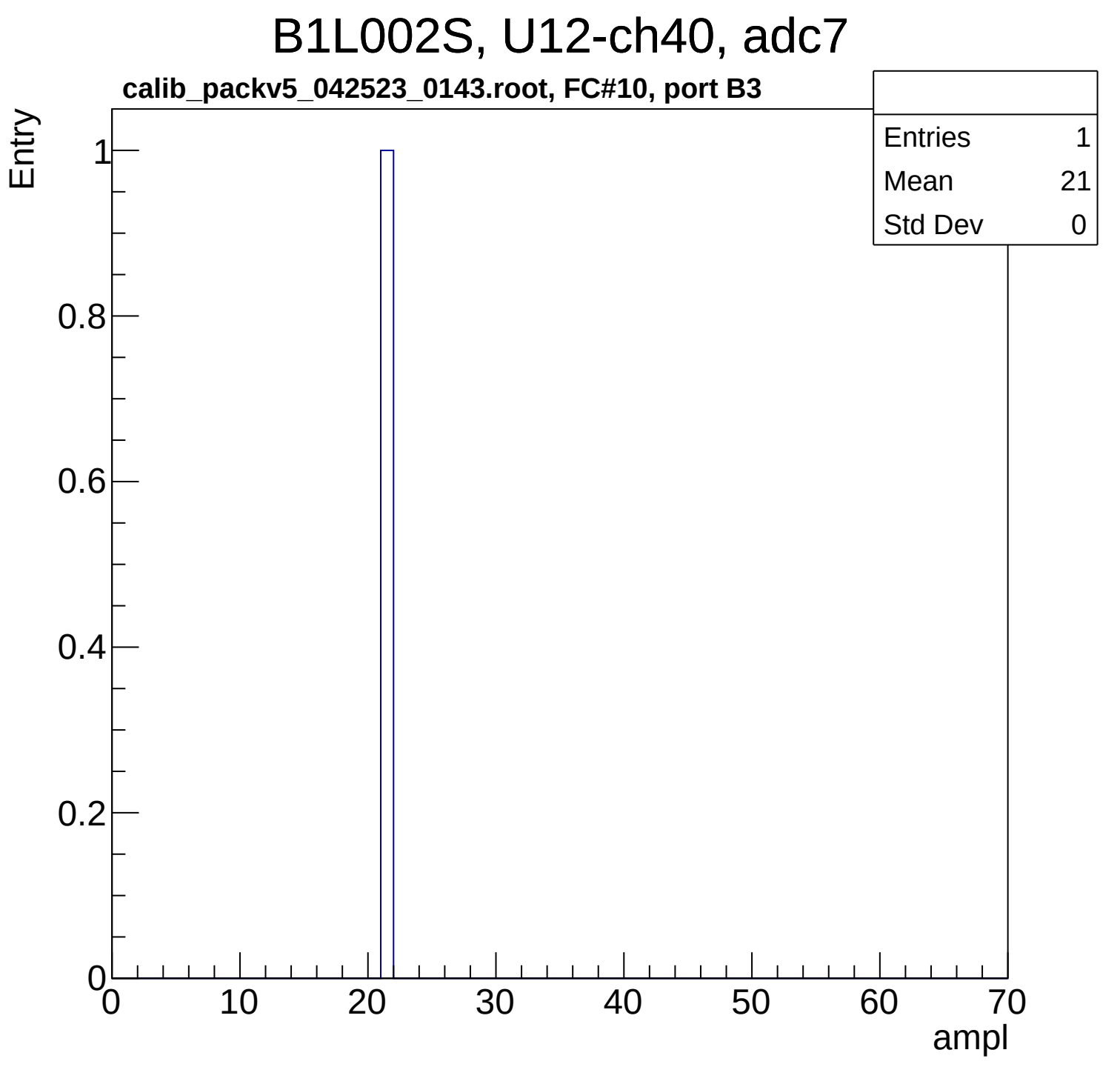
B1L002S, U12-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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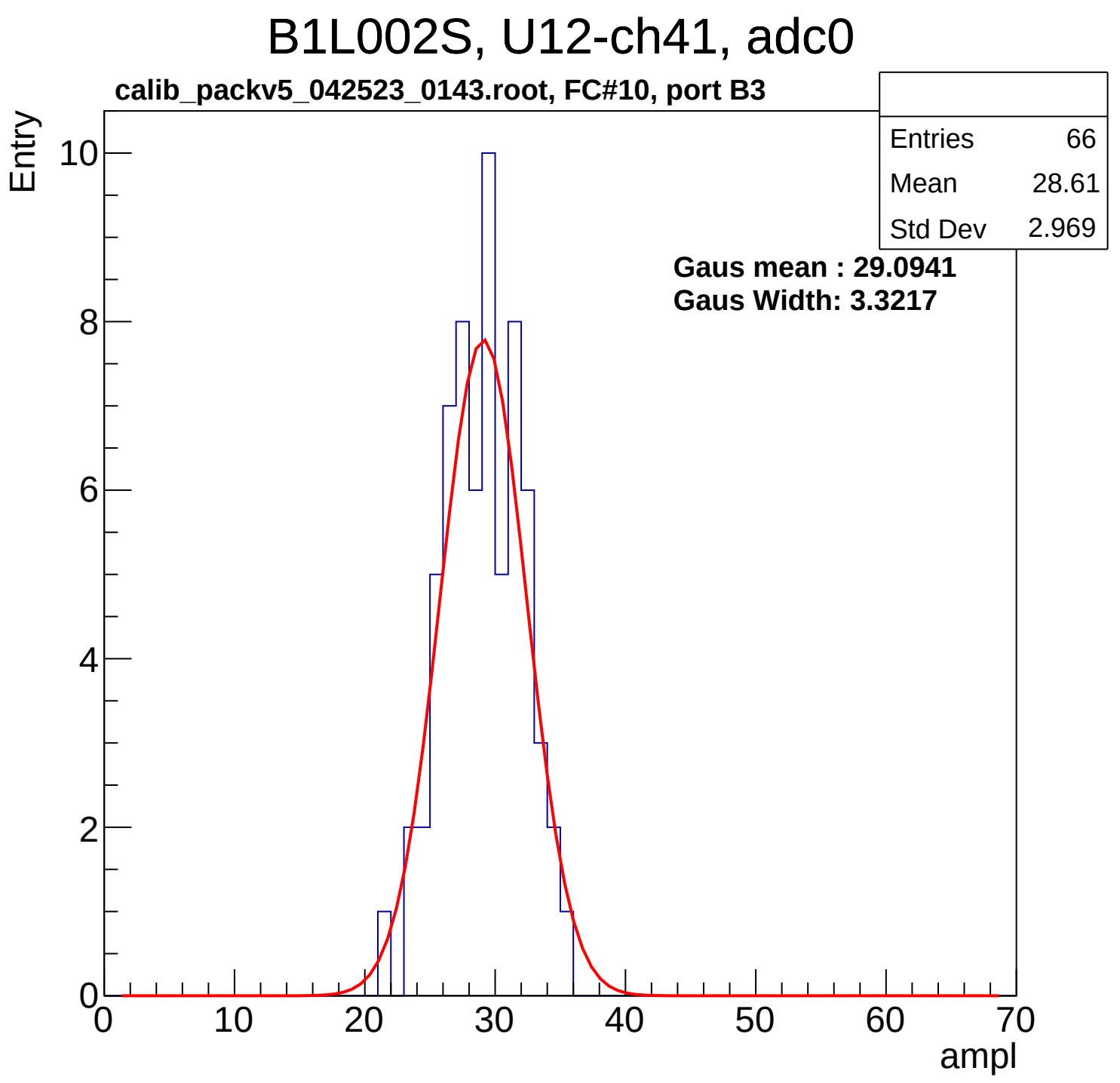
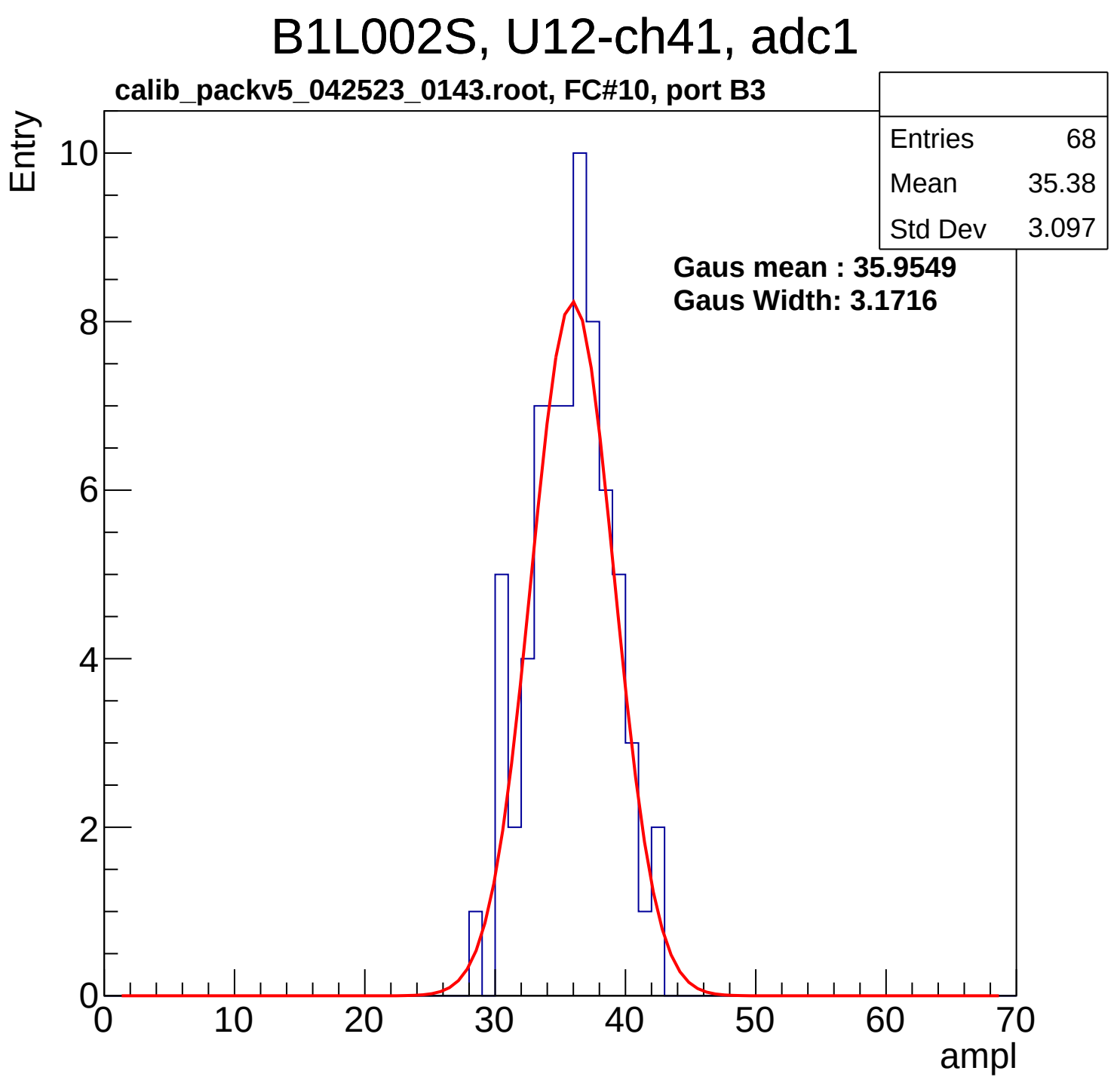
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.61
Std Dev 2.969
Gaus mean : 29.0941
Gaus Width: 3.3217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.38
Std Dev 3.097
Gaus mean : 35.9549
Gaus Width: 3.1716
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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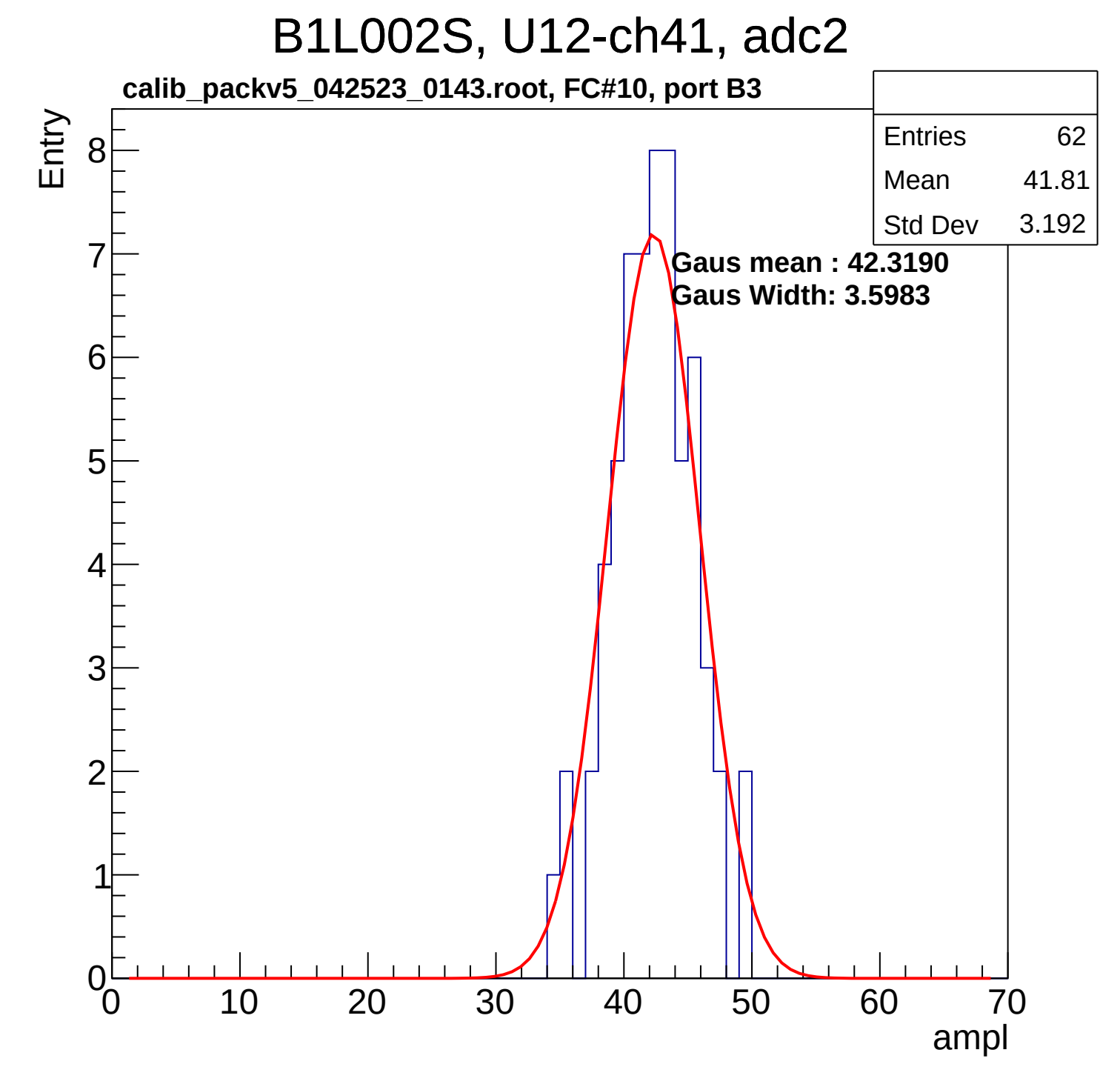
B1L002S, U12-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.81
Std Dev 3.192
Gaus mean : 42.3190
Gaus Width: 3.5983
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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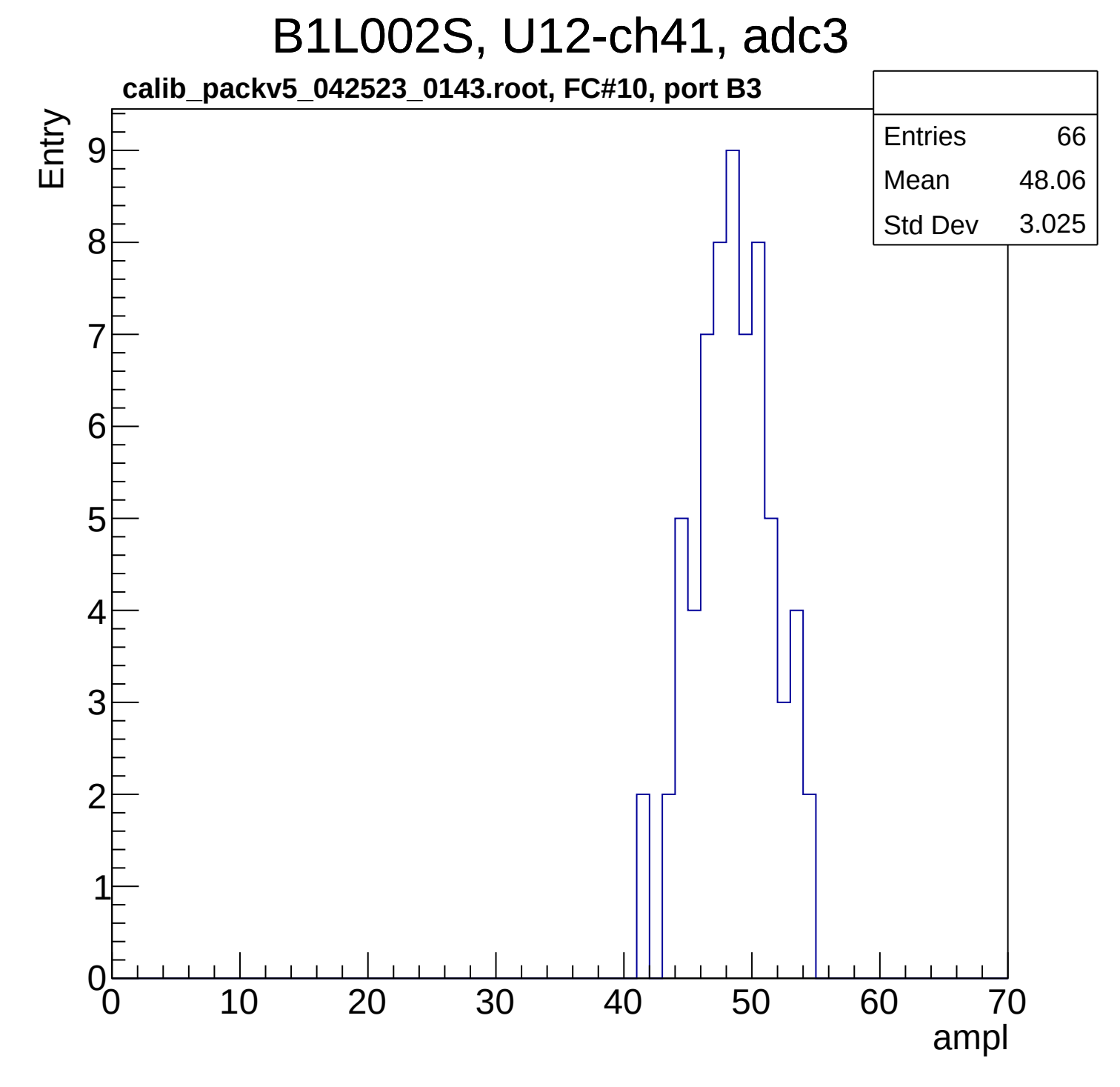
B1L002S, U12-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.06
Std Dev 3.025
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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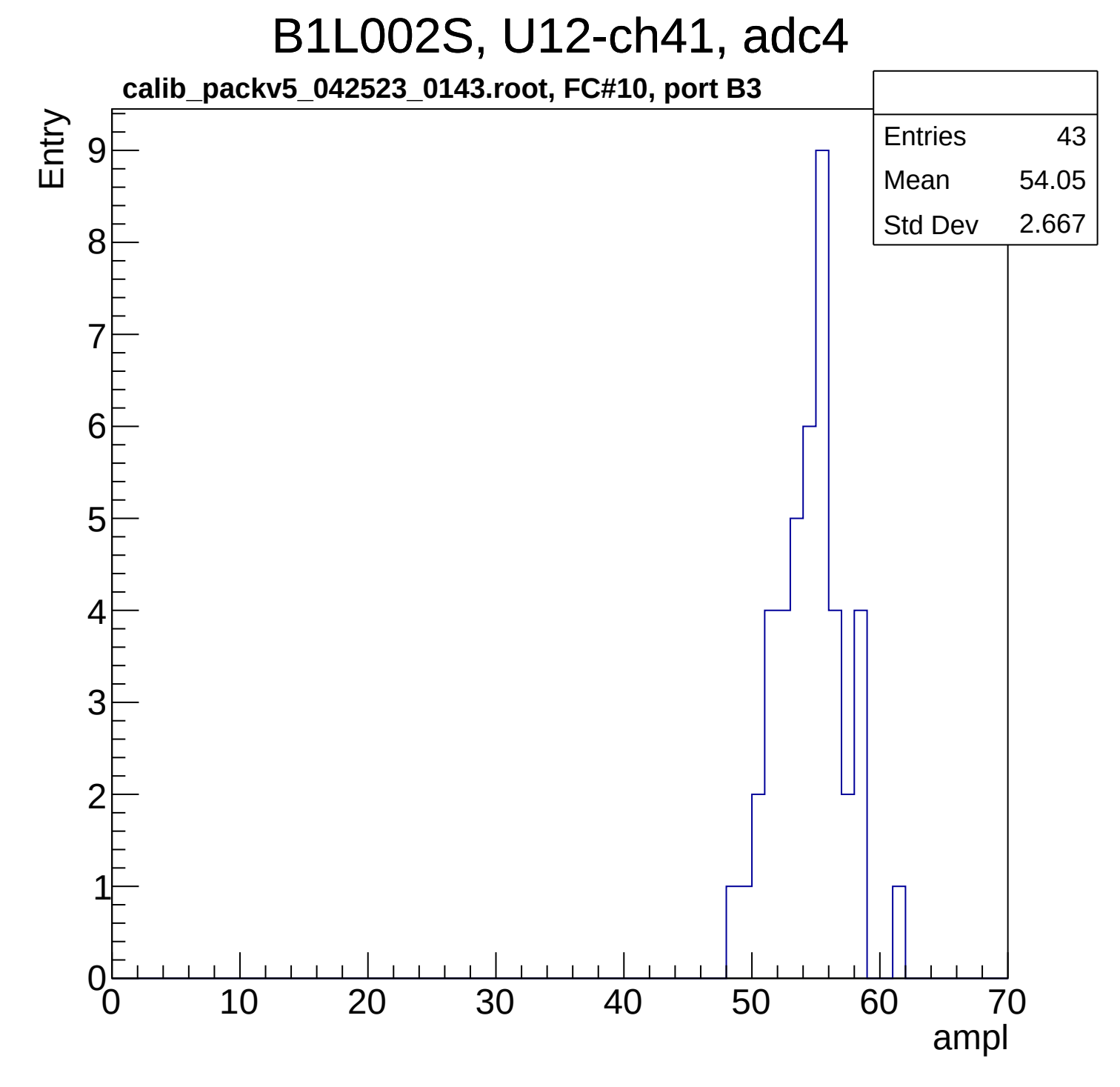
B1L002S, U12-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 54.05
Std Dev 2.667
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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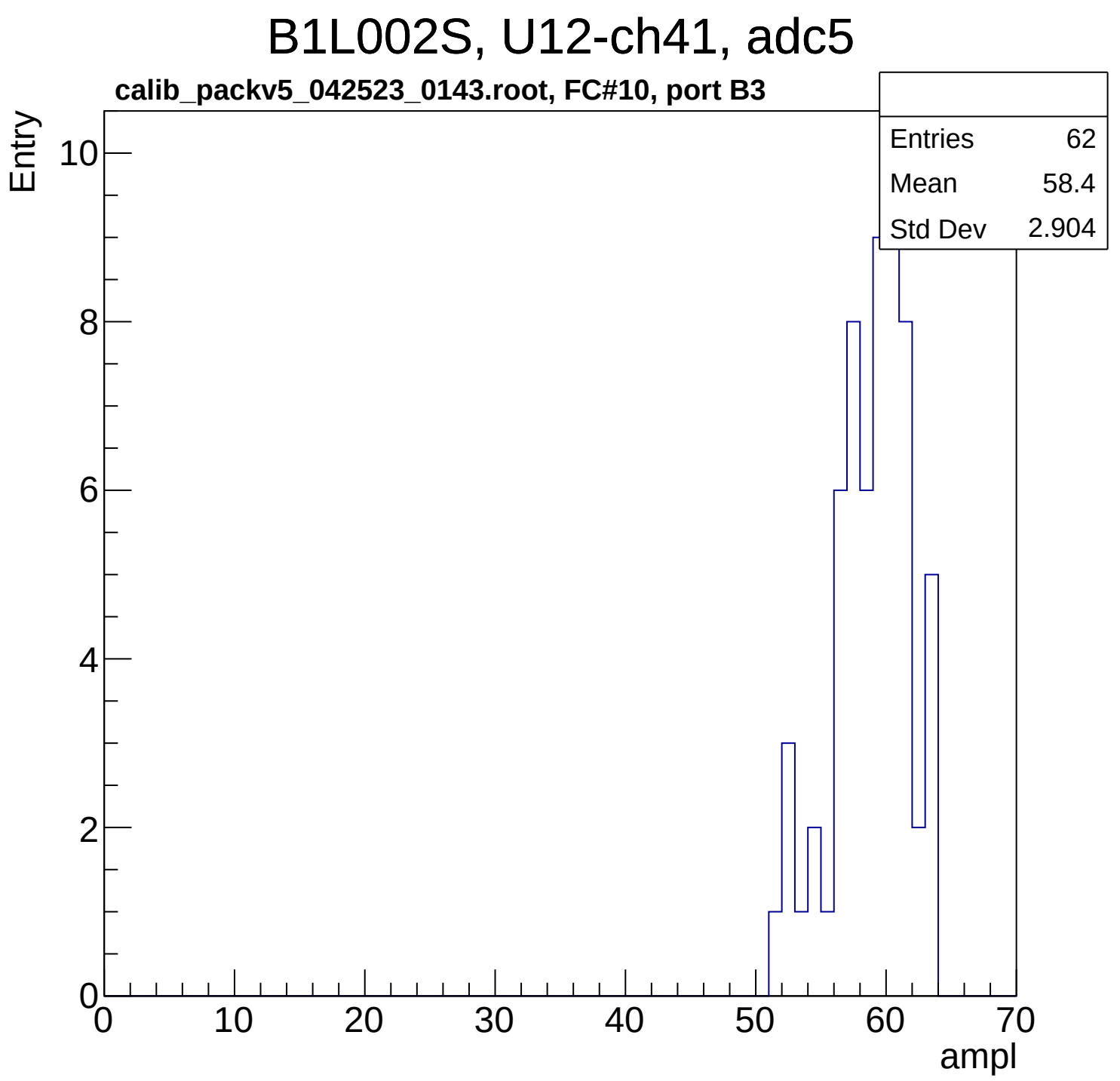
B1L002S, U12-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.4
Std Dev 2.904
Entry
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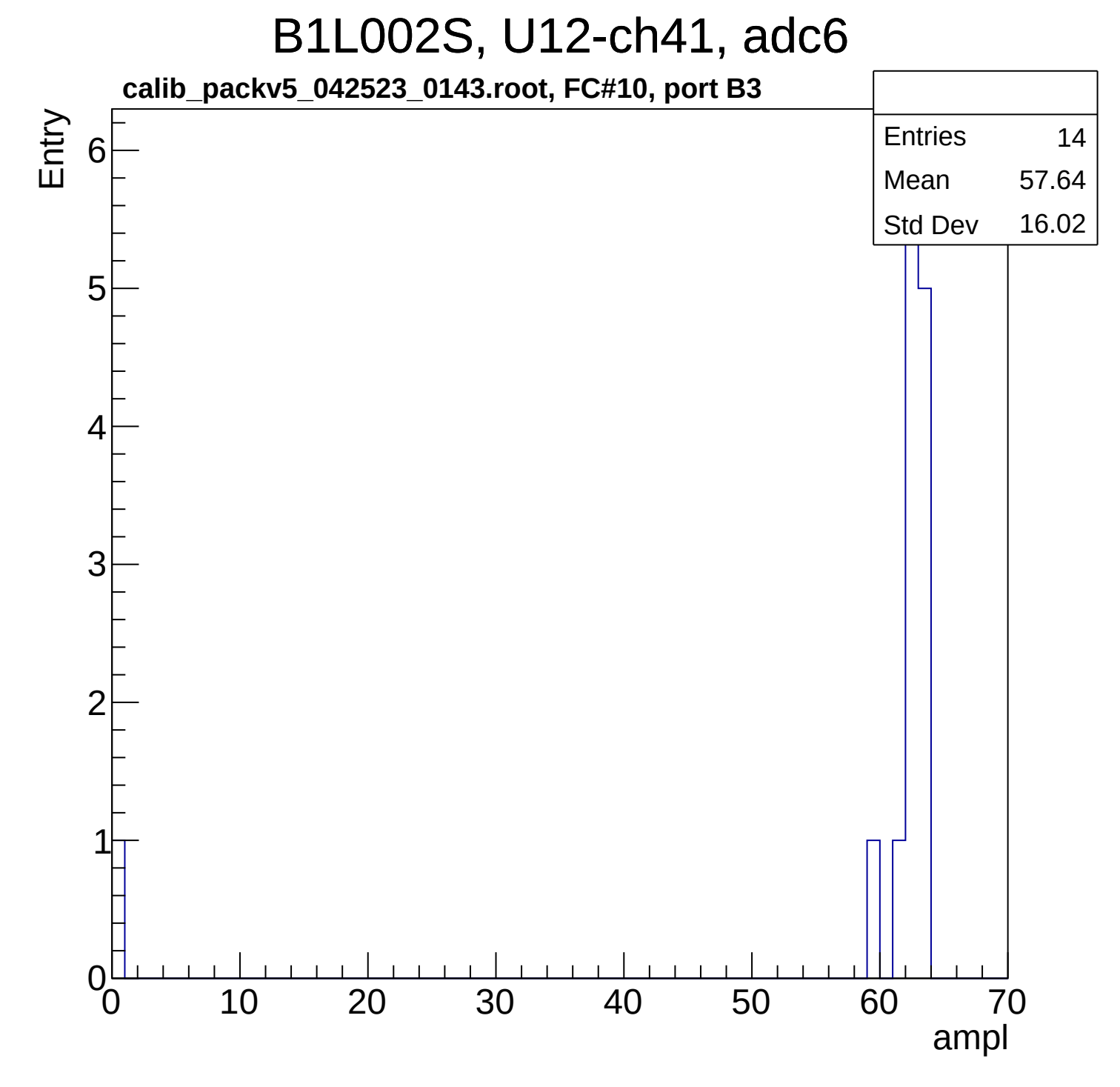
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.64
Std Dev 16.02
Entry
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2
3
4
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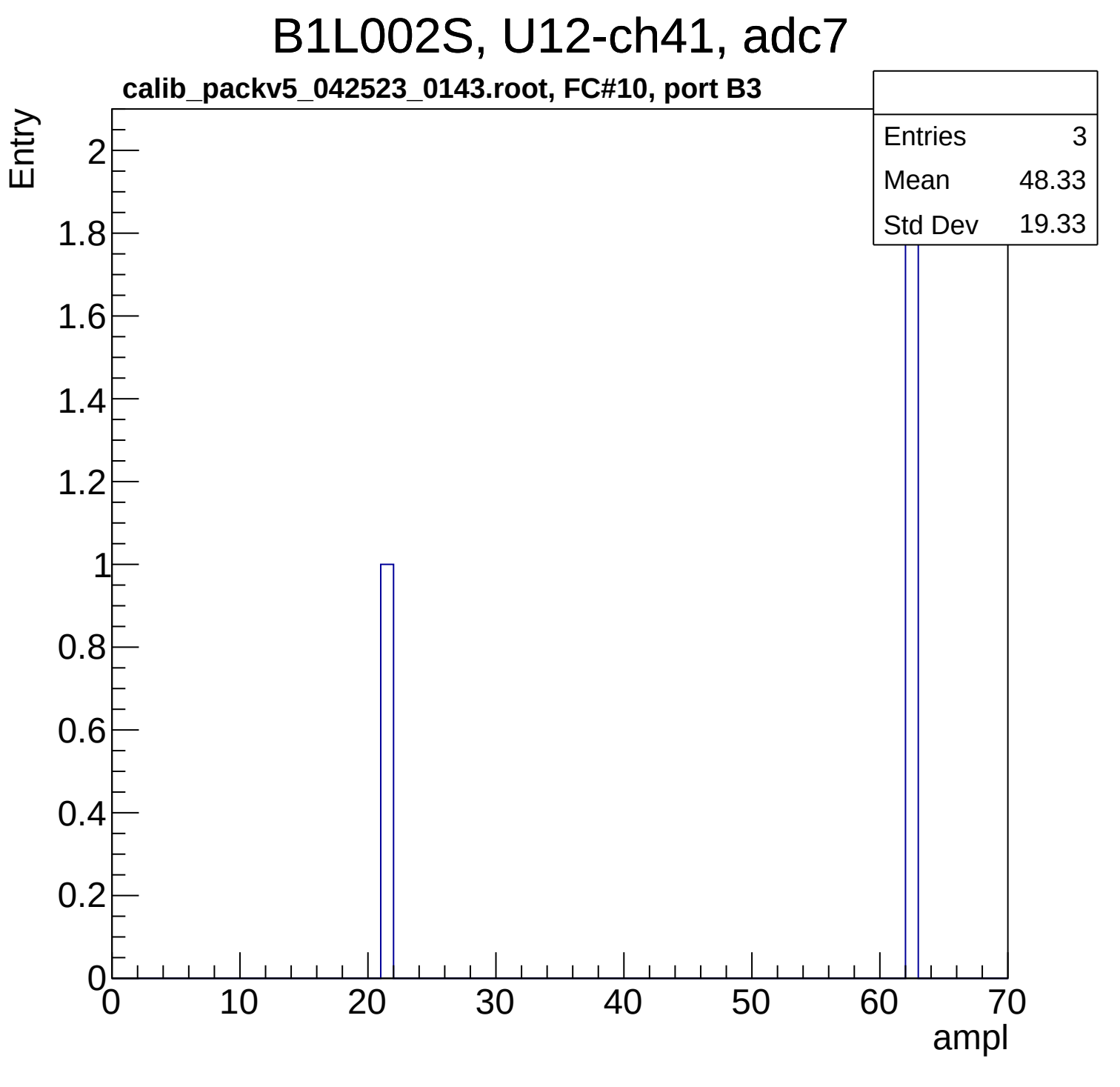
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 19.33
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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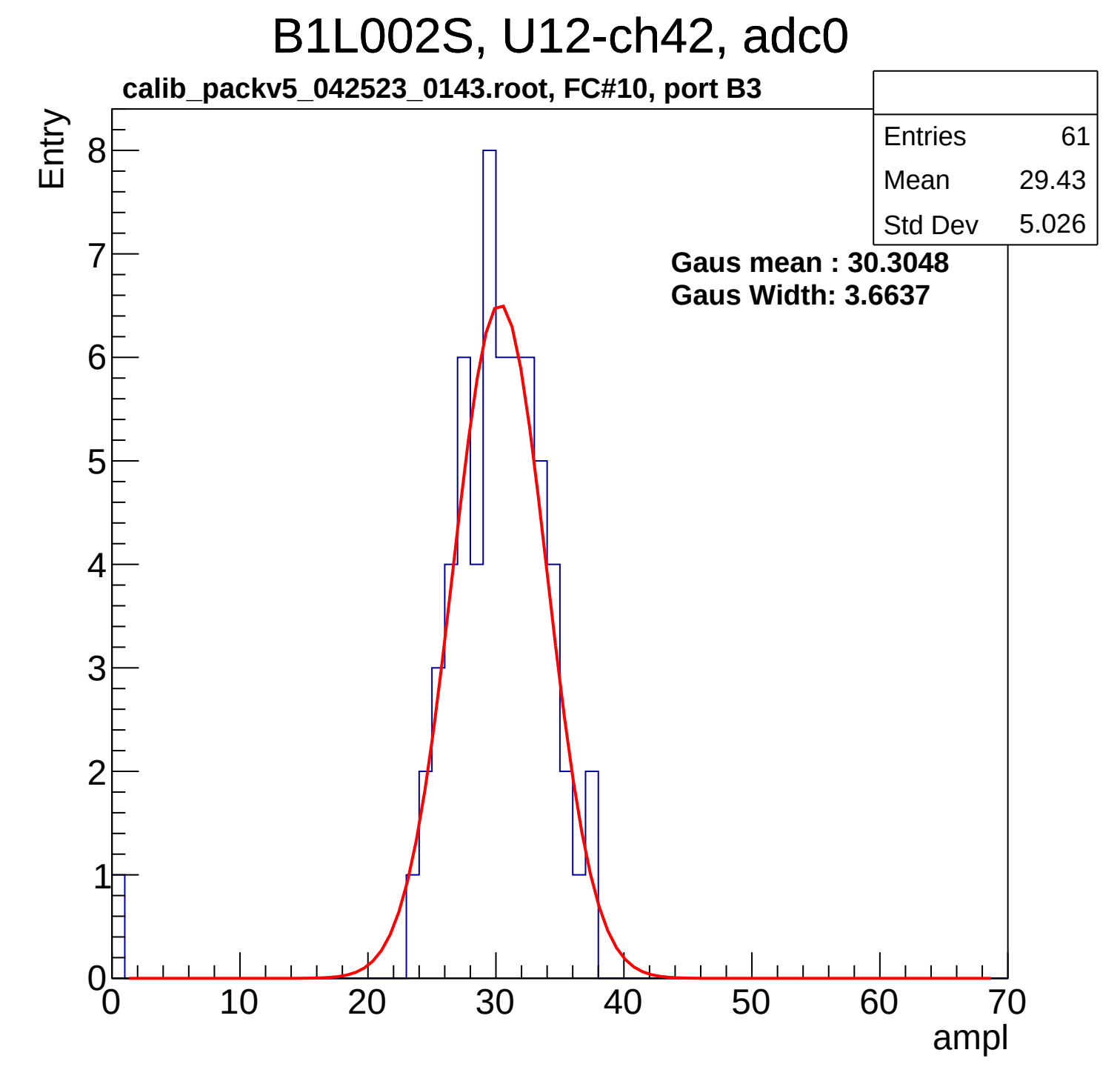
B1L002S, U12-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.43
Std Dev 5.026
Gaus mean : 30.3048
Gaus Width: 3.6637
Entry
ampl
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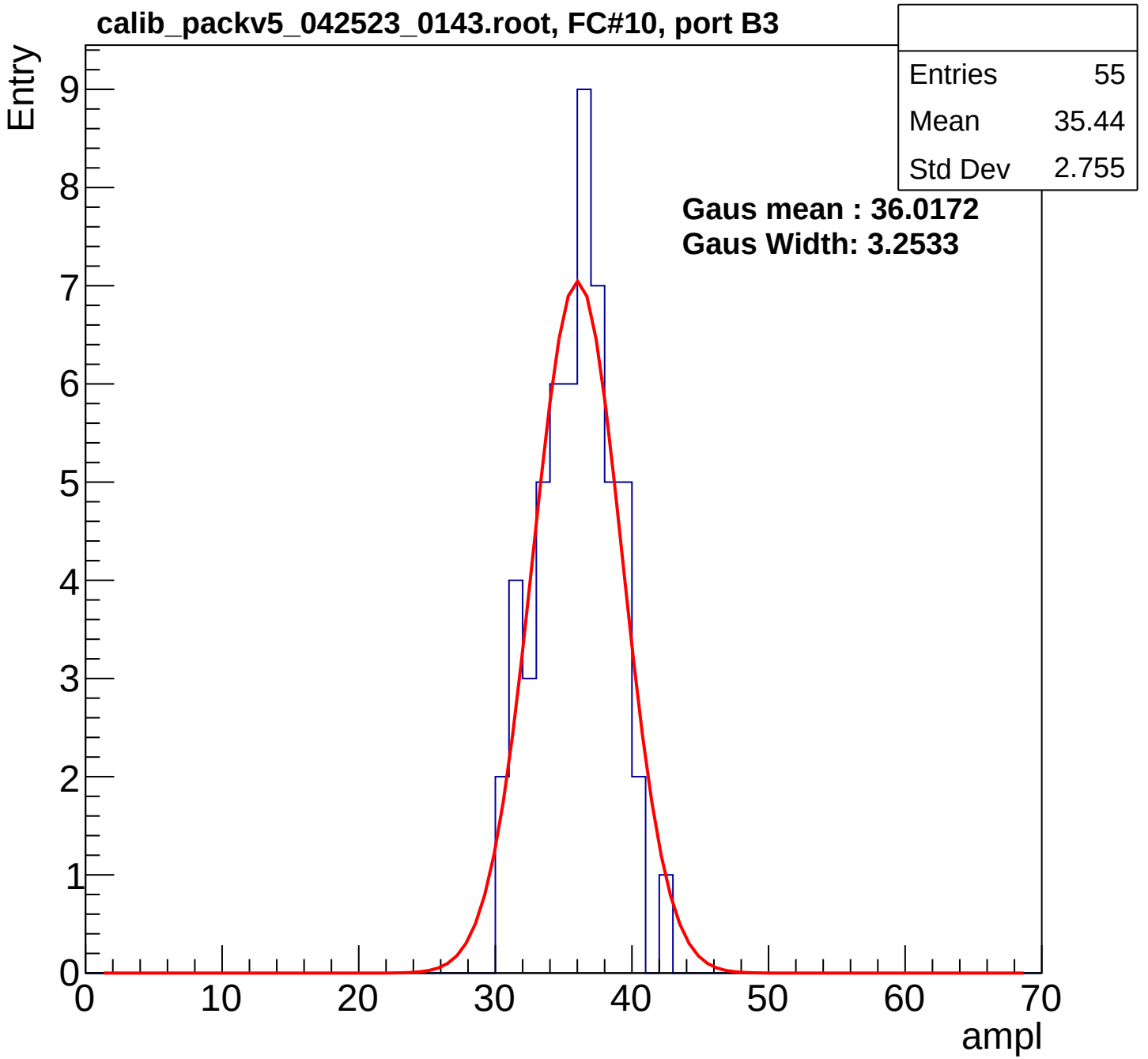
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.44
Std Dev 2.755
Gaus mean : 36.0172
Gaus Width: 3.2533
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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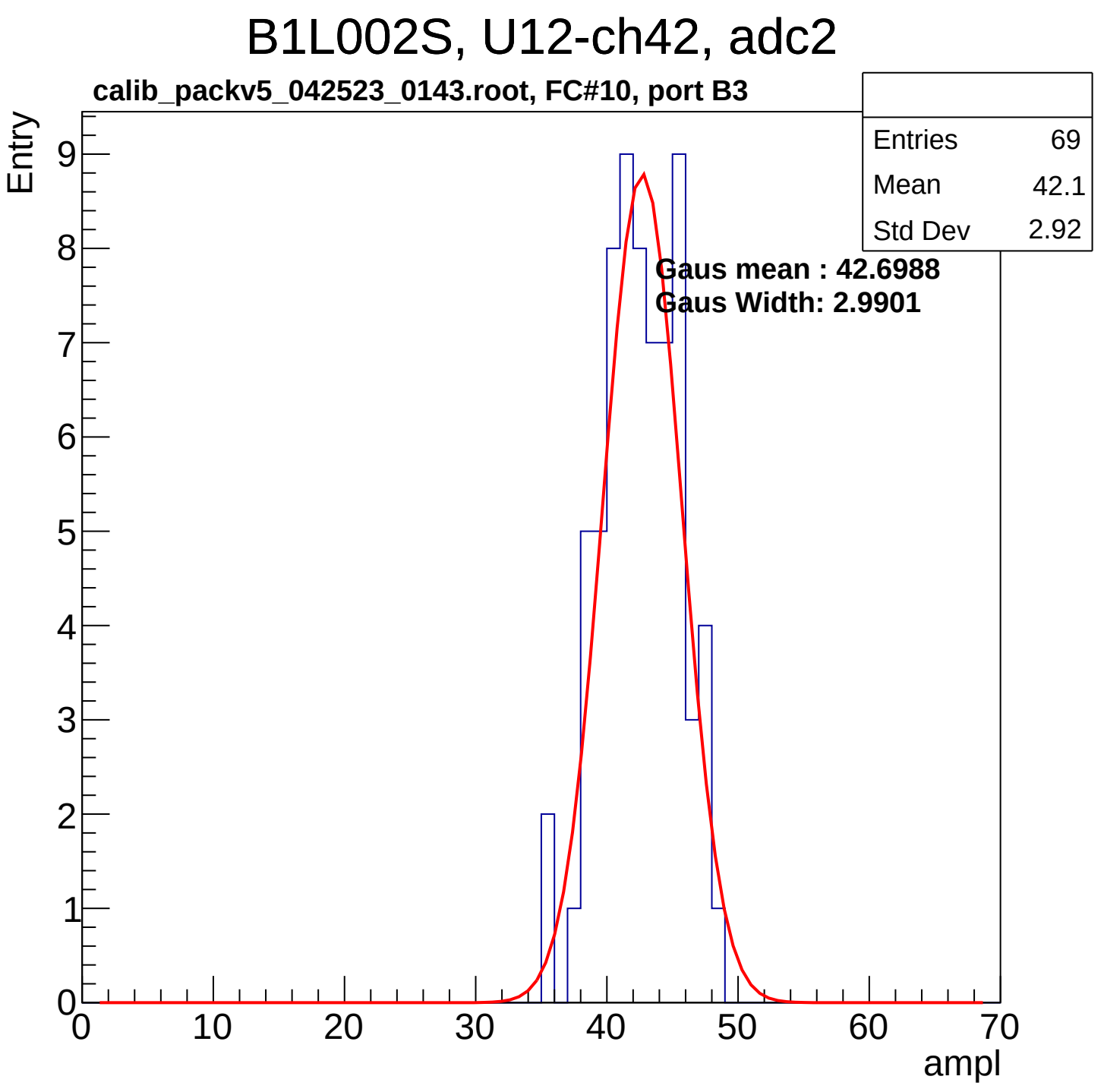
B1L002S, U12-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.1
Std Dev 2.92
Gaus mean : 42.6988
Gaus Width: 2.9901
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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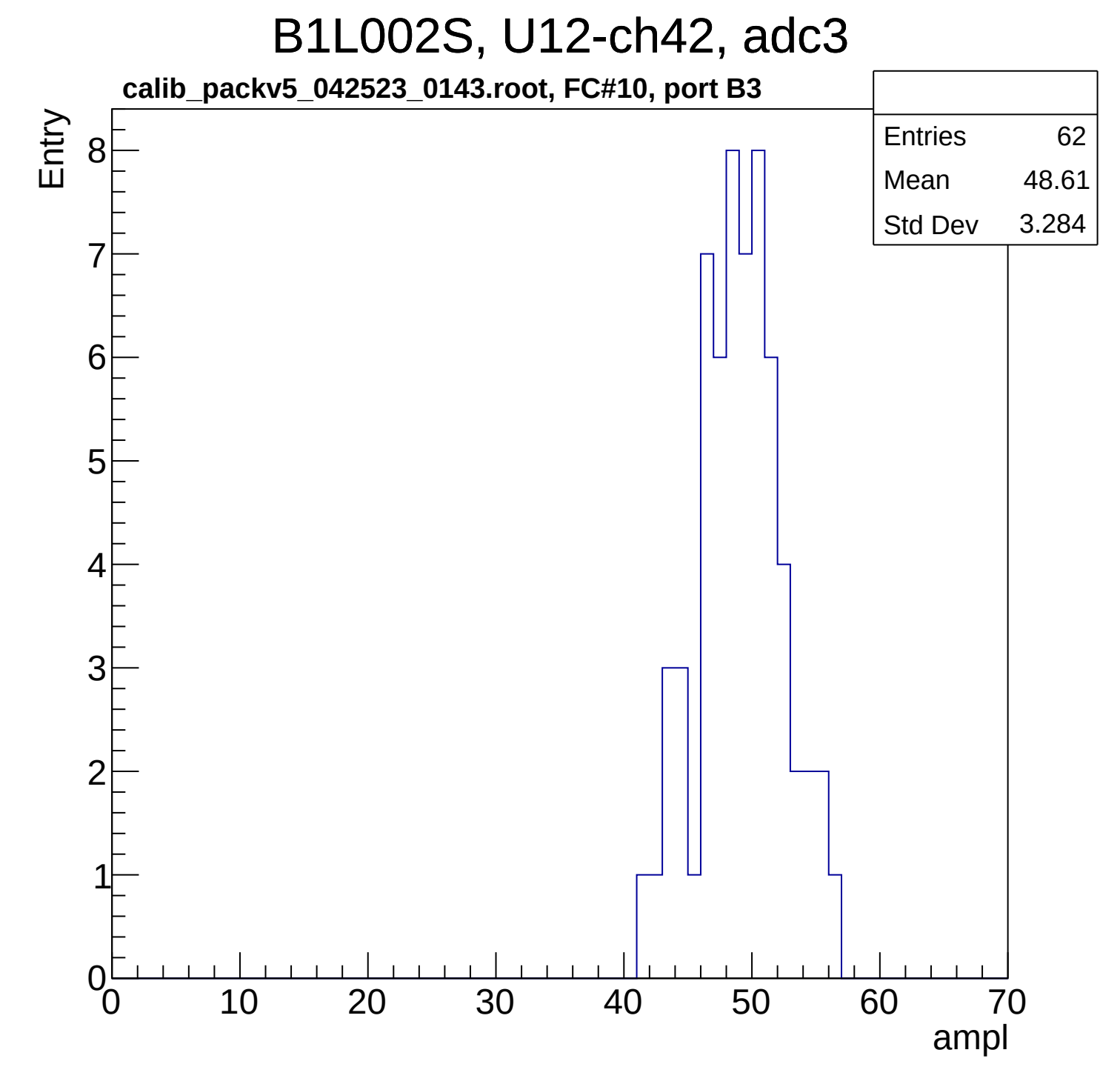
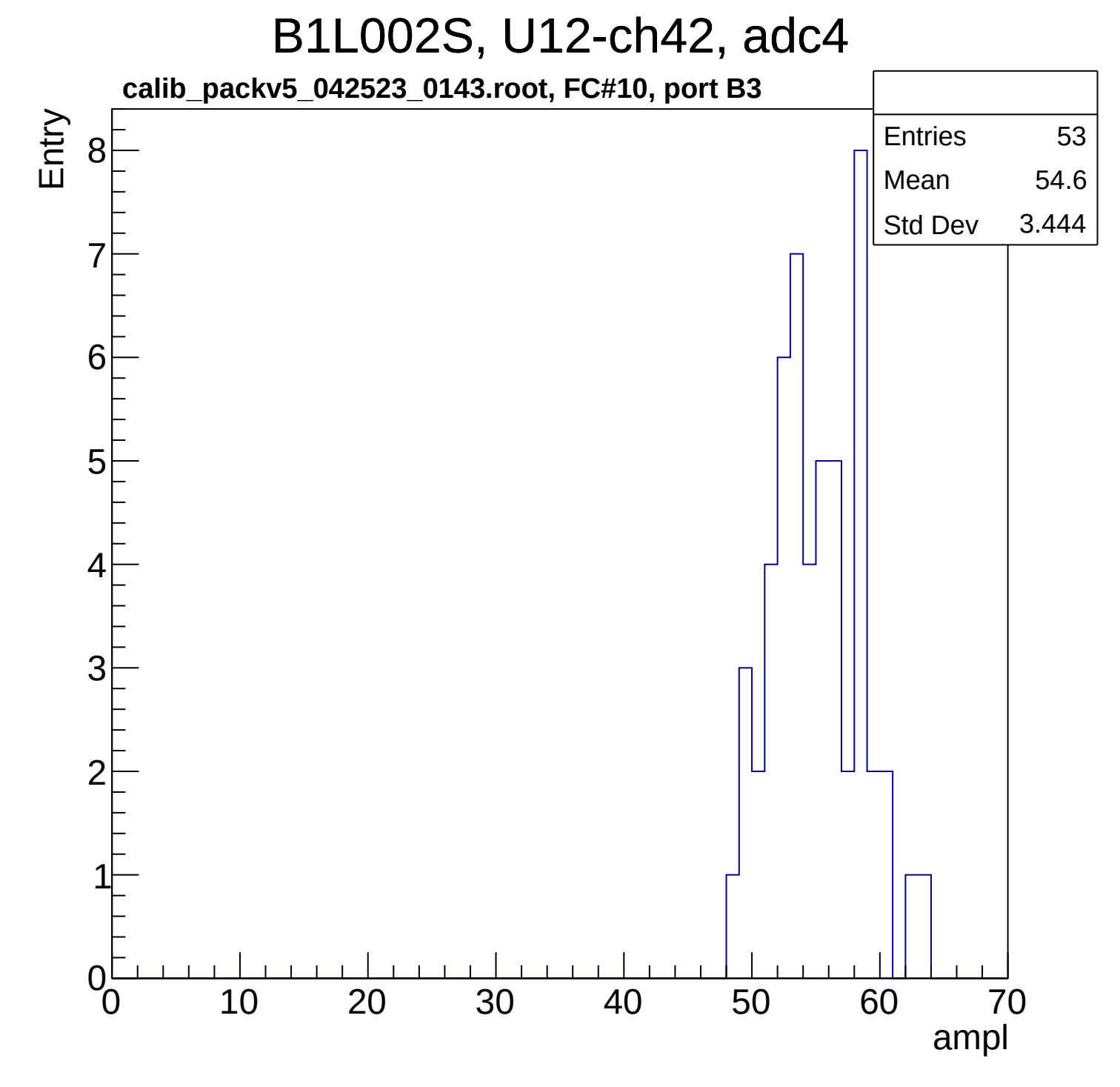

B1L002S, U12-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 48.61
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.6
Std Dev 3.444
Entry
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2
3
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5
6
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8
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20
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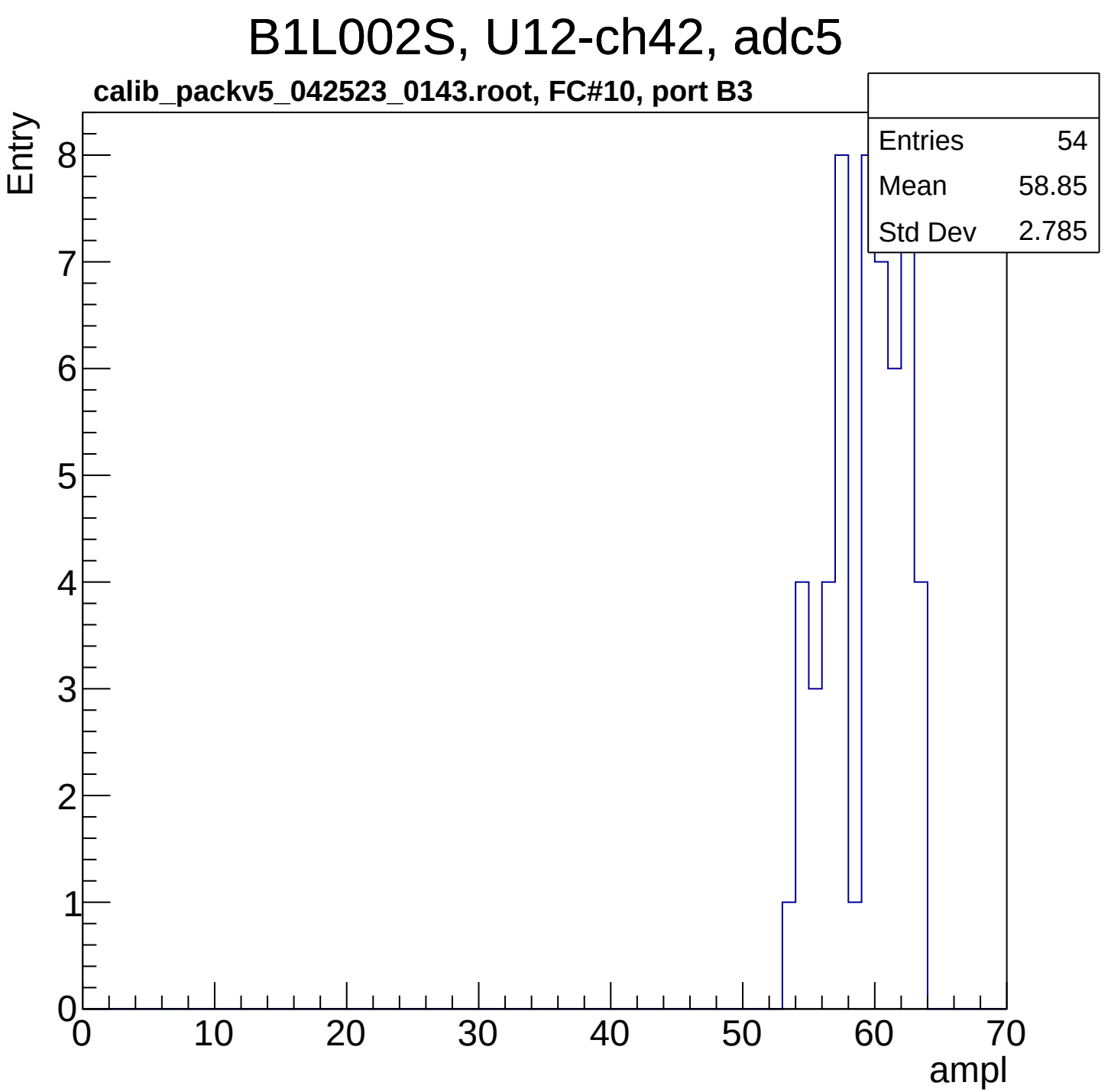
50
60
70
ampl


B1L002S, U12-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.85
Std Dev 2.785
Entry
ampl
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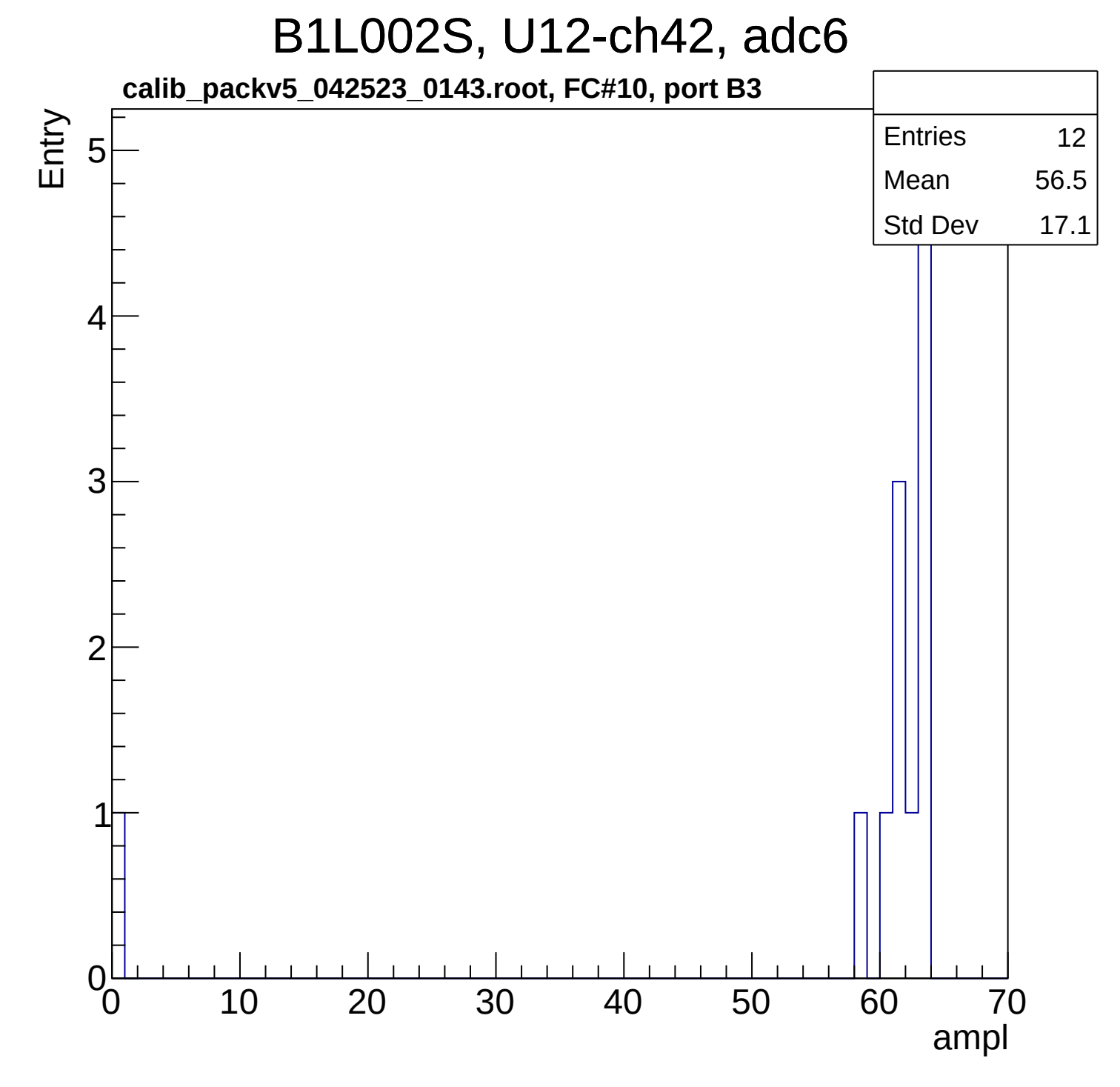
B1L002S, U12-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.5
Std Dev 17.1
Entry
0
1
2
3
4
5
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10
20
30
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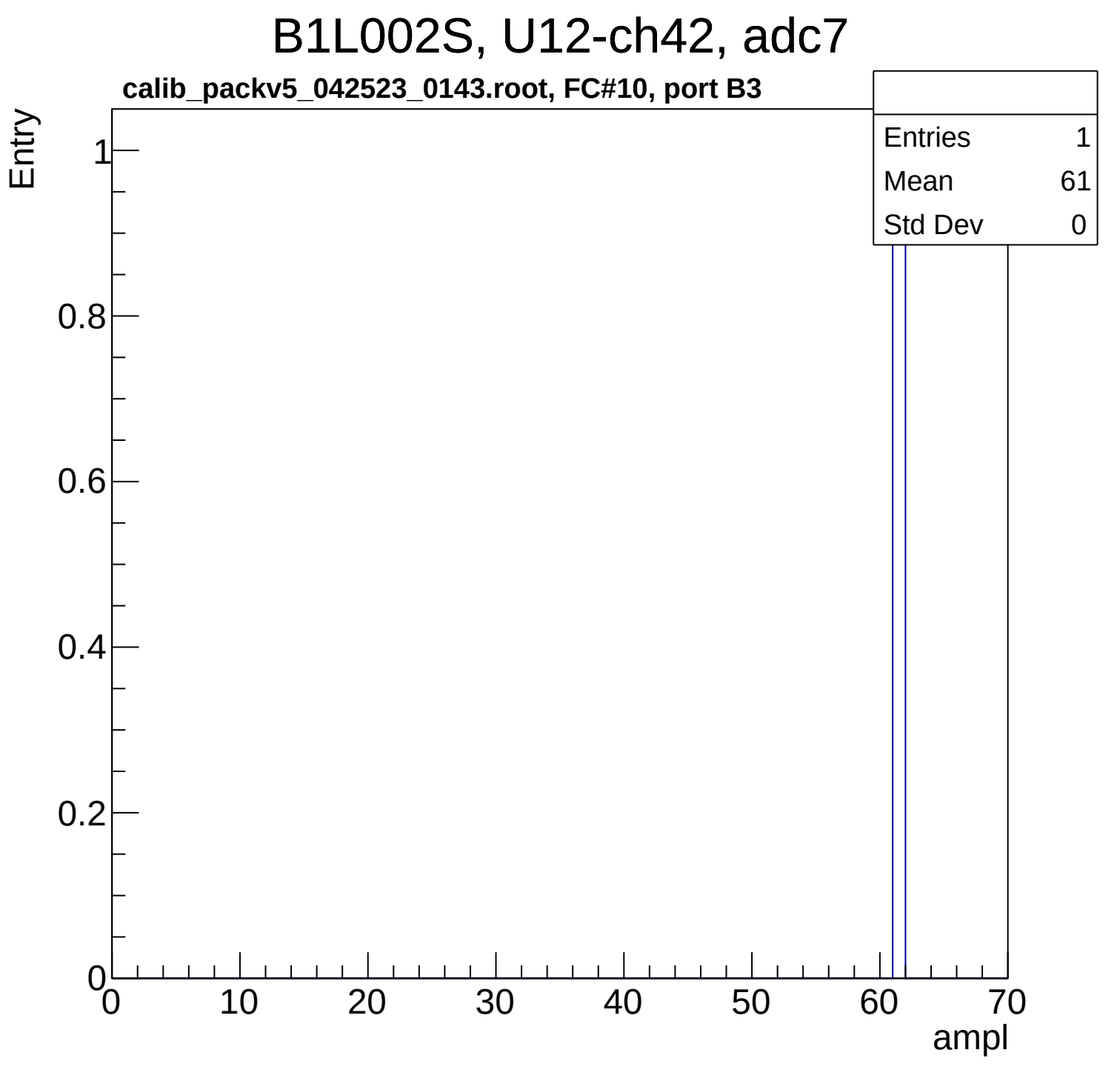
50
60
70
ampl


B1L002S, U12-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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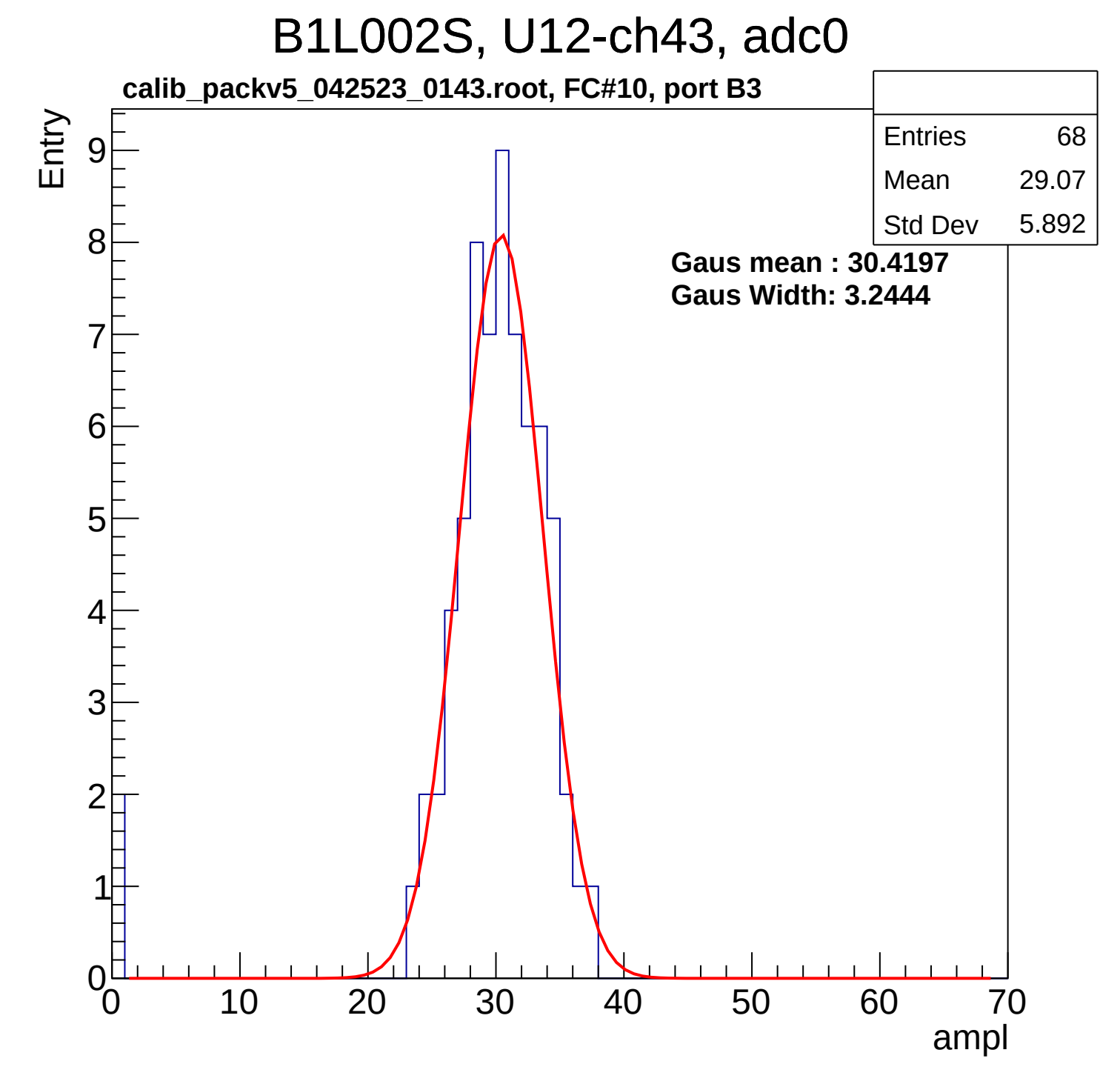
40
50
60
70
ampl


B1L002S, U12-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.07
Std Dev 5.892
Gaus mean : 30.4197
Gaus Width: 3.2444
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
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60
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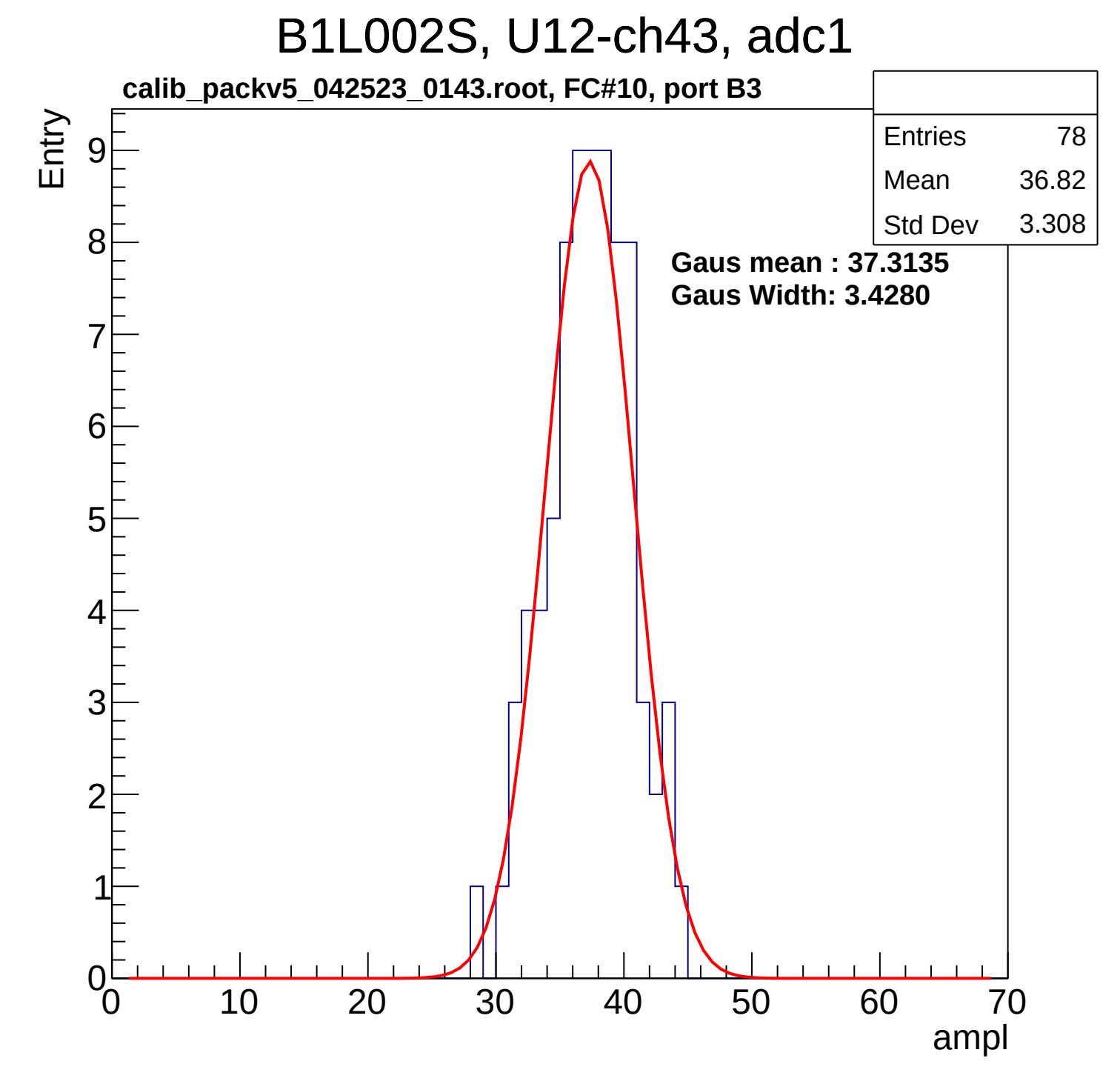
B1L002S, U12-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 36.82
Std Dev 3.308
Gaus mean : 37.3135
Gaus Width: 3.4280
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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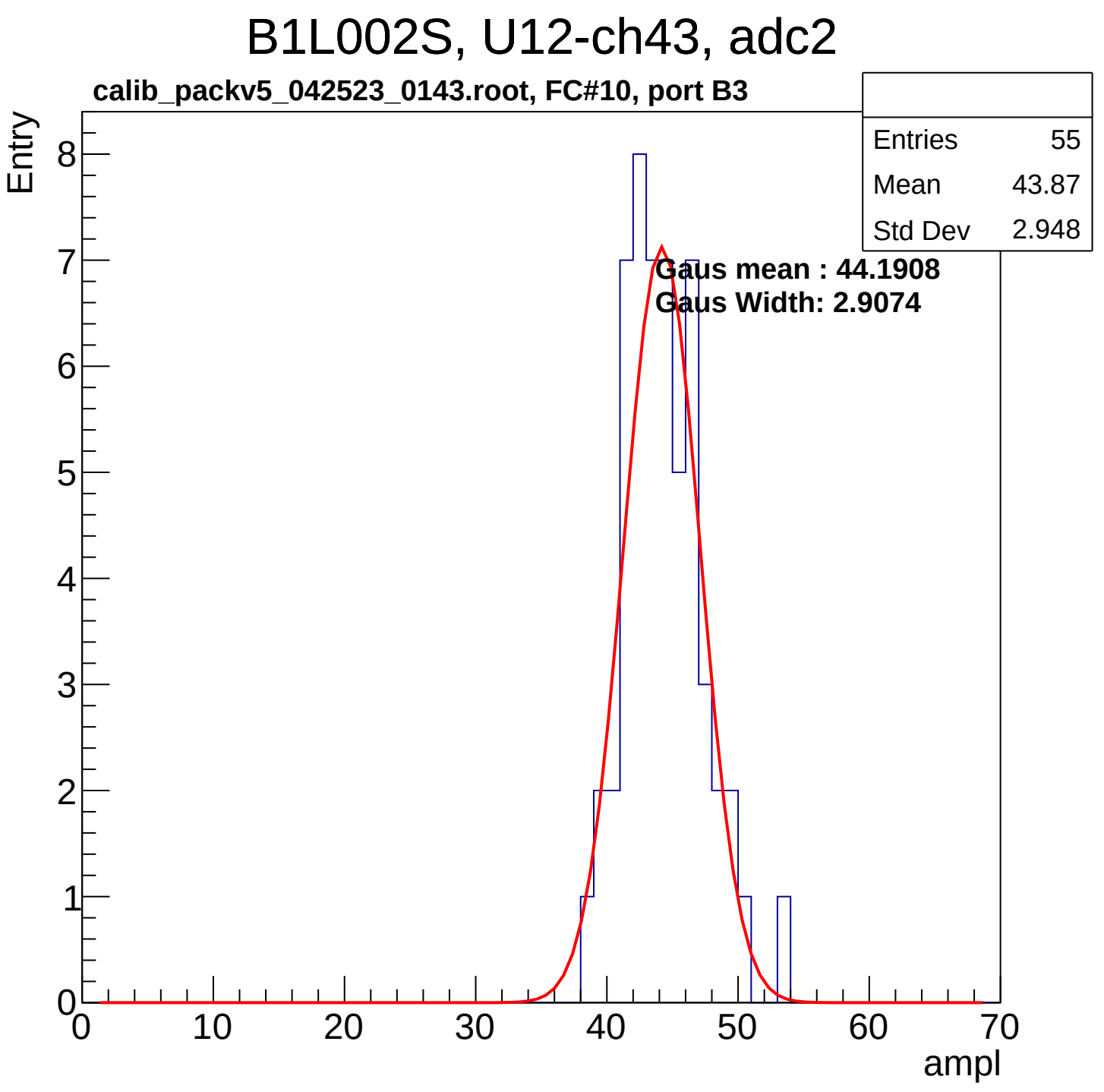
B1L002S, U12-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 43.87
Std Dev 2.948
Gaus mean : 44.1908
Gaus Width: 2.9074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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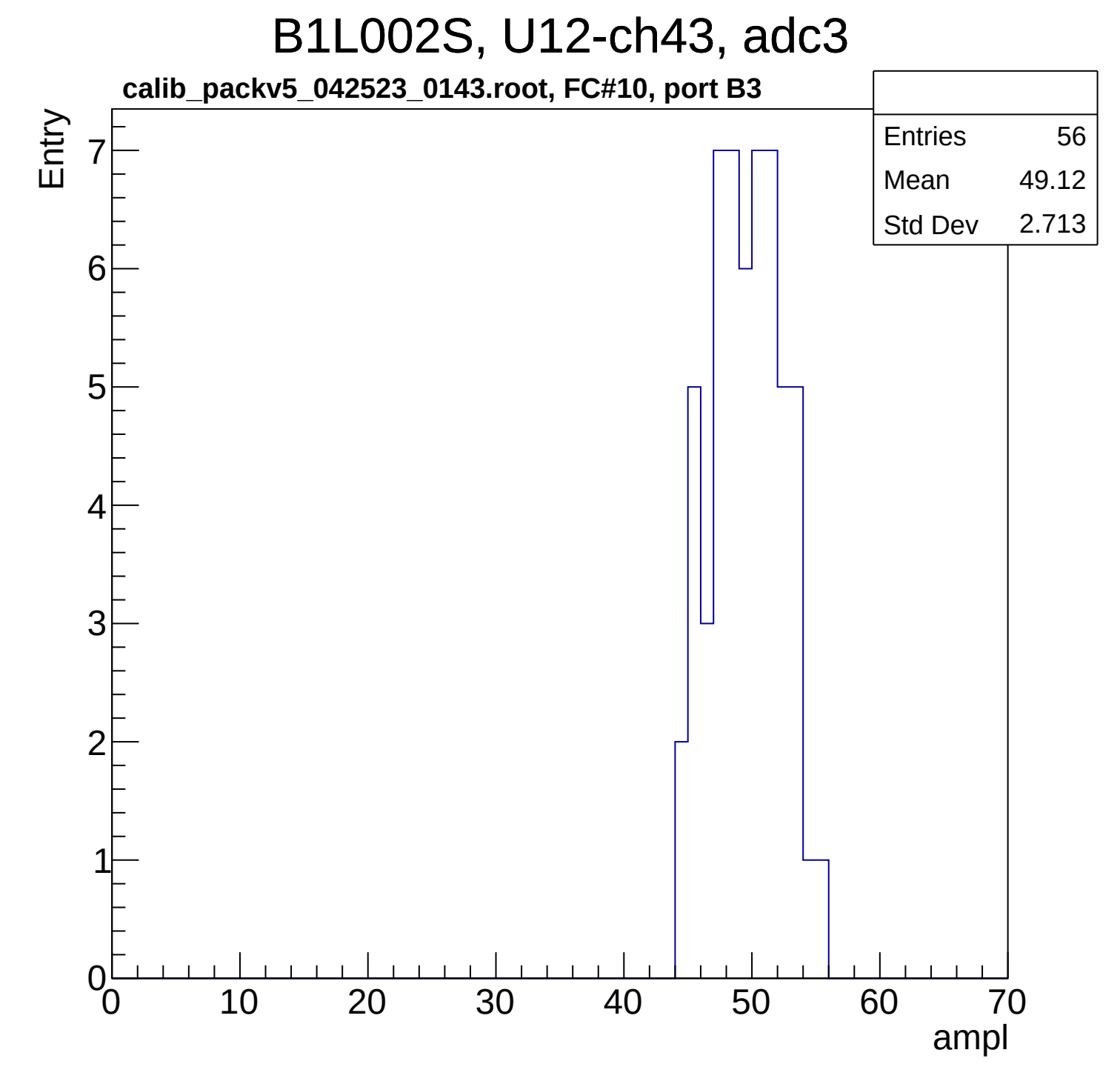
B1L002S, U12-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 49.12
Std Dev 2.713
Entry
ampl
0
1
2
3
4
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0
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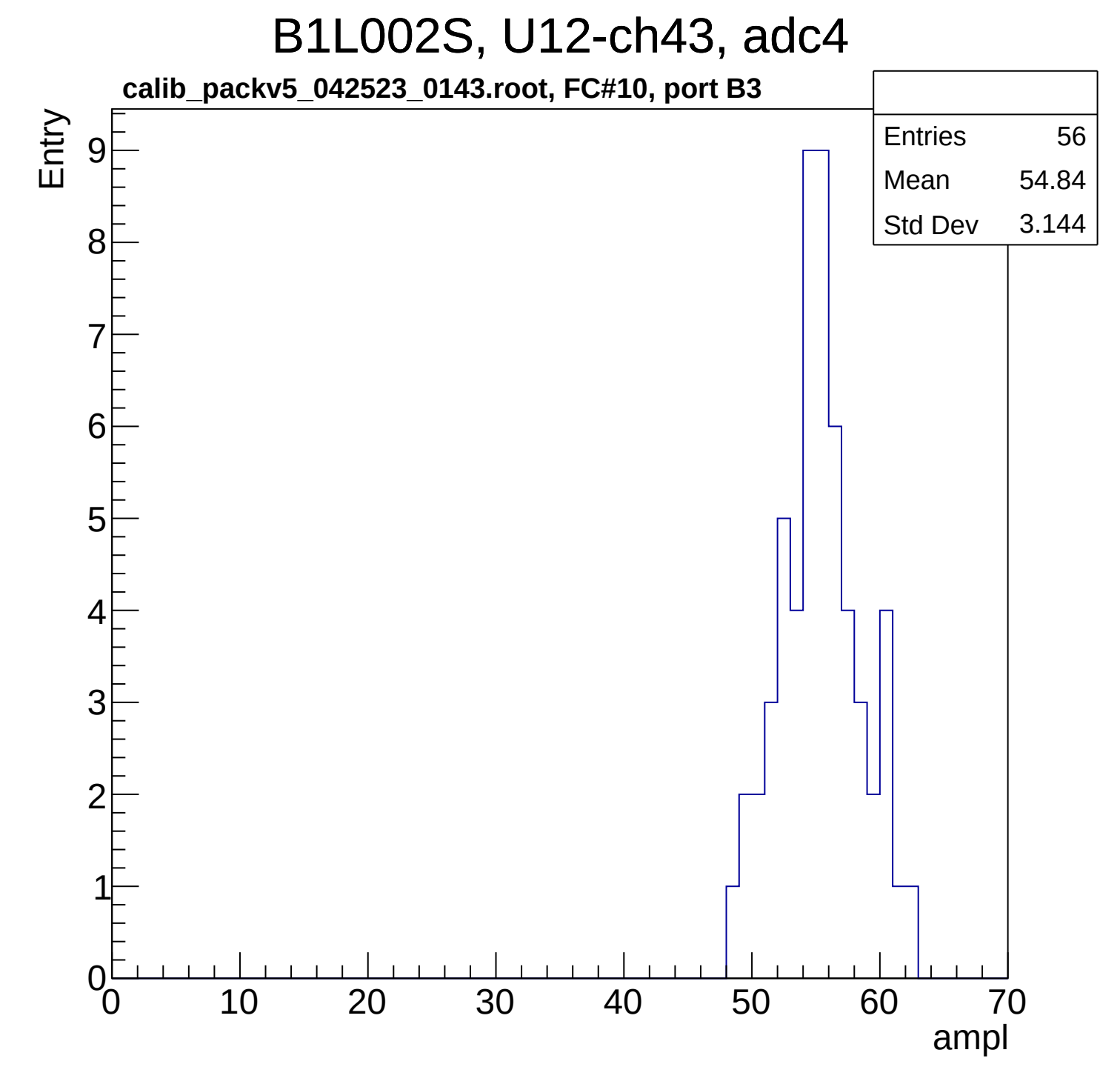
B1L002S, U12-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.84
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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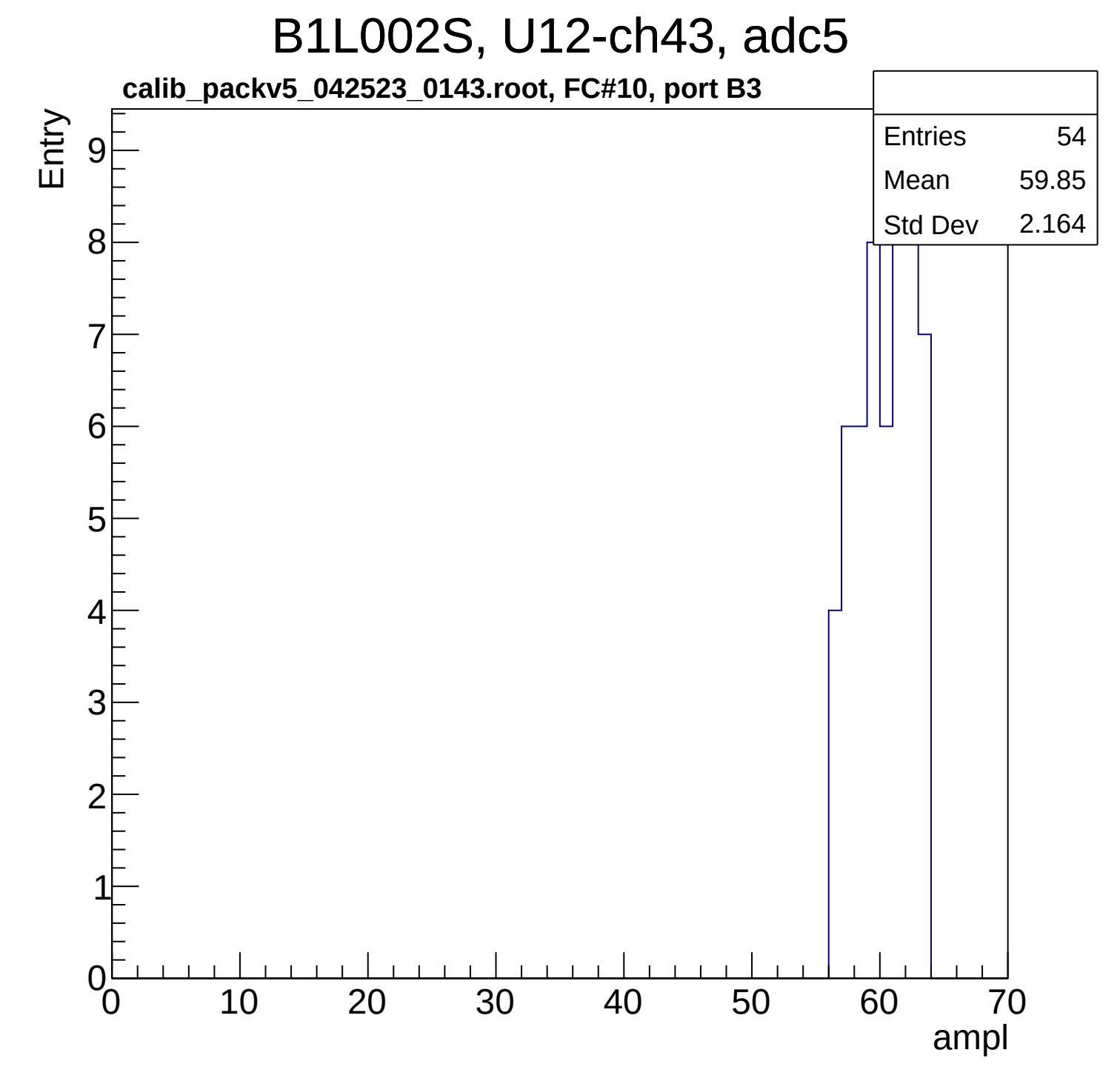
B1L002S, U12-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.85
Std Dev 2.164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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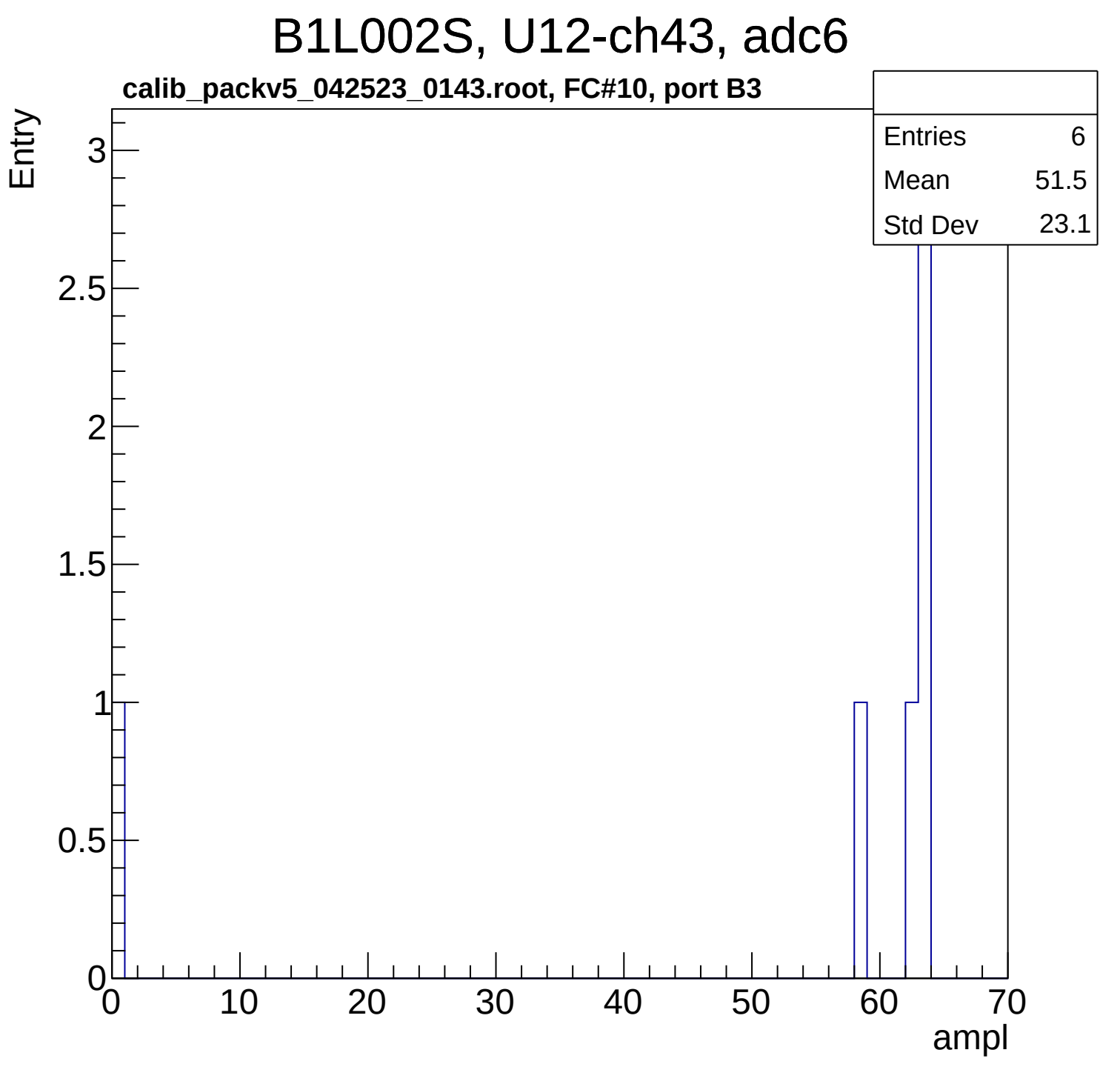
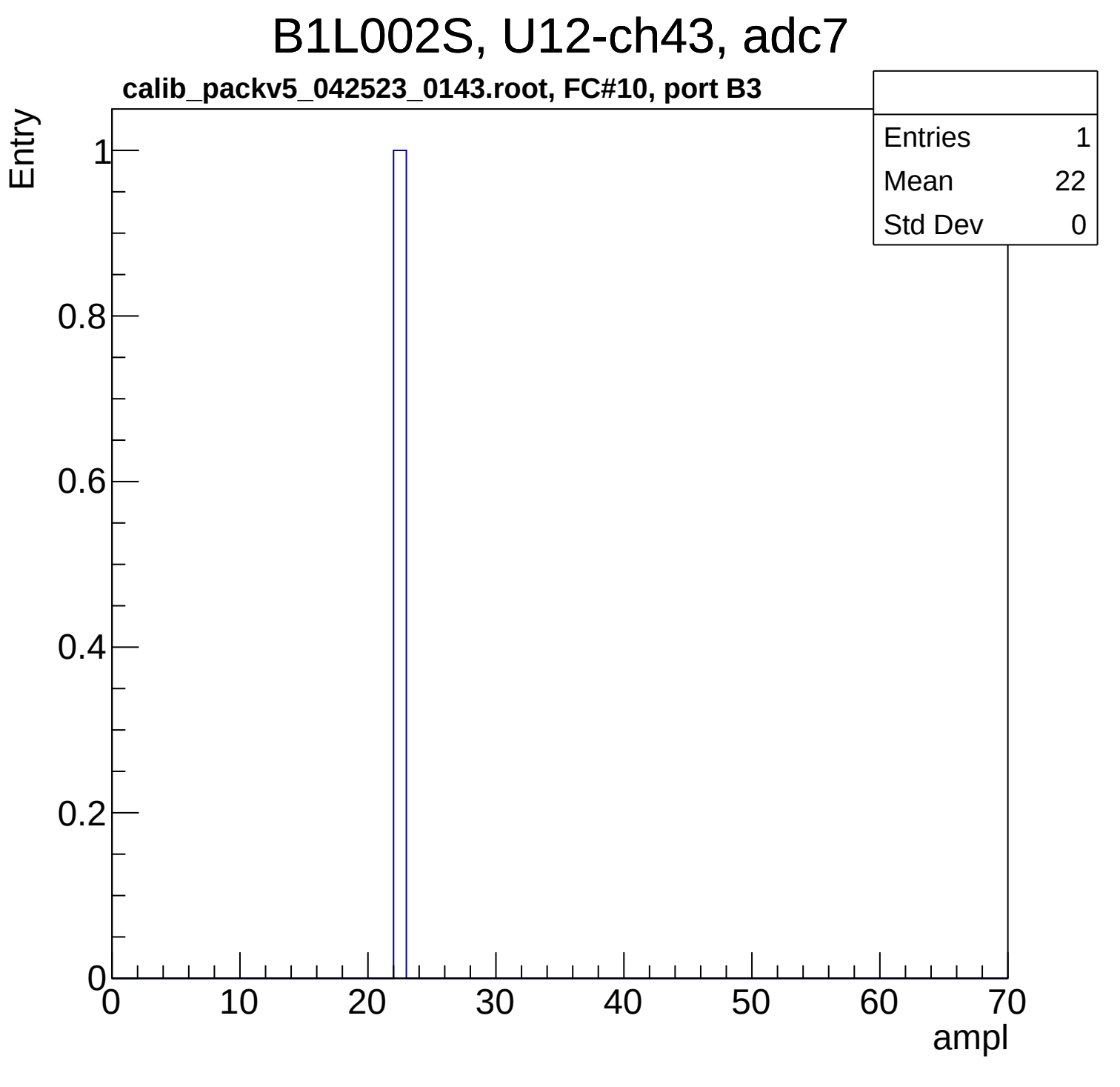

B1L002S, U12-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51.5
Std Dev 23.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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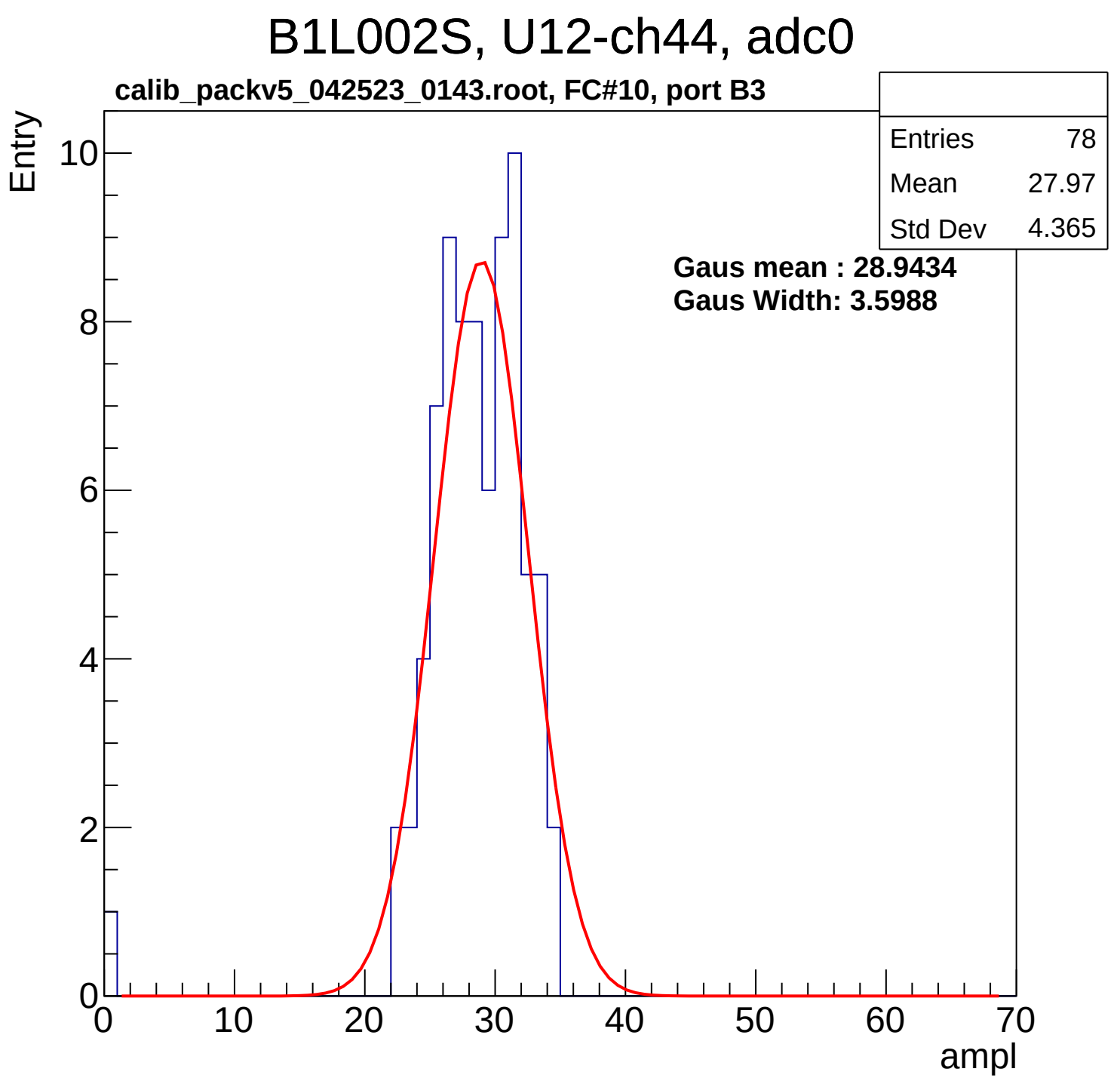
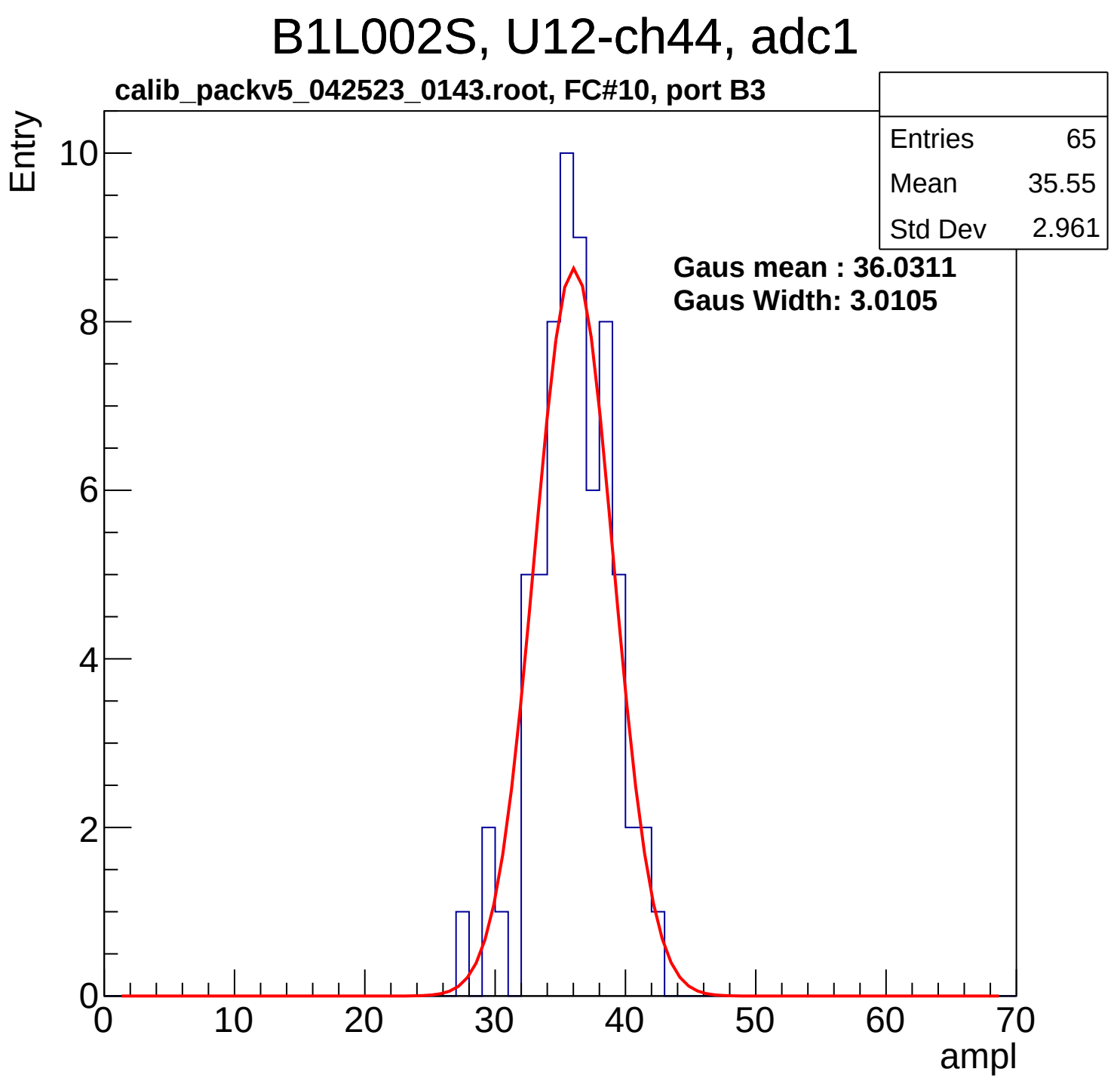
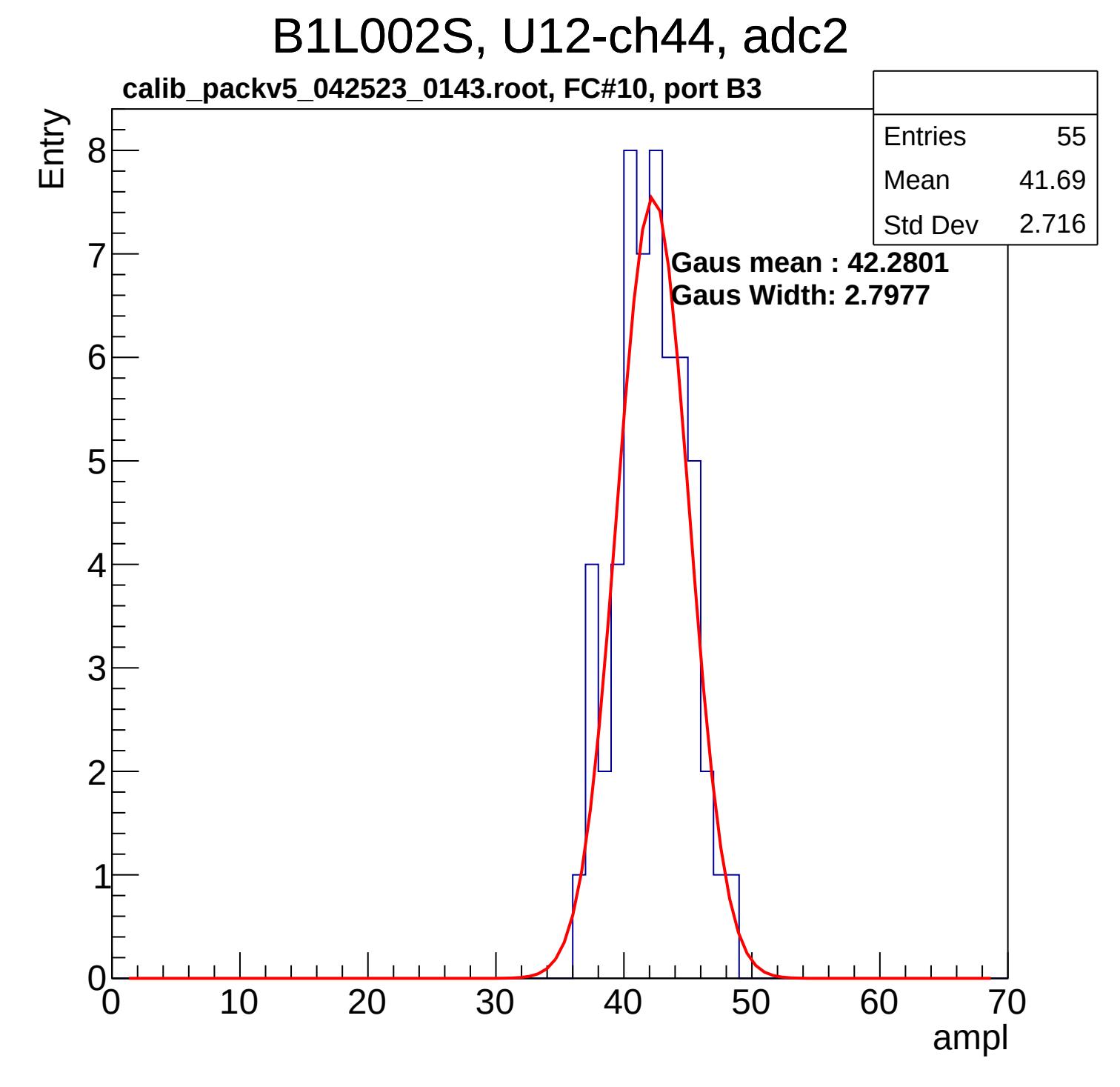
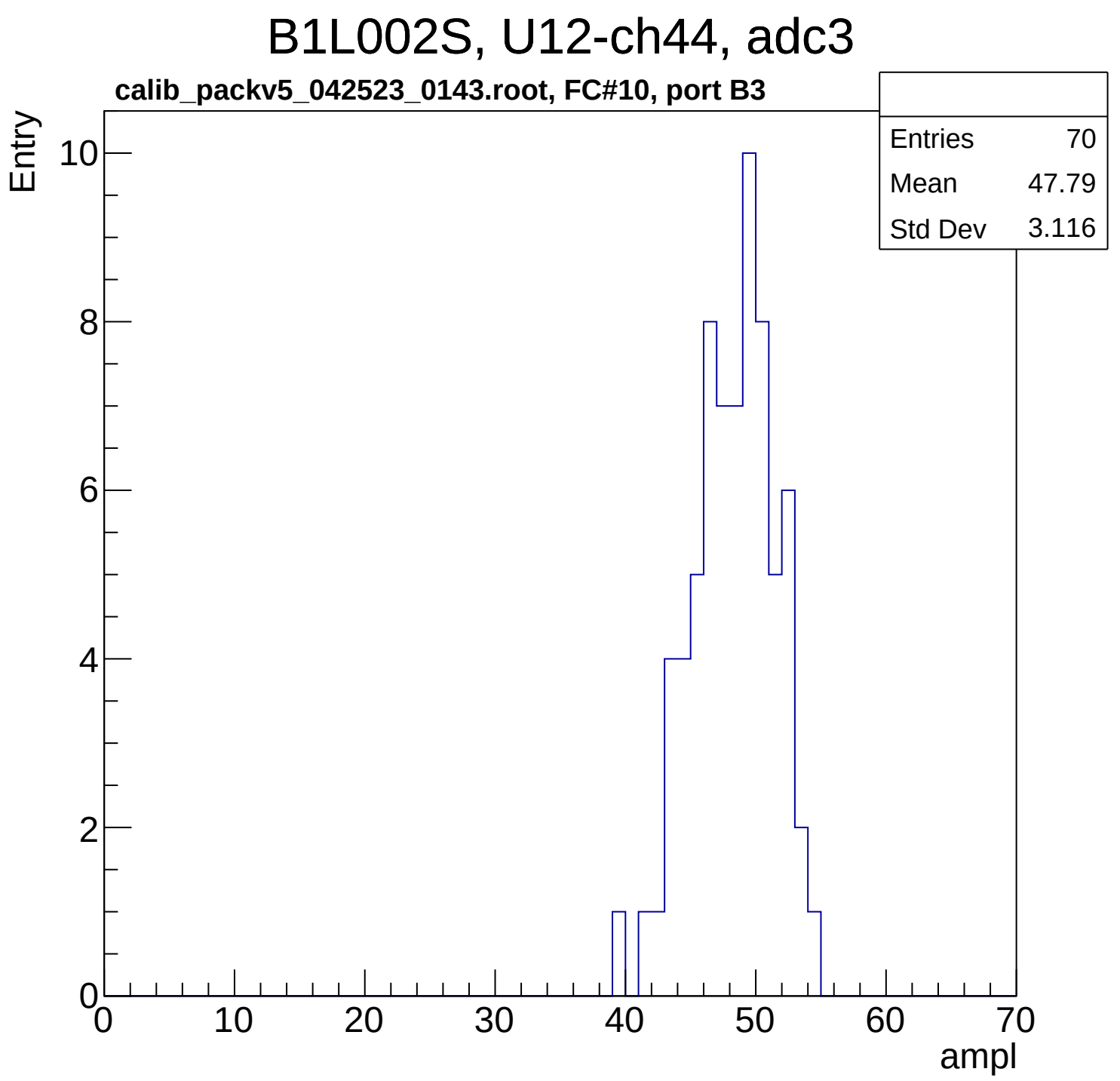
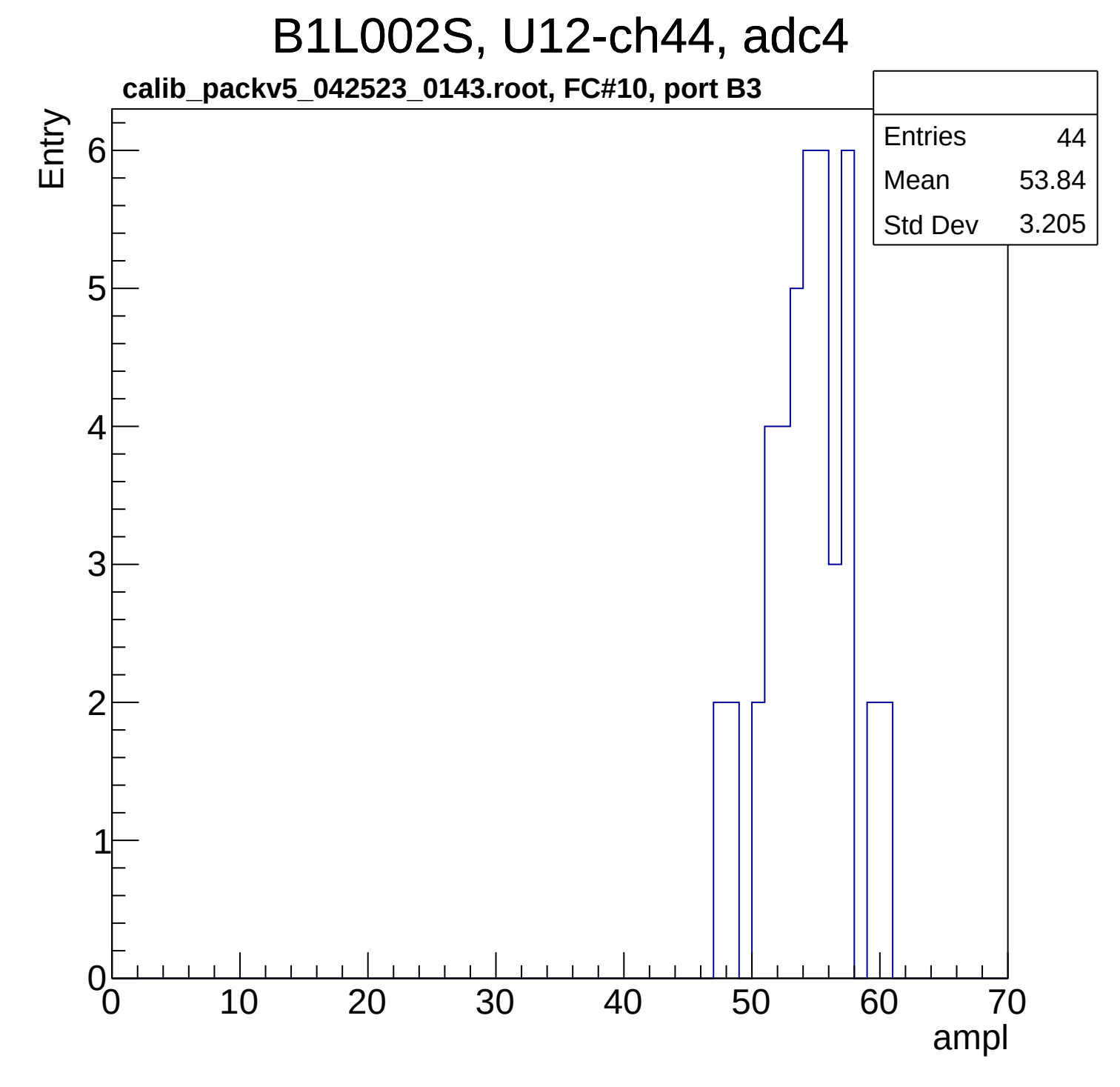
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

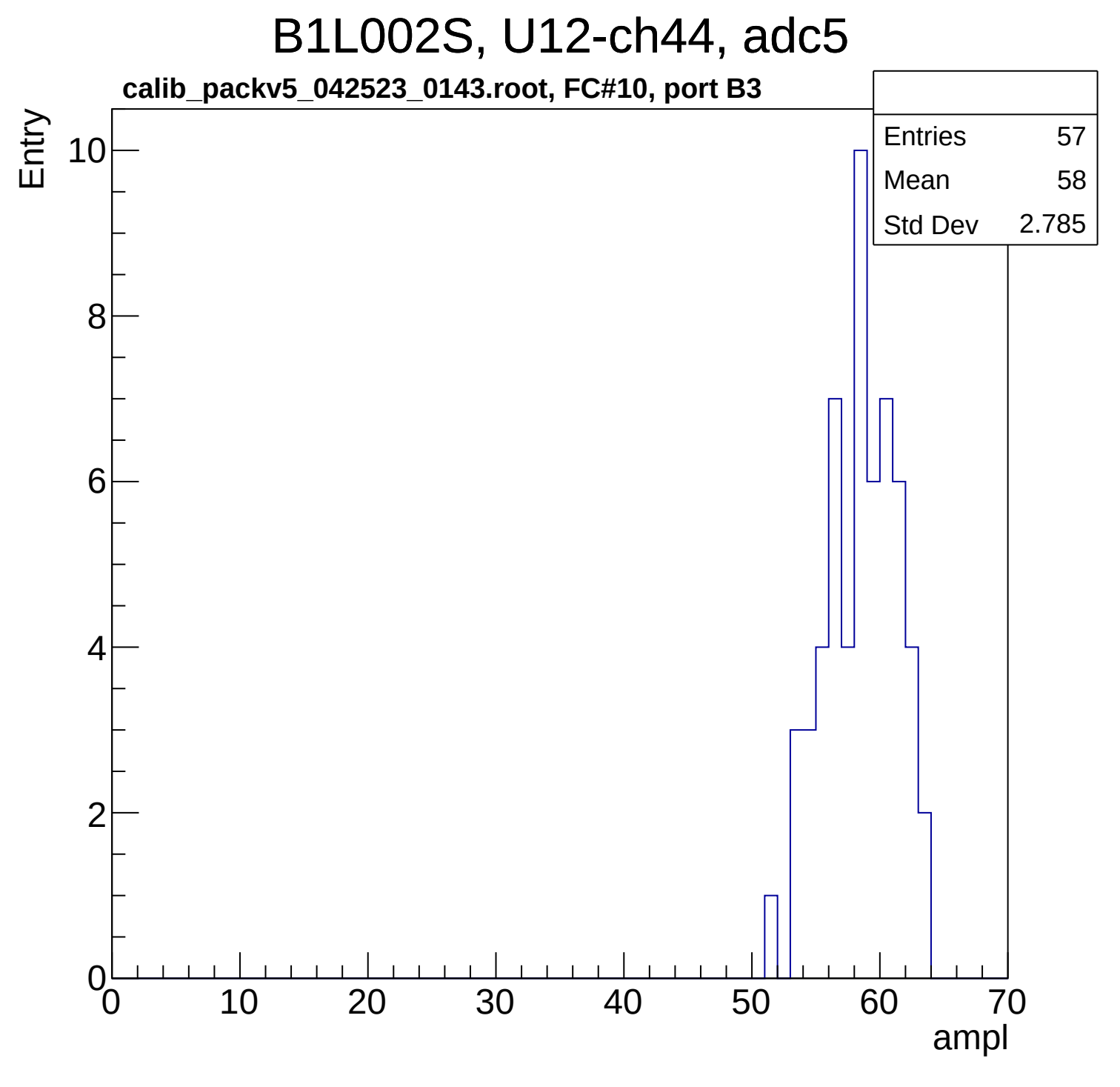

B1L002S, U12-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.97
Std Dev 4.365
Gaus mean : 28.9434
Gaus Width: 3.5988
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.55
Std Dev 2.961
Gaus mean : 36.0311
Gaus Width: 3.0105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.69
Std Dev 2.716
Gaus mean : 42.2801
Gaus Width: 2.7977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.79
Std Dev 3.116
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.84
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58
Std Dev 2.785
Entry
ampl
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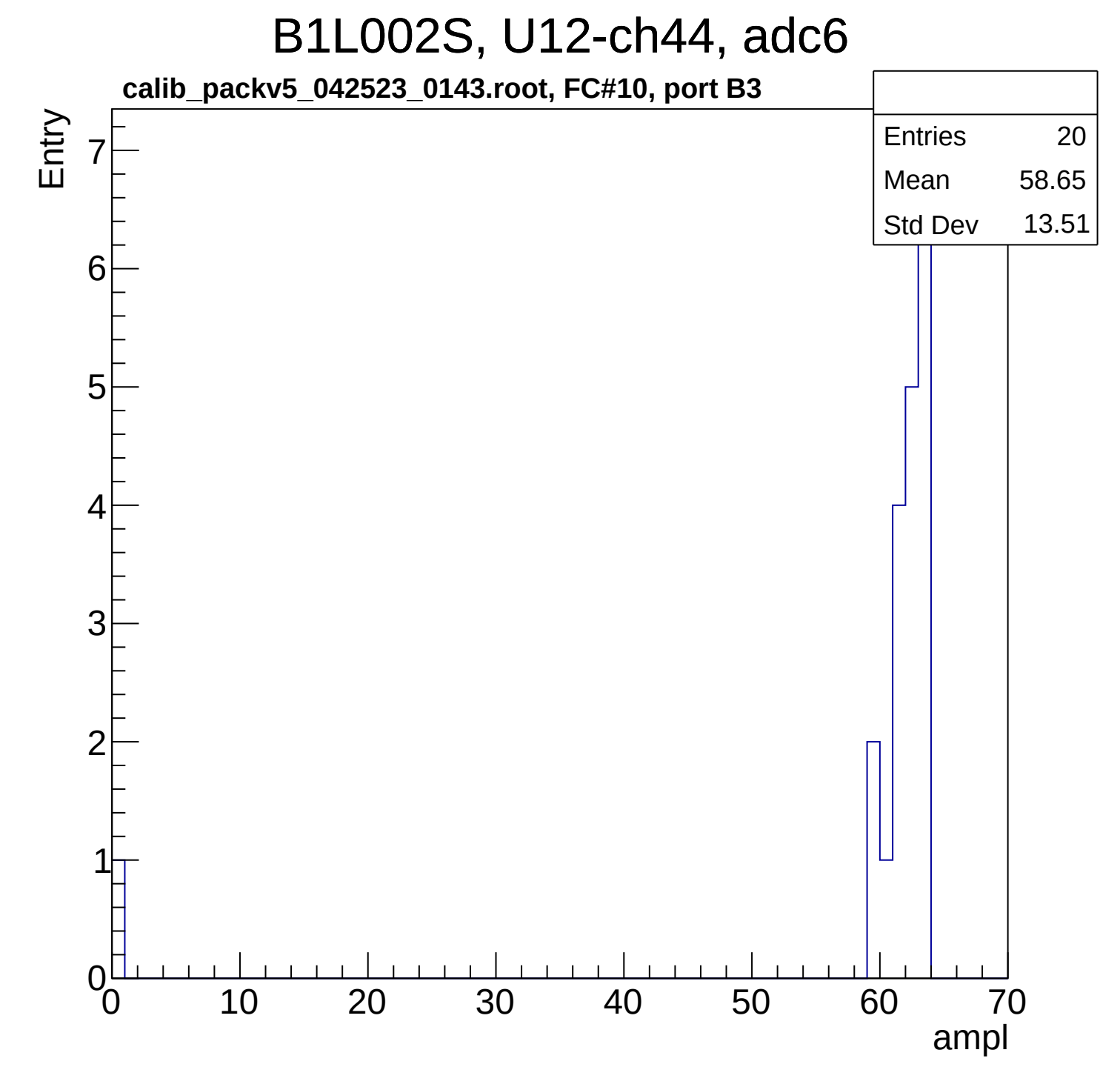
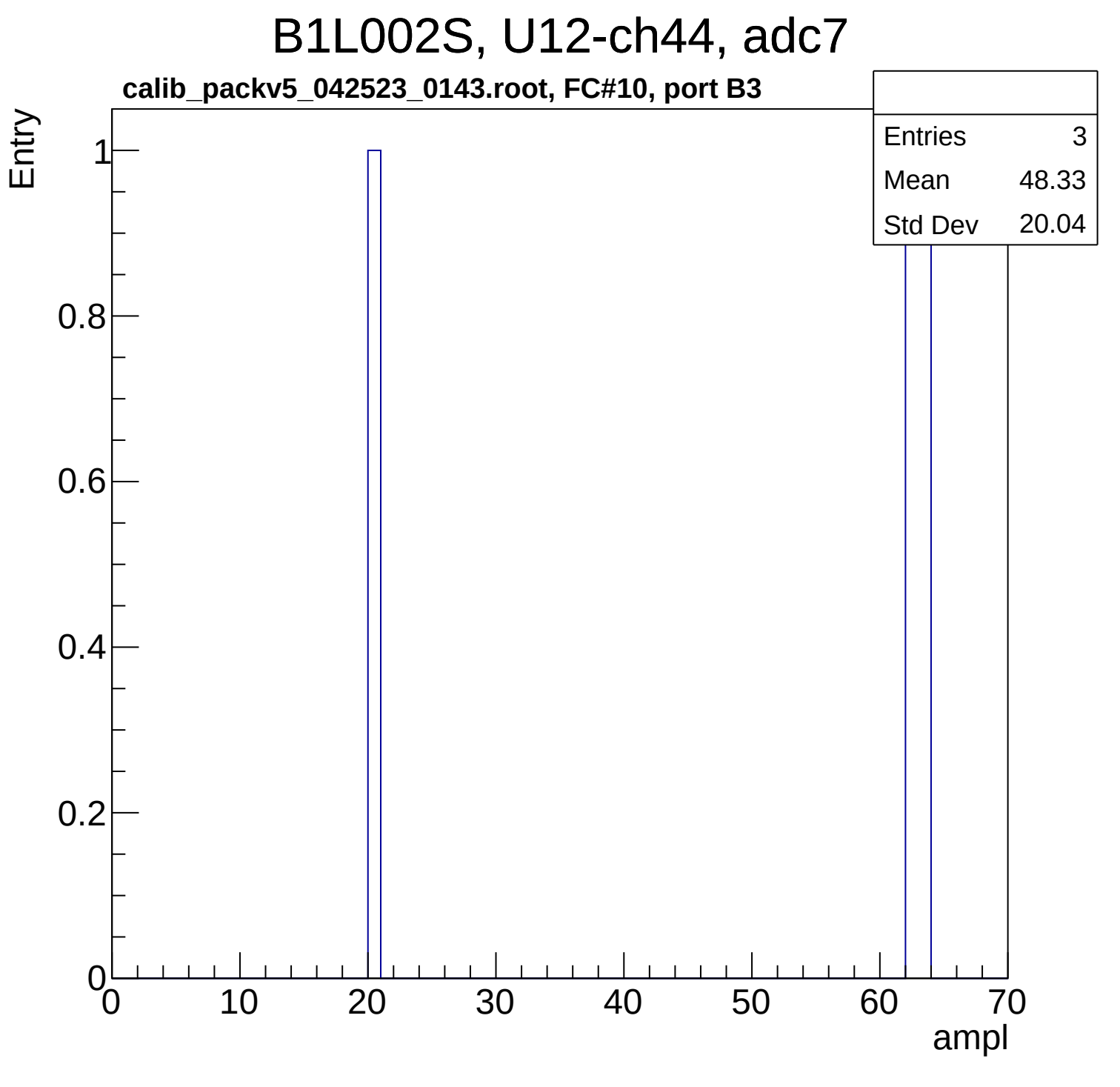
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.65
Std Dev 13.51
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 20.04
Entry
ampl
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0.4
0.6
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0
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60
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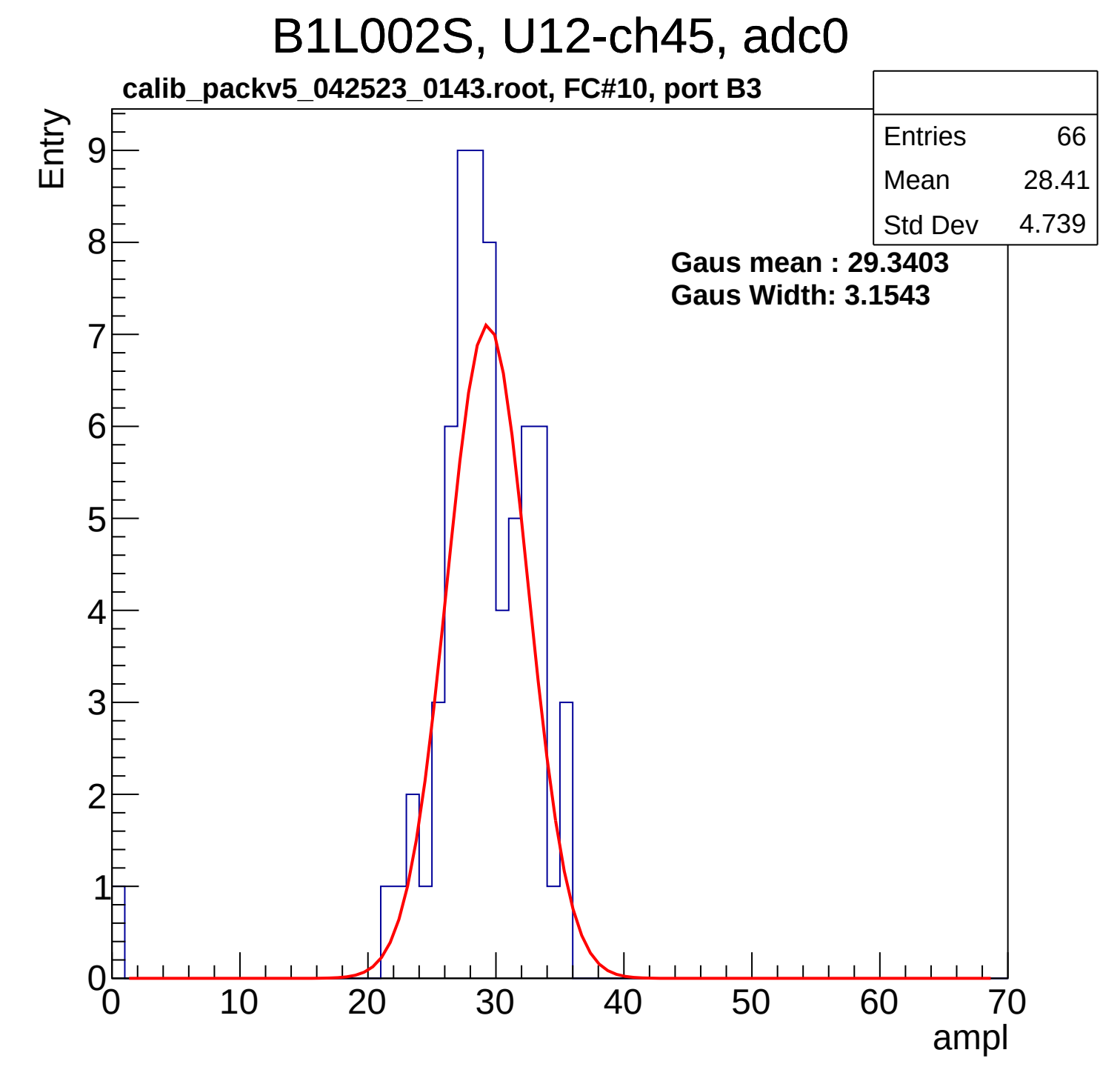
B1L002S, U12-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.41
Std Dev 4.739
Gaus mean : 29.3403
Gaus Width: 3.1543
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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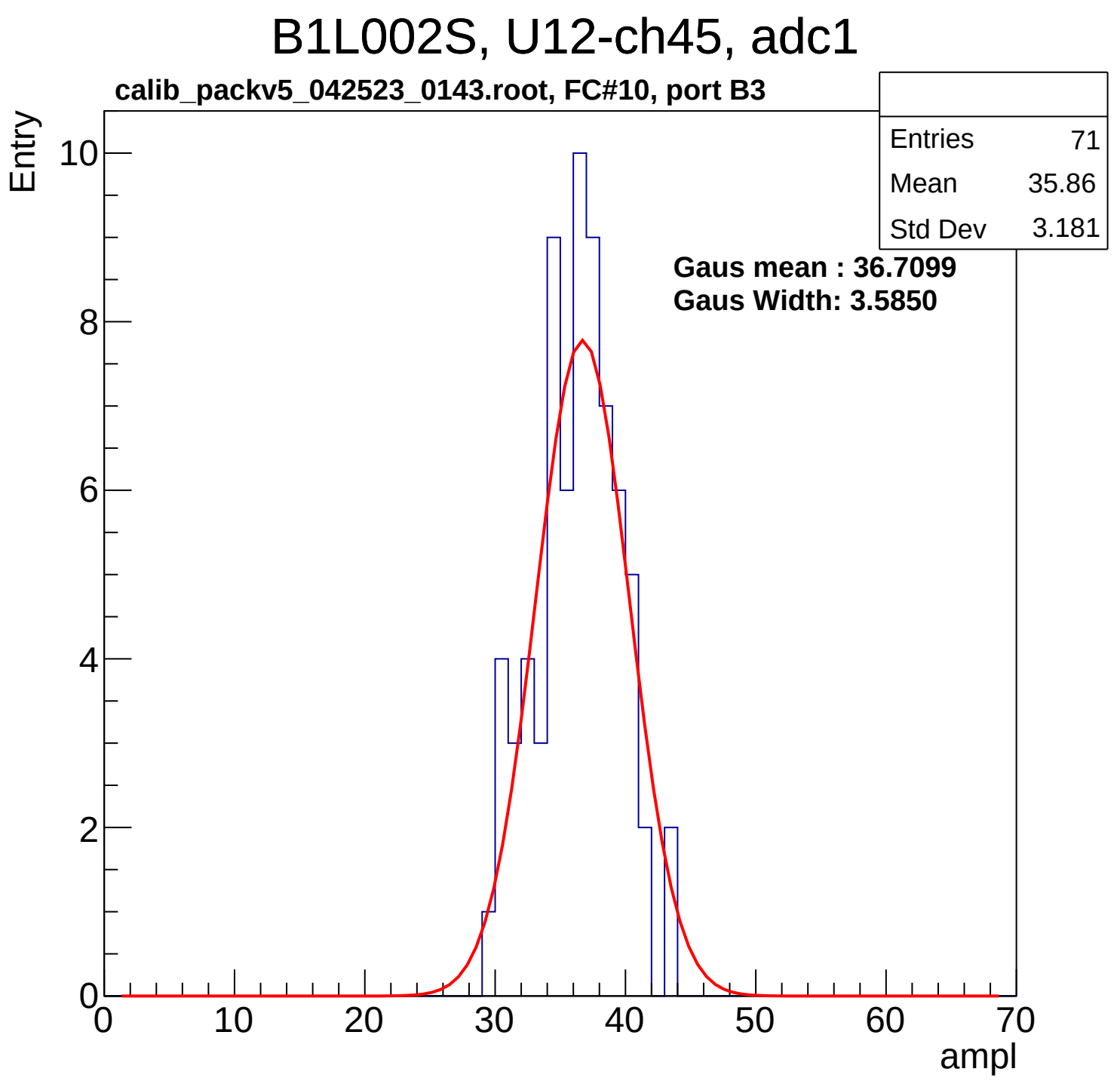
B1L002S, U12-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.86
Std Dev 3.181
Gaus mean : 36.7099
Gaus Width: 3.5850
Entry
ampl
0
2
4
6
8
10
0
10
20
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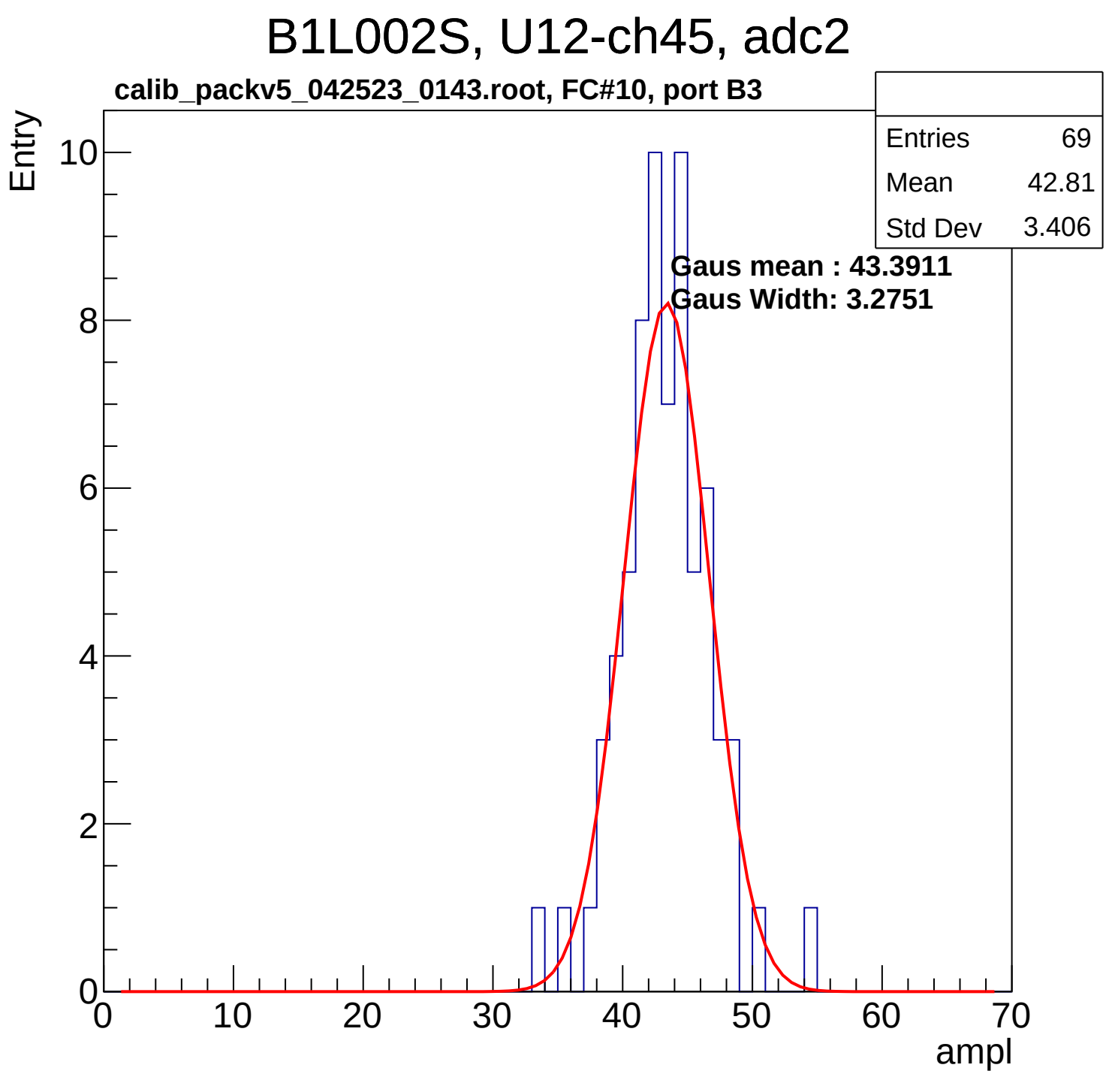
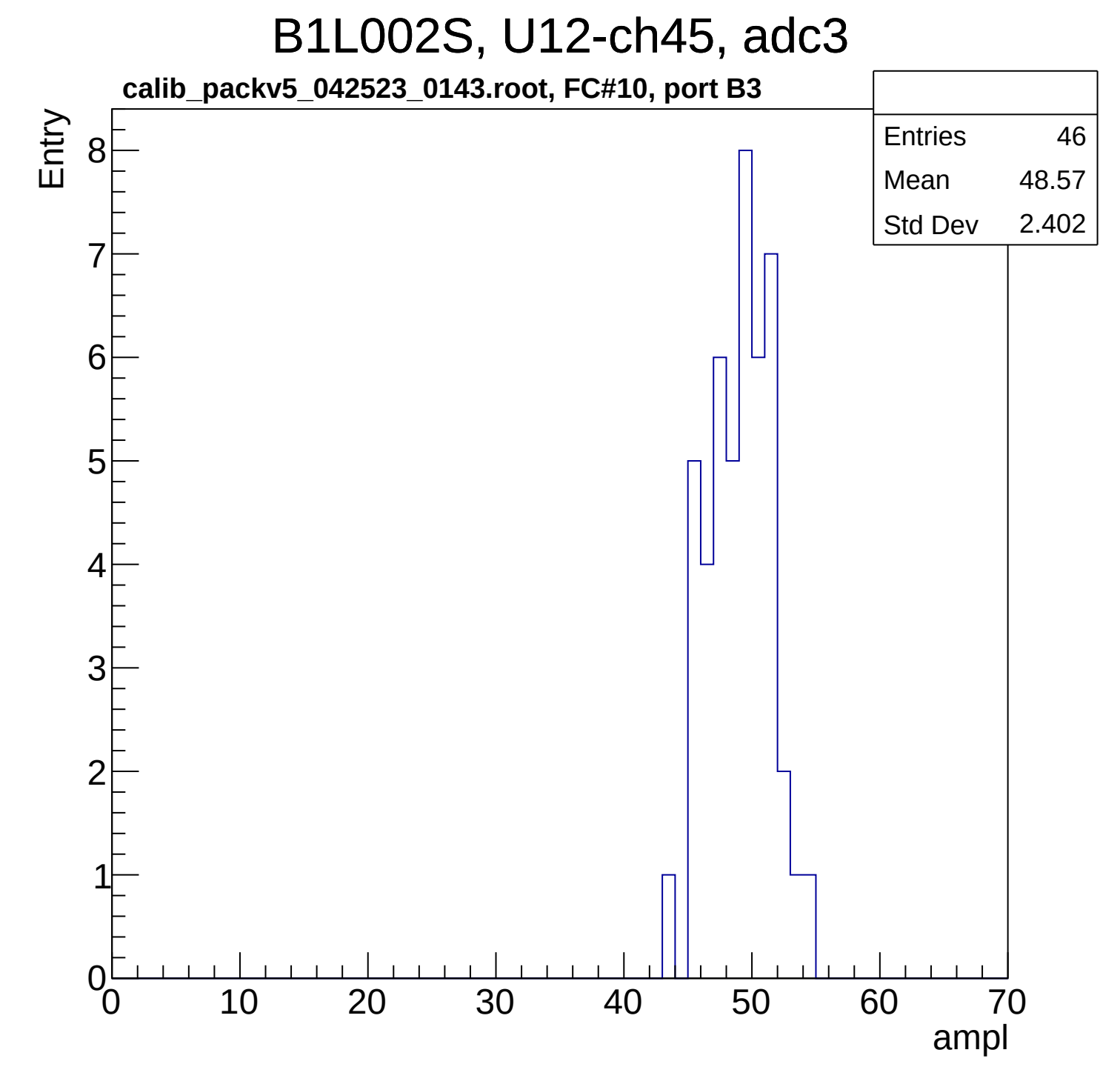
40
50
60
70


B1L002S, U12-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 42.81
Std Dev 3.406
Gaus mean : 43.3911
Gaus Width: 3.2751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 48.57
Std Dev 2.402
Entry
ampl
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4
5
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7
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0
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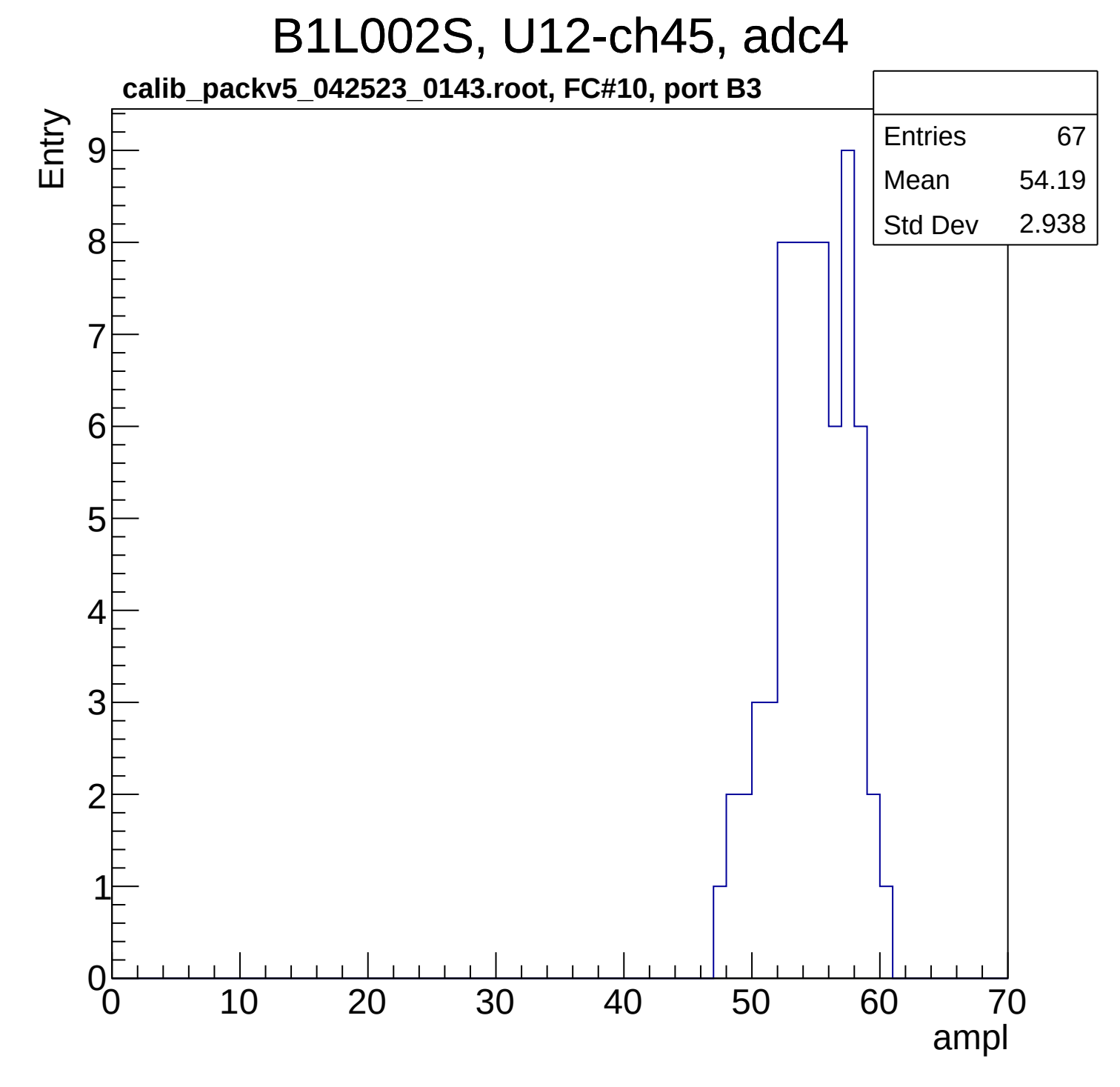
60
70


B1L002S, U12-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 54.19
Std Dev 2.938
Entry
ampl
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5
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0
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20
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60
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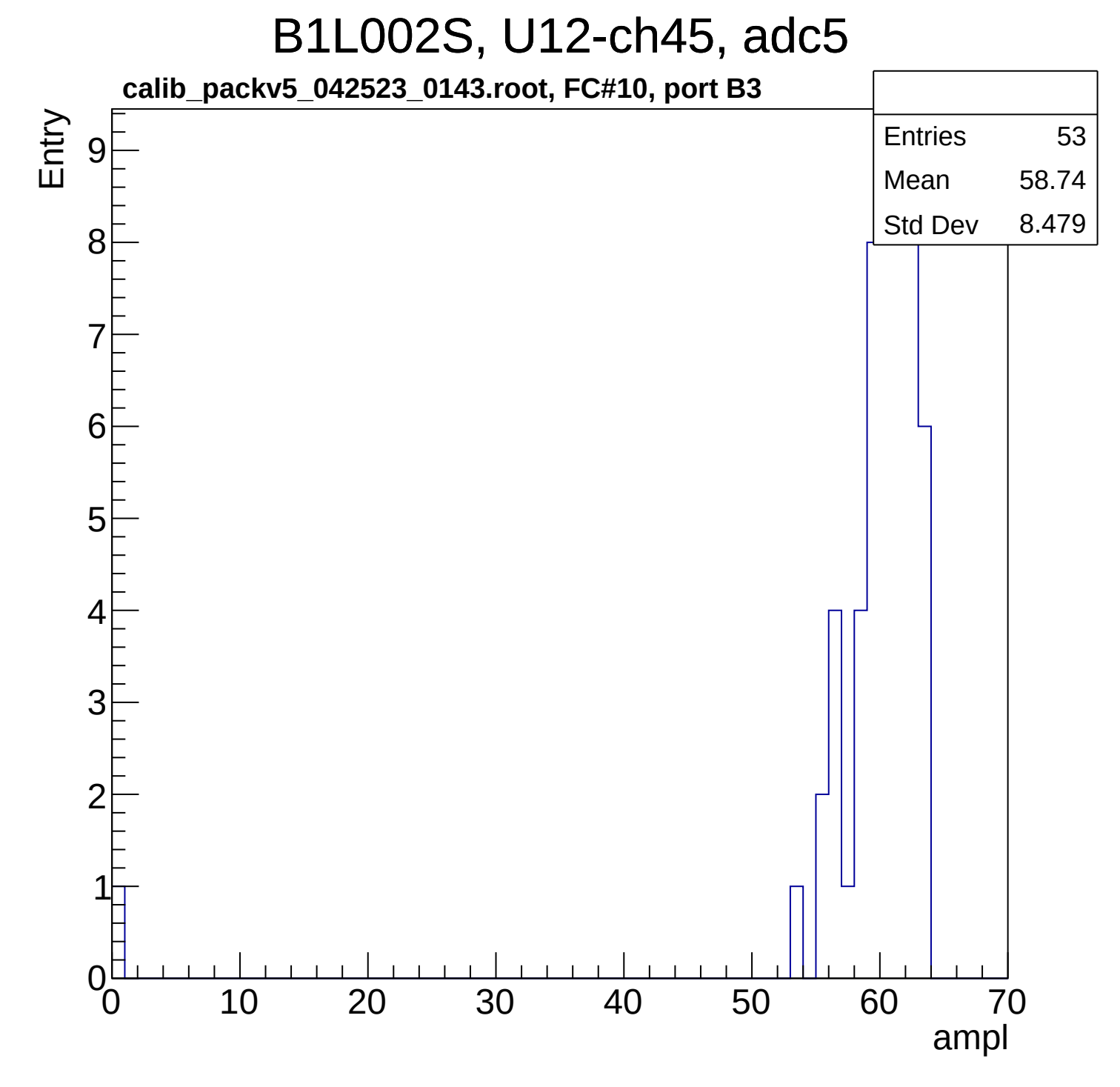
B1L002S, U12-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.74
Std Dev 8.479
Entry
ampl
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3
4
5
6
7
8
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0
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20
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60
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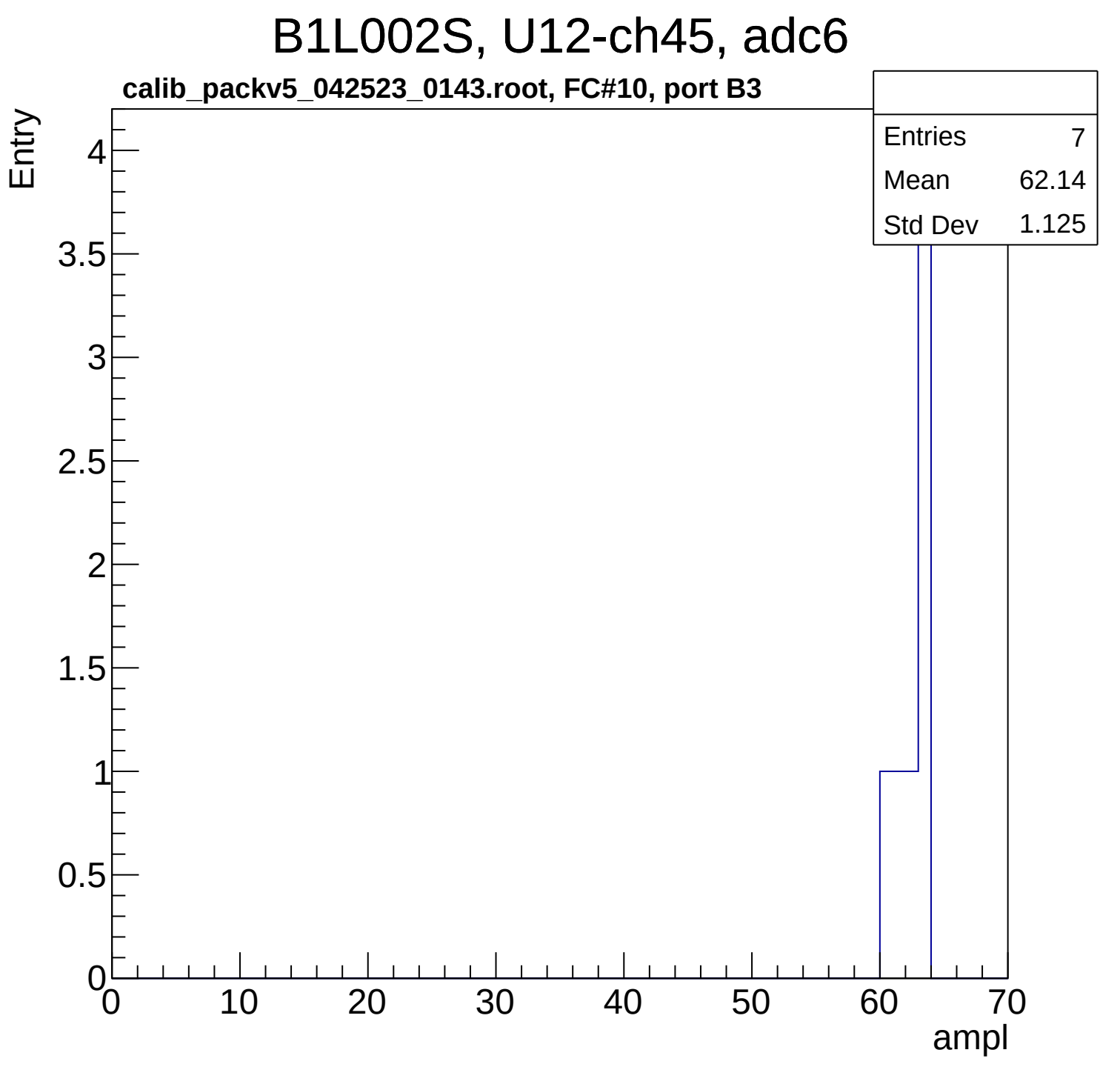
B1L002S, U12-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
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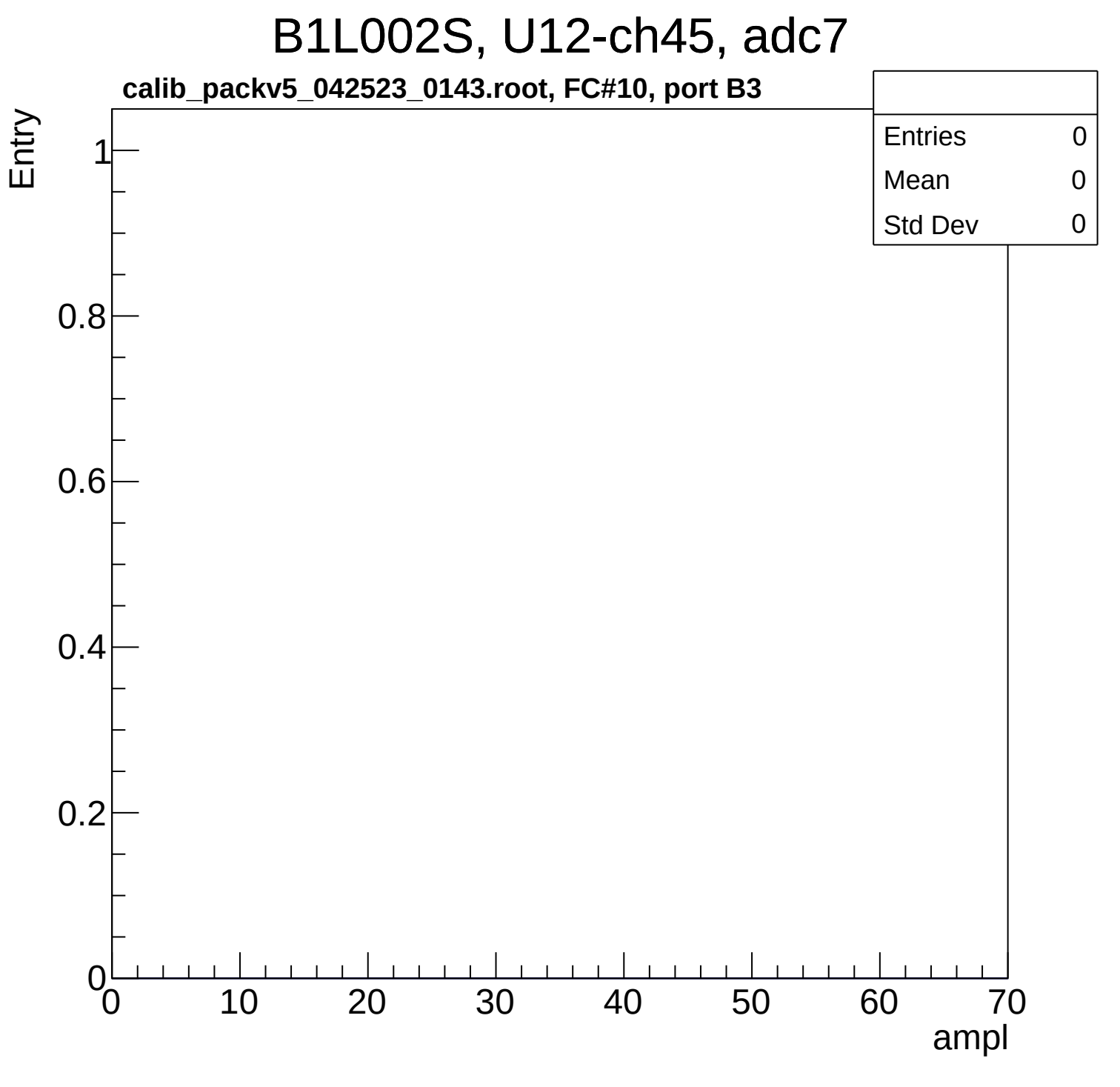
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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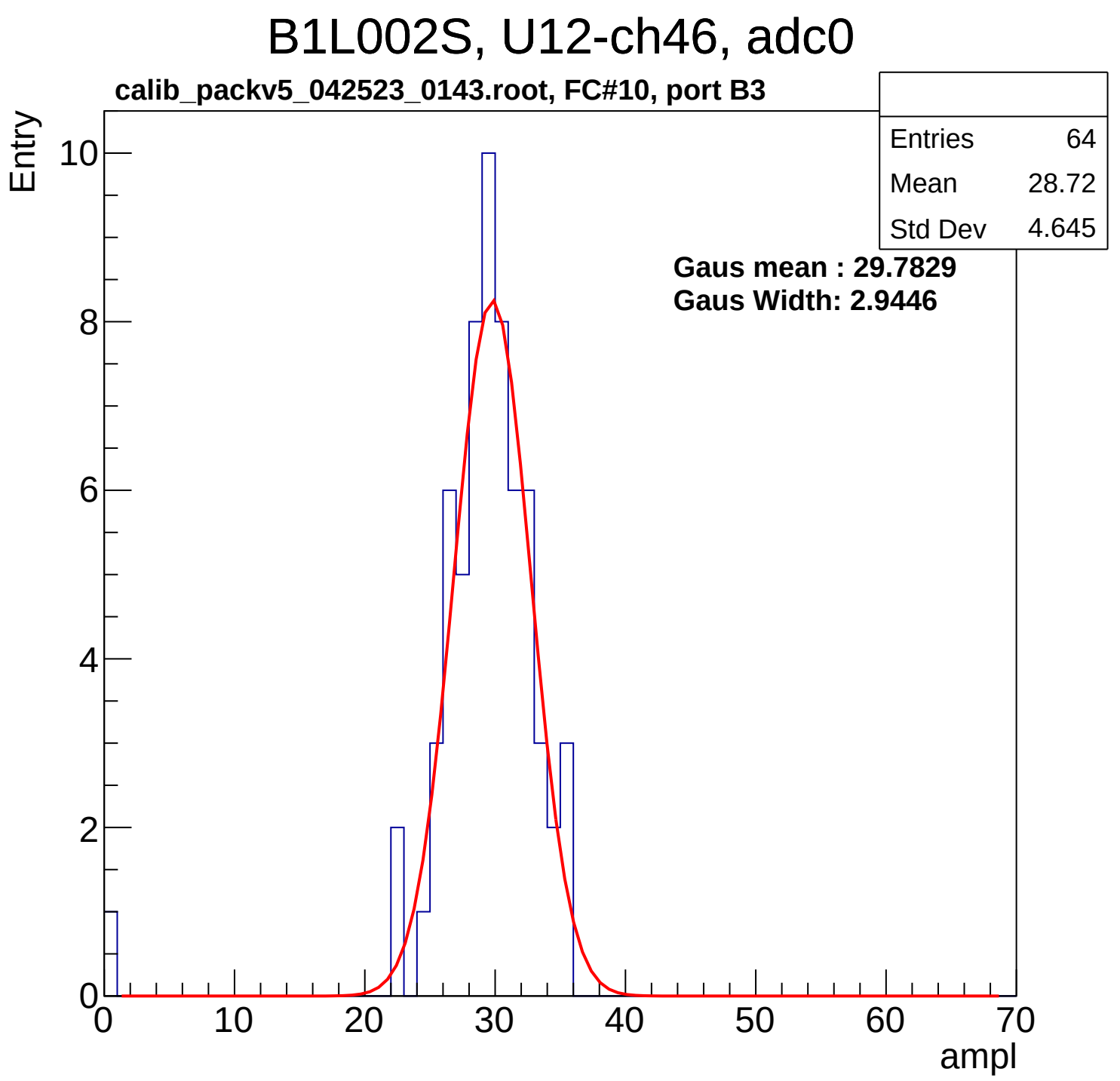
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.72
Std Dev 4.645
Gaus mean : 29.7829
Gaus Width: 2.9446
Entry
ampl
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2
4
6
8
10
0
10
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30
40
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60
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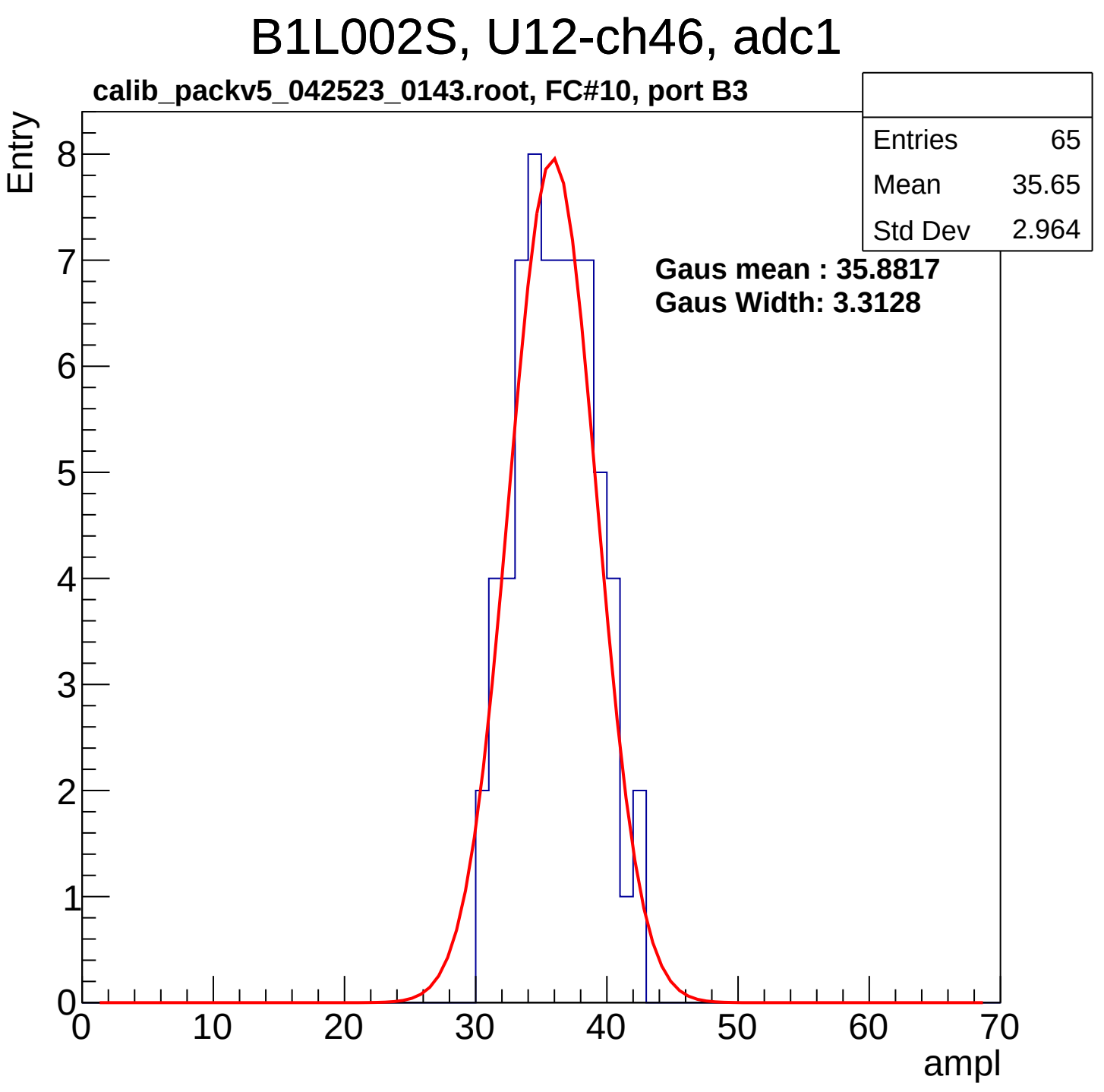
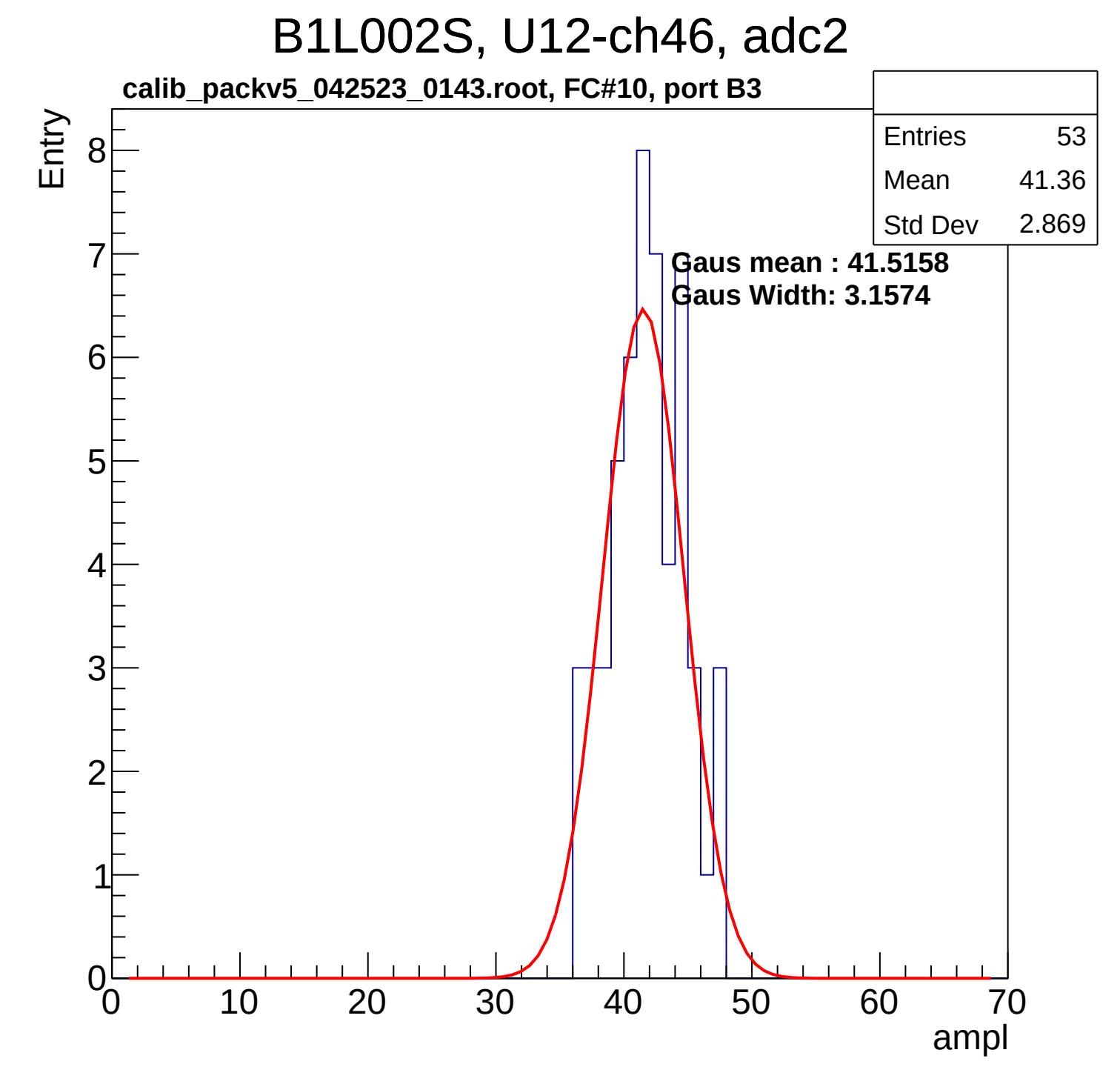
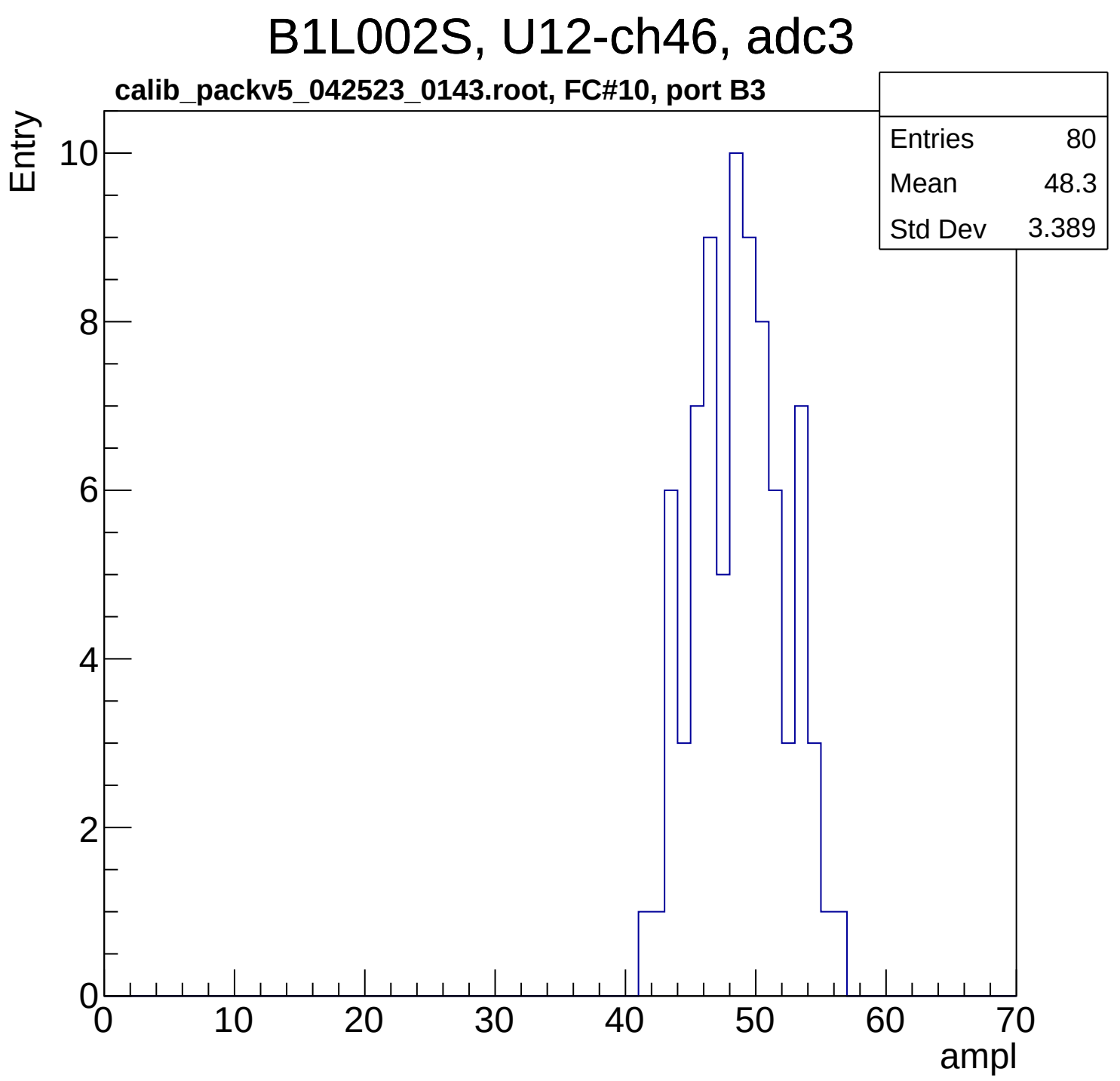

B1L002S, U12-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.65
Std Dev 2.964
Gaus mean : 35.8817
Gaus Width: 3.3128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.36
Std Dev 2.869
Gaus mean : 41.5158
Gaus Width: 3.1574
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 48.3
Std Dev 3.389
Entry
ampl
0
2
4
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0
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60
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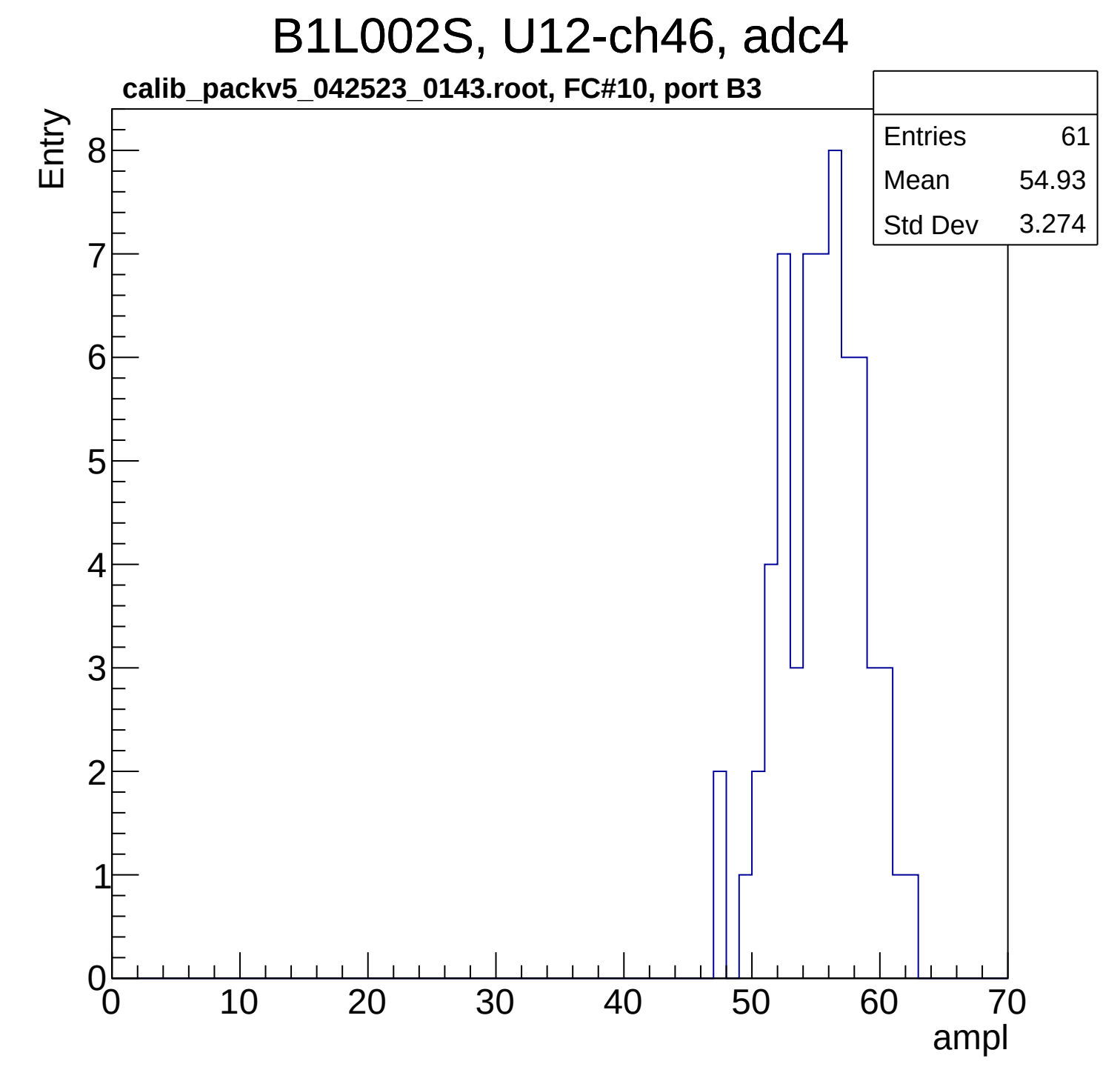
B1L002S, U12-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.93
Std Dev 3.274
Entry
0
1
2
3
4
5
6
7
8
0
10
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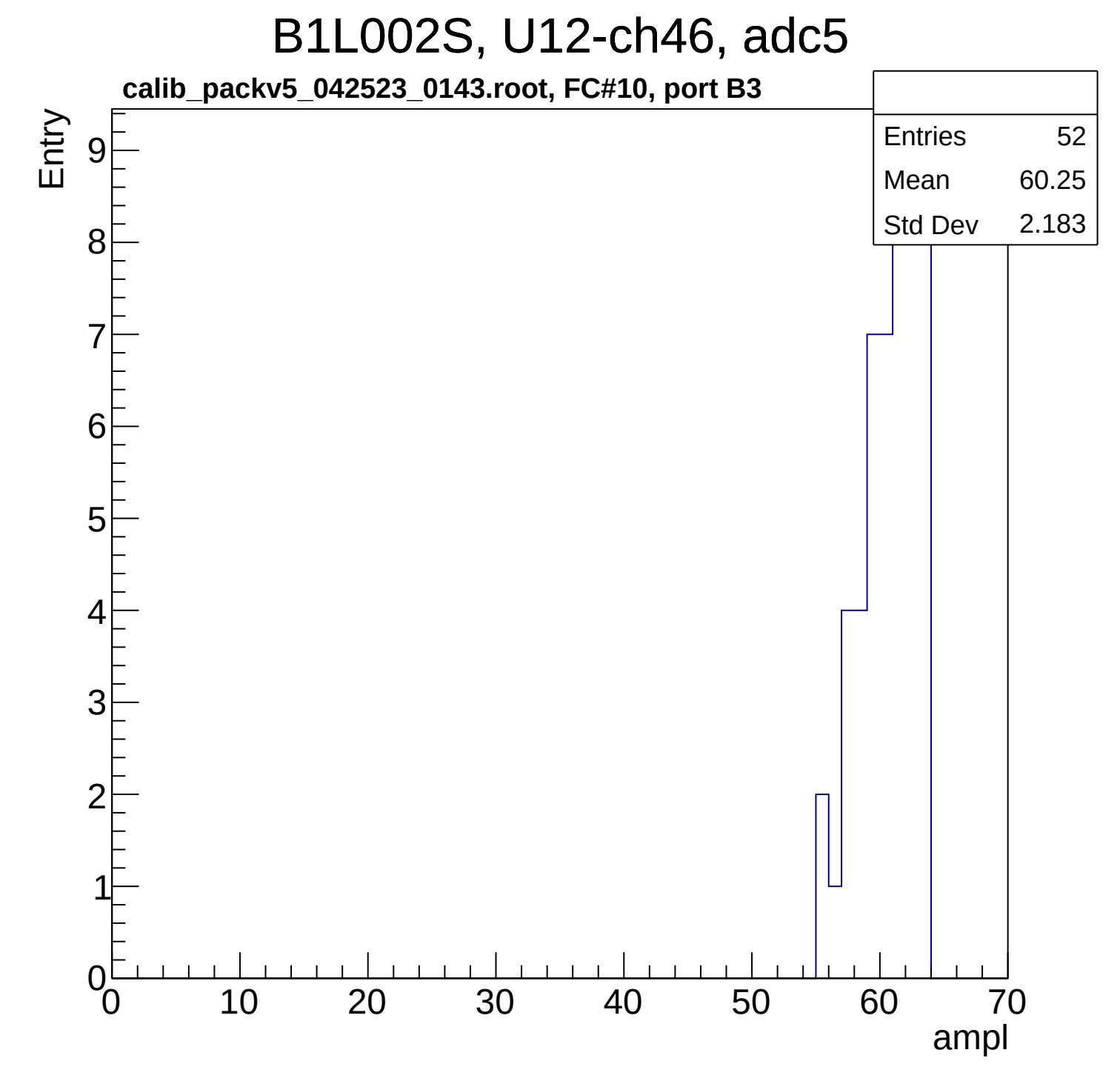
30
40
50
60
70
ampl


B1L002S, U12-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 60.25
Std Dev 2.183
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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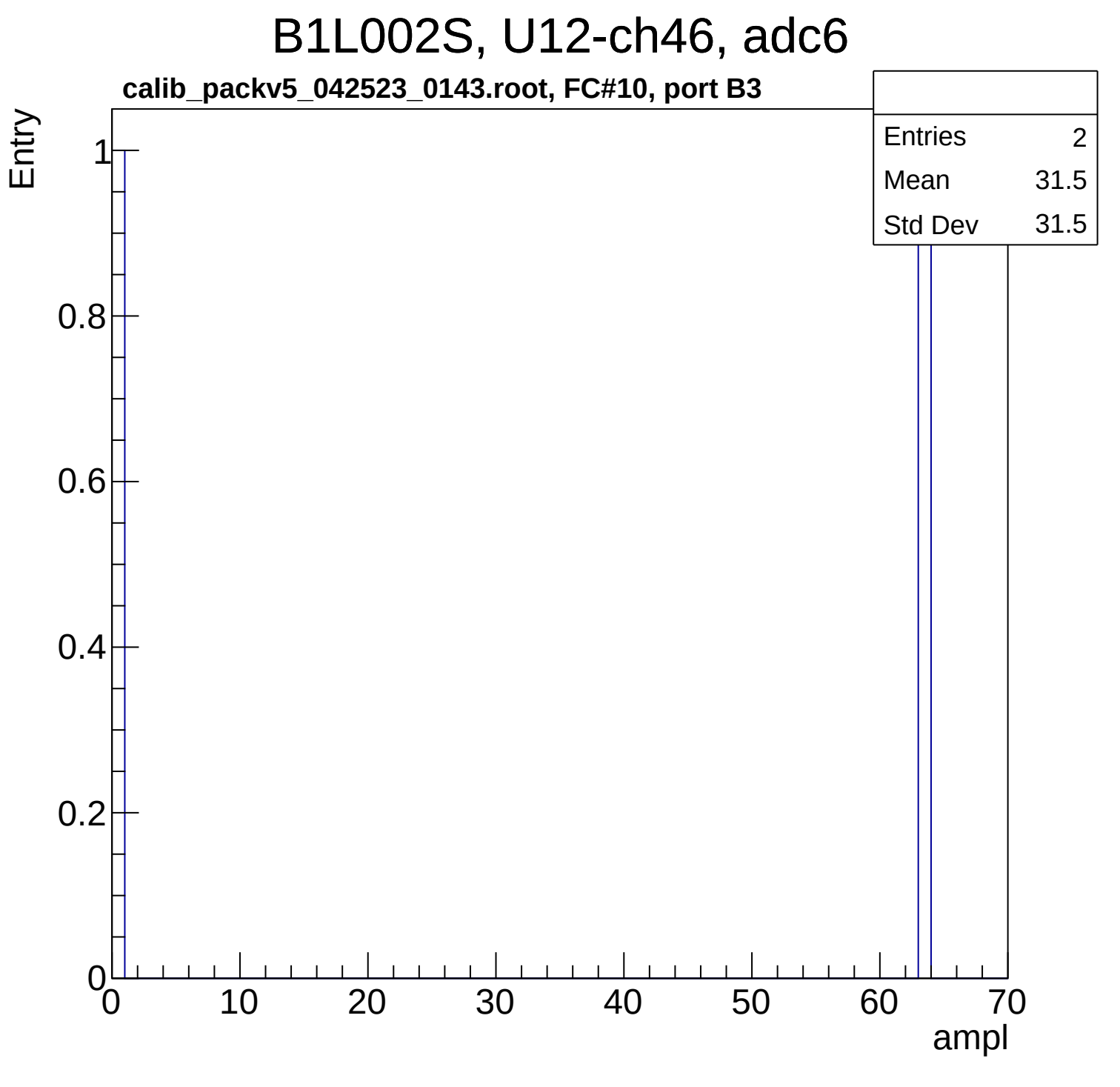
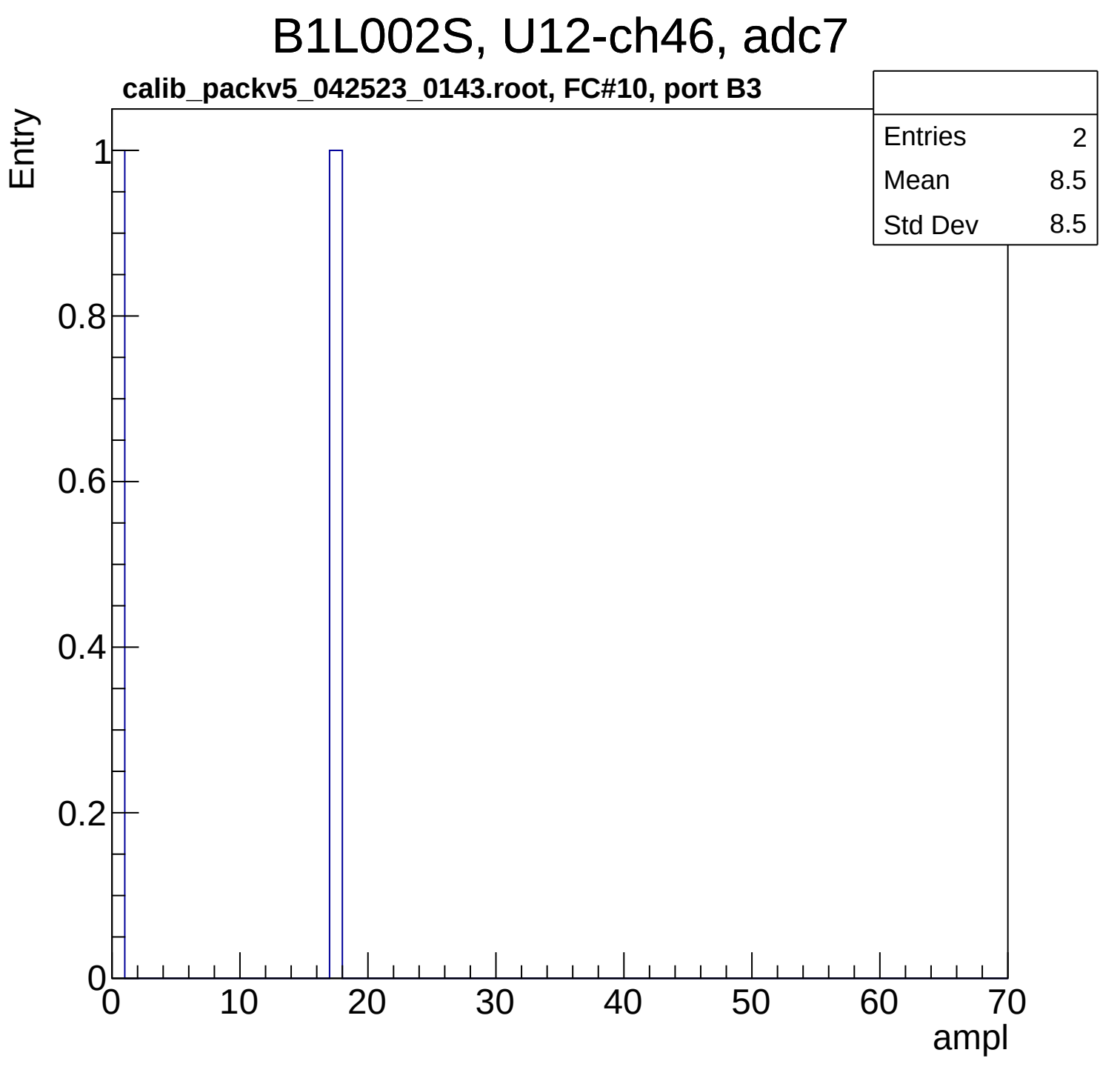

B1L002S, U12-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 8.5
Std Dev 8.5
Entry
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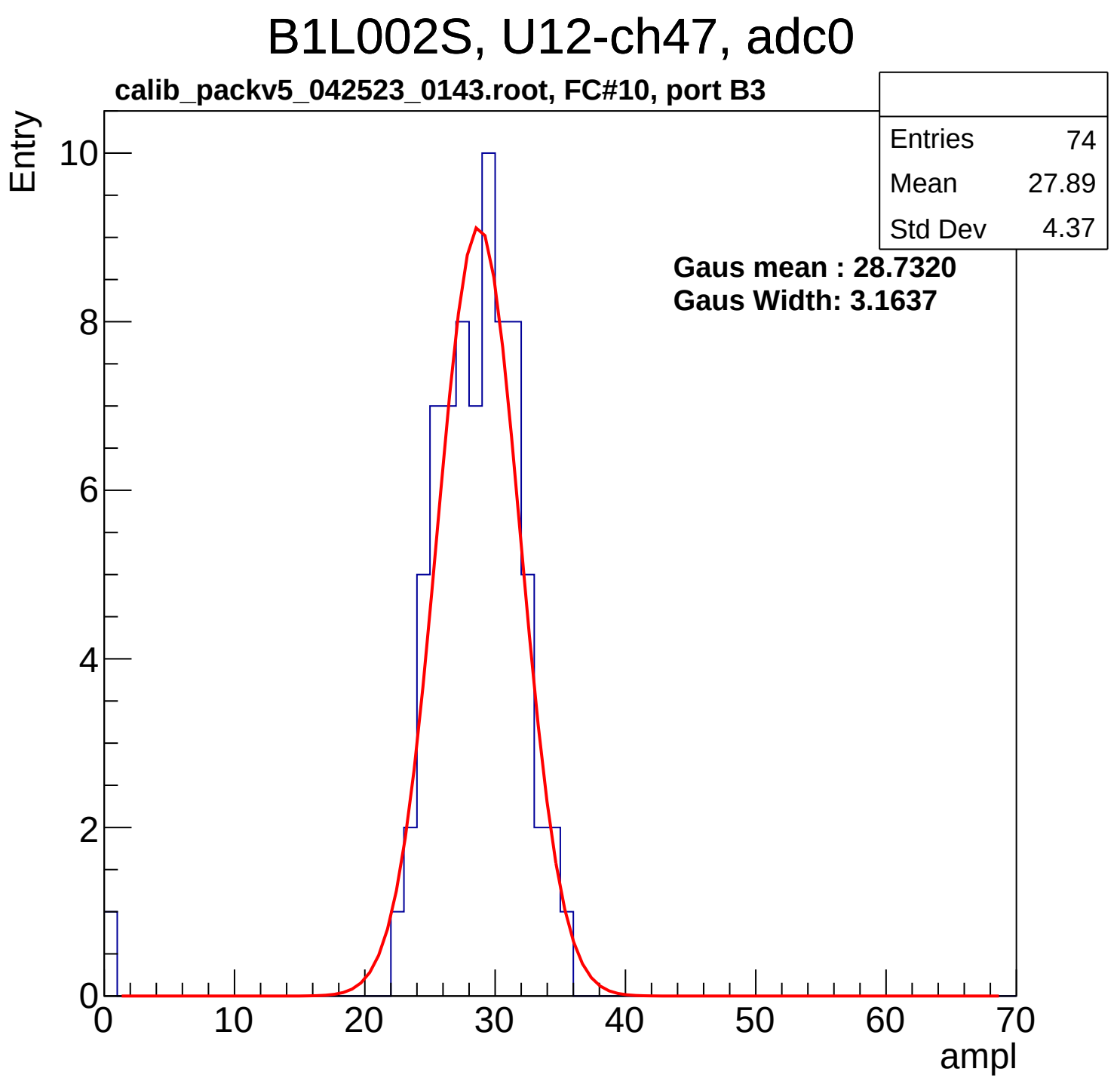
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.89
Std Dev 4.37
Gaus mean : 28.7320
Gaus Width: 3.1637
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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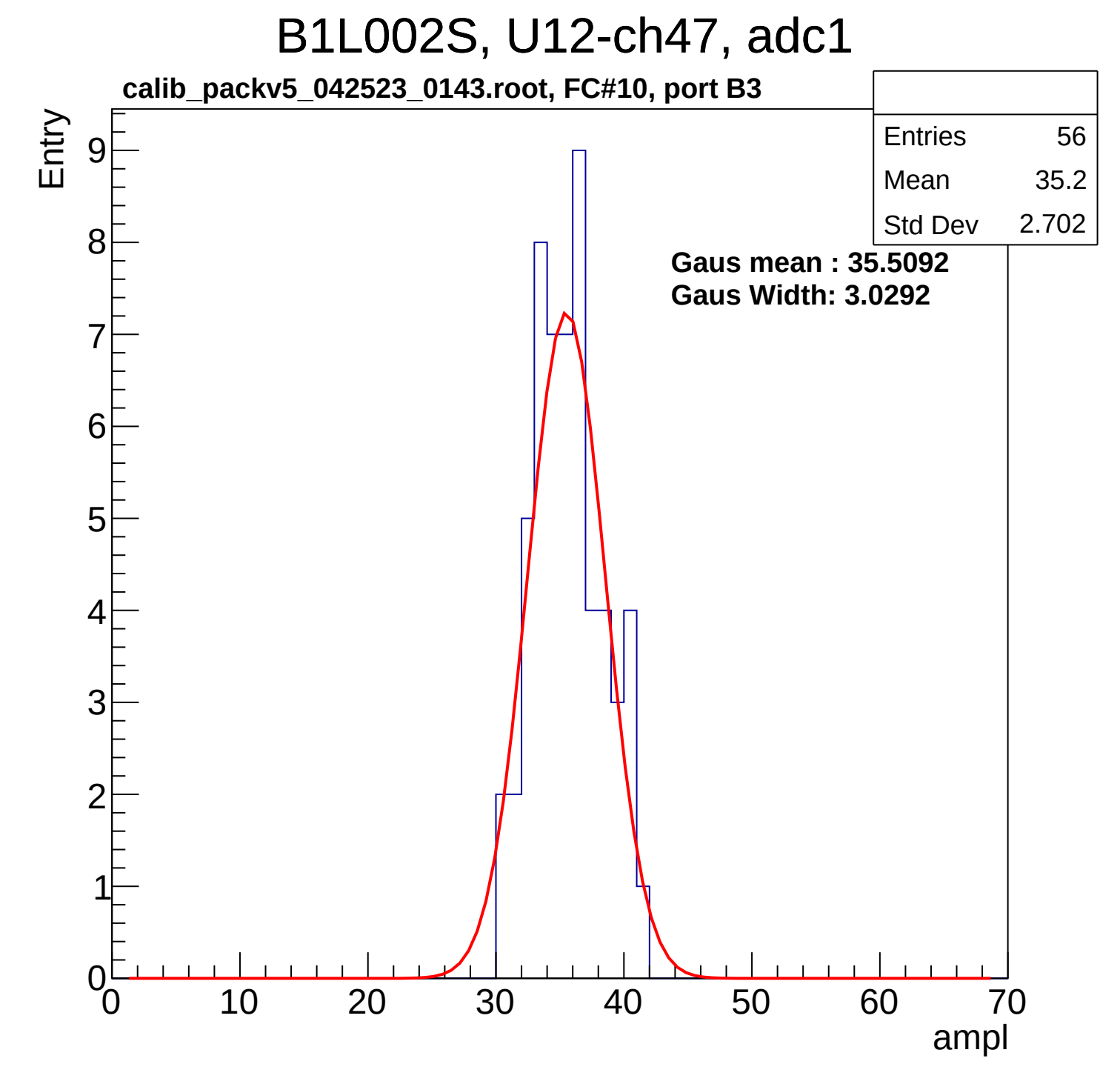
B1L002S, U12-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.2
Std Dev 2.702
Gaus mean : 35.5092
Gaus Width: 3.0292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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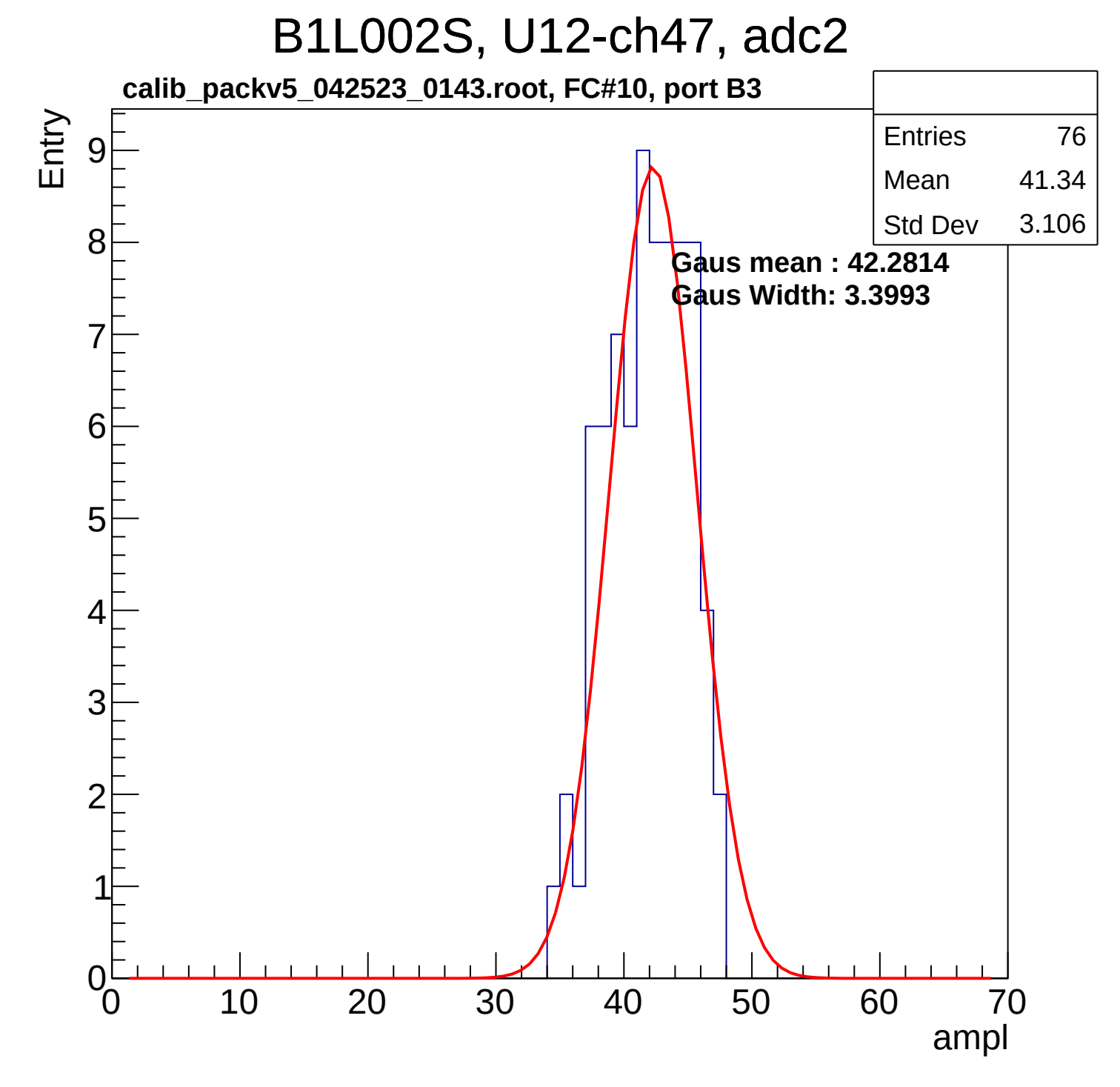
B1L002S, U12-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 41.34
Std Dev 3.106
Gaus mean : 42.2814
Gaus Width: 3.3993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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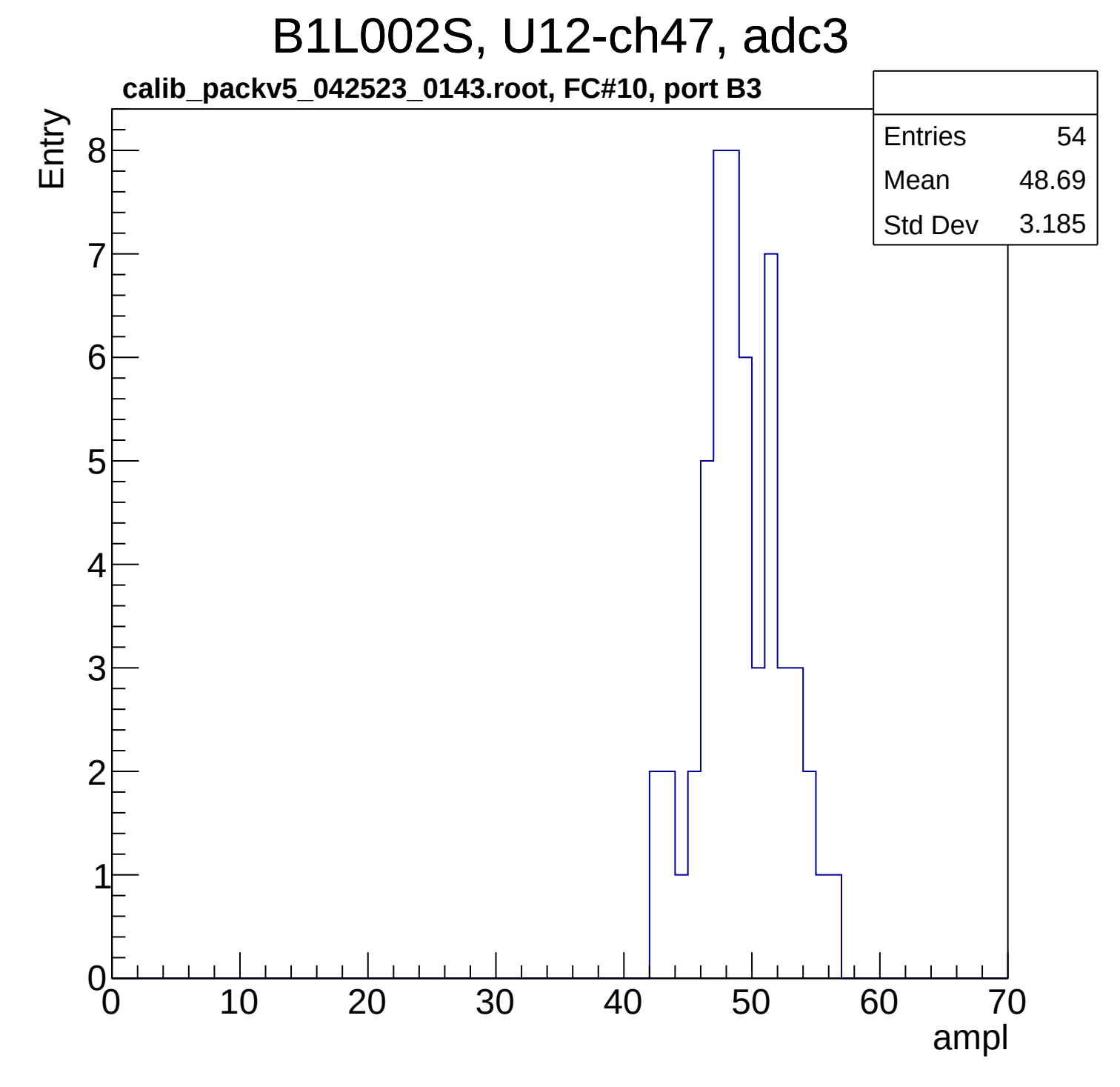
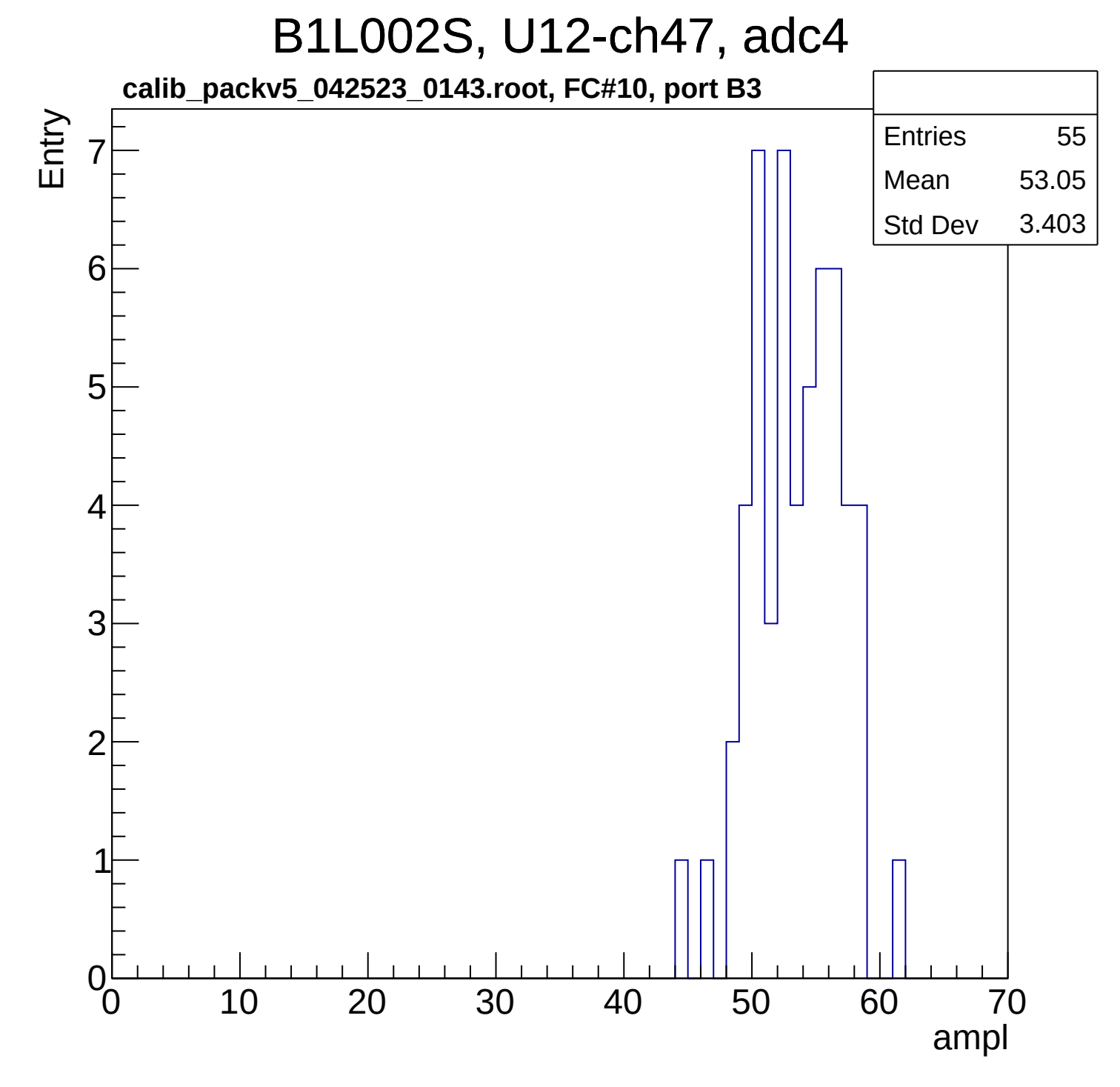

B1L002S, U12-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 48.69
Std Dev 3.185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.05
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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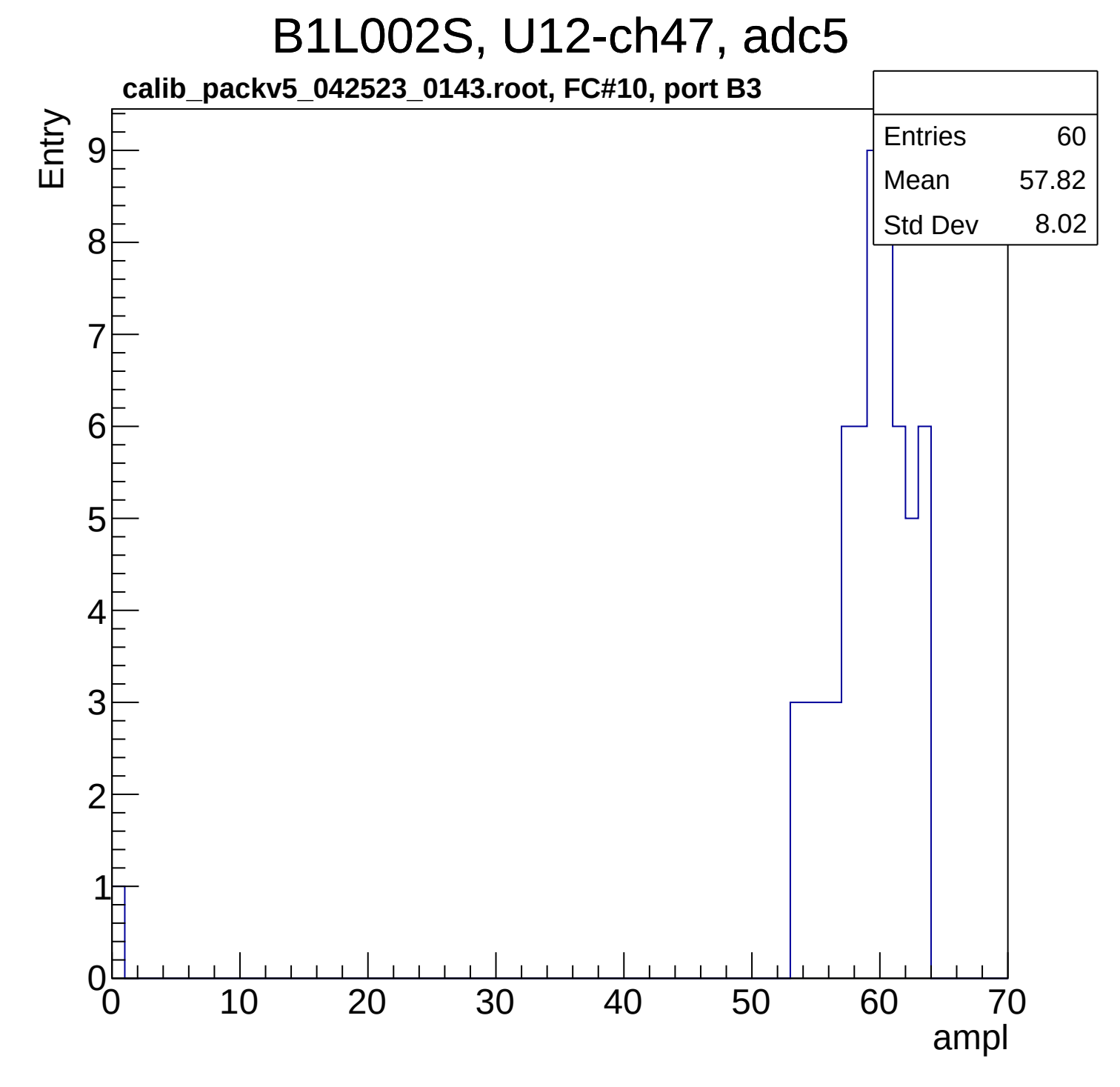
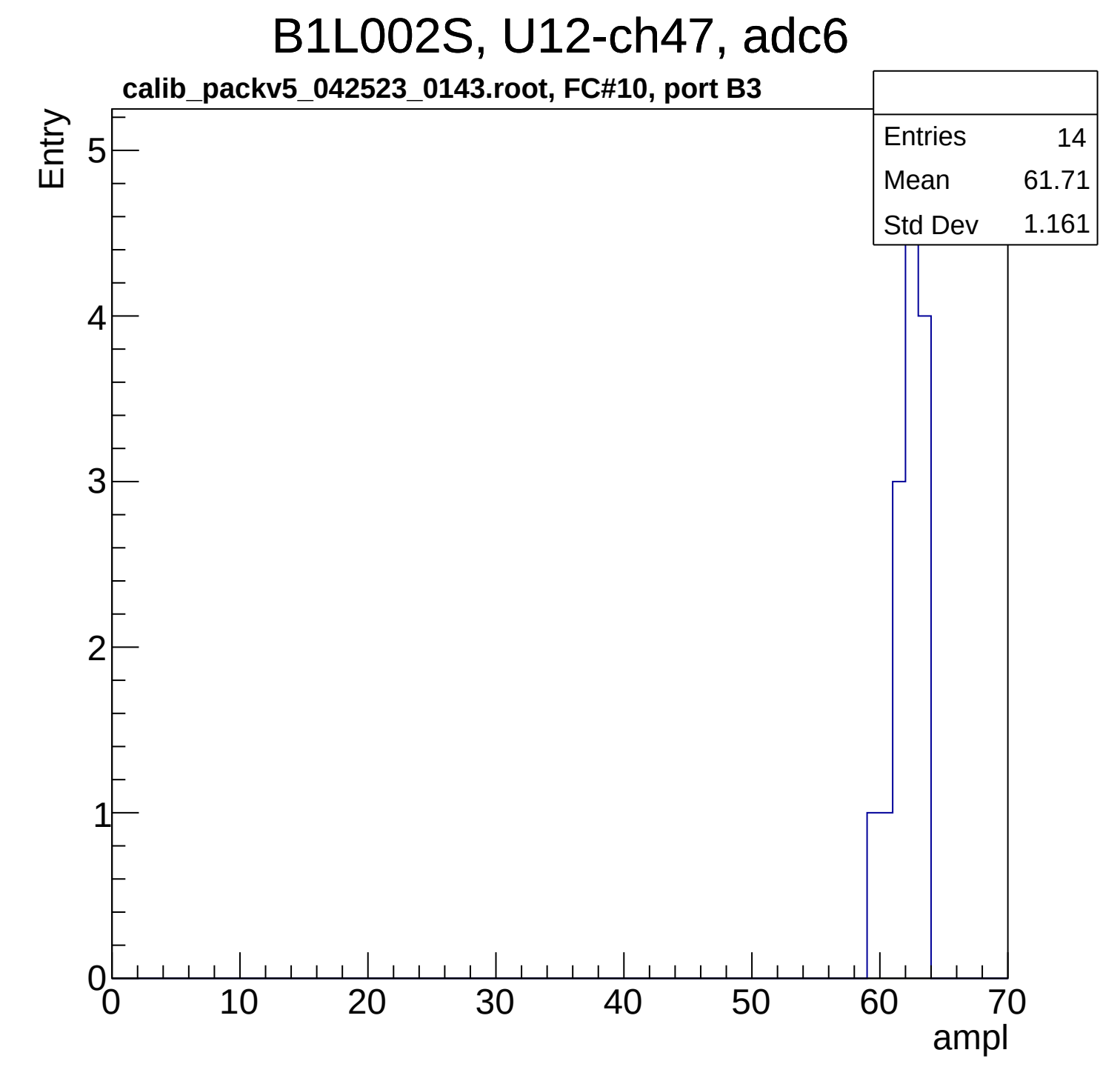
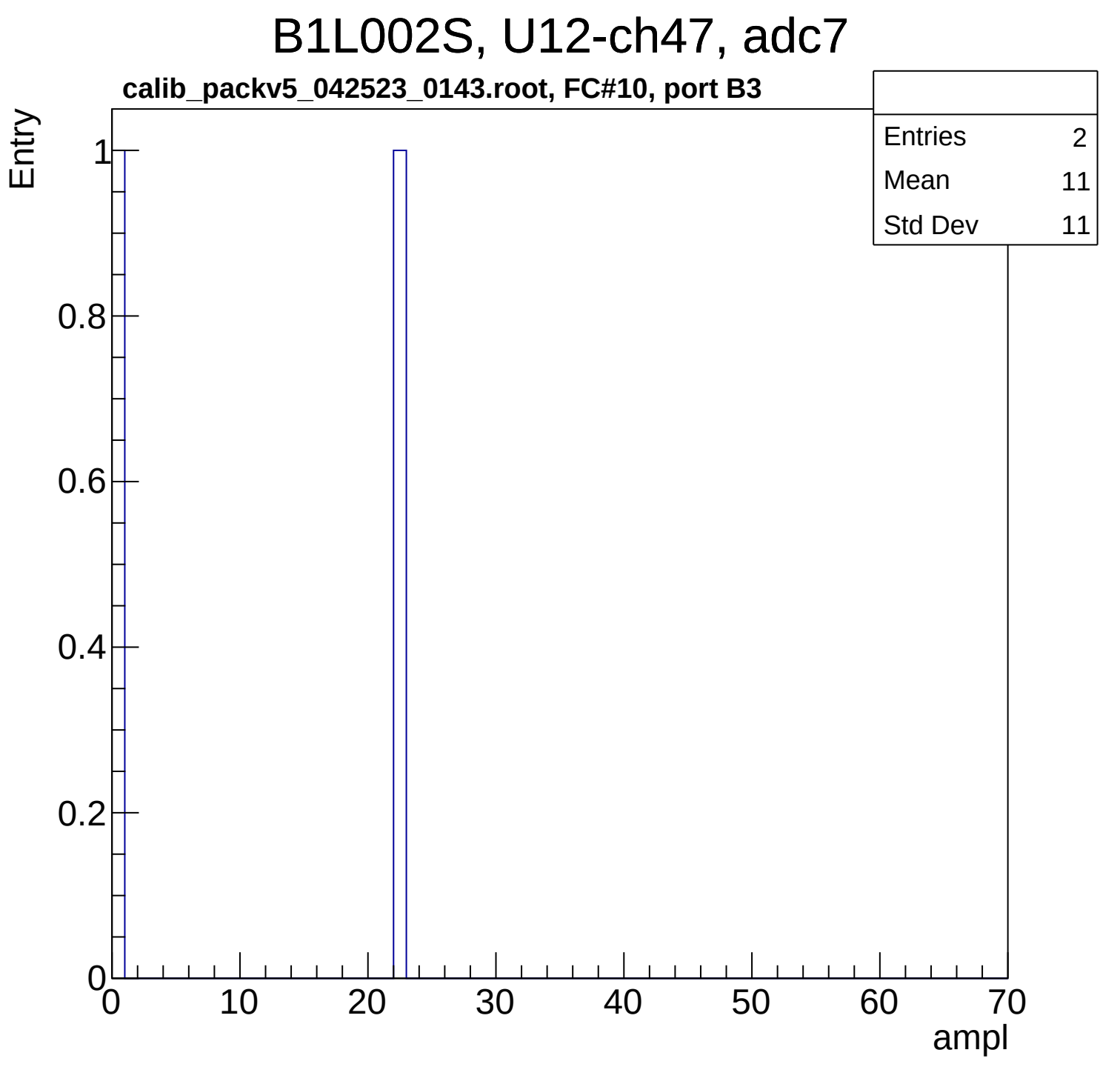

B1L002S, U12-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.82
Std Dev 8.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.71
Std Dev 1.161
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
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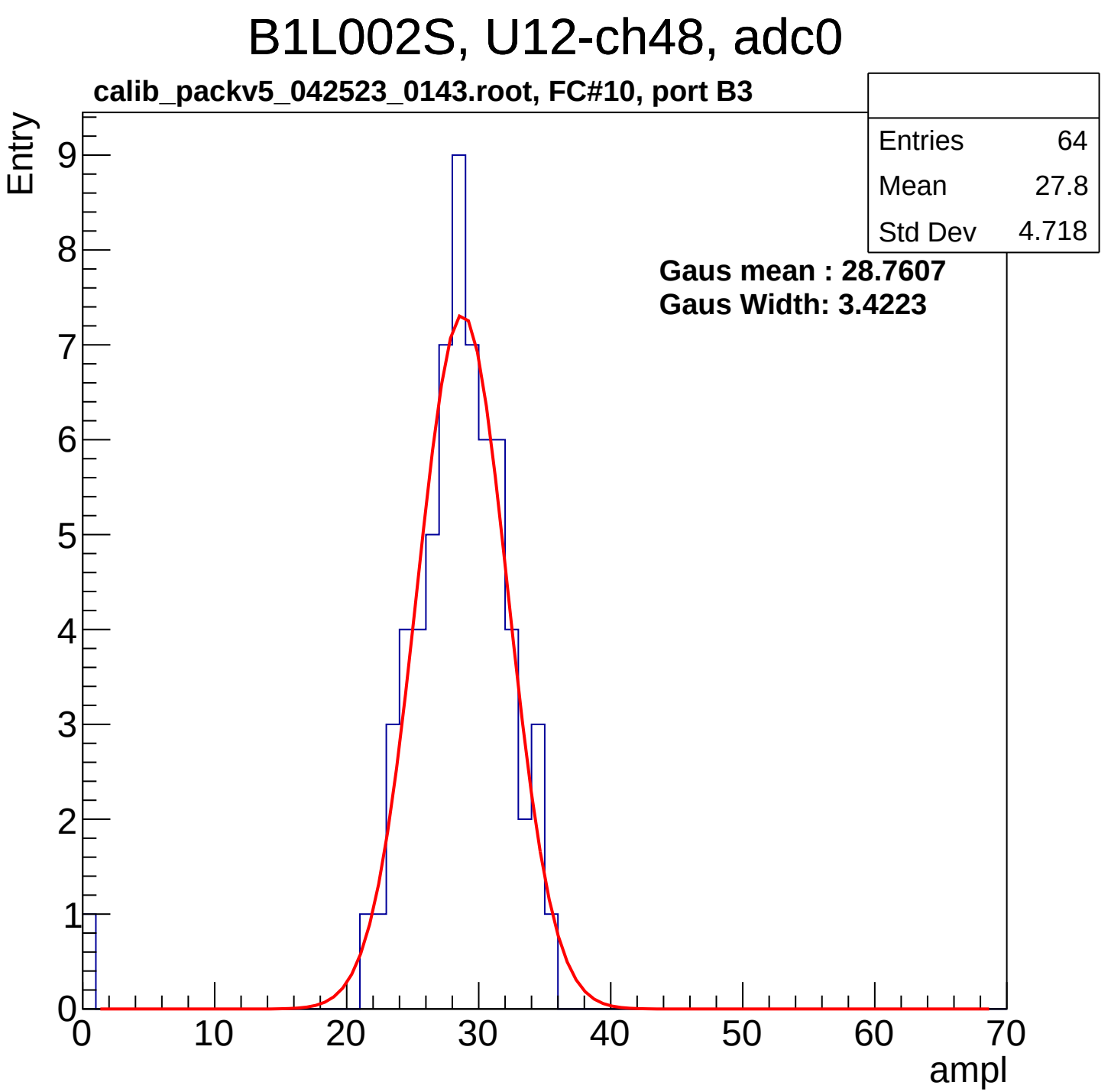
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.8
Std Dev 4.718
Gaus mean : 28.7607
Gaus Width: 3.4223
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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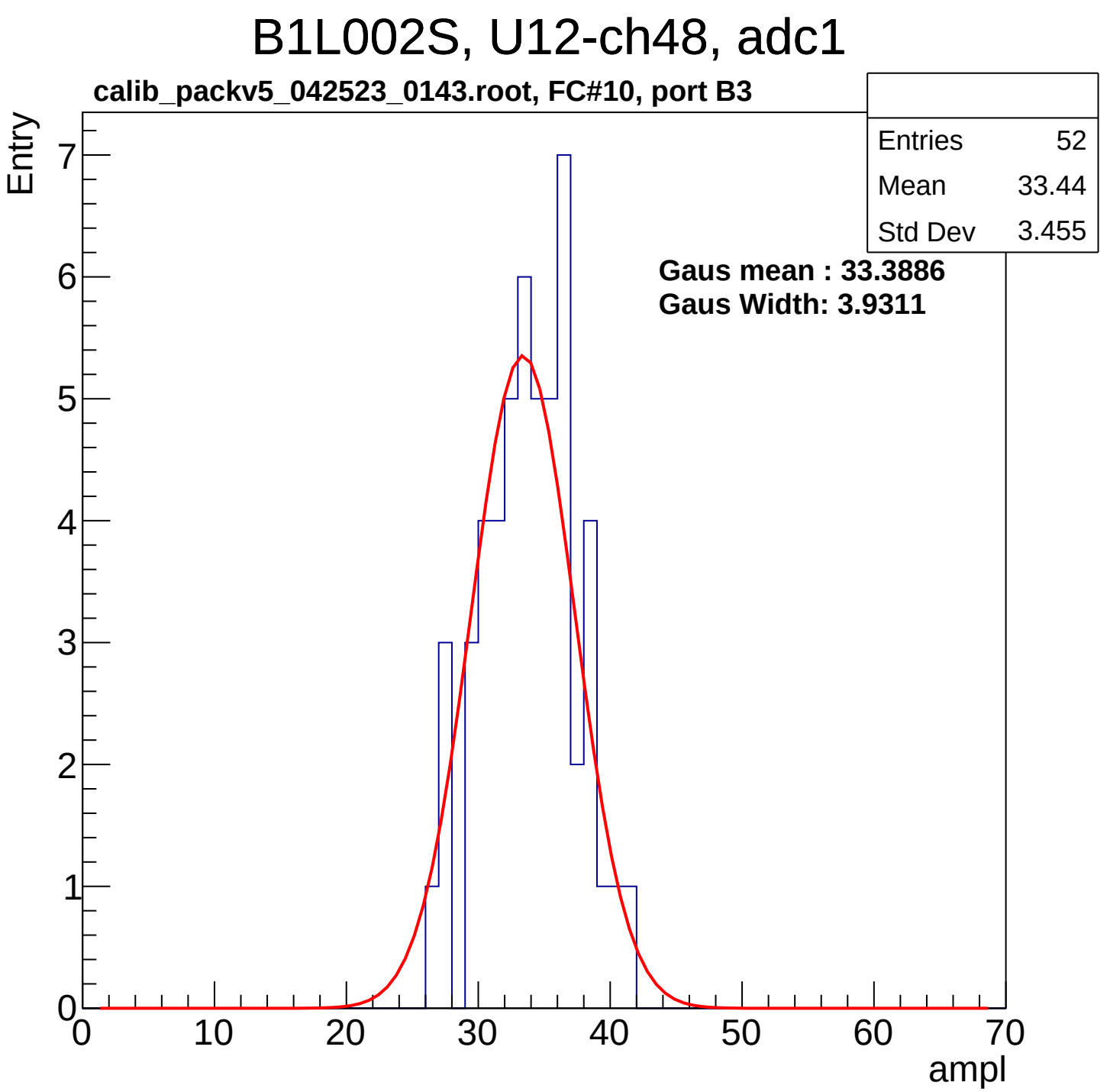
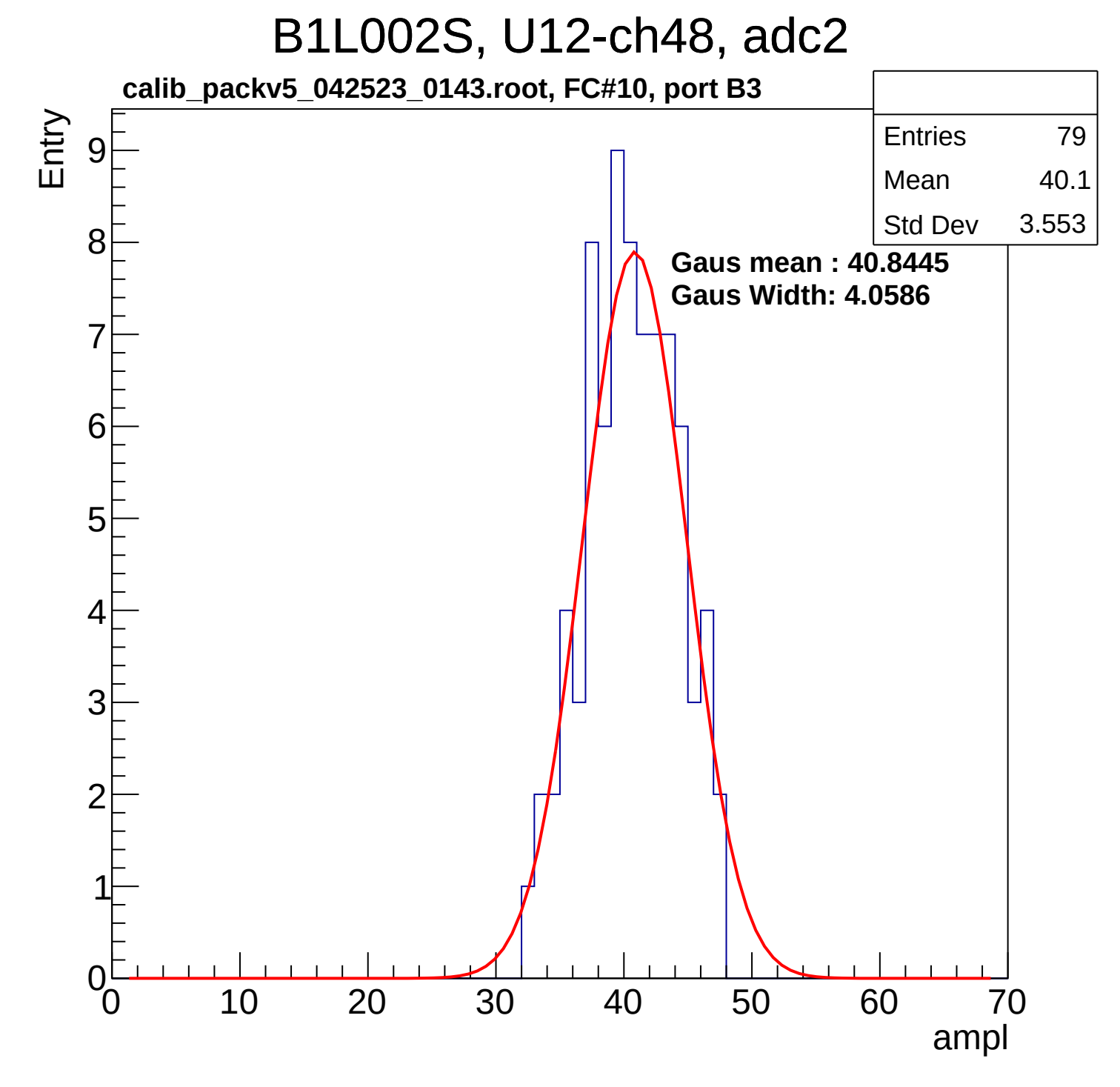
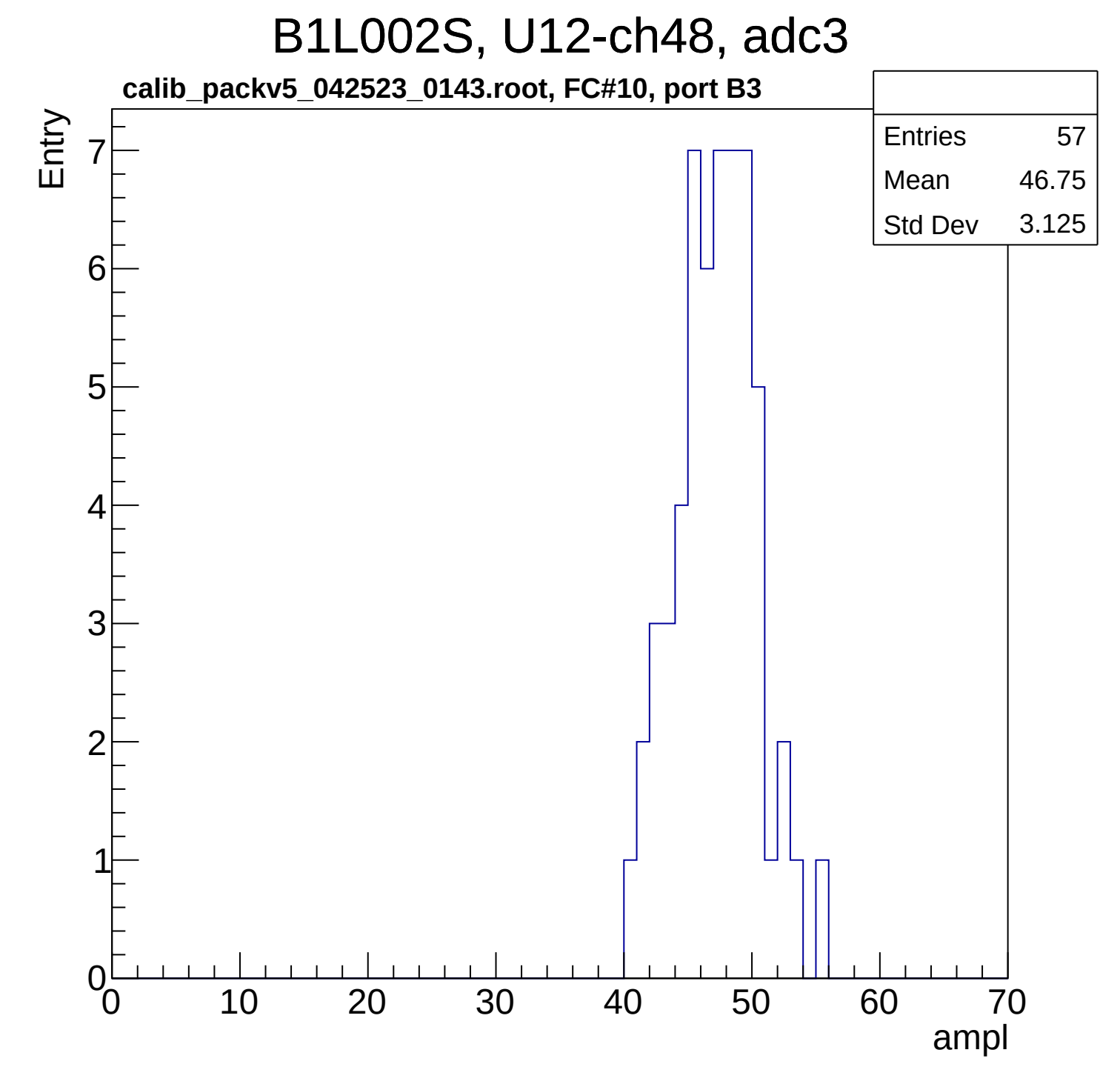
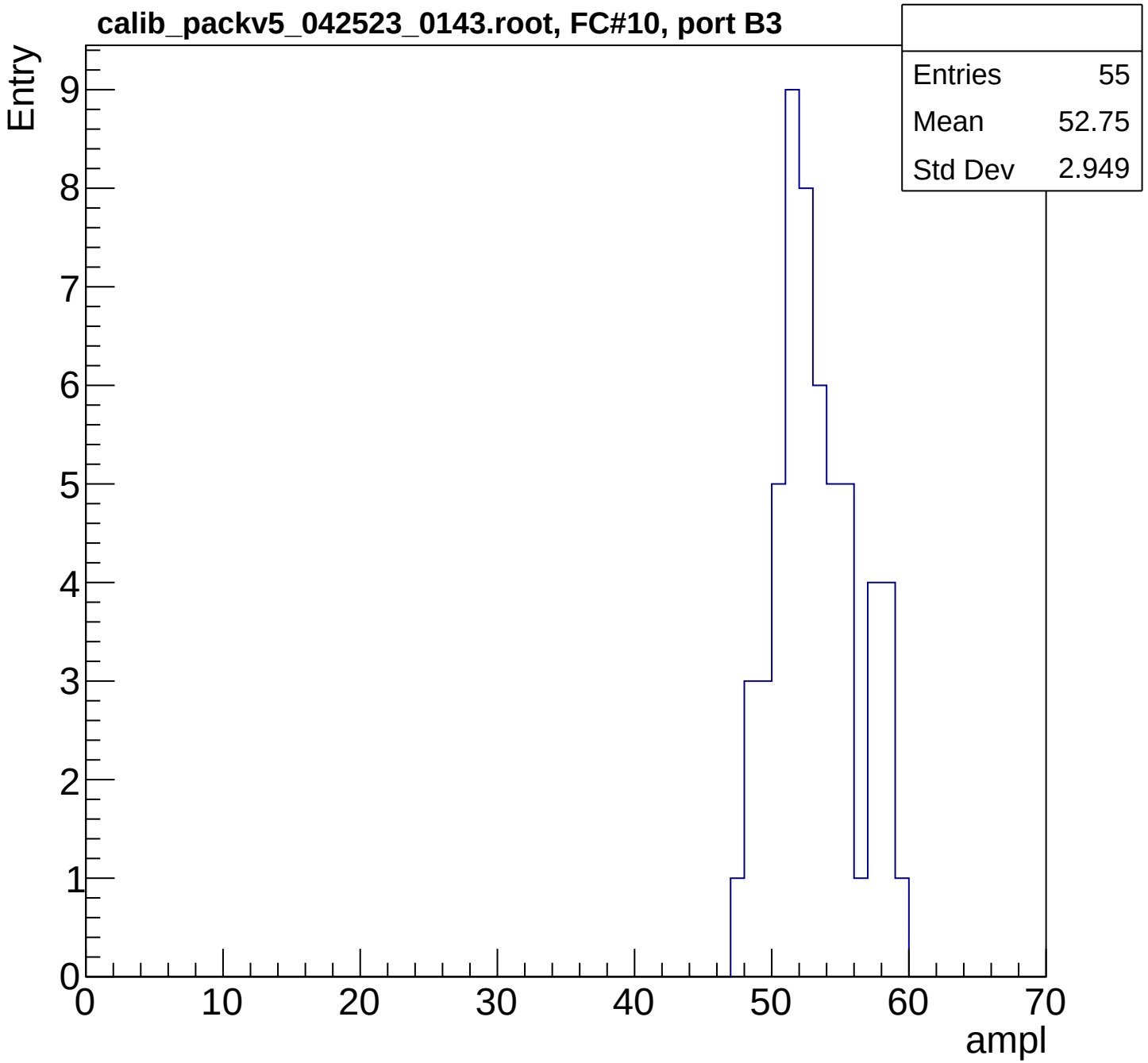

B1L002S, U12-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 33.44
Std Dev 3.455
Gaus mean : 33.3886
Gaus Width: 3.9311
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 40.1
Std Dev 3.553
Gaus mean : 40.8445
Gaus Width: 4.0586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.75
Std Dev 3.125
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.75
Std Dev 2.949
Entry
ampl
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3
4
5
6
7
8
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0
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20
30
40
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60
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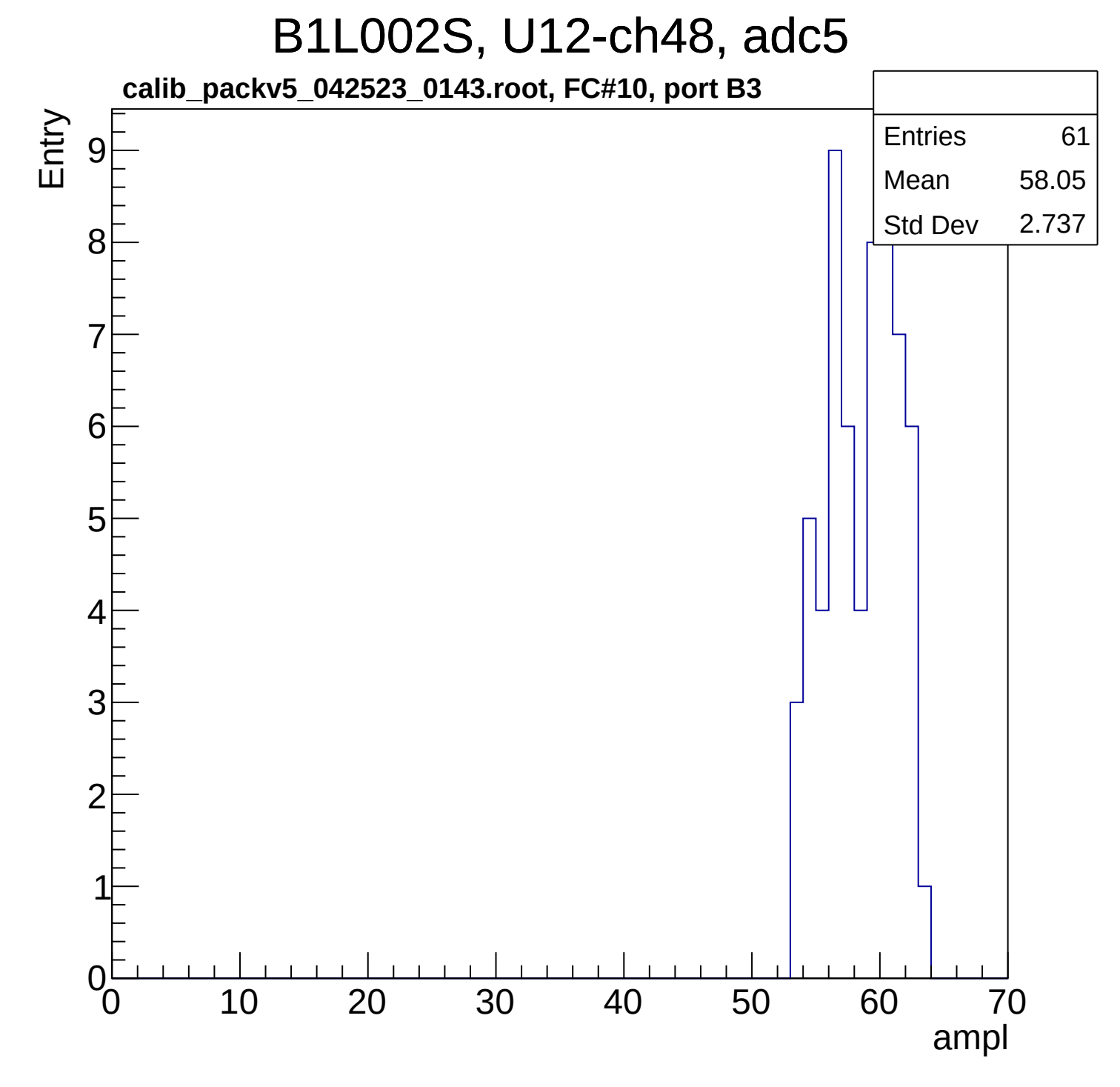
B1L002S, U12-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.05
Std Dev 2.737
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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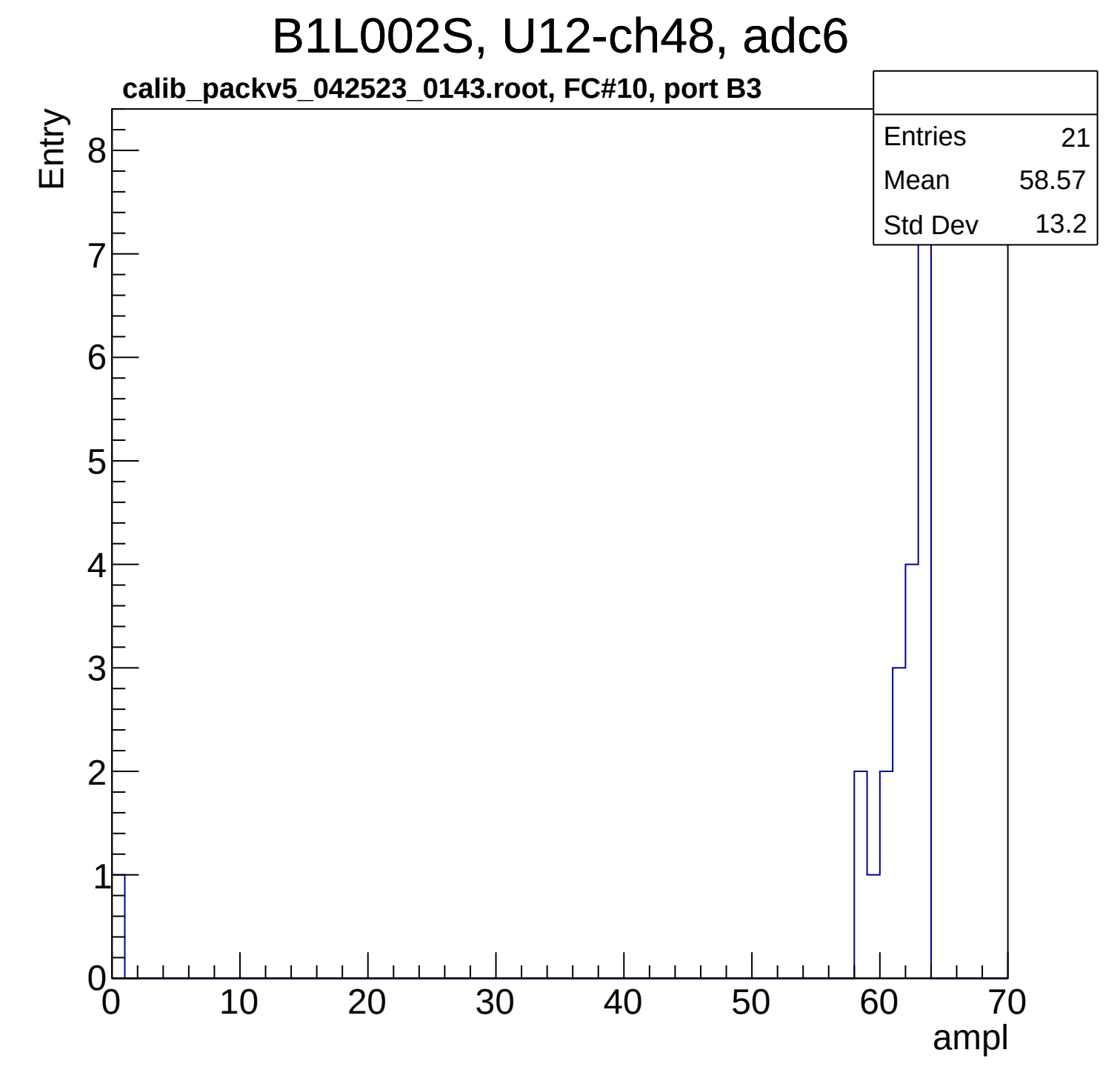
B1L002S, U12-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.57
Std Dev 13.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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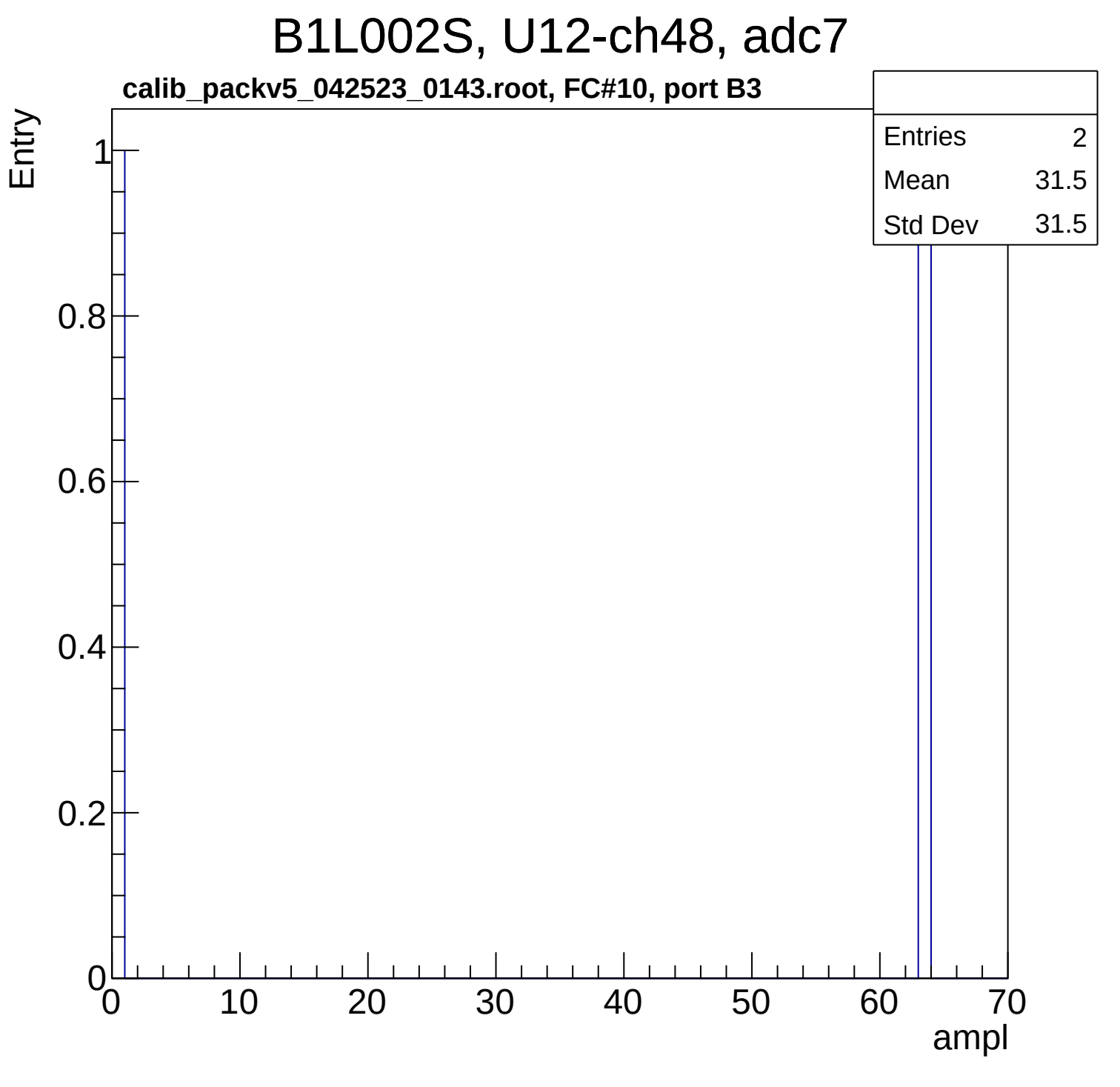
40
50
60
70


B1L002S, U12-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
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30
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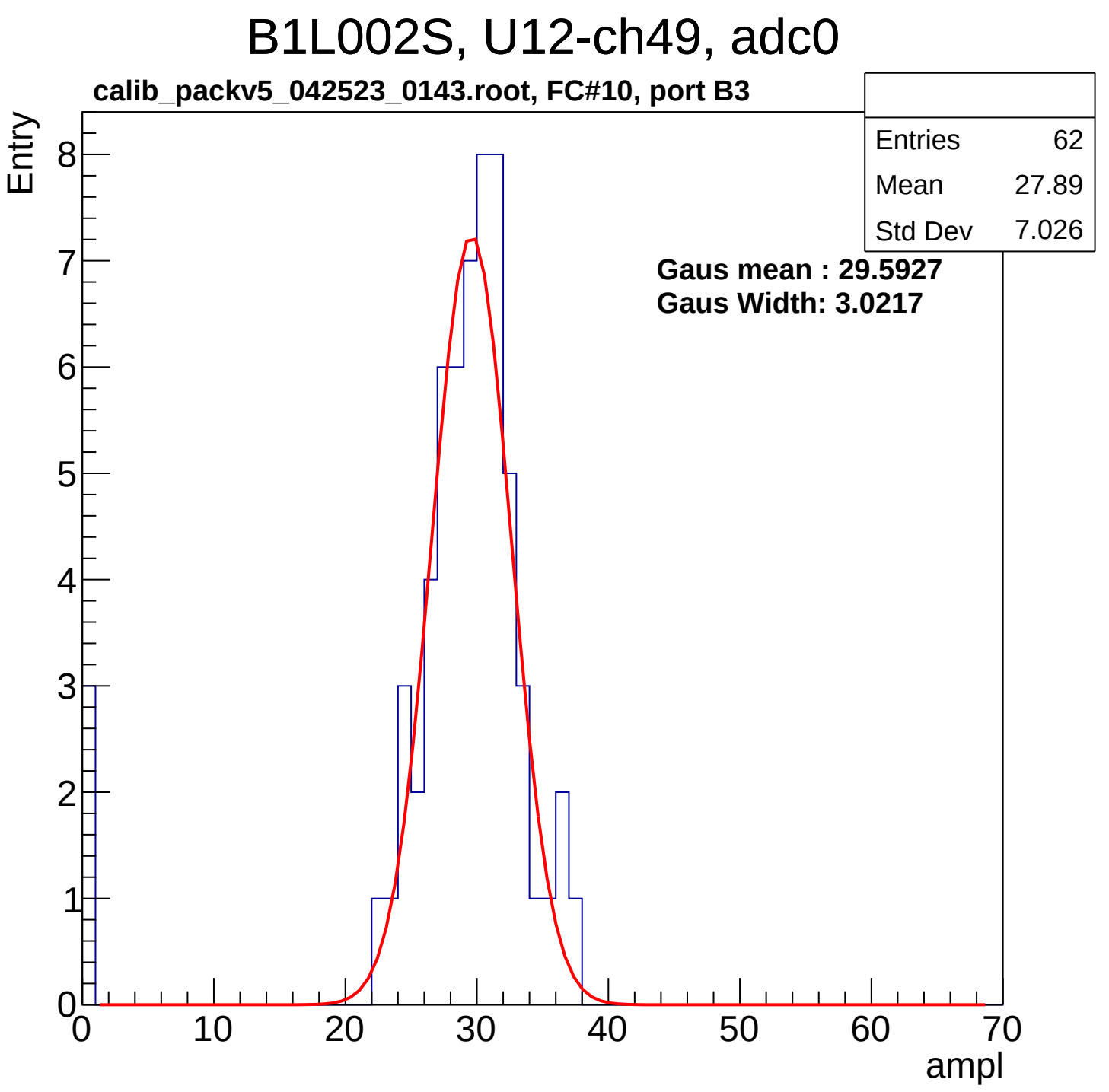
50
60
70
ampl


B1L002S, U12-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.89
Std Dev 7.026
Gaus mean : 29.5927
Gaus Width: 3.0217
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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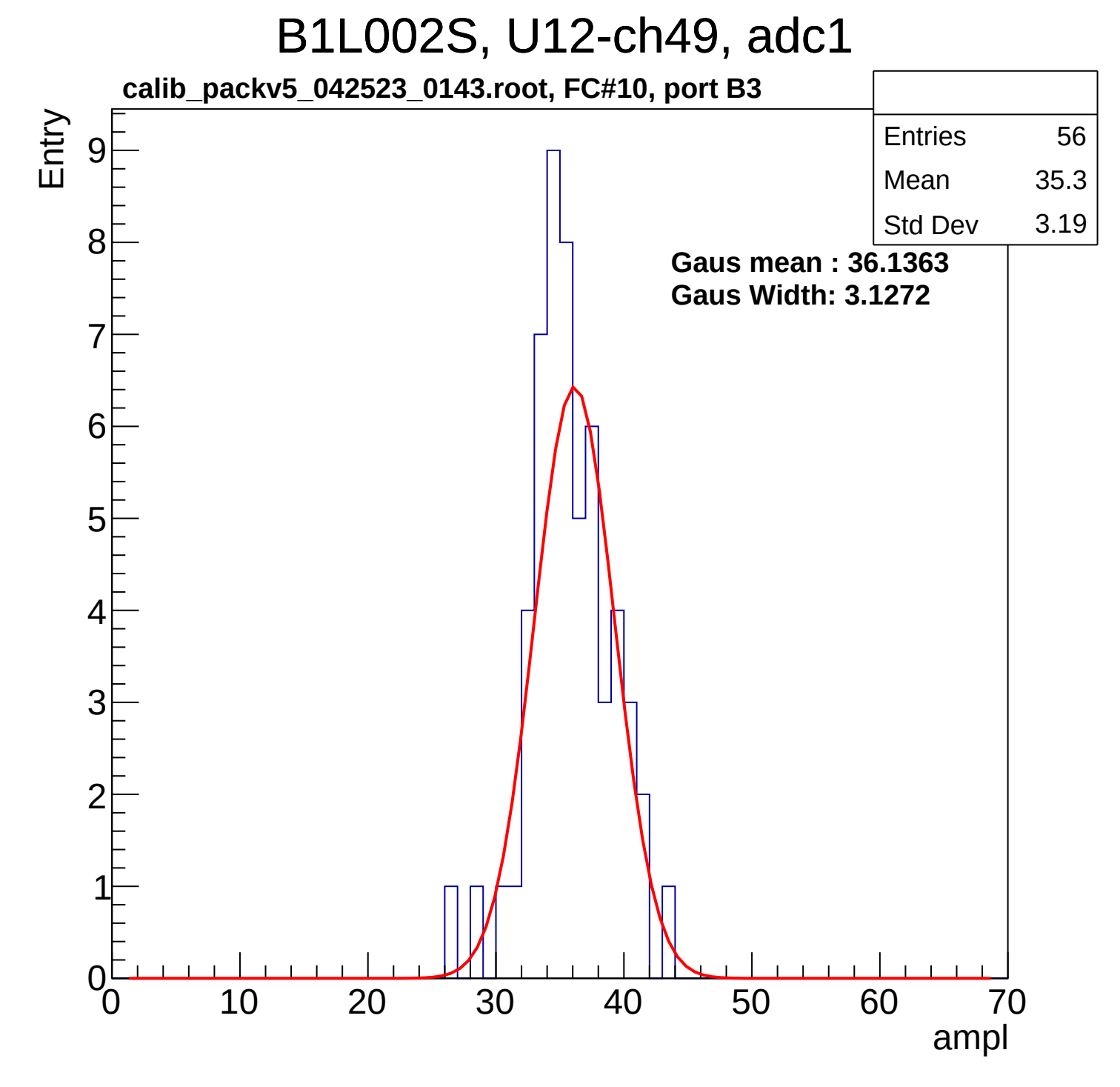
40
50
60
70


B1L002S, U12-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 35.3
Std Dev 3.19
Gaus mean : 36.1363
Gaus Width: 3.1272
Entry
ampl
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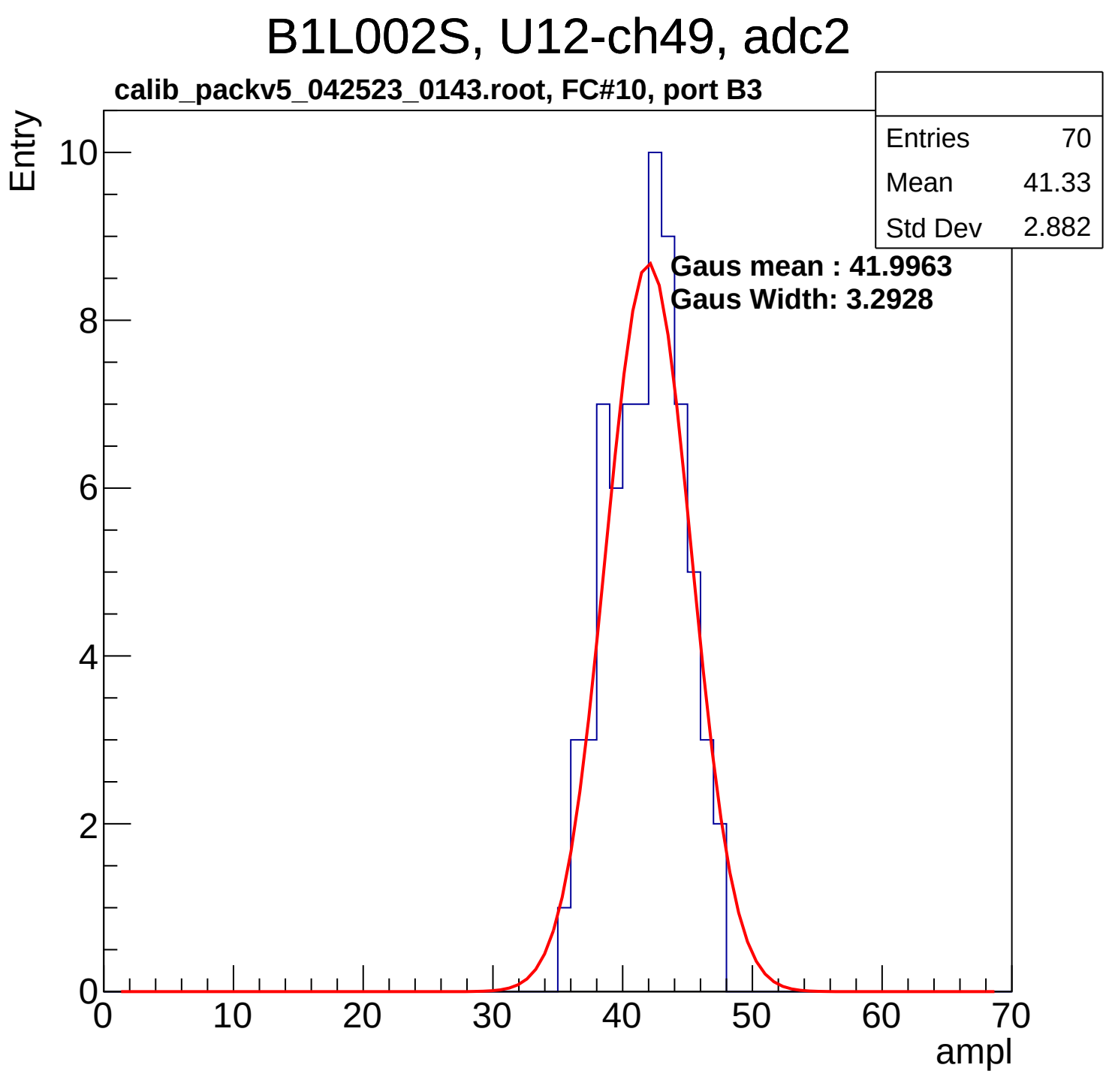
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 41.33
Std Dev 2.882
Gaus mean : 41.9963
Gaus Width: 3.2928
Entry
ampl
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6
8
10
0
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20
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40
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60
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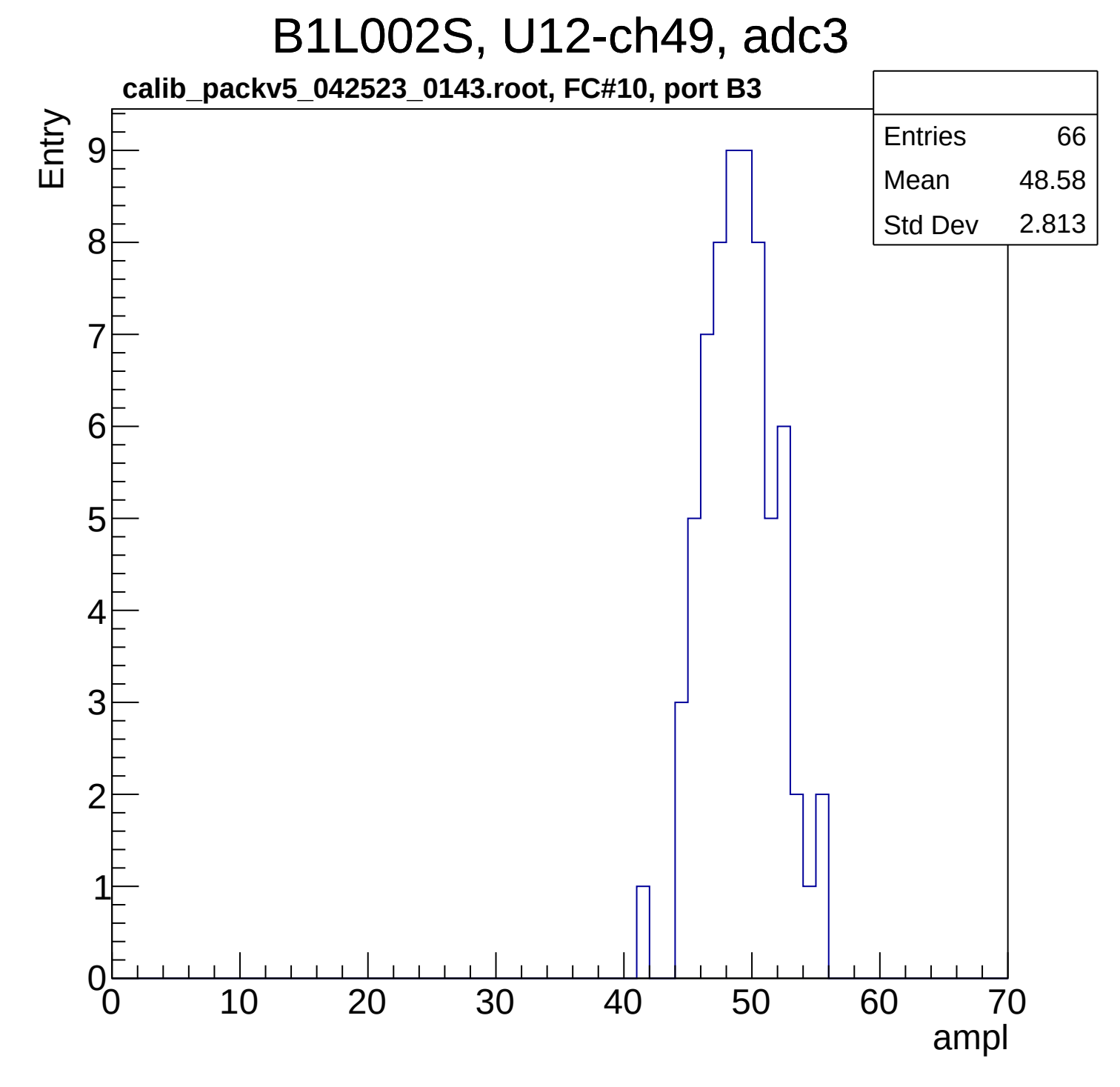
B1L002S, U12-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.58
Std Dev 2.813
Entry
ampl
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6
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0
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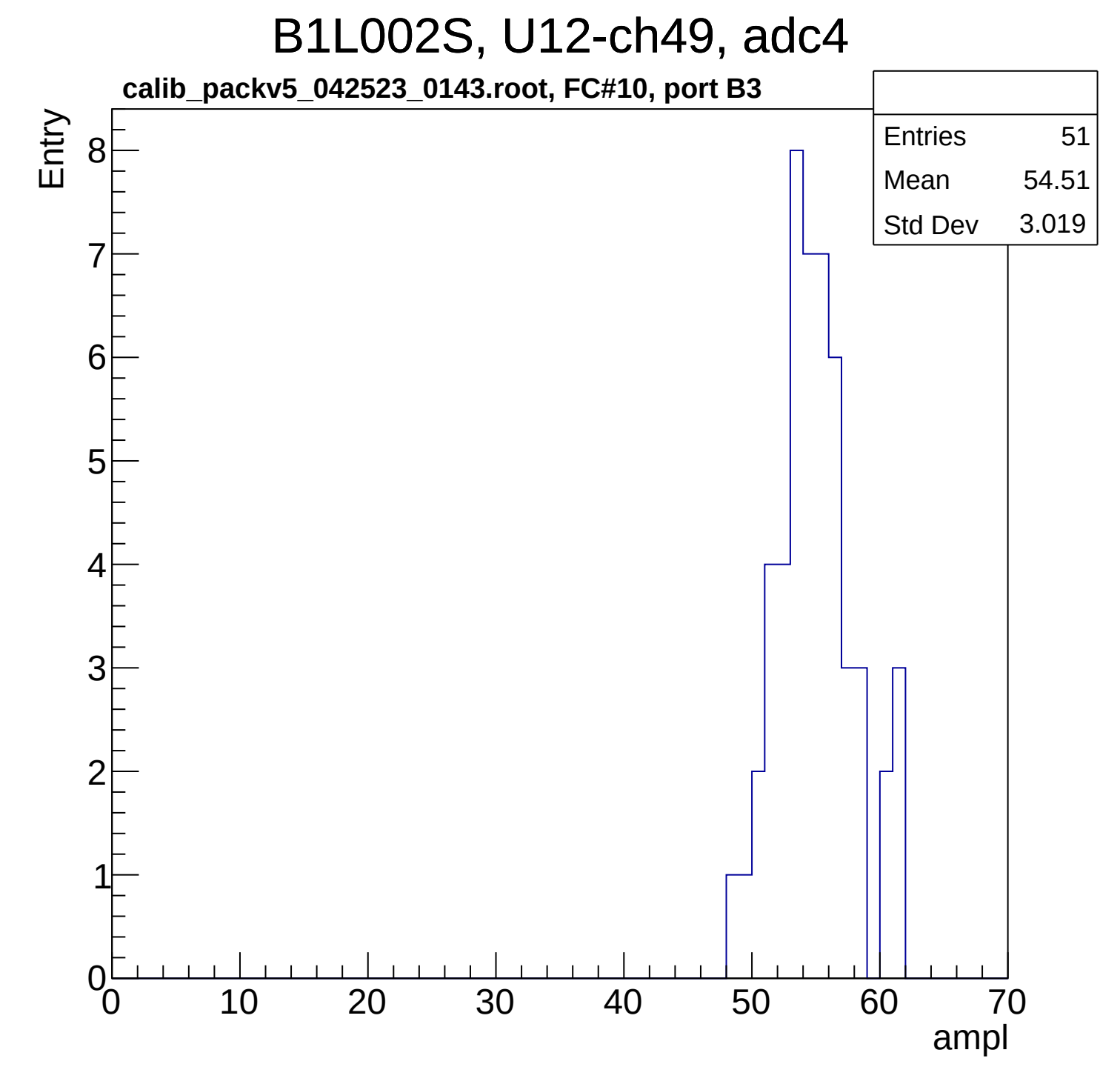
B1L002S, U12-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.51
Std Dev 3.019
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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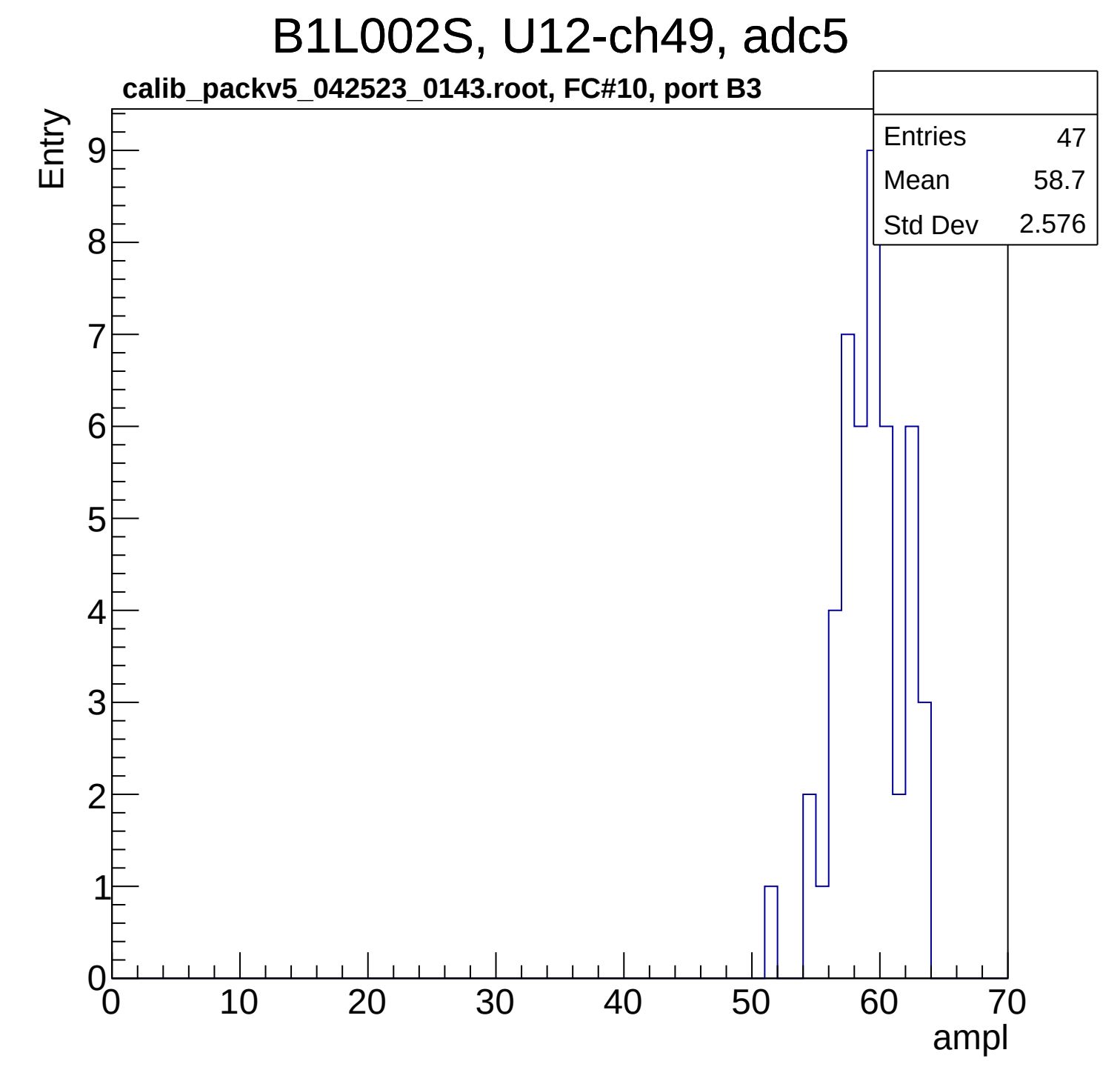
B1L002S, U12-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.7
Std Dev 2.576
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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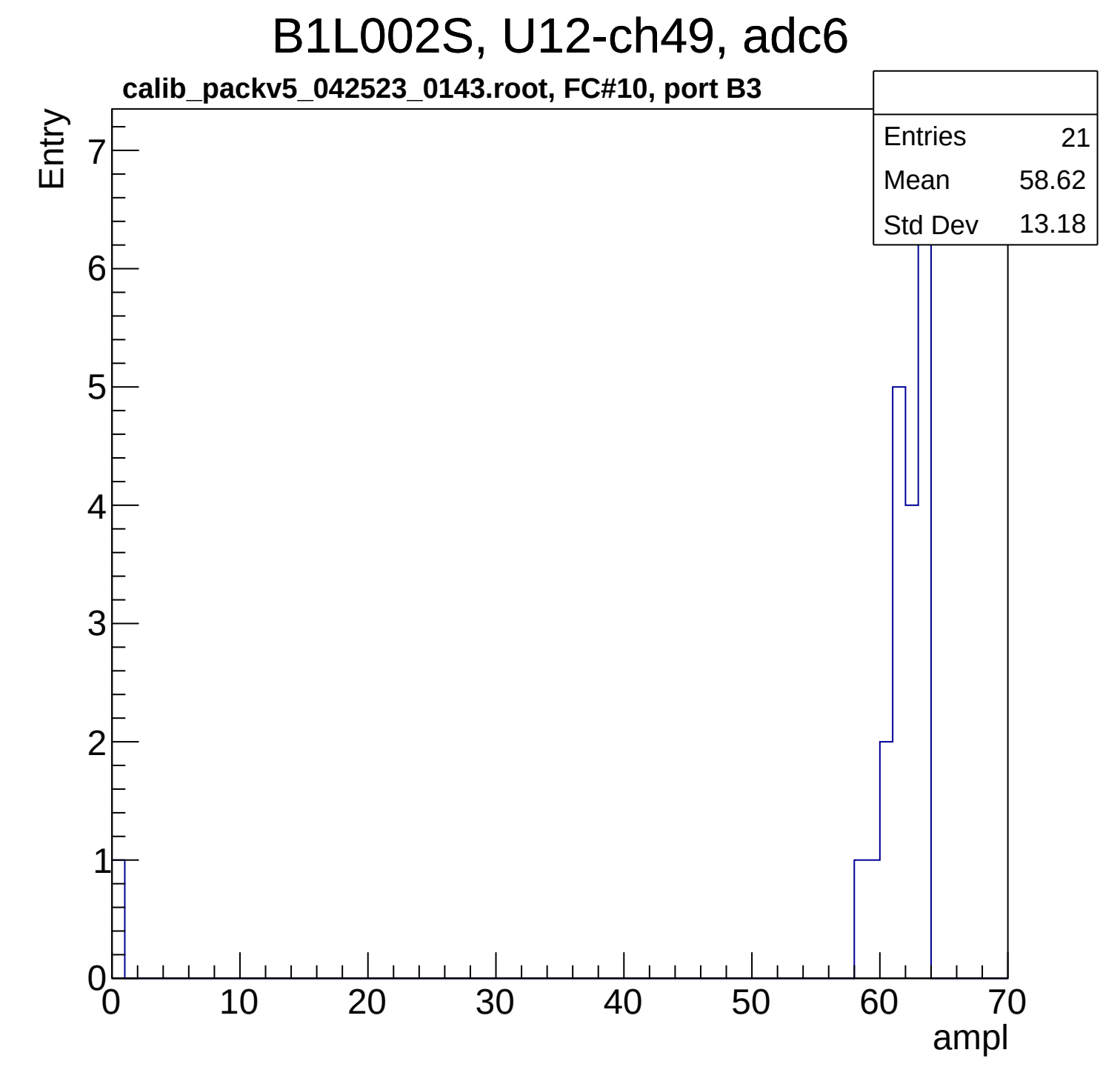
B1L002S, U12-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.62
Std Dev 13.18
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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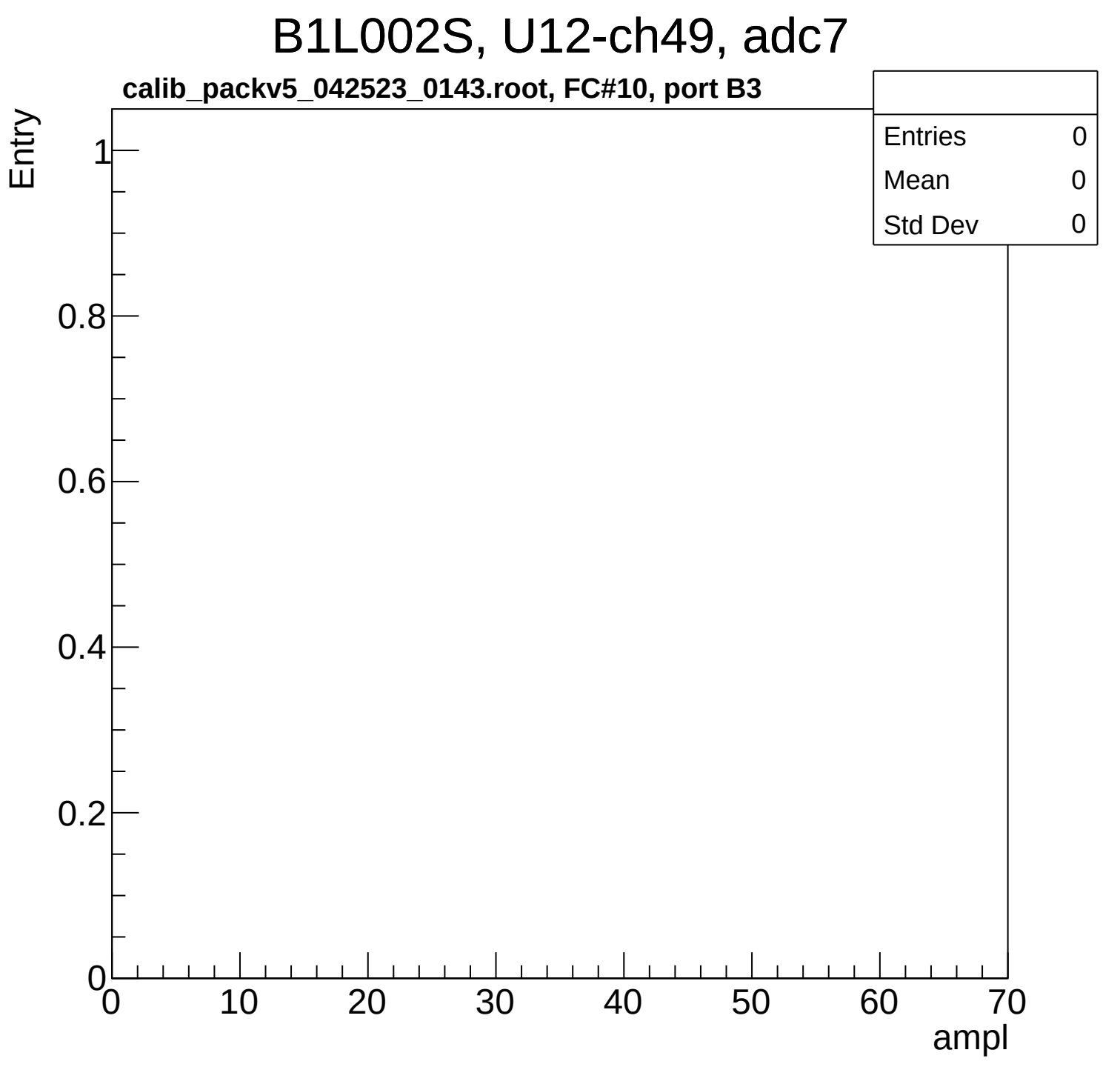
B1L002S, U12-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
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0
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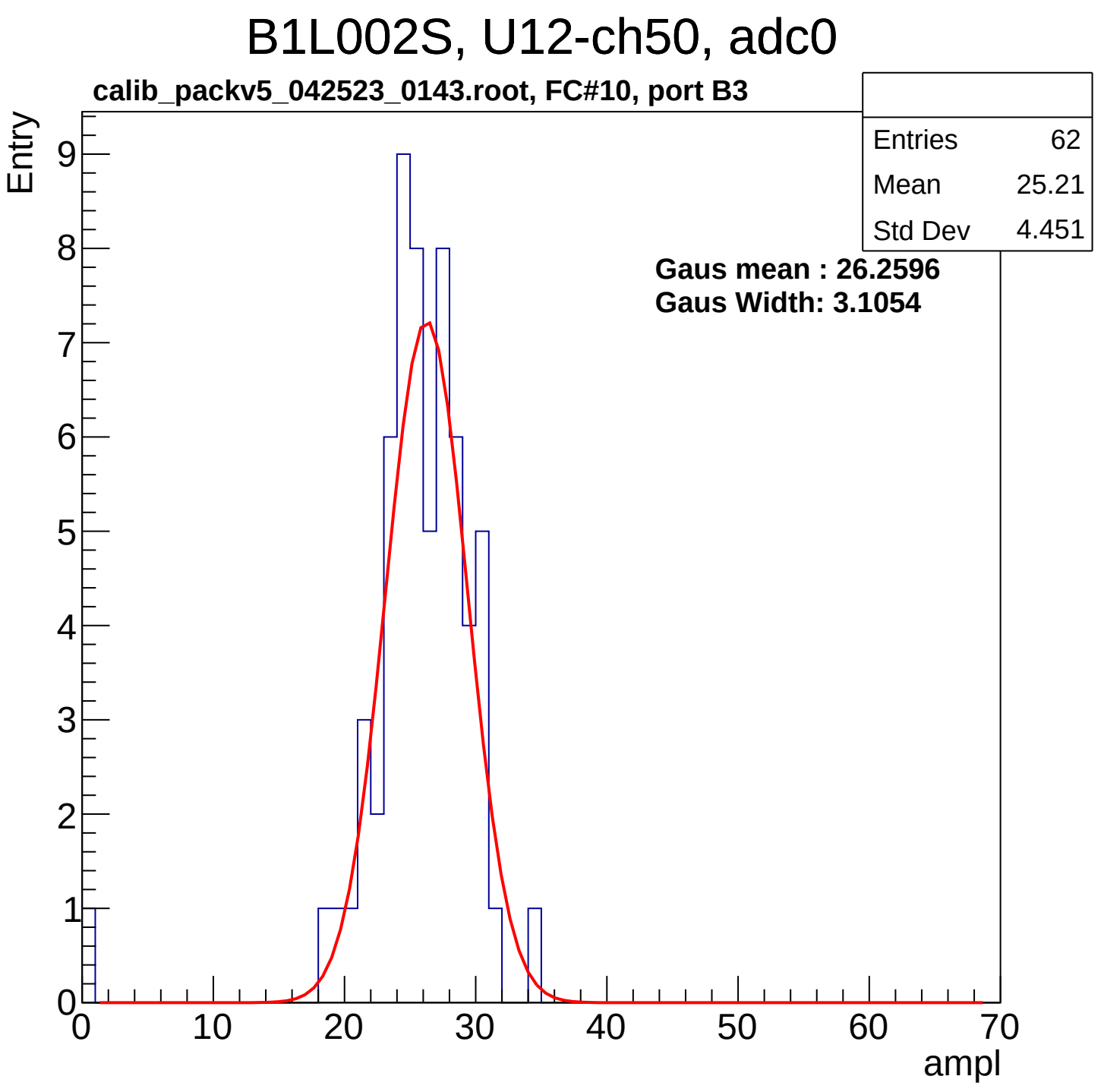
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 25.21
Std Dev 4.451
Gaus mean : 26.2596
Gaus Width: 3.1054
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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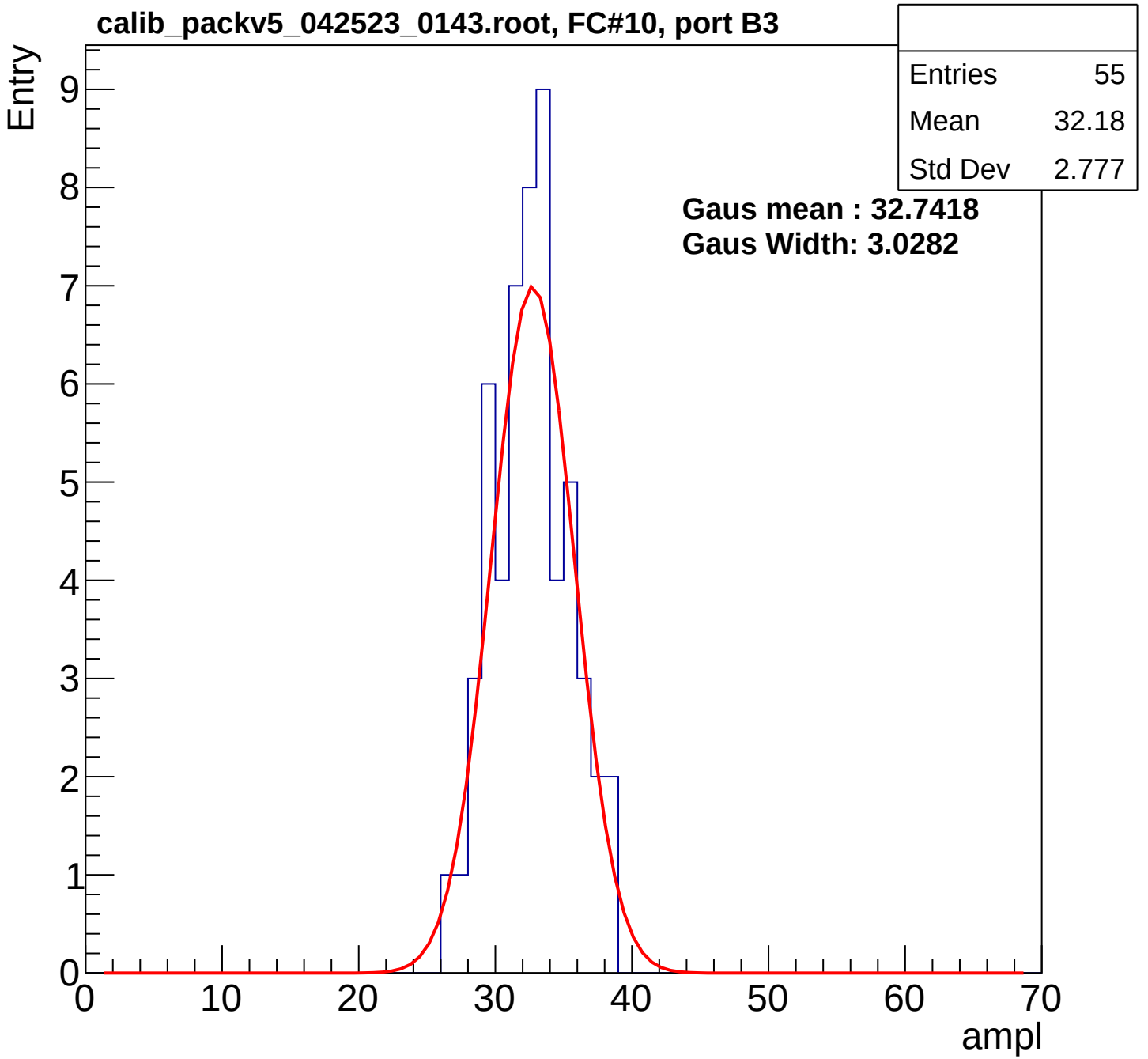
B1L002S, U12-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 32.18
Std Dev 2.777
Gaus mean : 32.7418
Gaus Width: 3.0282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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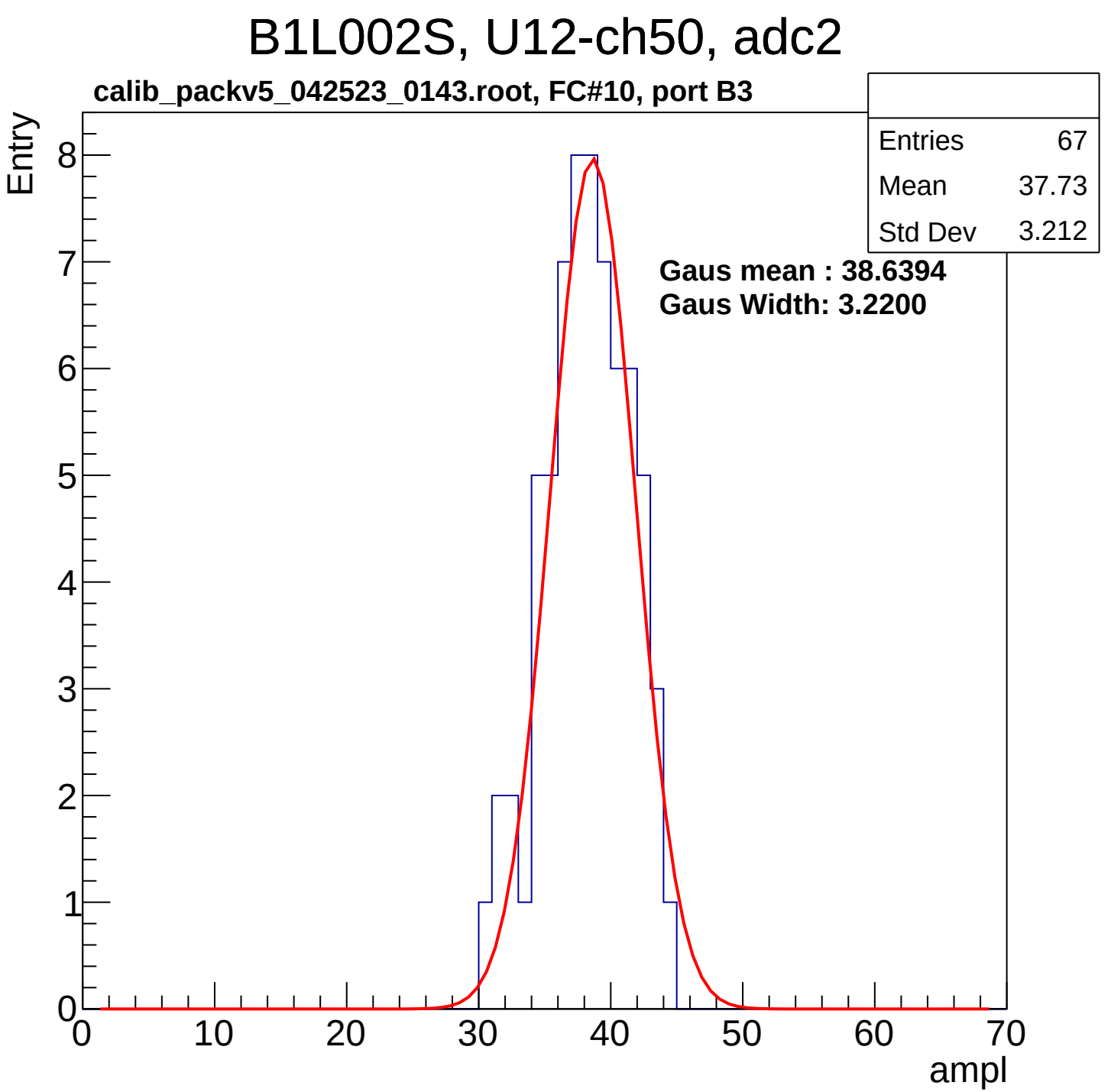
B1L002S, U12-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 37.73
Std Dev 3.212
Gaus mean : 38.6394
Gaus Width: 3.2200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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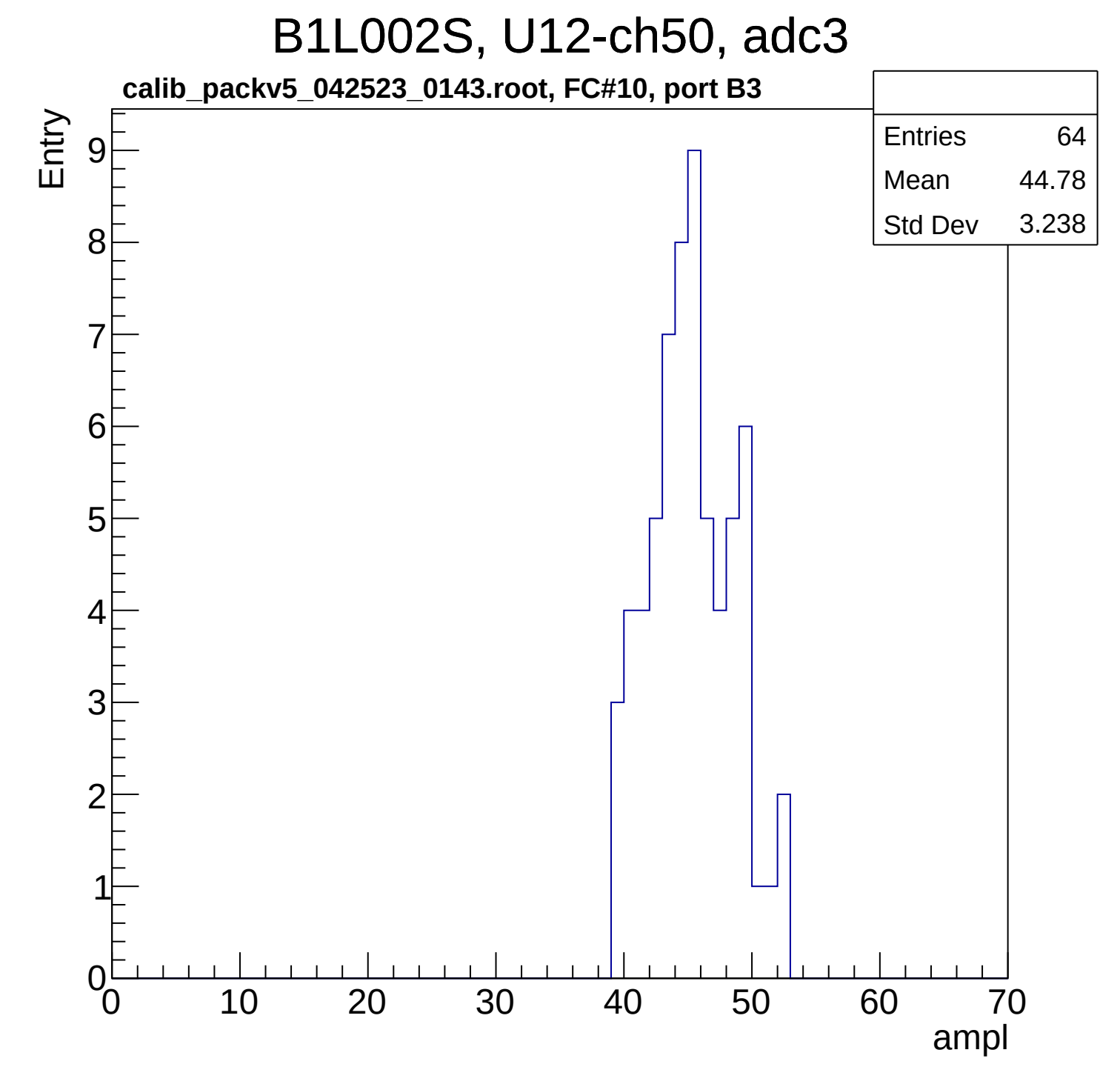
B1L002S, U12-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 44.78
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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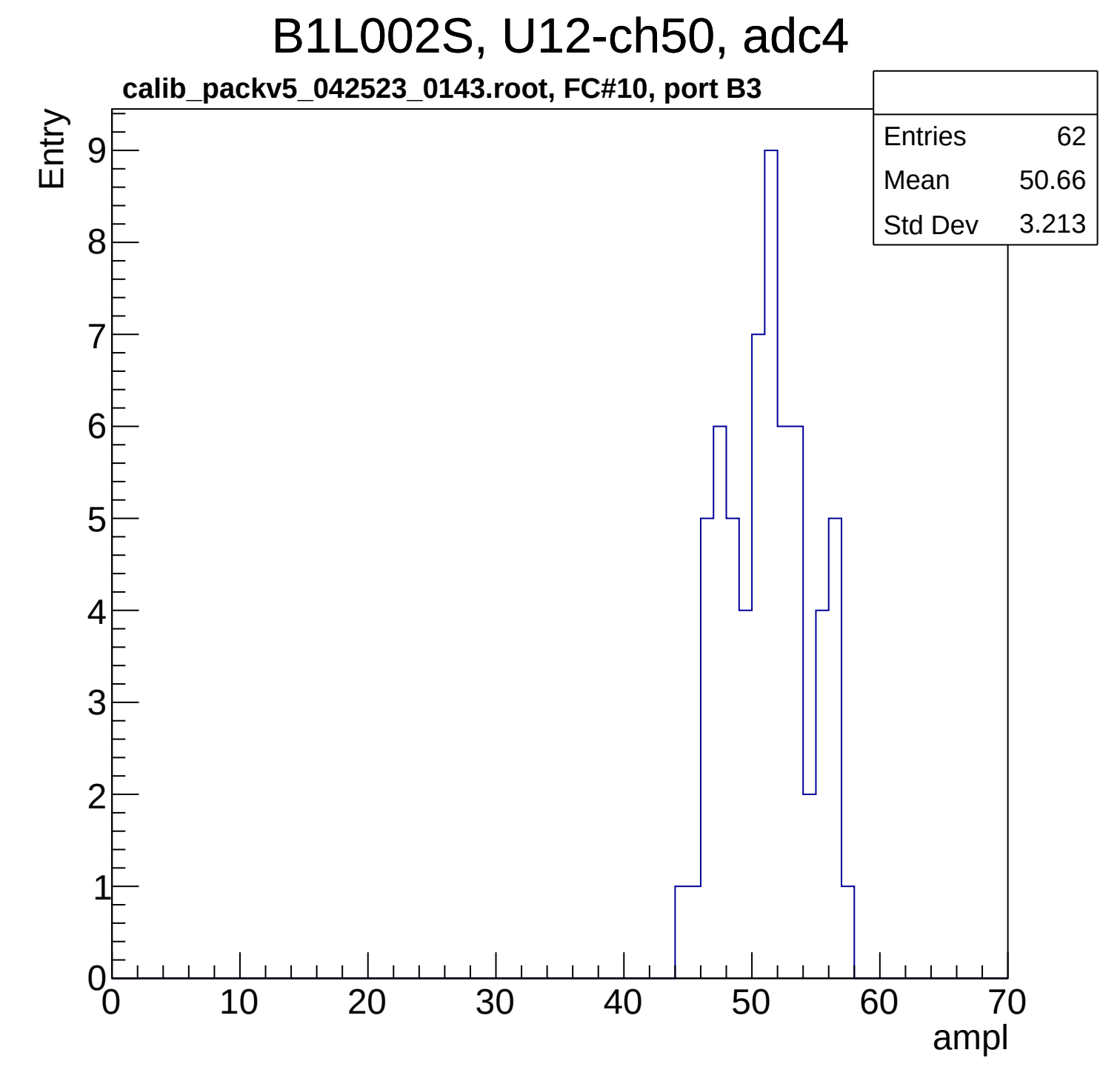
B1L002S, U12-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 50.66
Std Dev 3.213
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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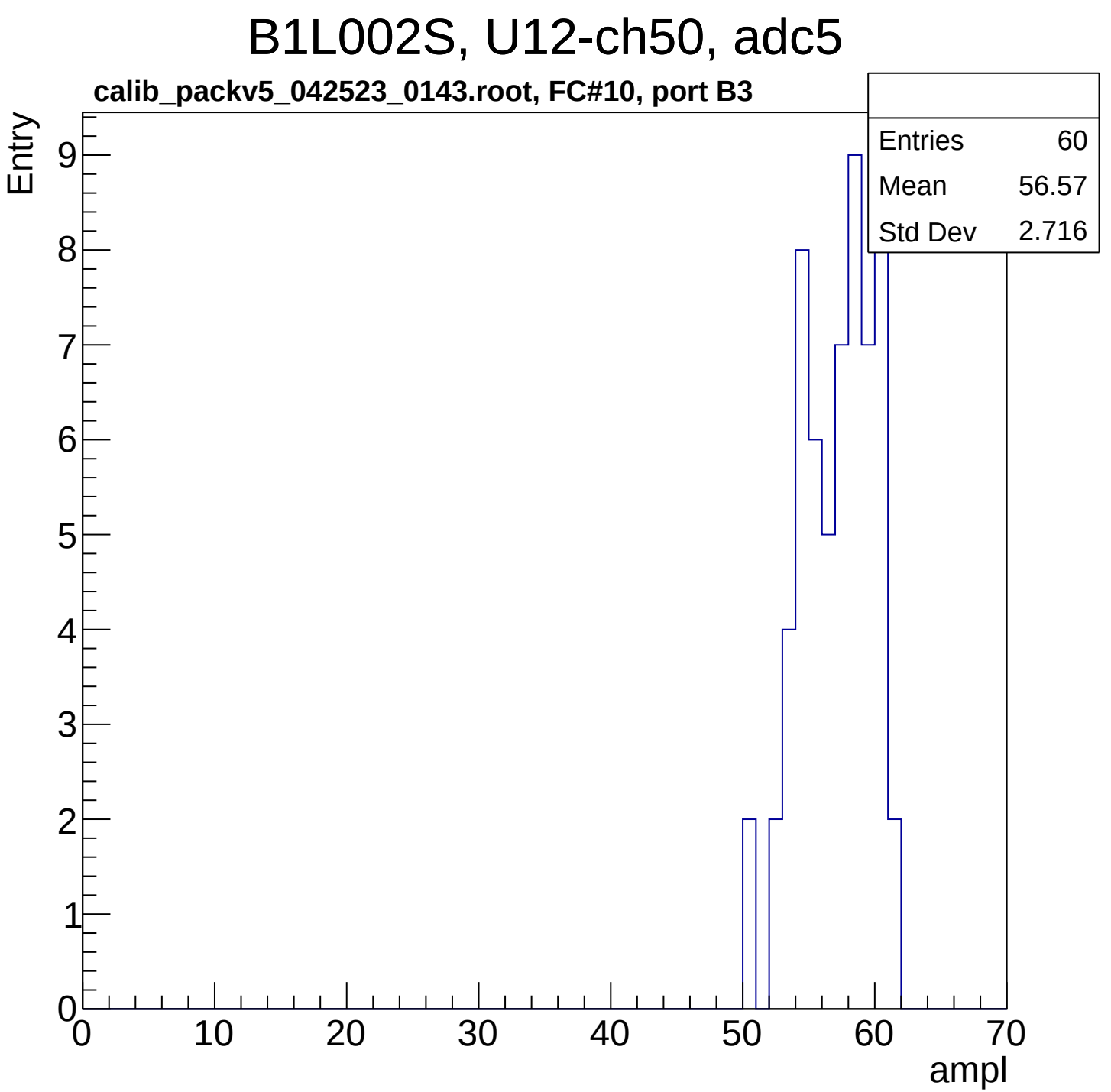
B1L002S, U12-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 56.57
Std Dev 2.716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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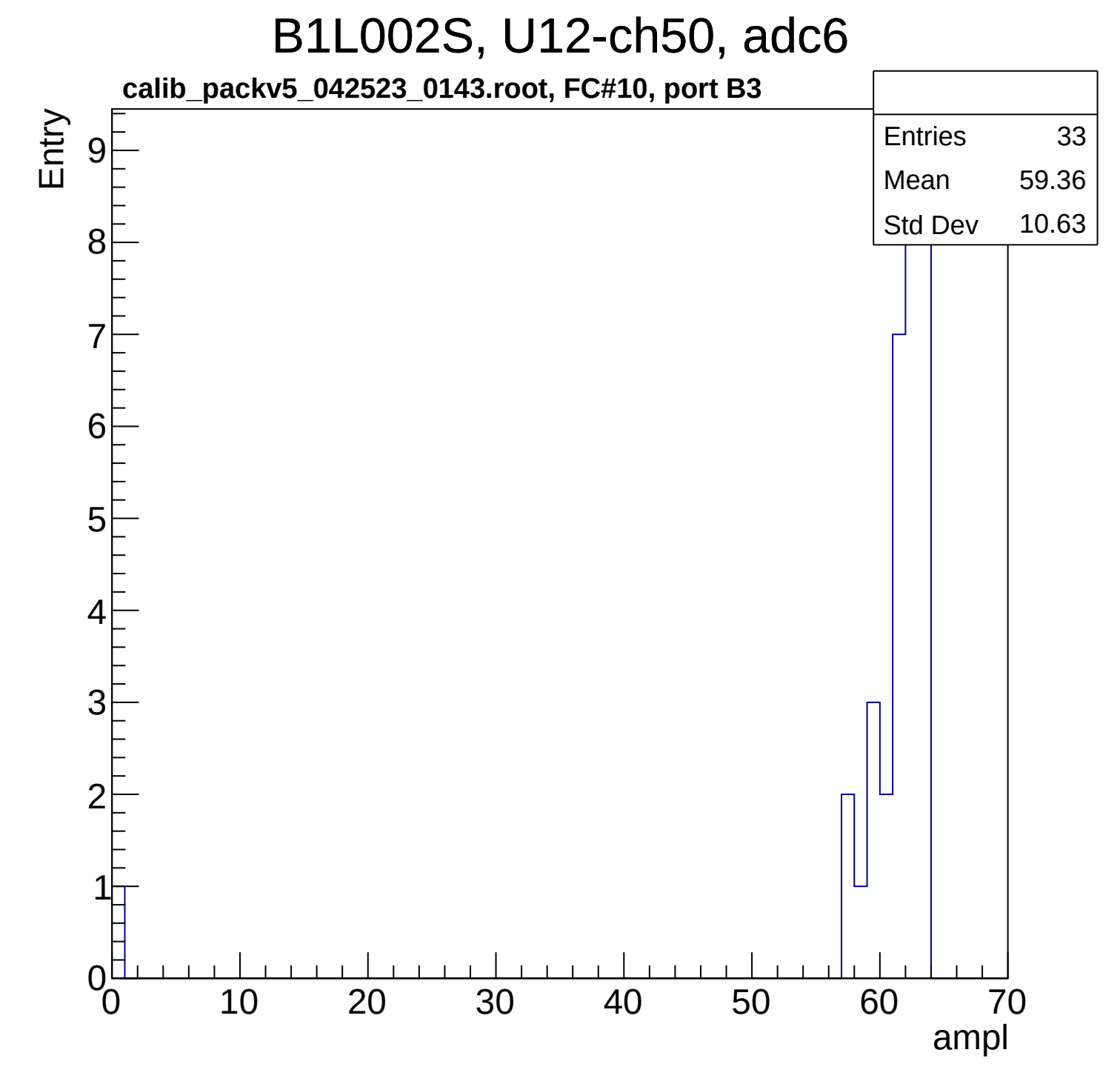
B1L002S, U12-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 59.36
Std Dev 10.63
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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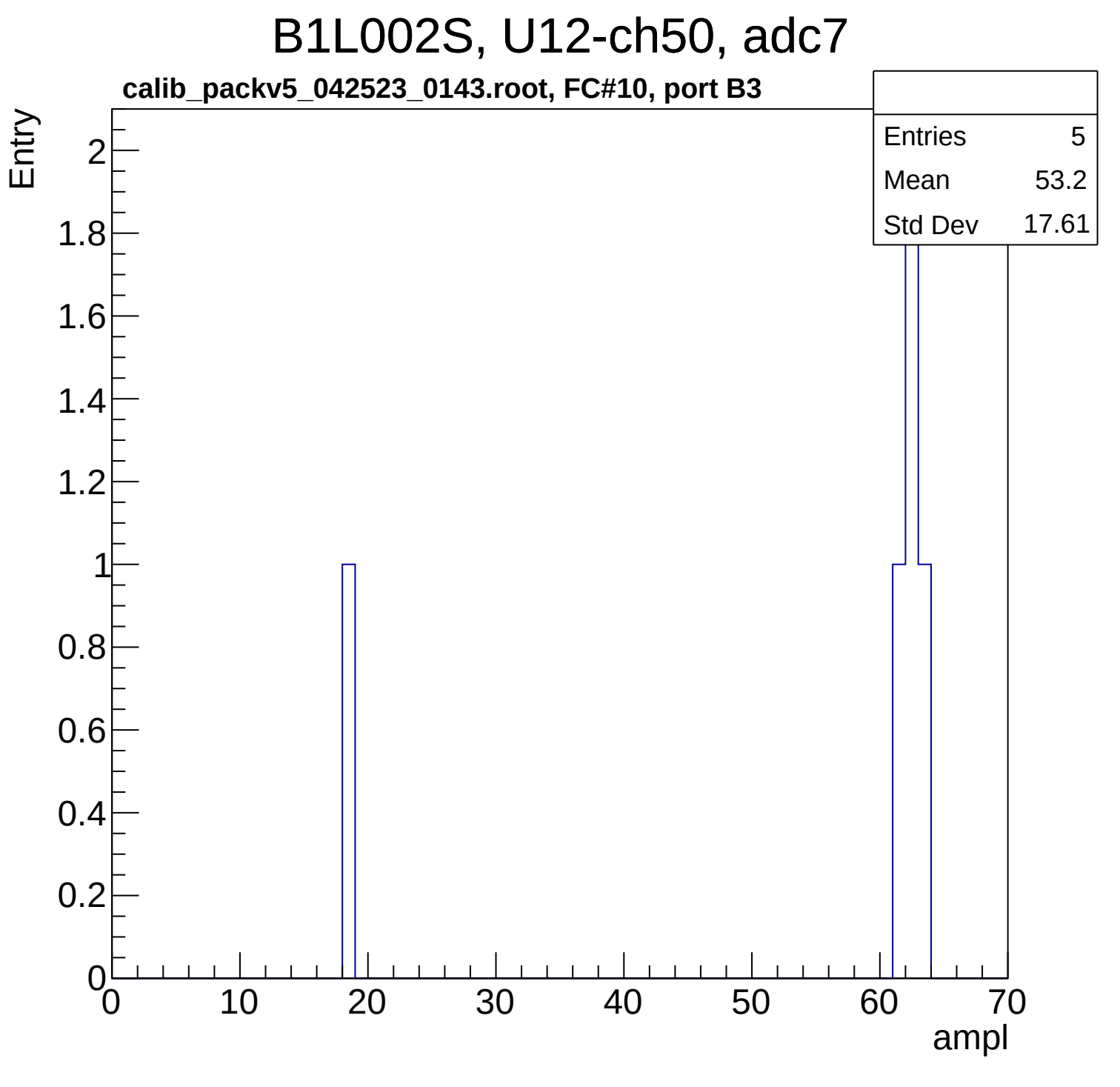
B1L002S, U12-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 53.2
Std Dev 17.61
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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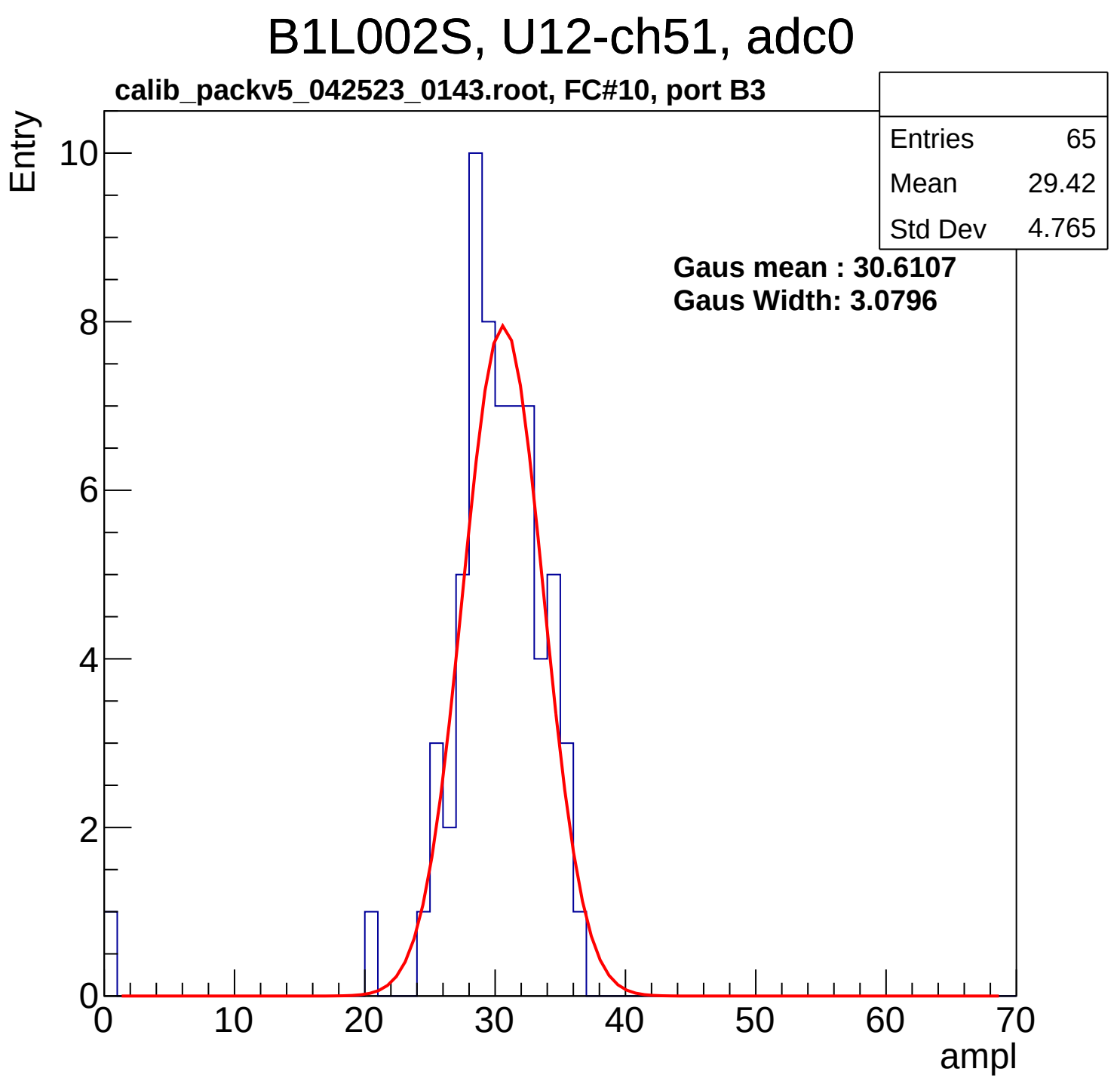
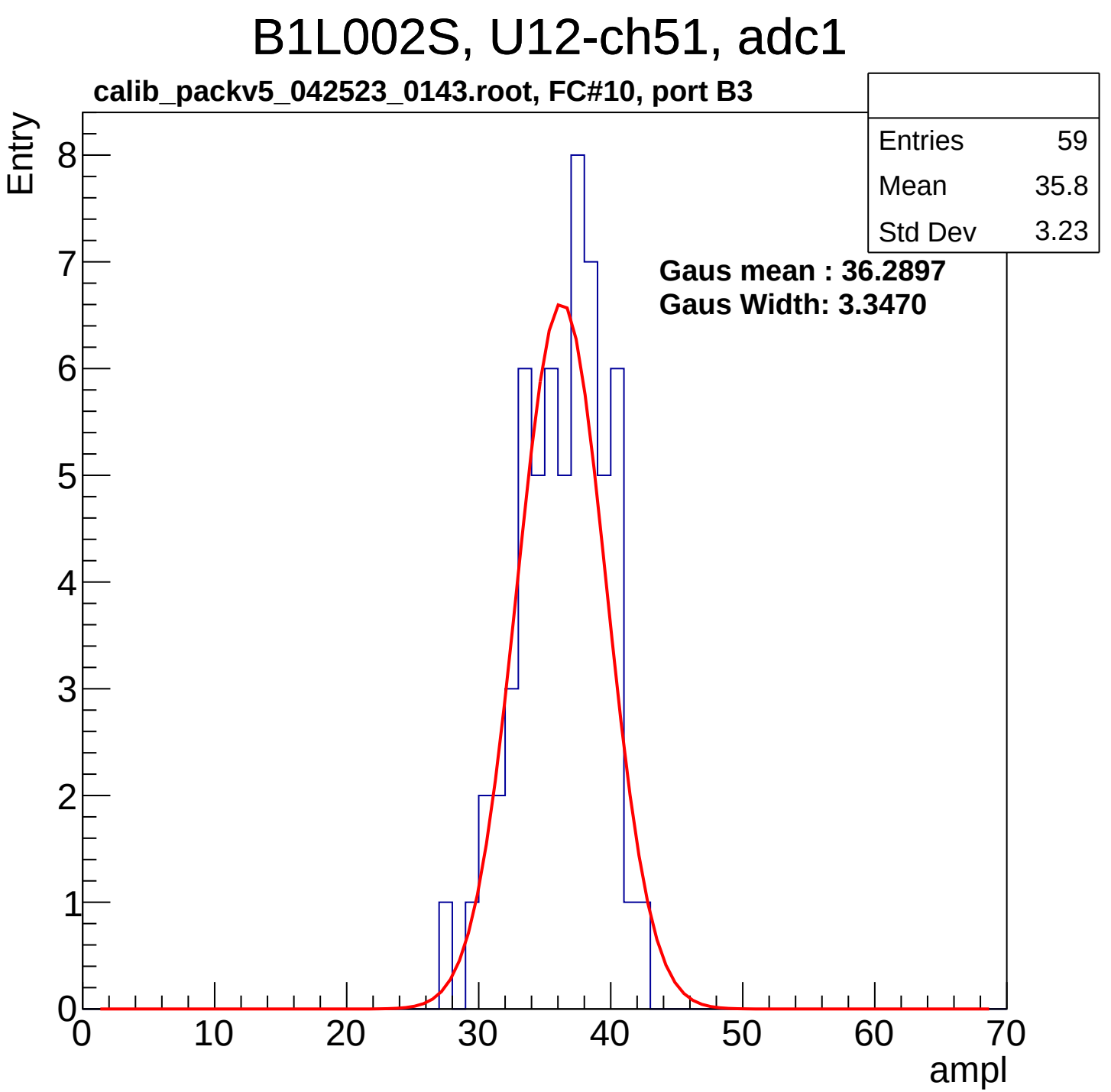

B1L002S, U12-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.42
Std Dev 4.765
Gaus mean : 30.6107
Gaus Width: 3.0796
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 35.8
Std Dev 3.23
Gaus mean : 36.2897
Gaus Width: 3.3470
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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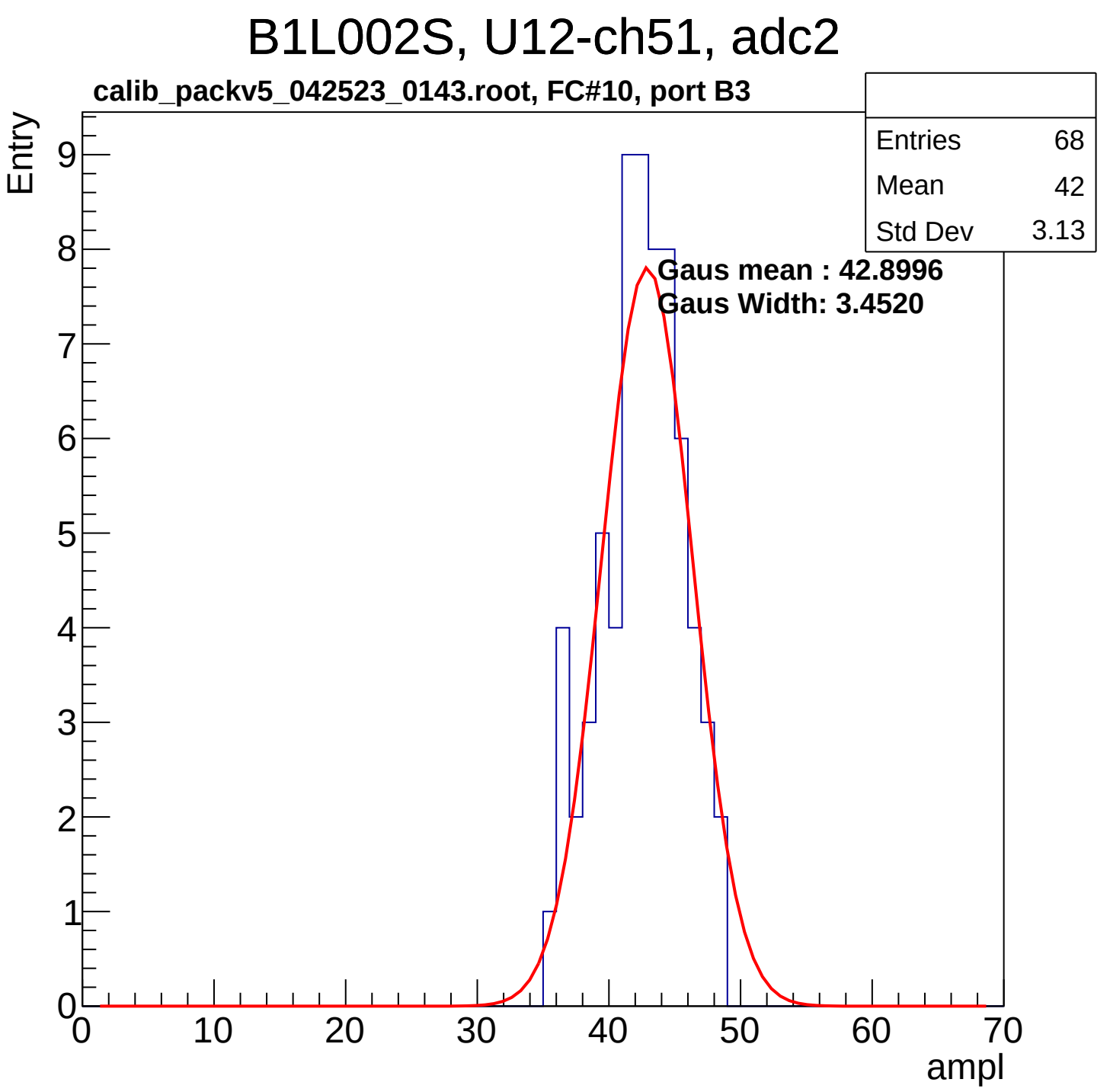
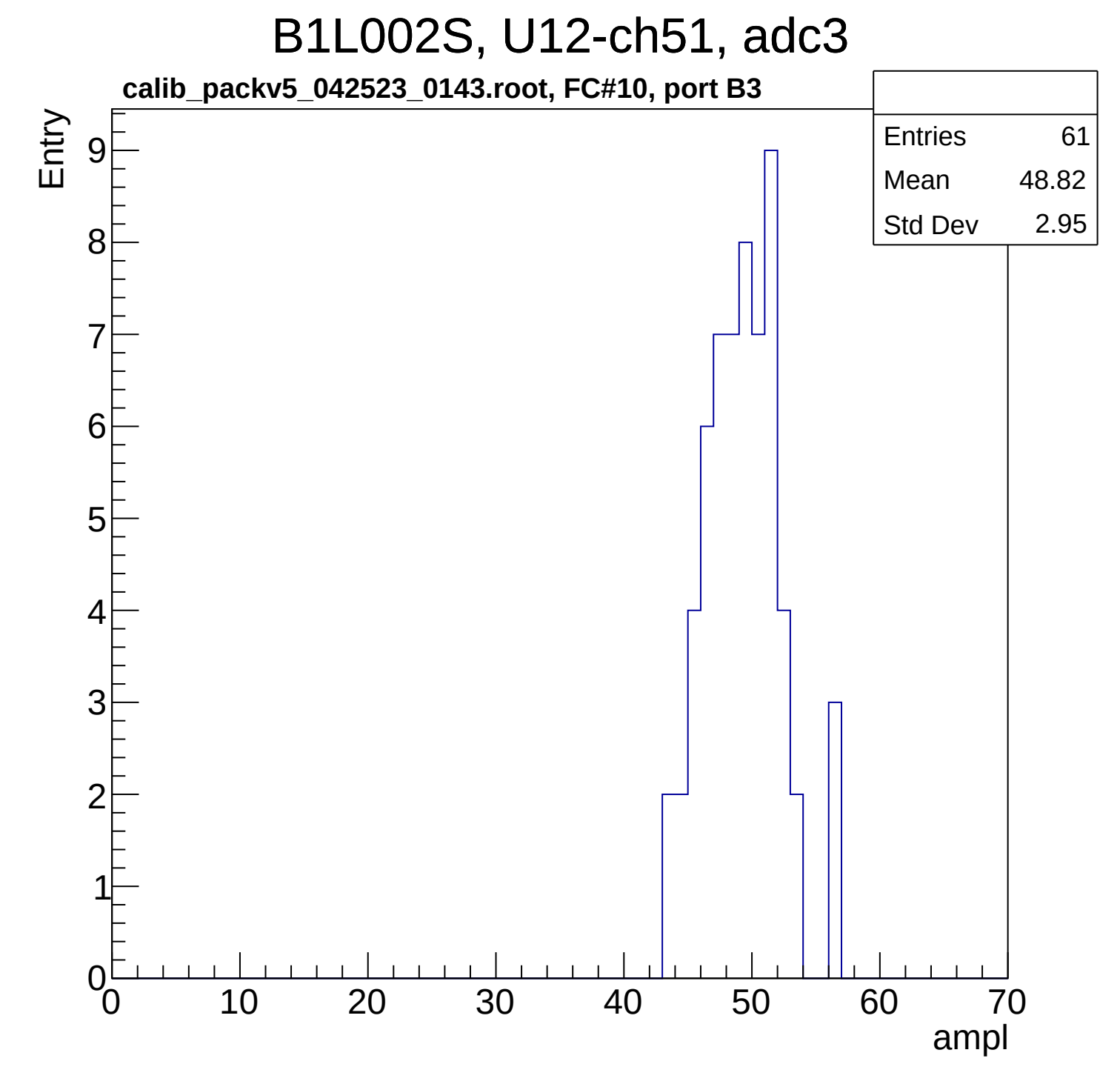
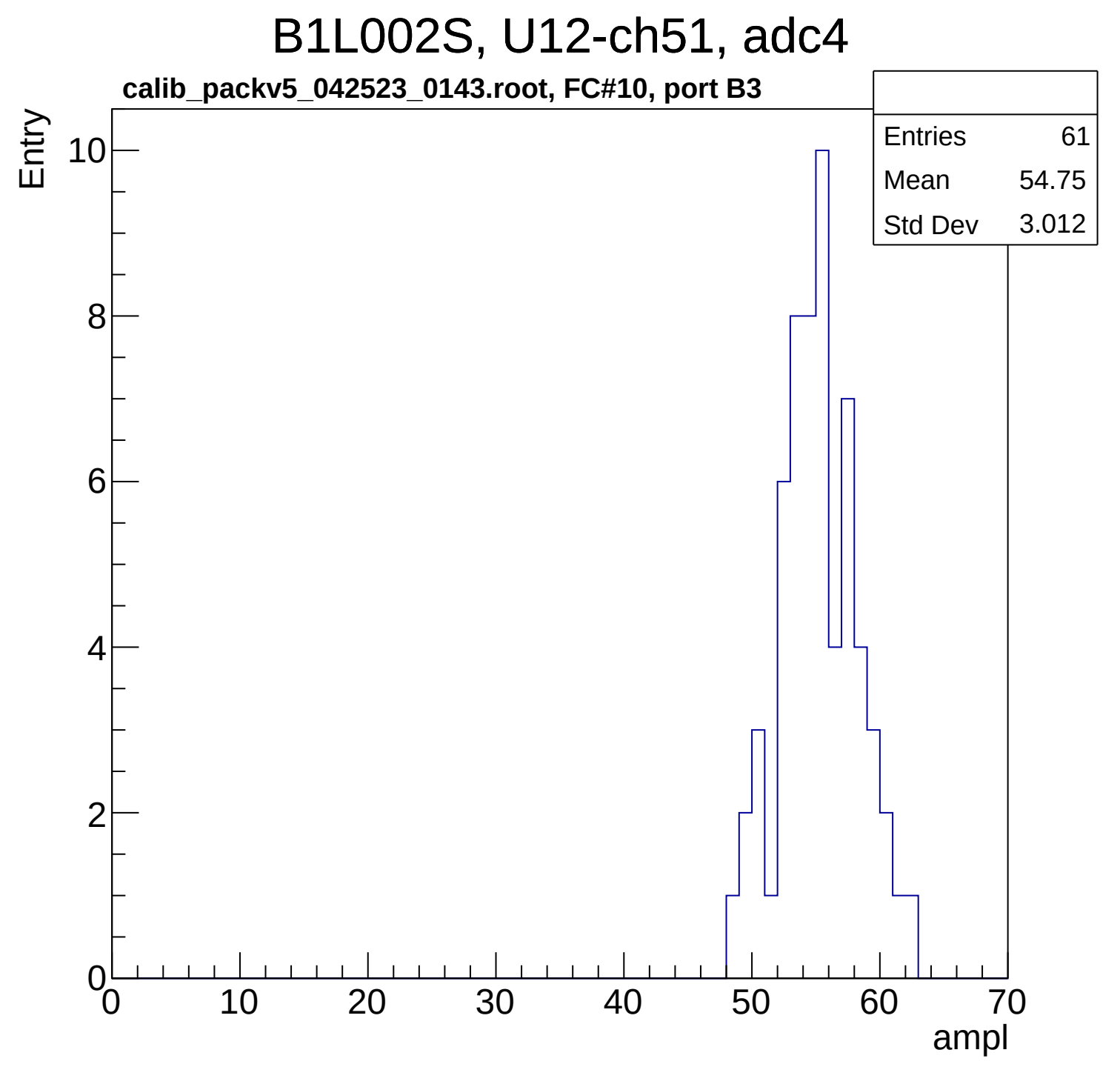
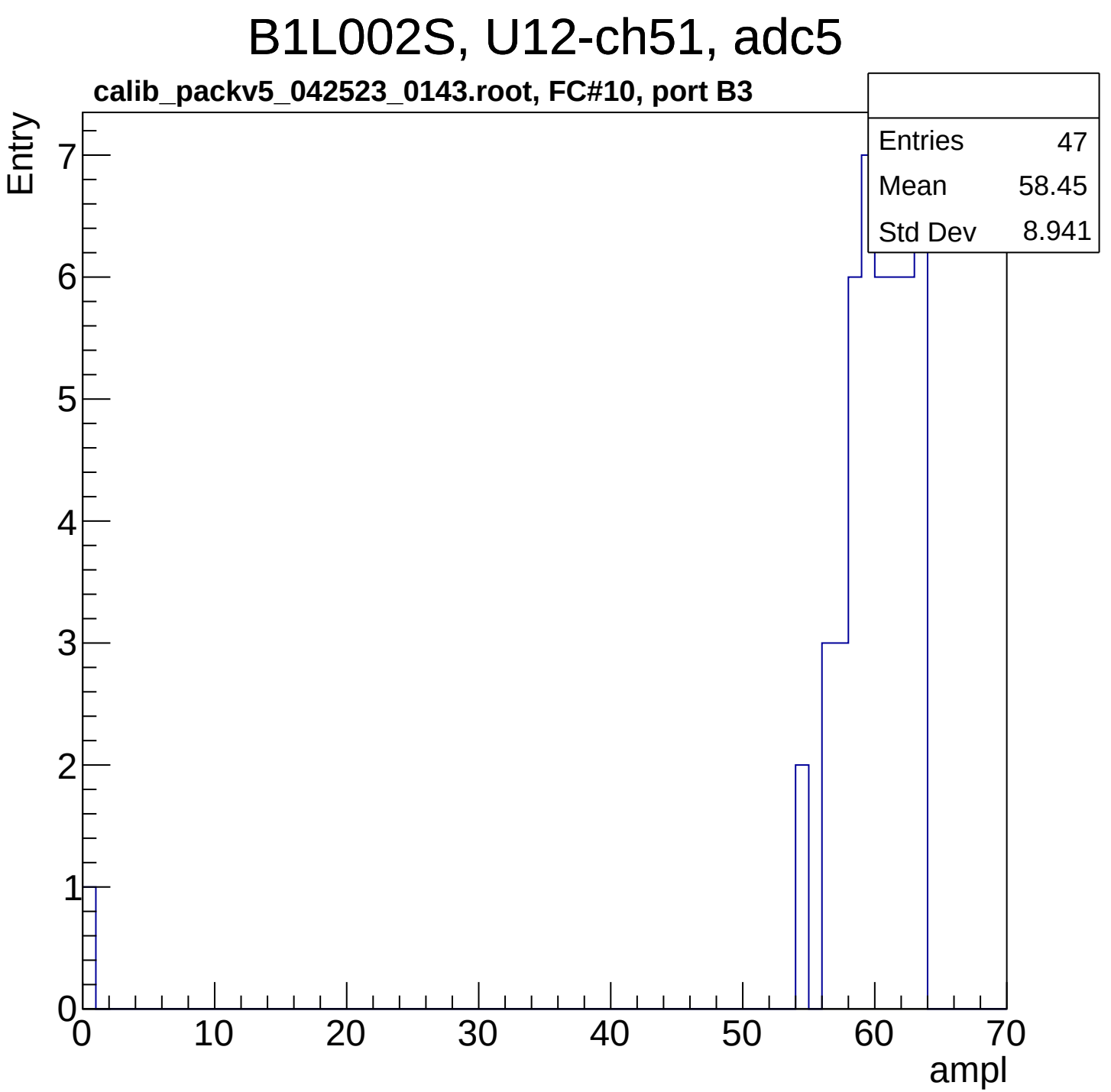
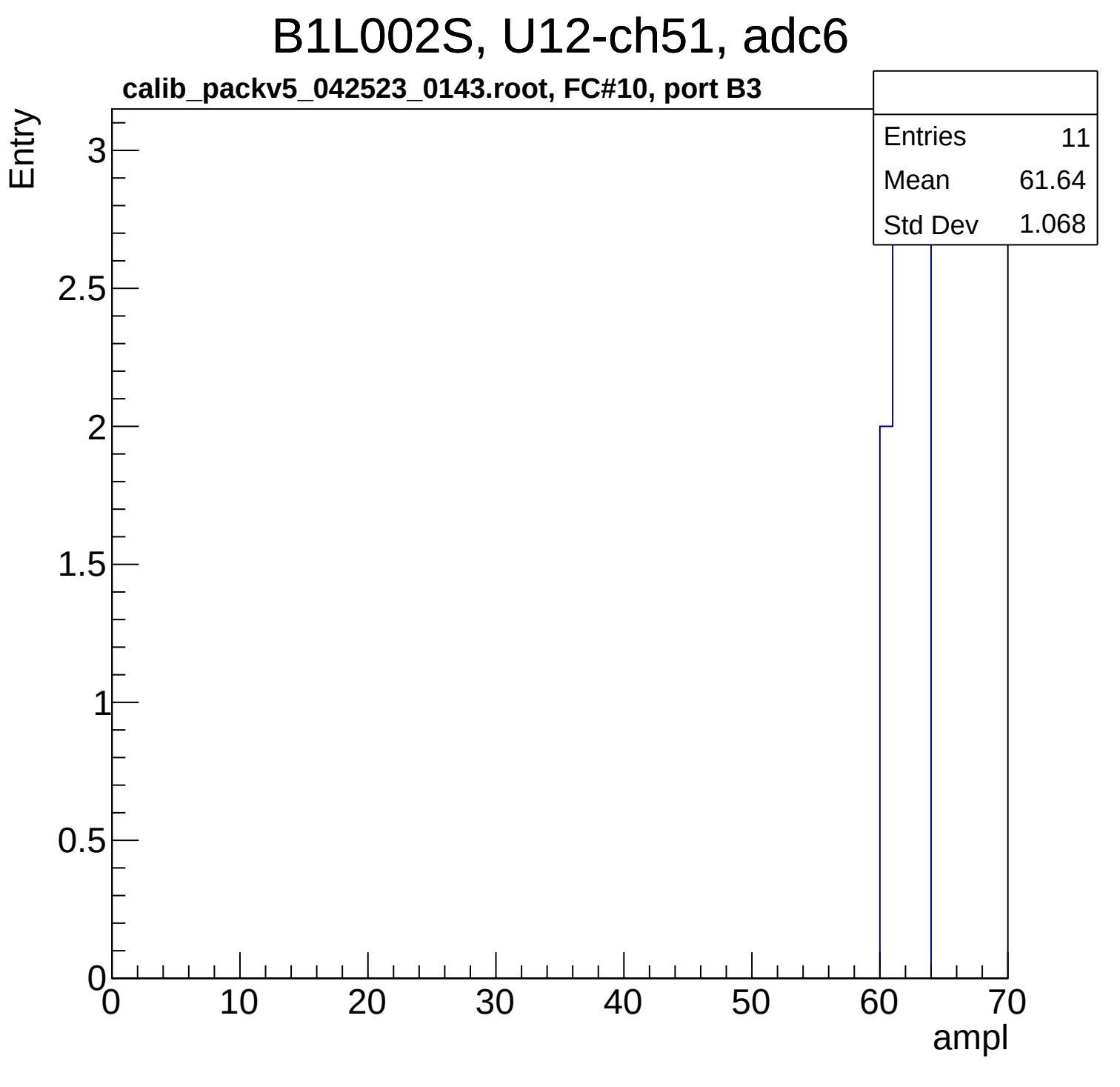
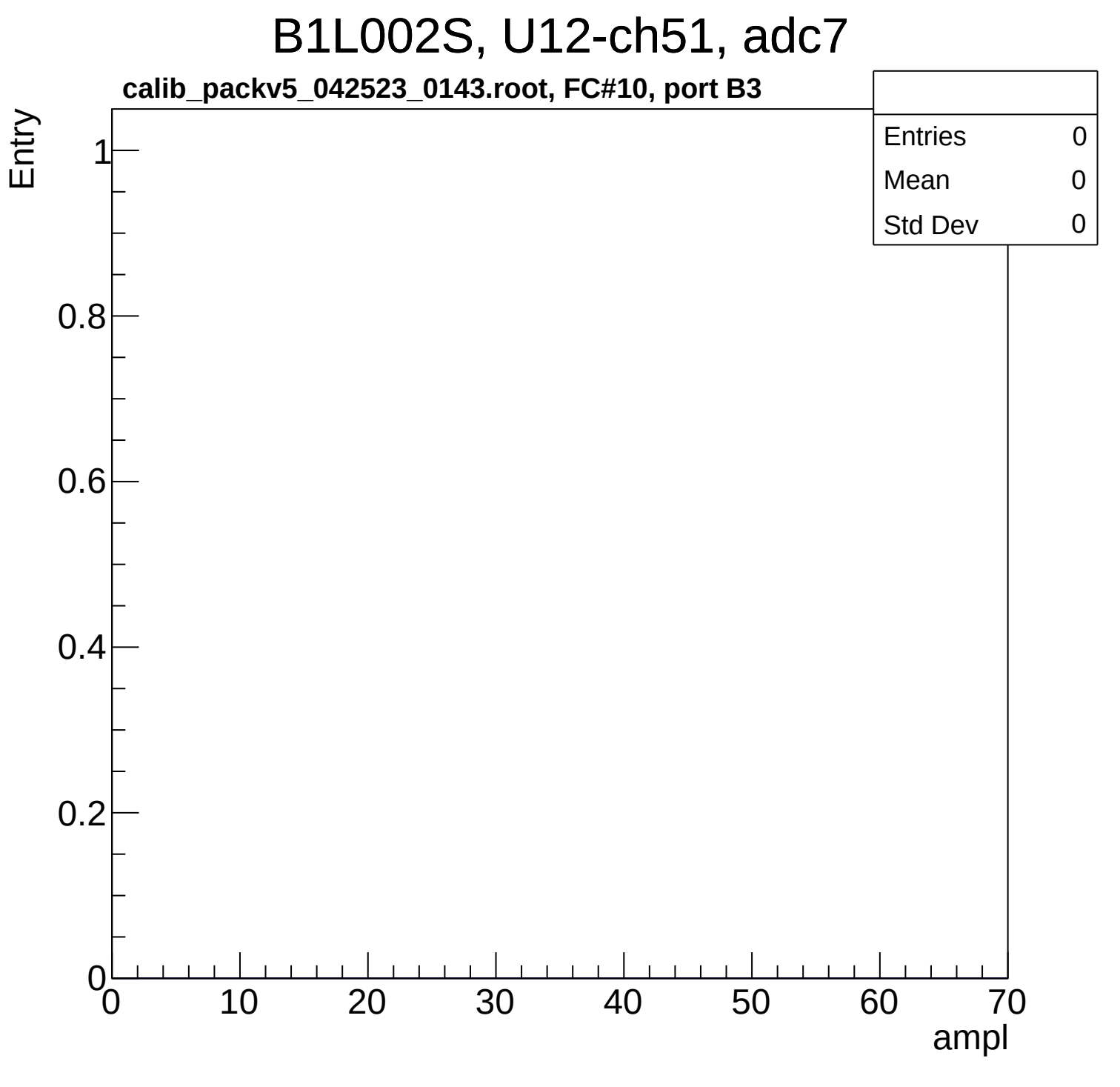

B1L002S, U12-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 42
Std Dev 3.13
Gaus mean : 42.8996
Gaus Width: 3.4520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

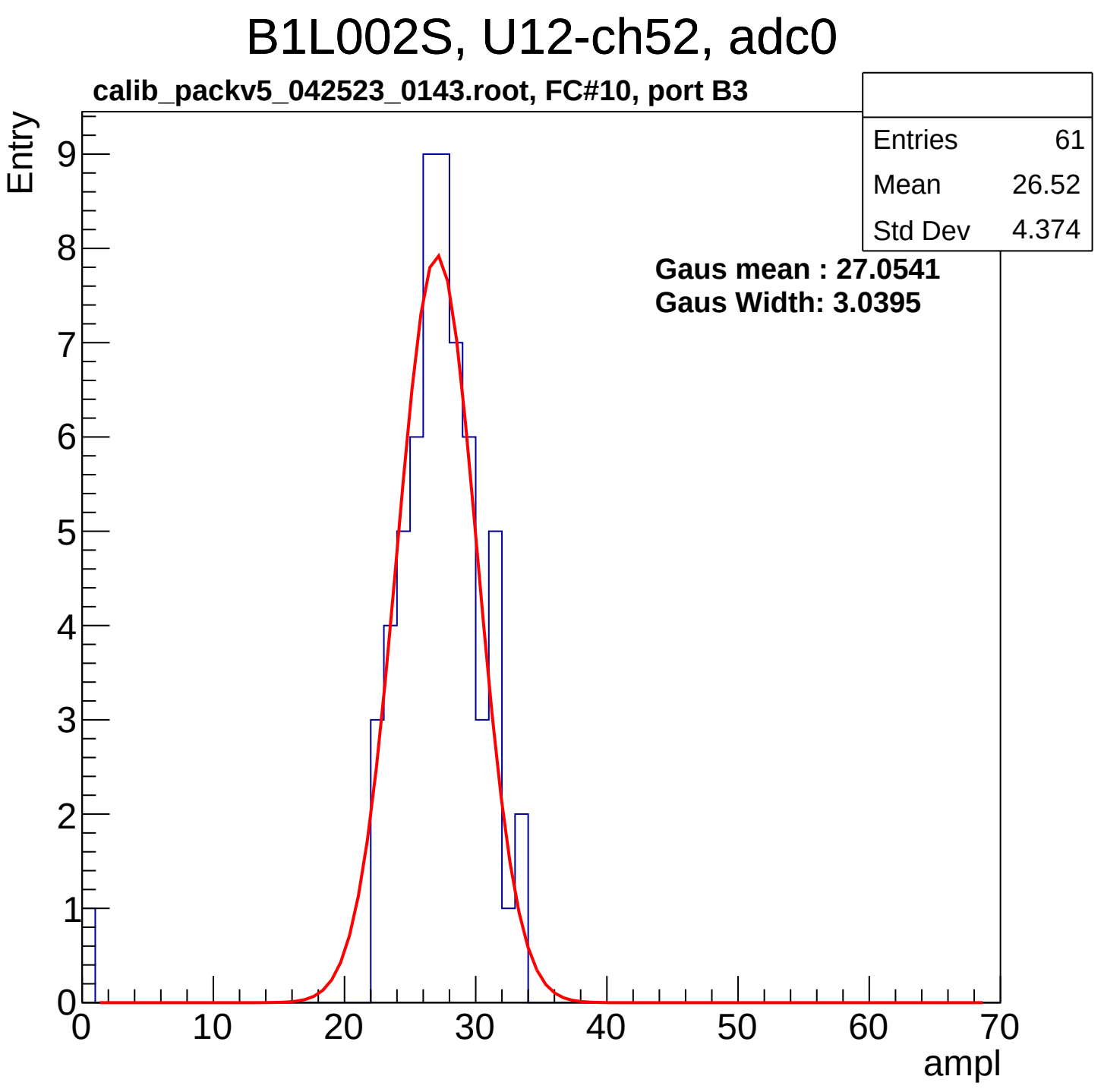

B1L002S, U12-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 48.82
Std Dev 2.95
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

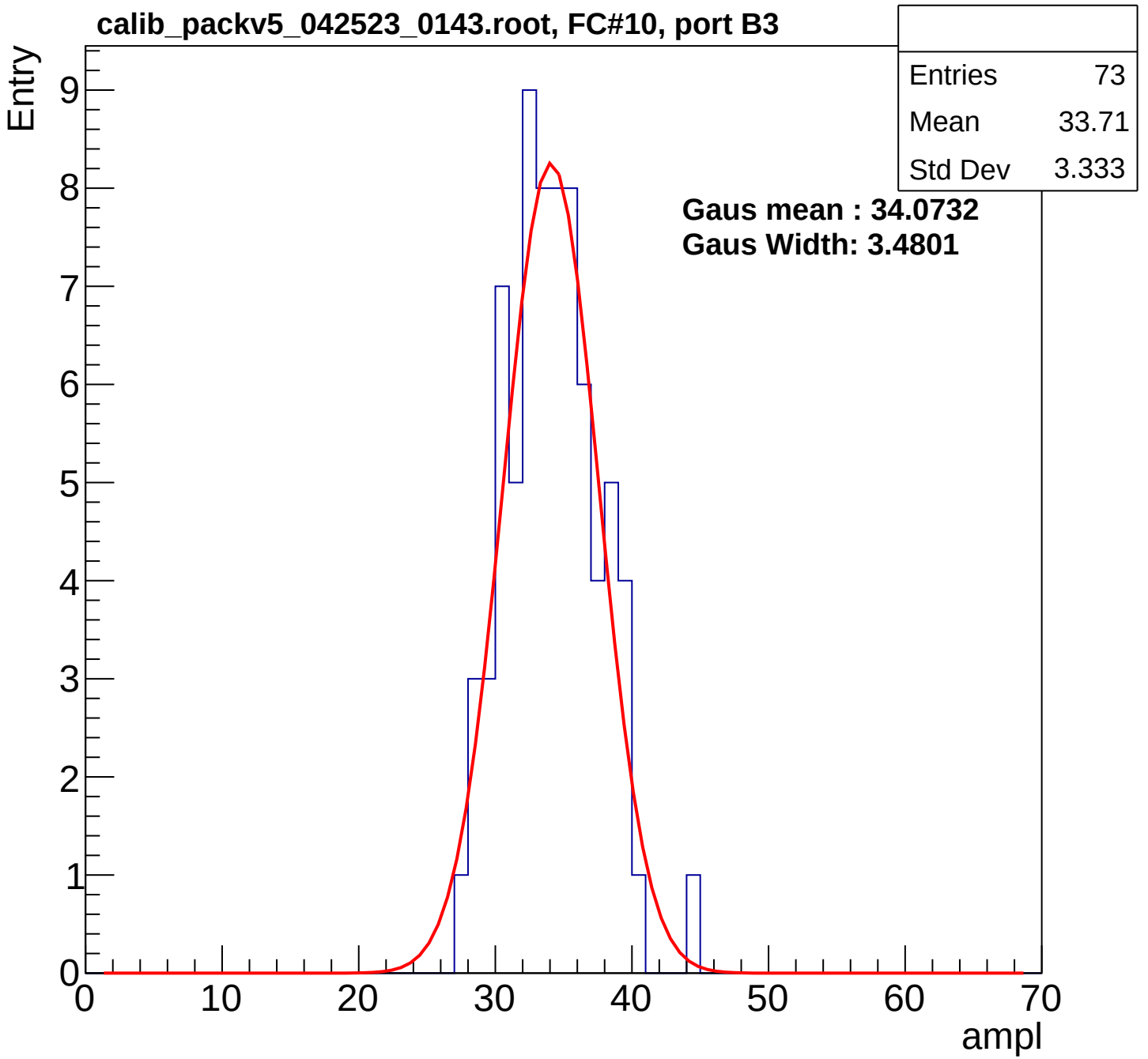

B1L002S, U12-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.75
Std Dev 3.012
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

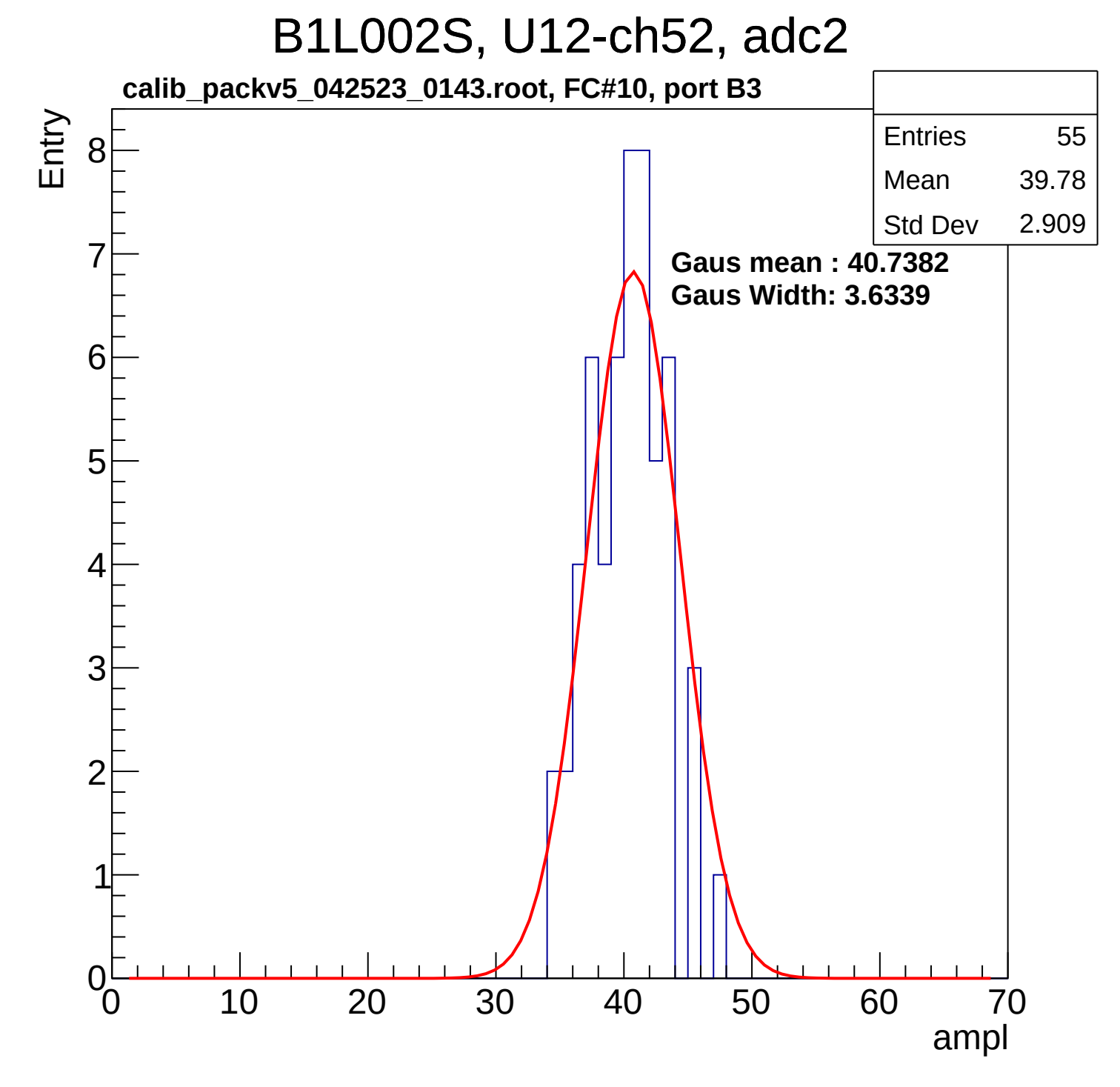

B1L002S, U12-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.45
Std Dev 8.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

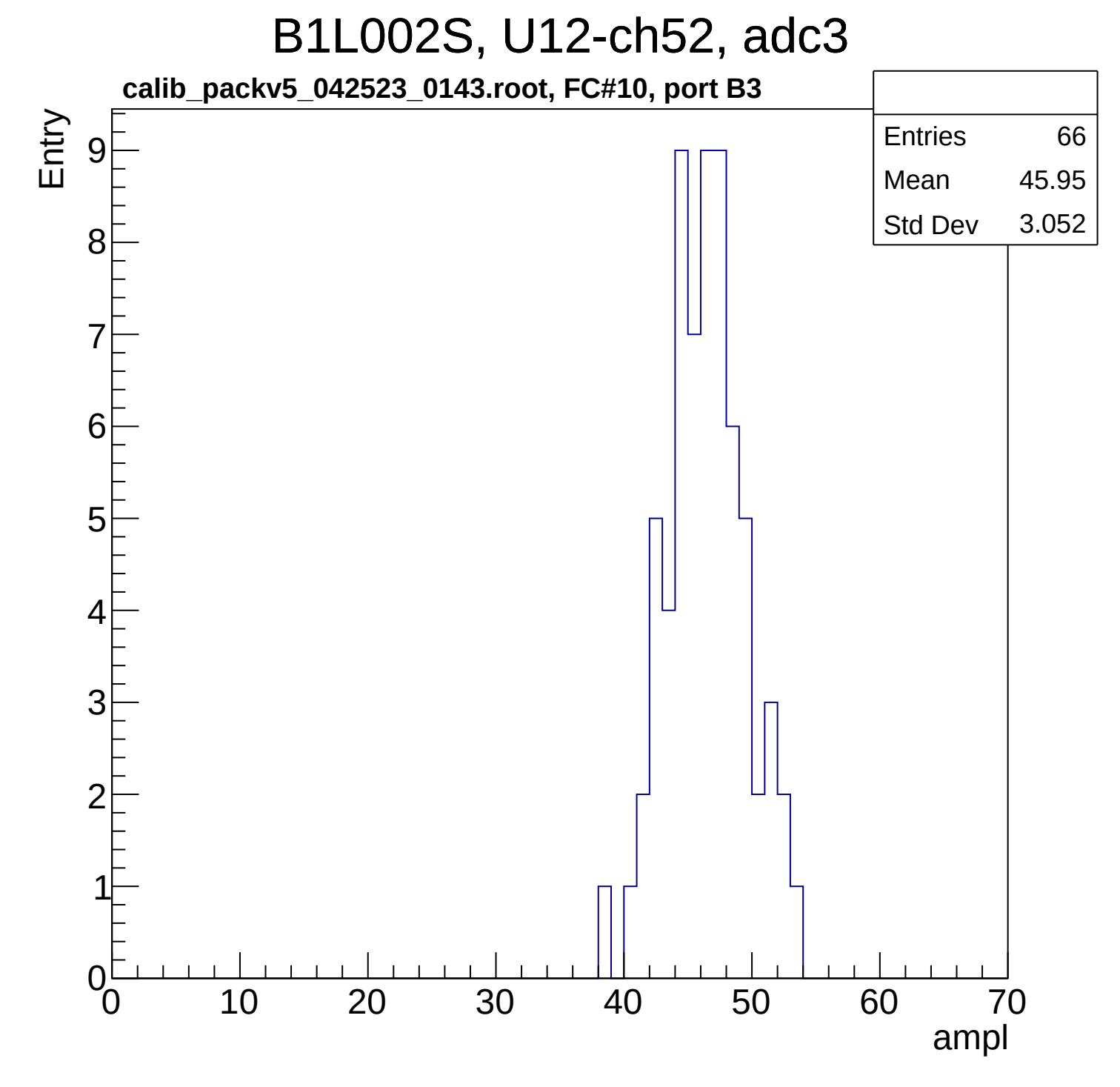

B1L002S, U12-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.64
Std Dev 1.068
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.52
Std Dev 4.374
Gaus mean : 27.0541
Gaus Width: 3.0395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 33.71
Std Dev 3.333
Gaus mean : 34.0732
Gaus Width: 3.4801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 39.78
Std Dev 2.909
Gaus mean : 40.7382
Gaus Width: 3.6339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.95
Std Dev 3.052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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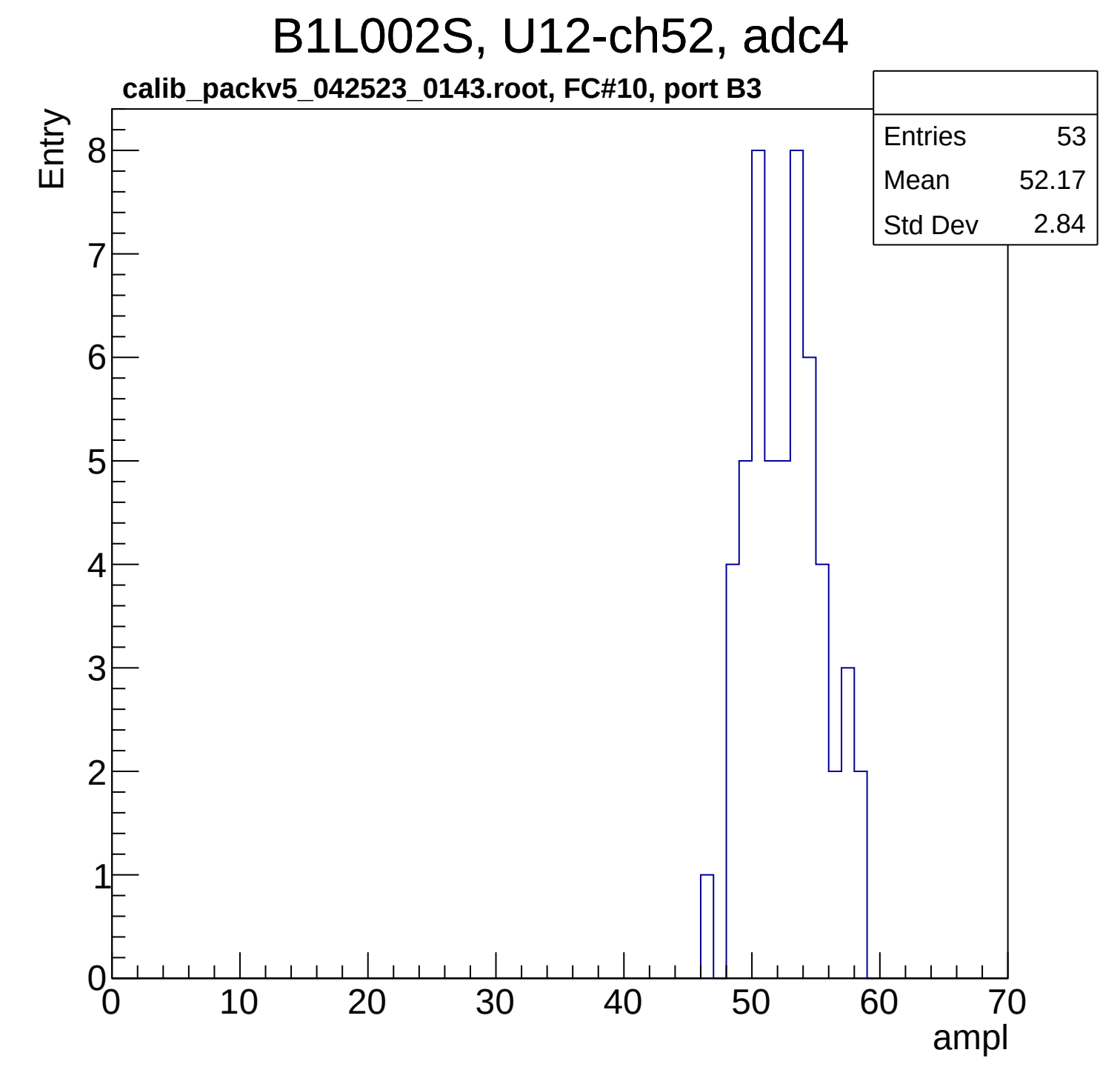
40
50
60
70


B1L002S, U12-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.17
Std Dev 2.84
Entry
ampl
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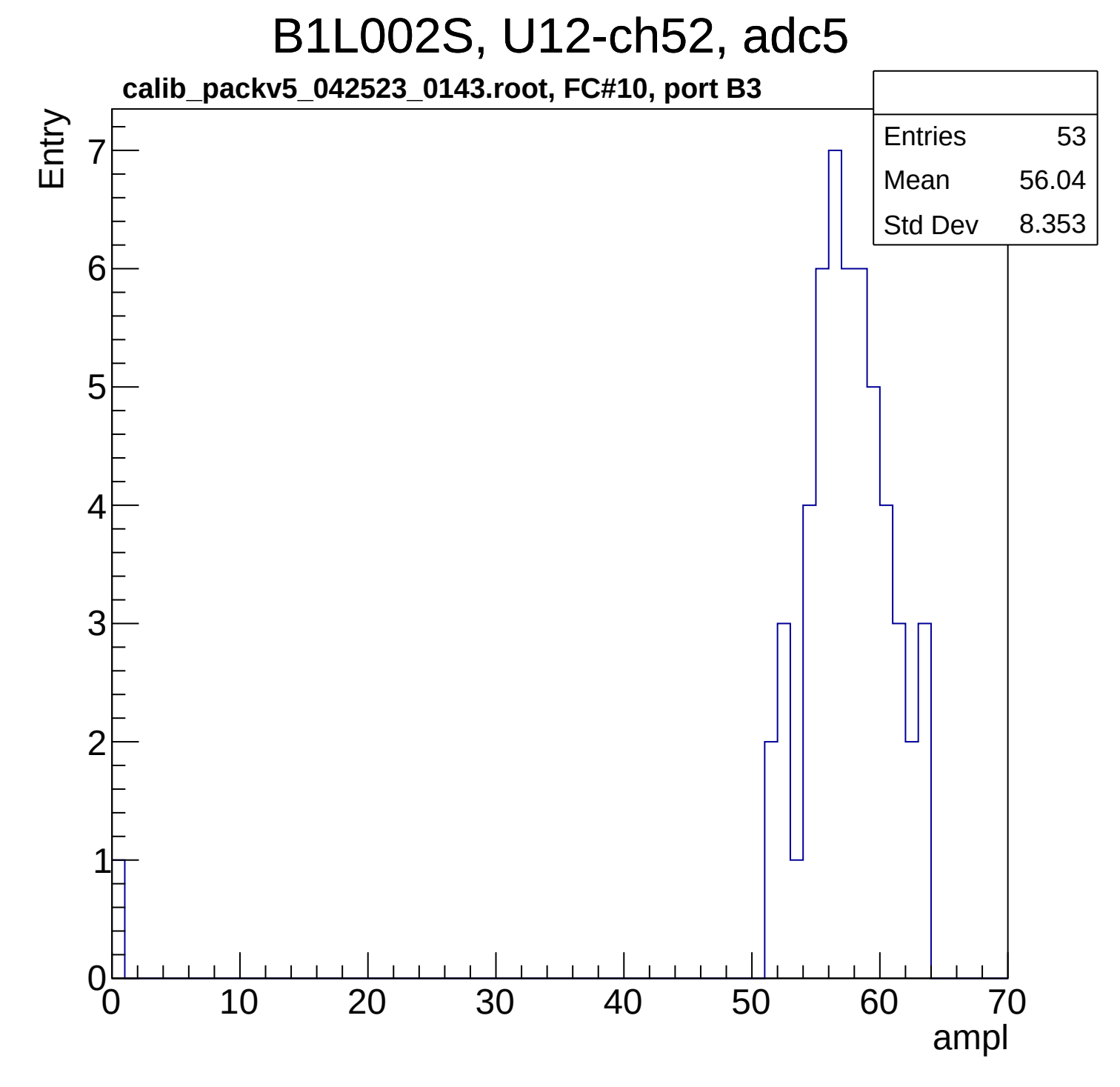
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.04
Std Dev 8.353
Entry
ampl
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2
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6
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0
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60
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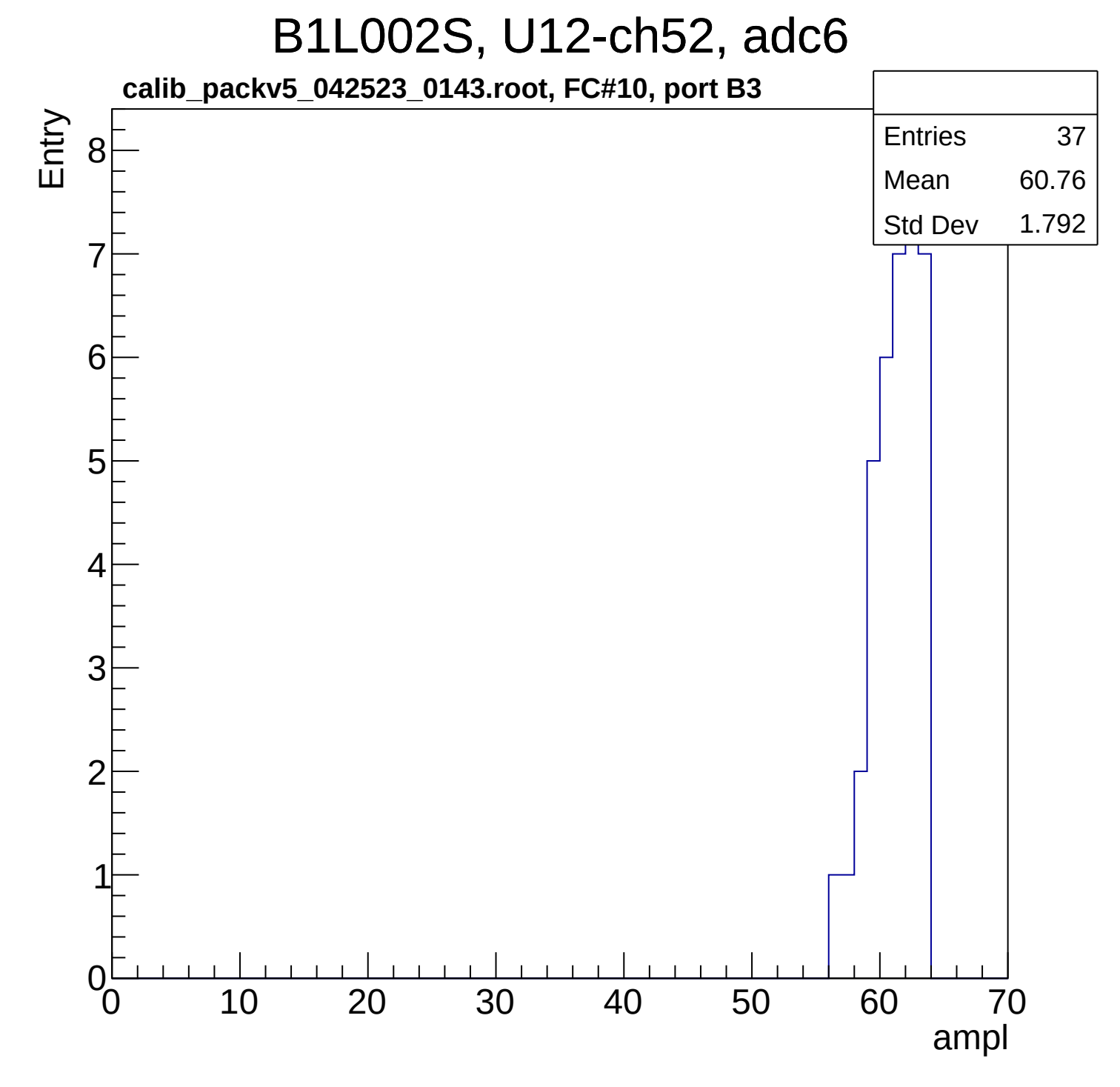
B1L002S, U12-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 60.76
Std Dev 1.792
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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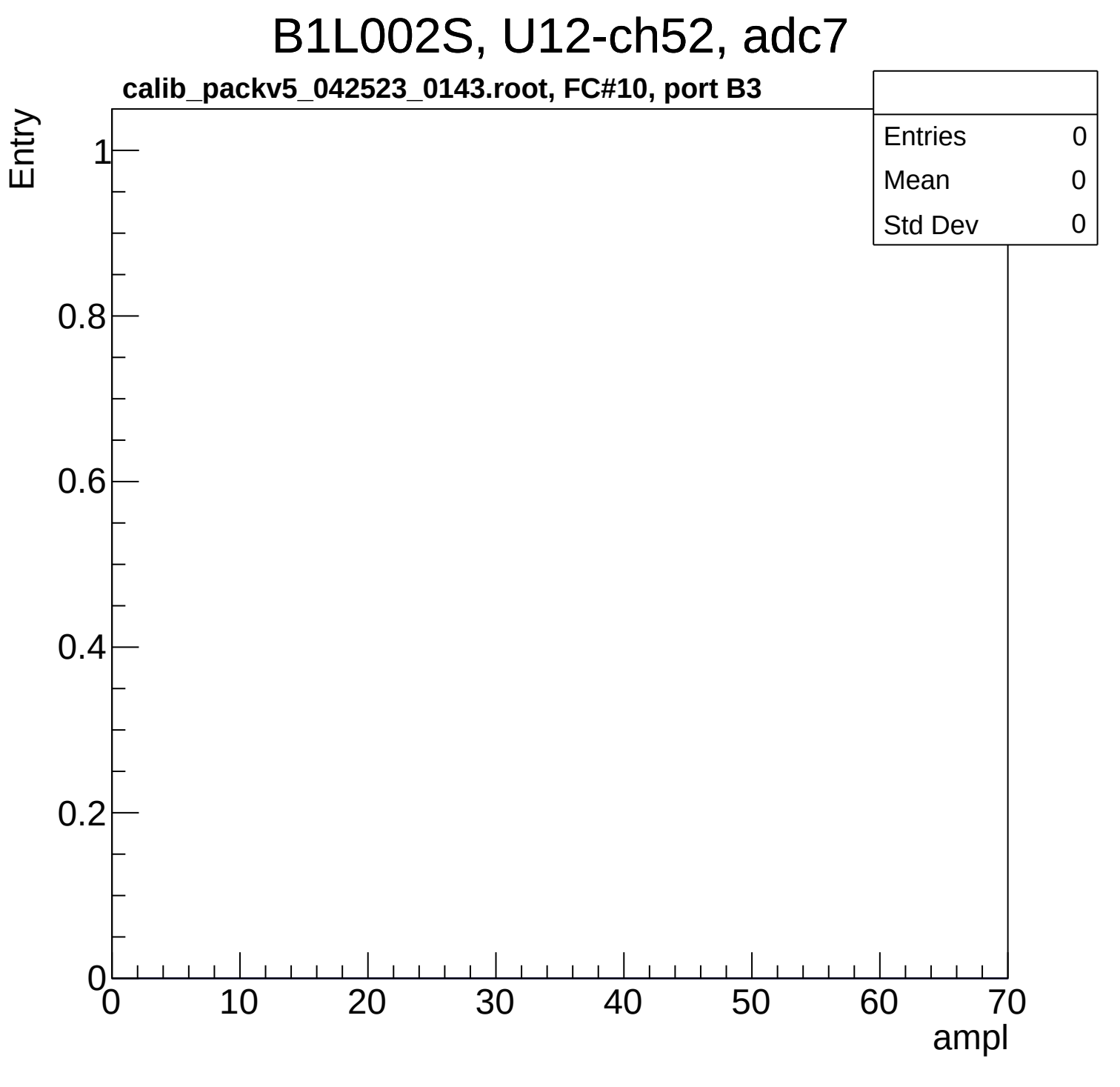
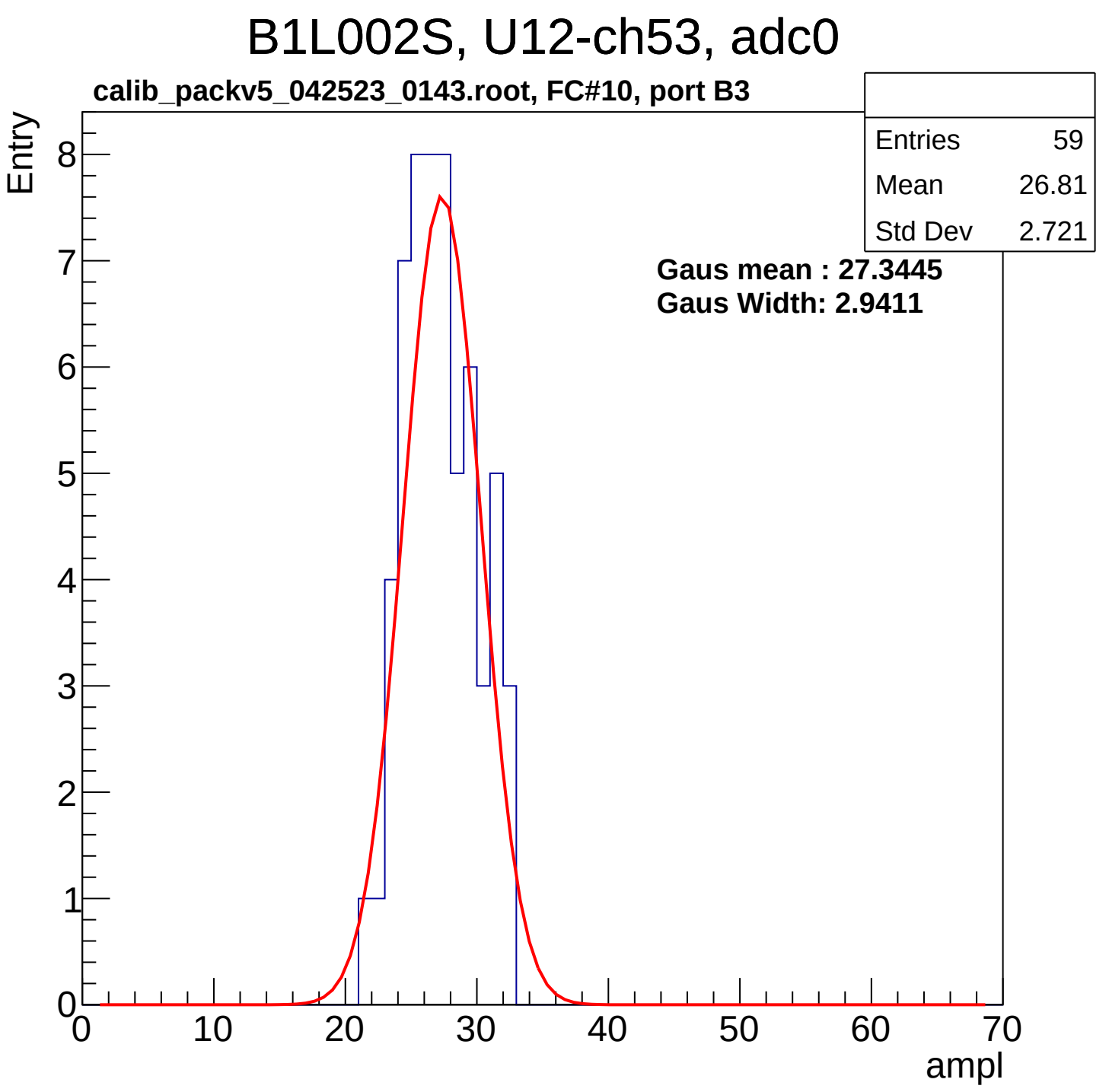
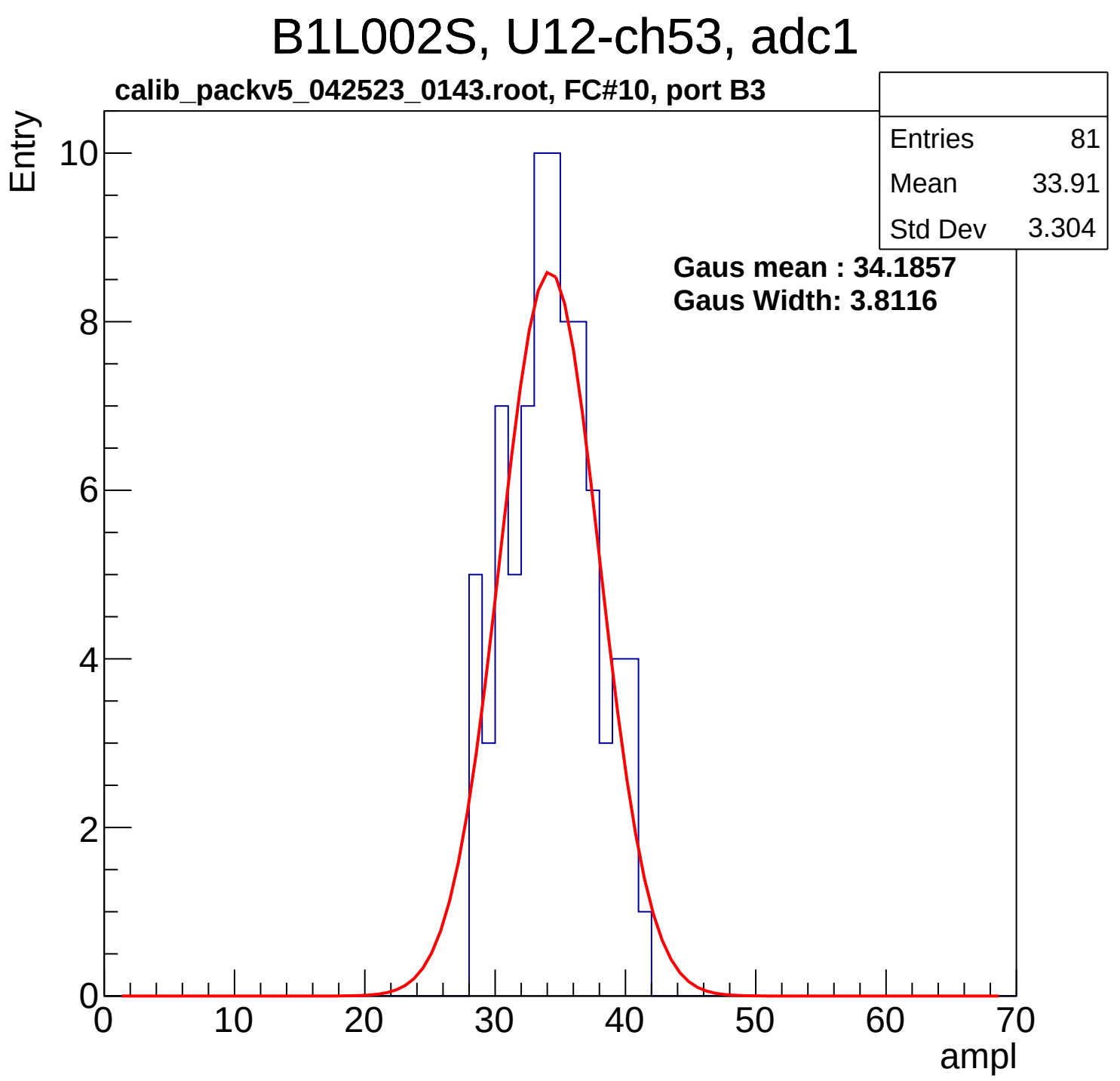
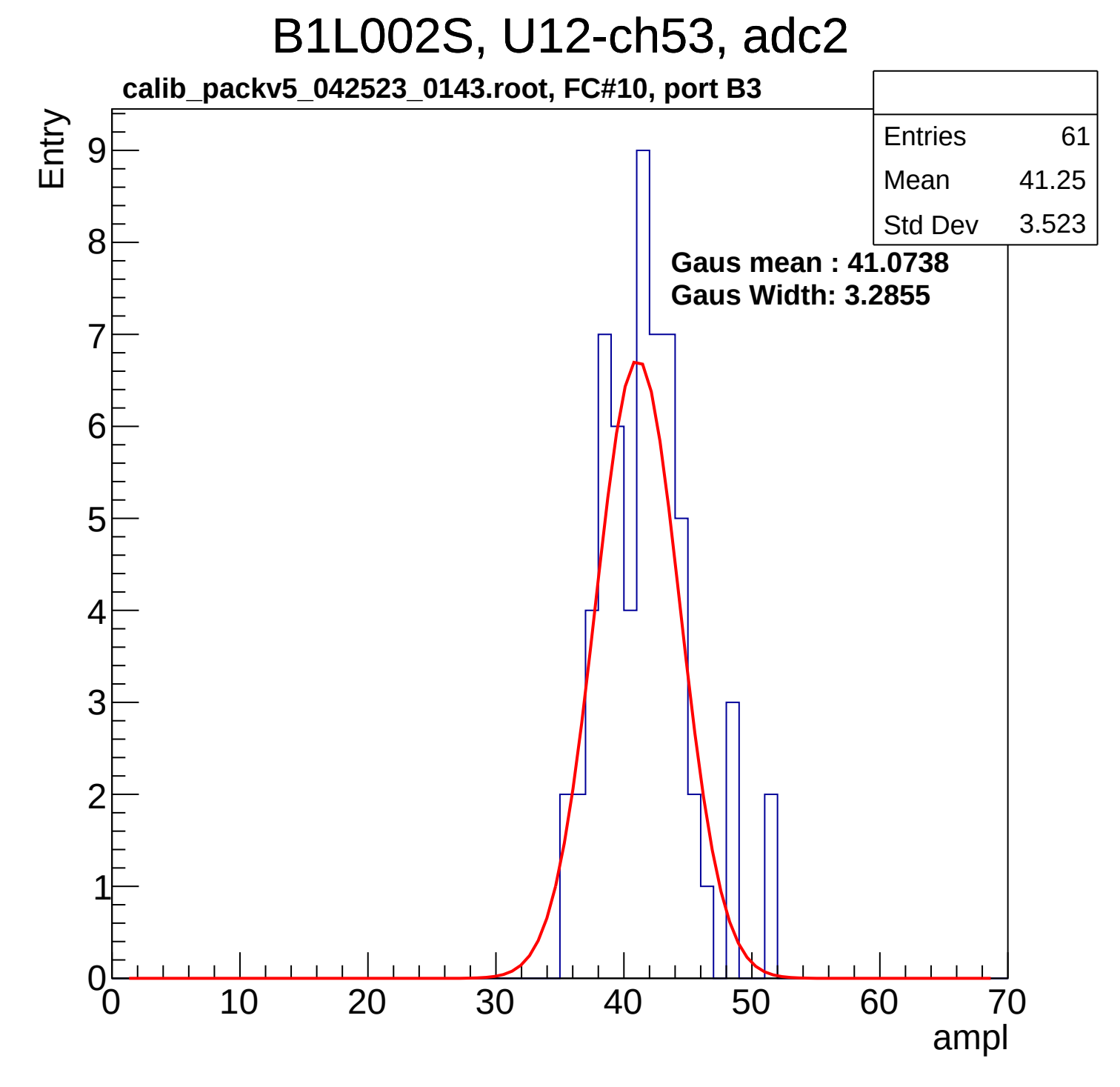
40
50
60
70


B1L002S, U12-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.81
Std Dev 2.721
Gaus mean : 27.3445
Gaus Width: 2.9411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 33.91
Std Dev 3.304
Gaus mean : 34.1857
Gaus Width: 3.8116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.25
Std Dev 3.523
Gaus mean : 41.0738
Gaus Width: 3.2855
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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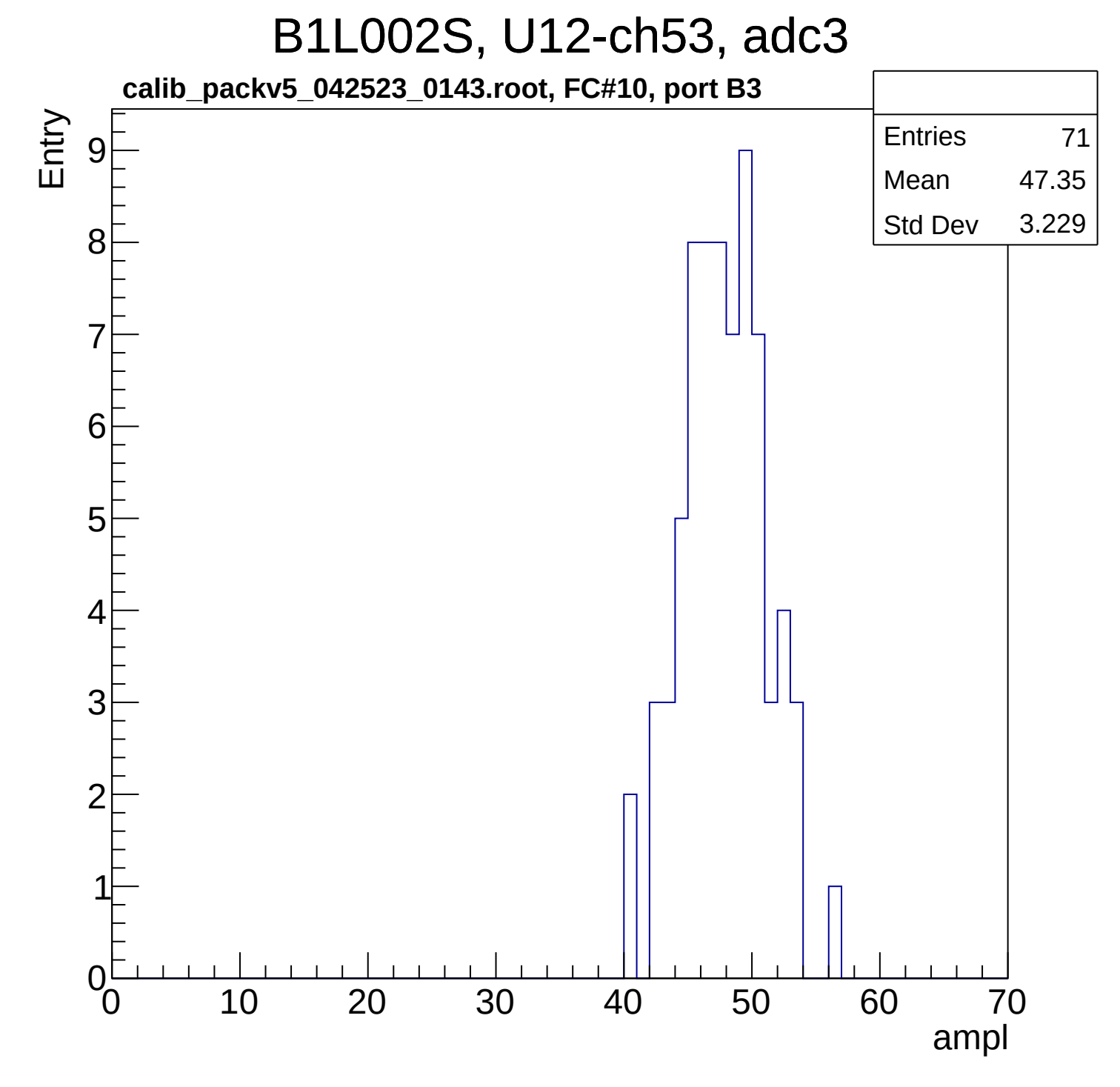
B1L002S, U12-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 47.35
Std Dev 3.229
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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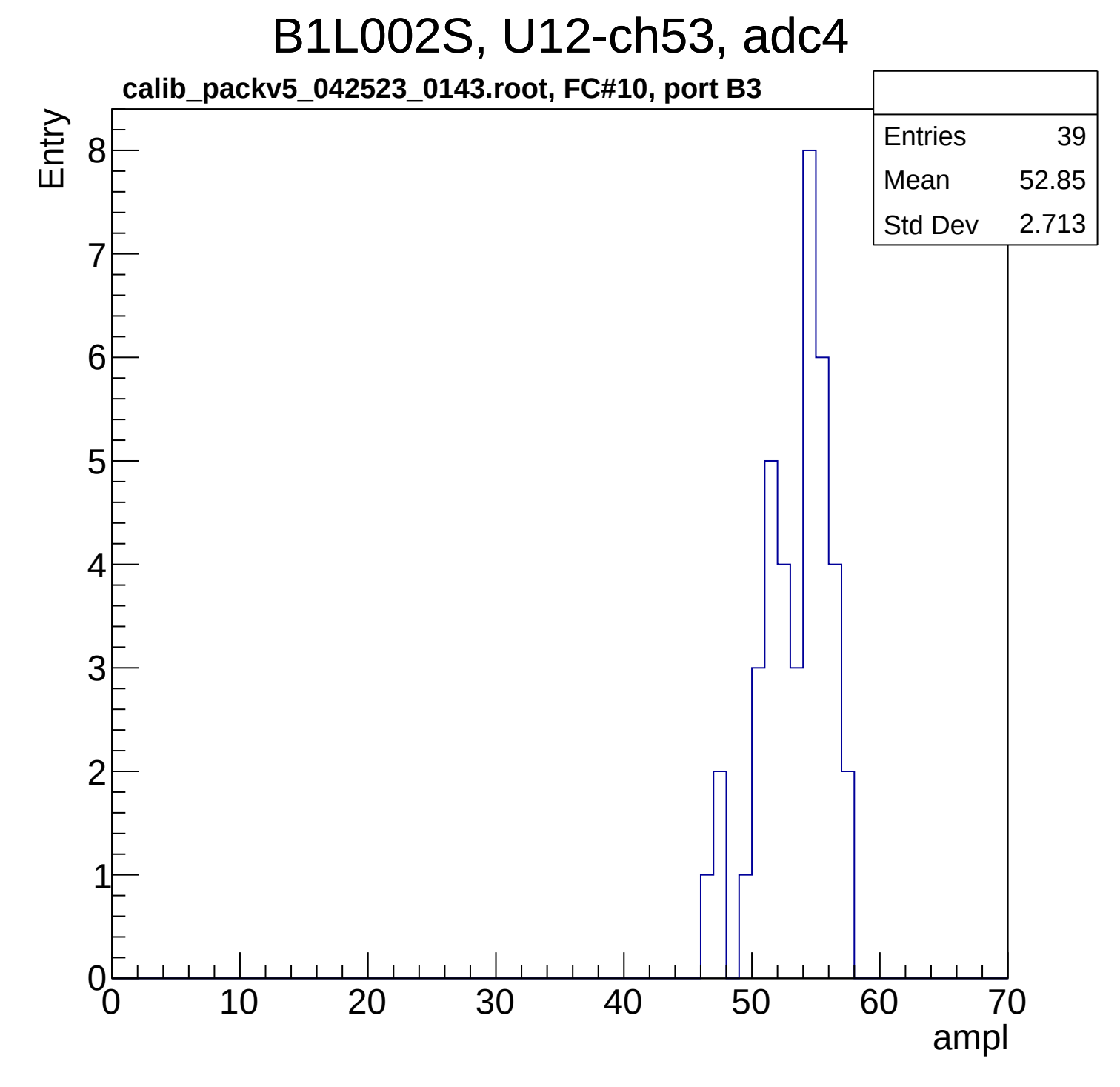
B1L002S, U12-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 52.85
Std Dev 2.713
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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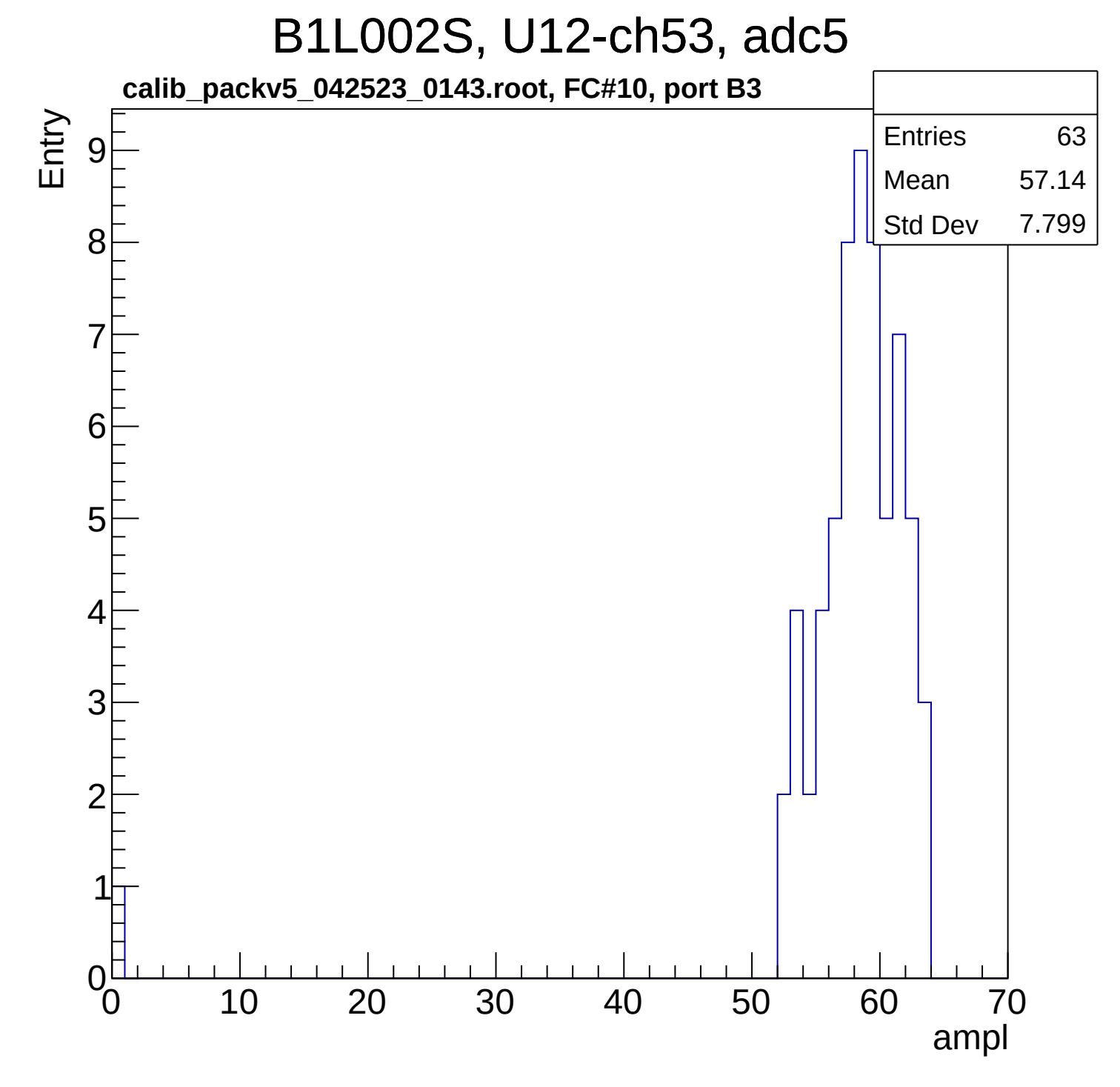
B1L002S, U12-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.14
Std Dev 7.799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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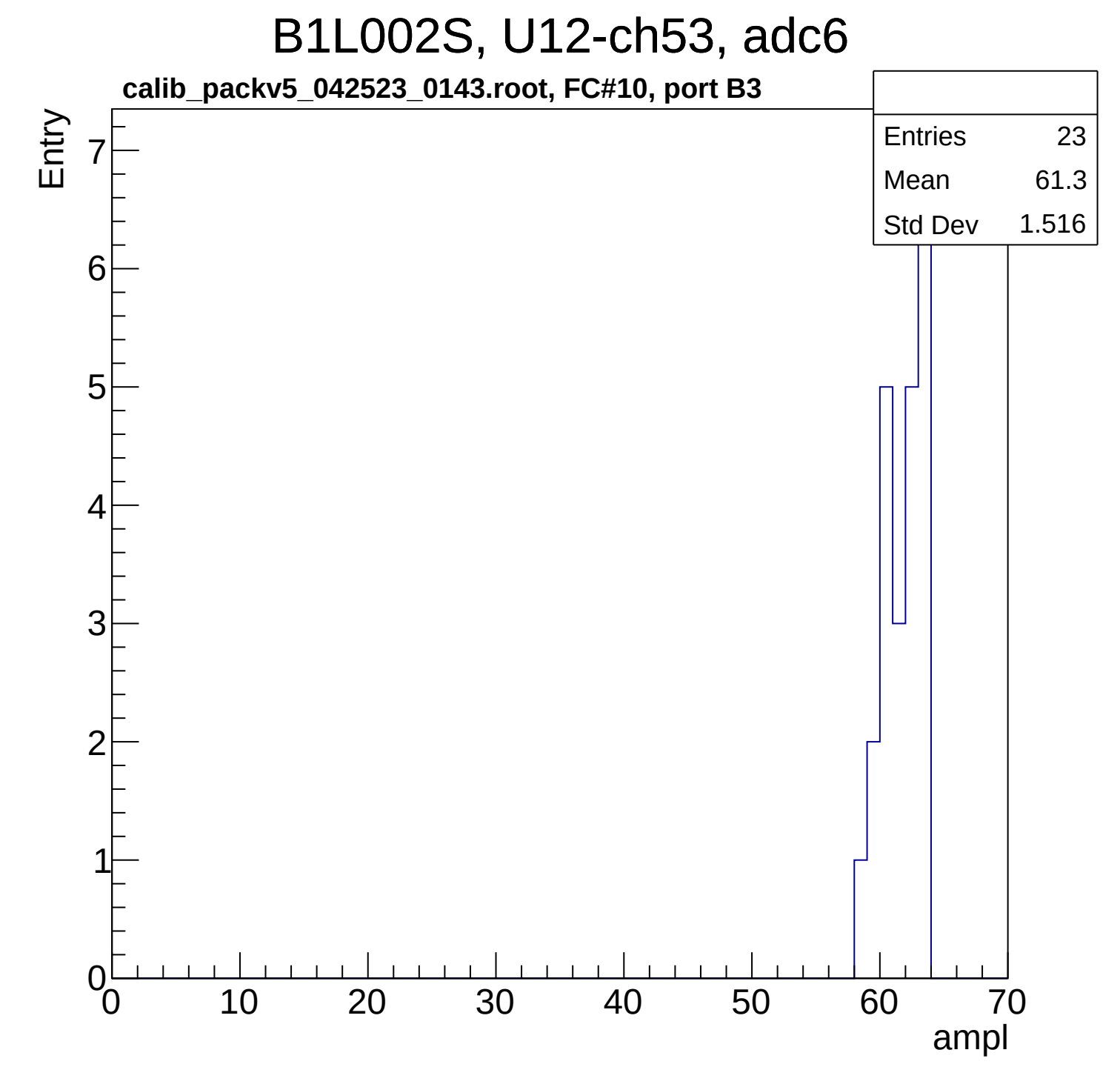
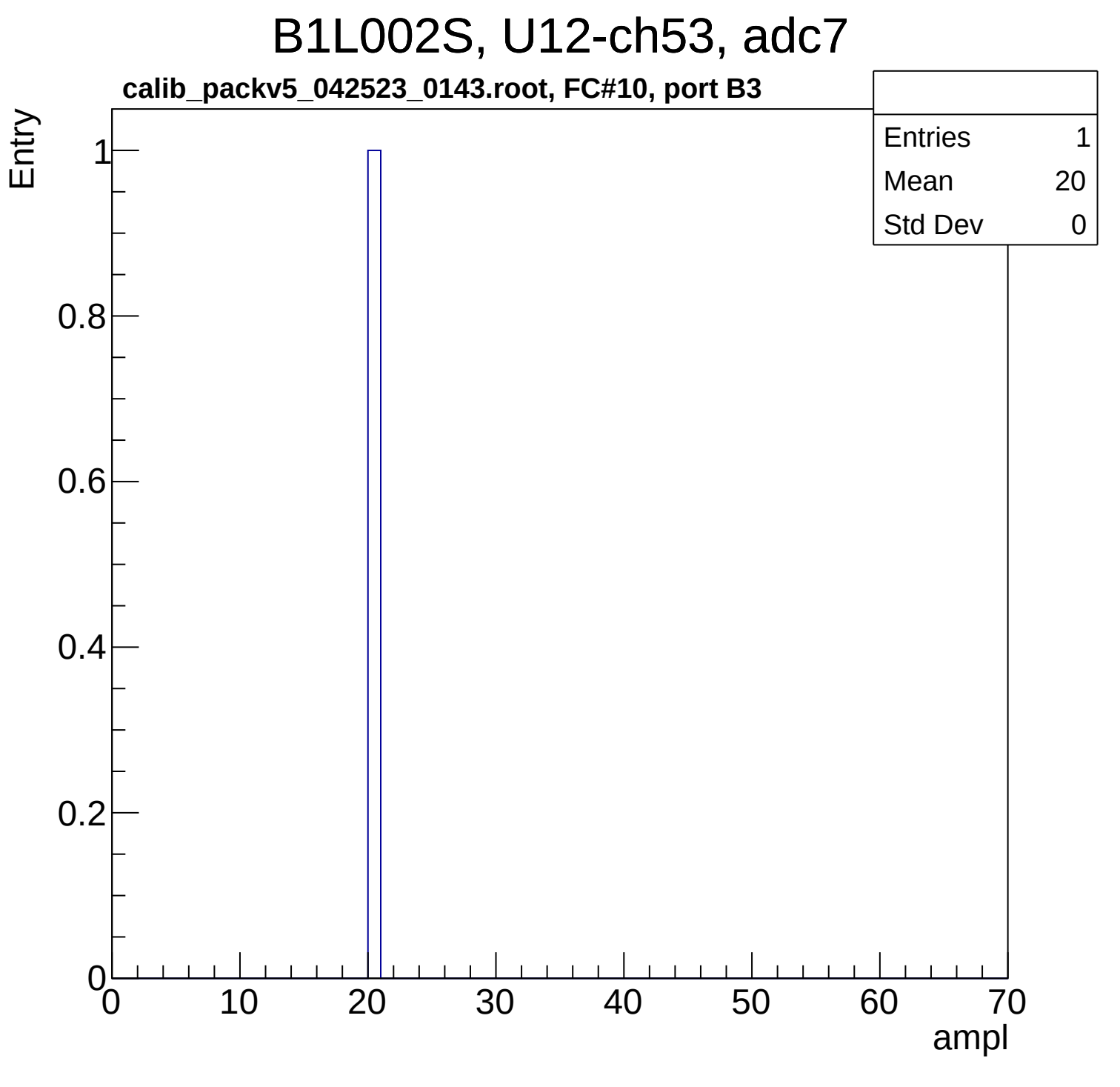

B1L002S, U12-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.3
Std Dev 1.516
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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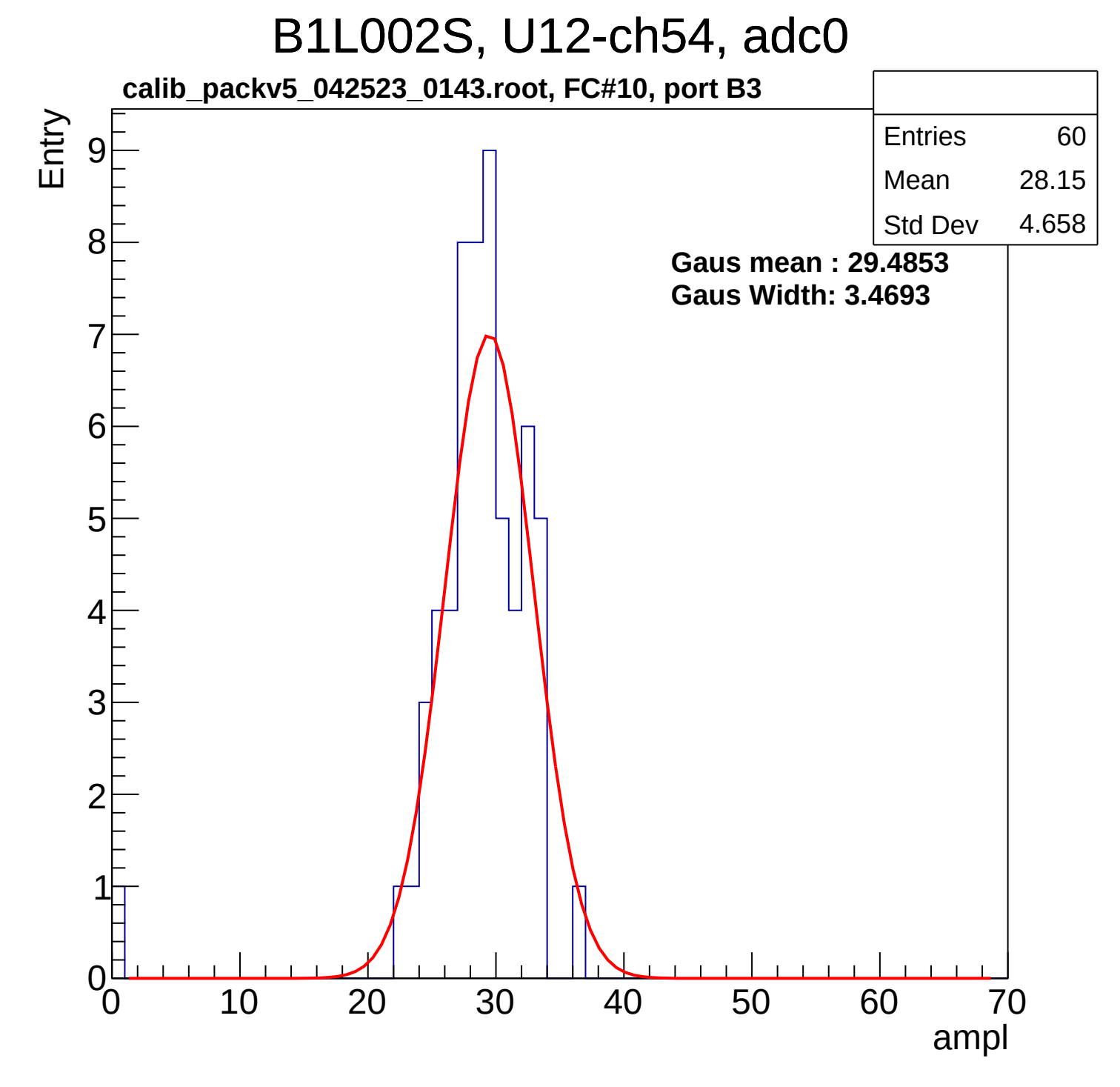
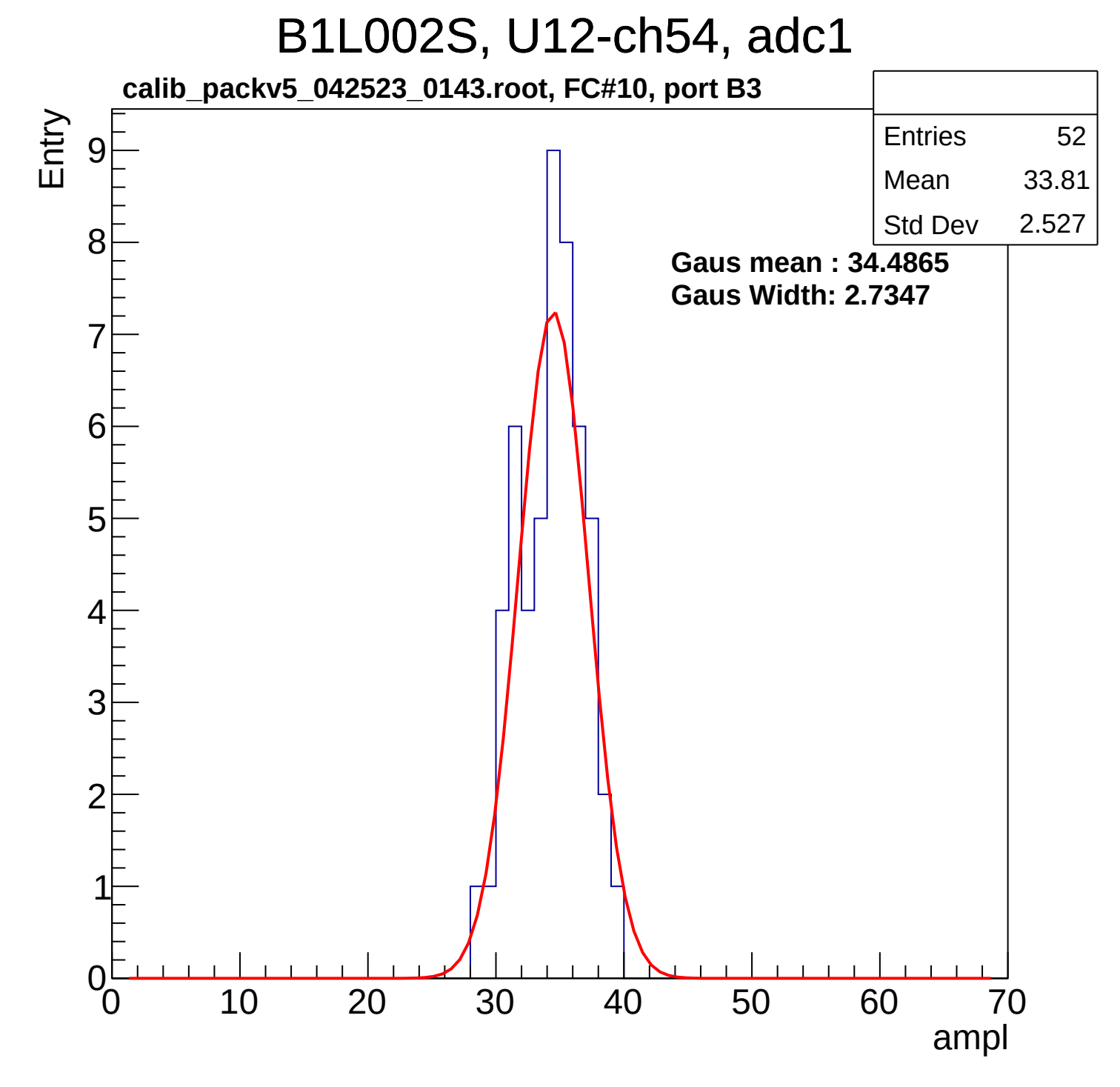
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.15
Std Dev 4.658
Gaus mean : 29.4853
Gaus Width: 3.4693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 33.81
Std Dev 2.527
Gaus mean : 34.4865
Gaus Width: 2.7347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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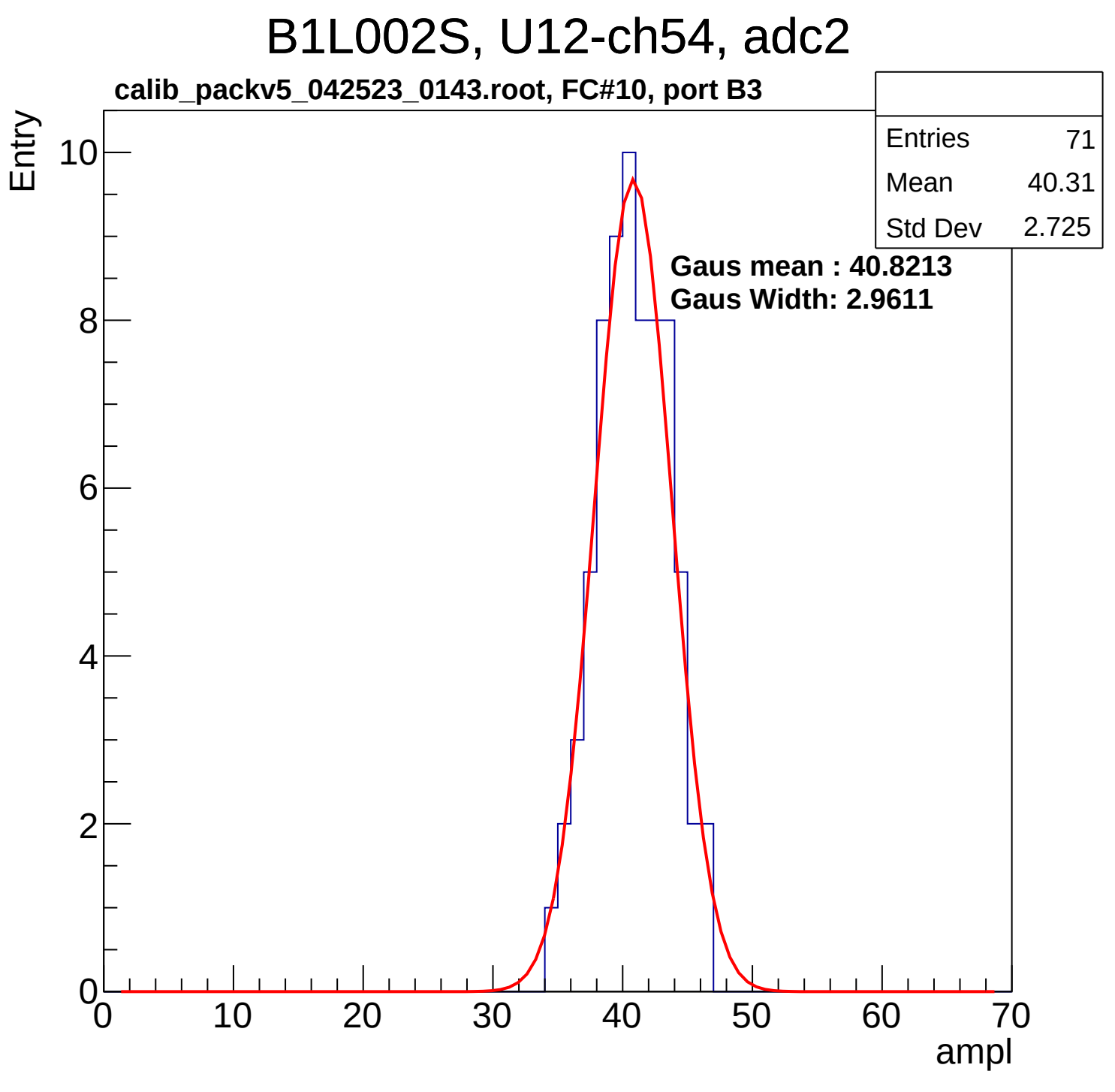
B1L002S, U12-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 40.31
Std Dev 2.725
Gaus mean : 40.8213
Gaus Width: 2.9611
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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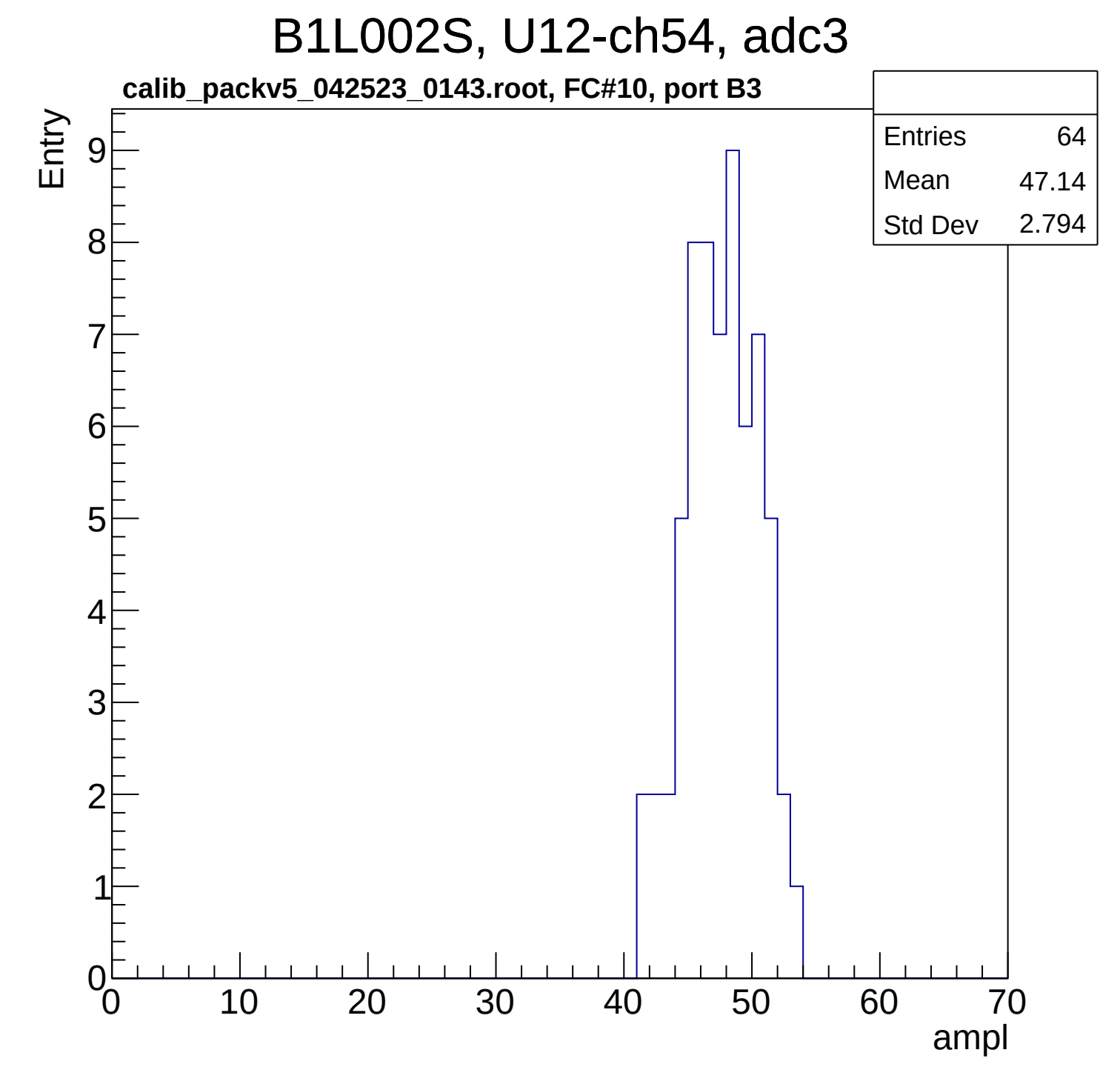
B1L002S, U12-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.14
Std Dev 2.794
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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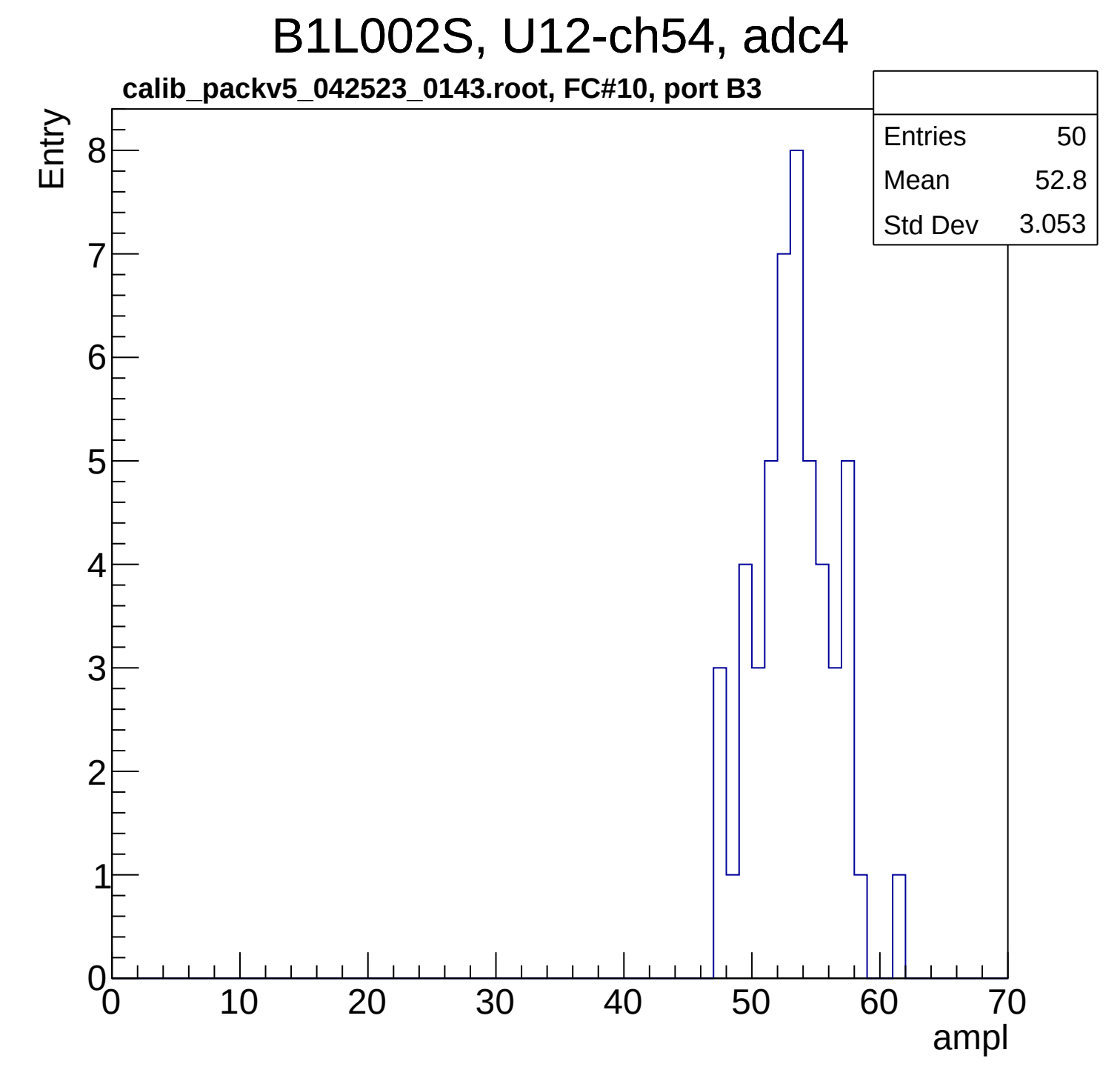
B1L002S, U12-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.8
Std Dev 3.053
Entry
ampl
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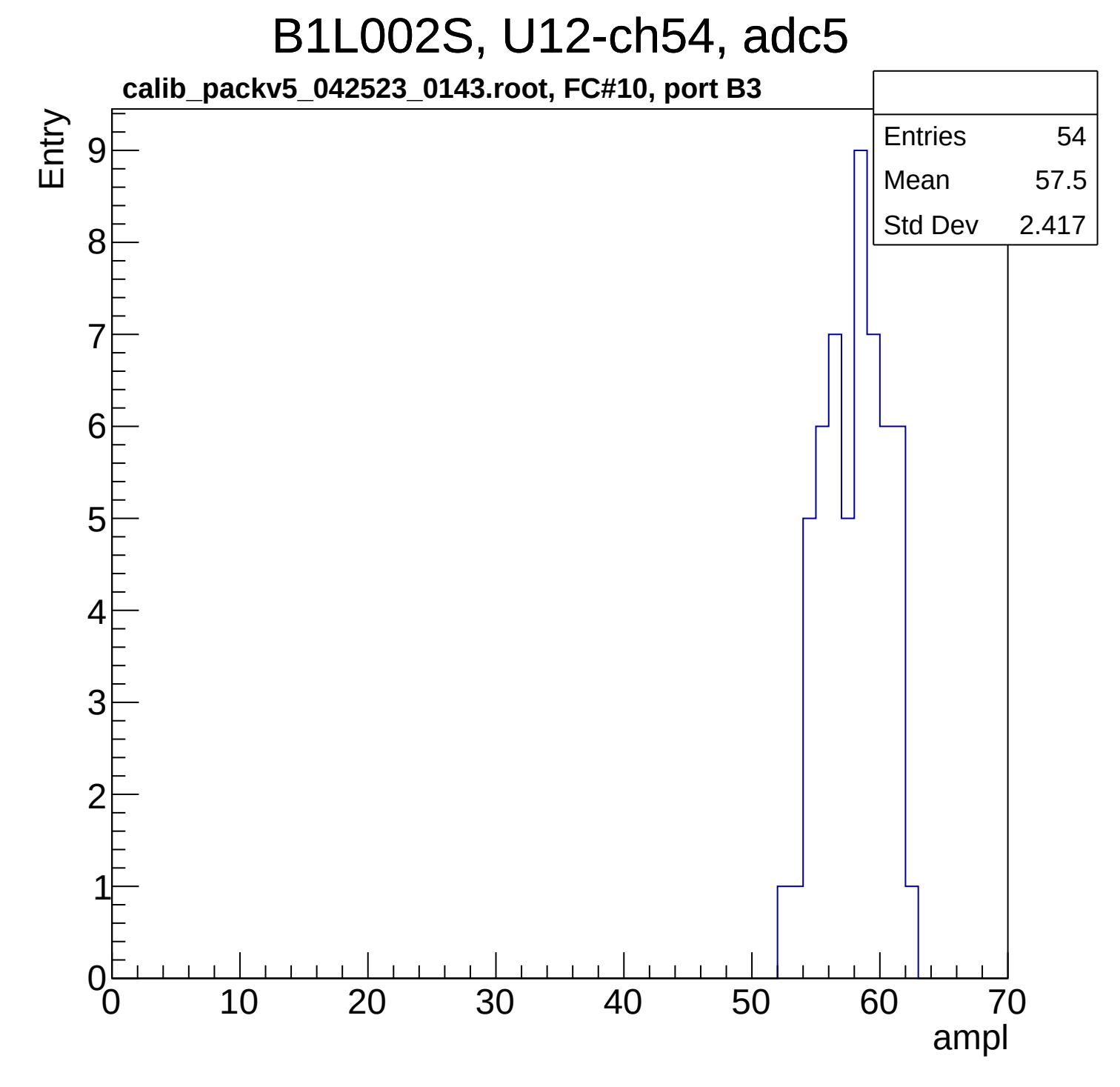
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.5
Std Dev 2.417
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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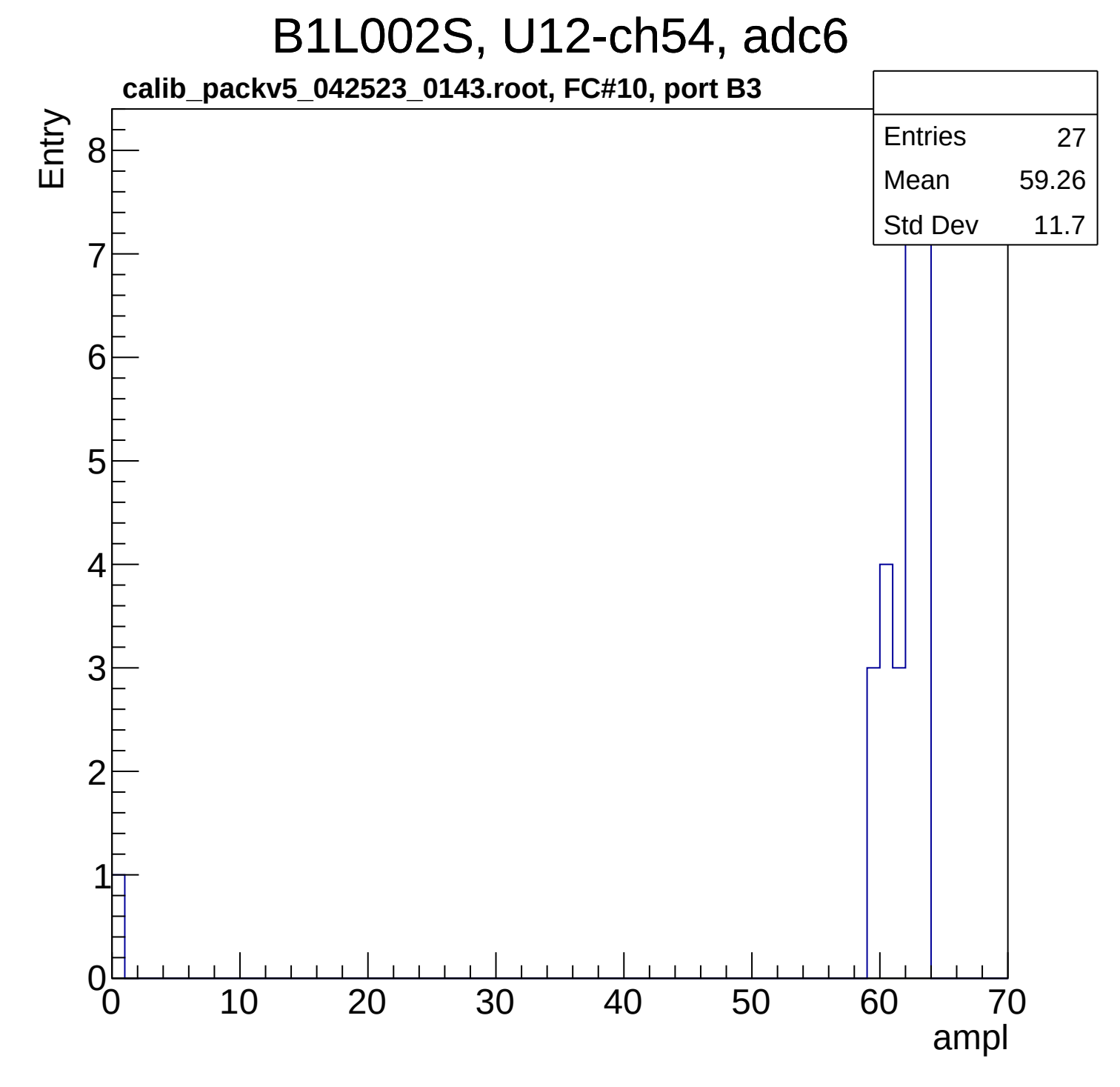
B1L002S, U12-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.26
Std Dev 11.7
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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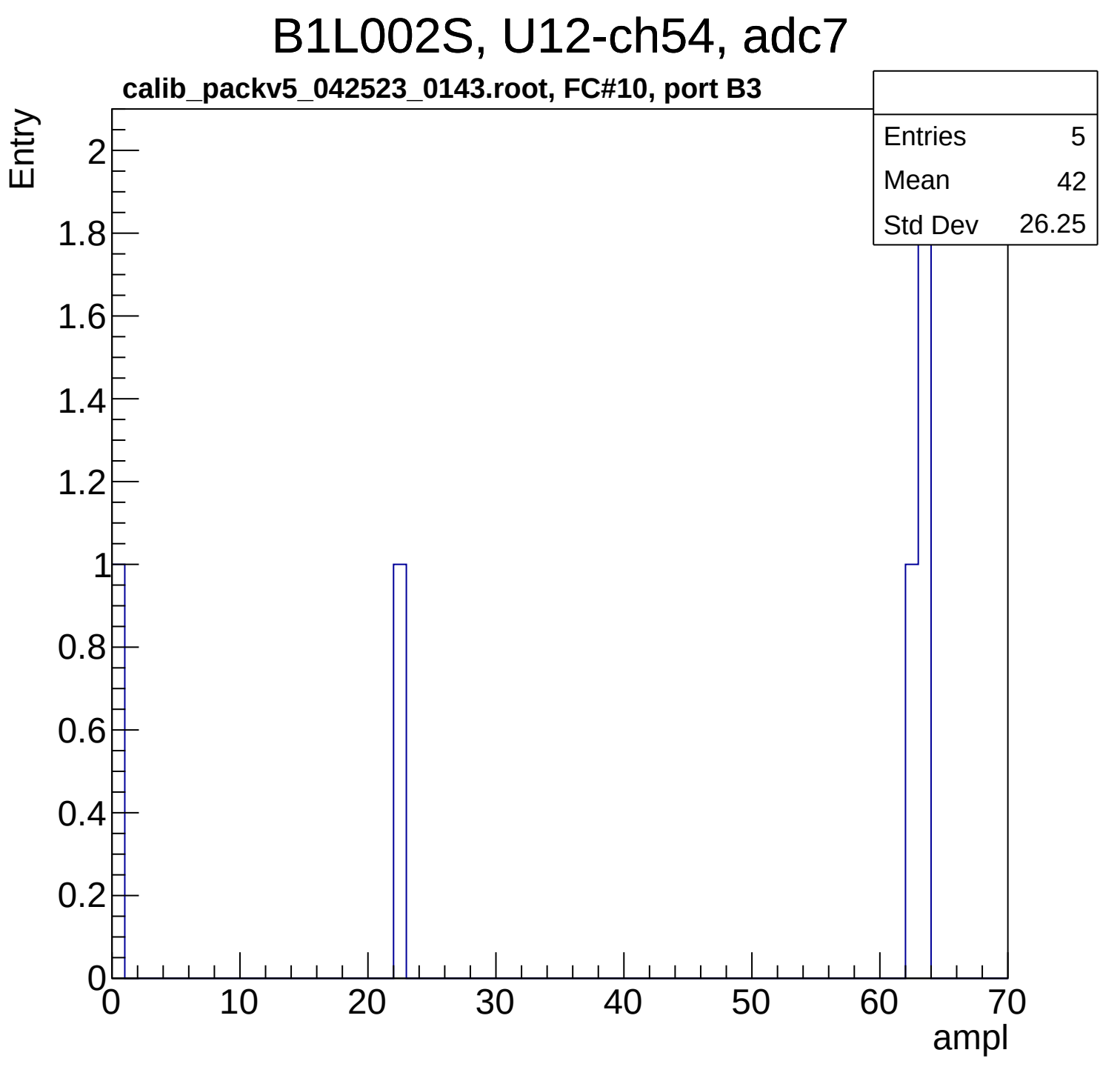
B1L002S, U12-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 42
Std Dev 26.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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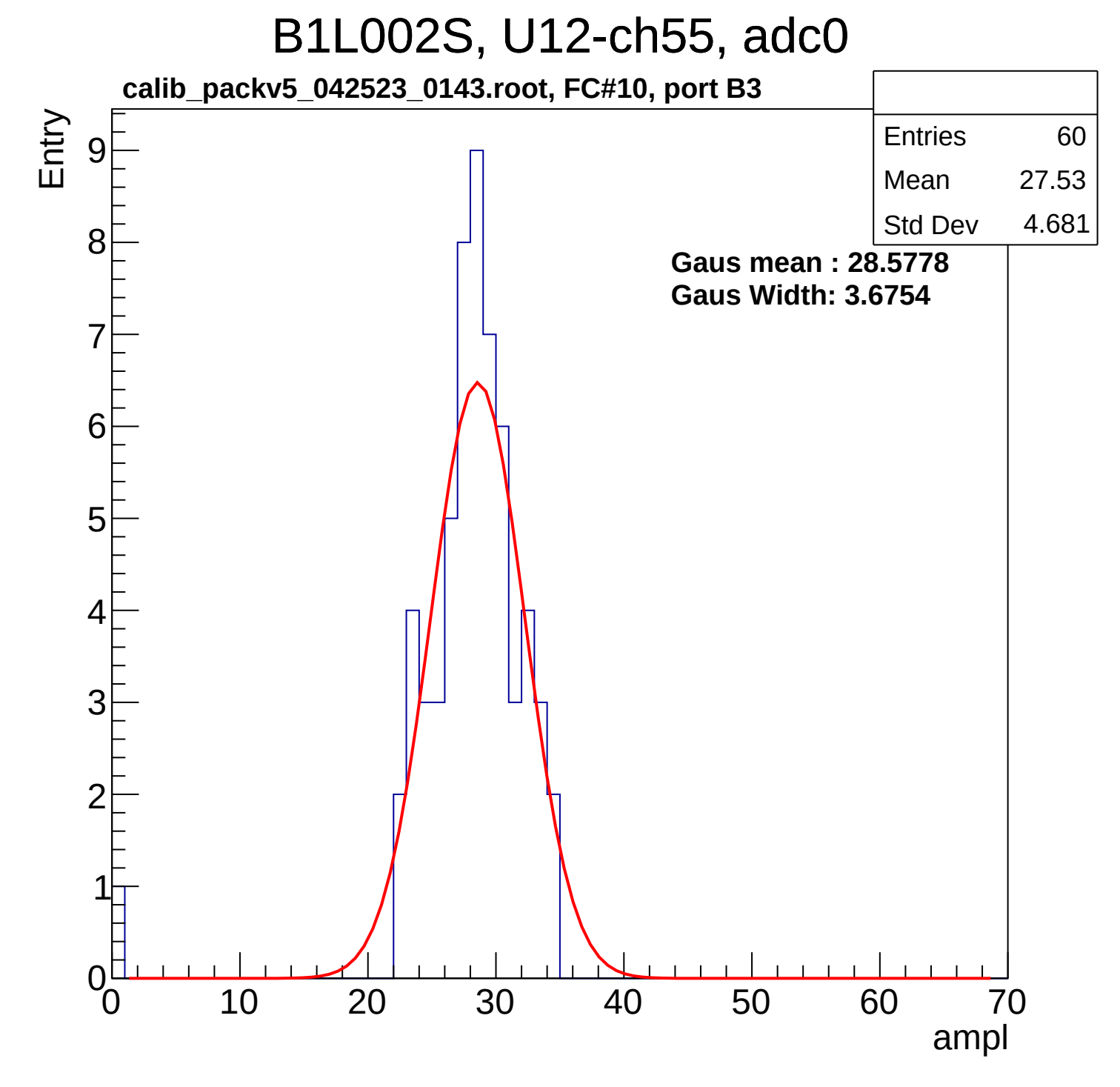
B1L002S, U12-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.53
Std Dev 4.681
Gaus mean : 28.5778
Gaus Width: 3.6754
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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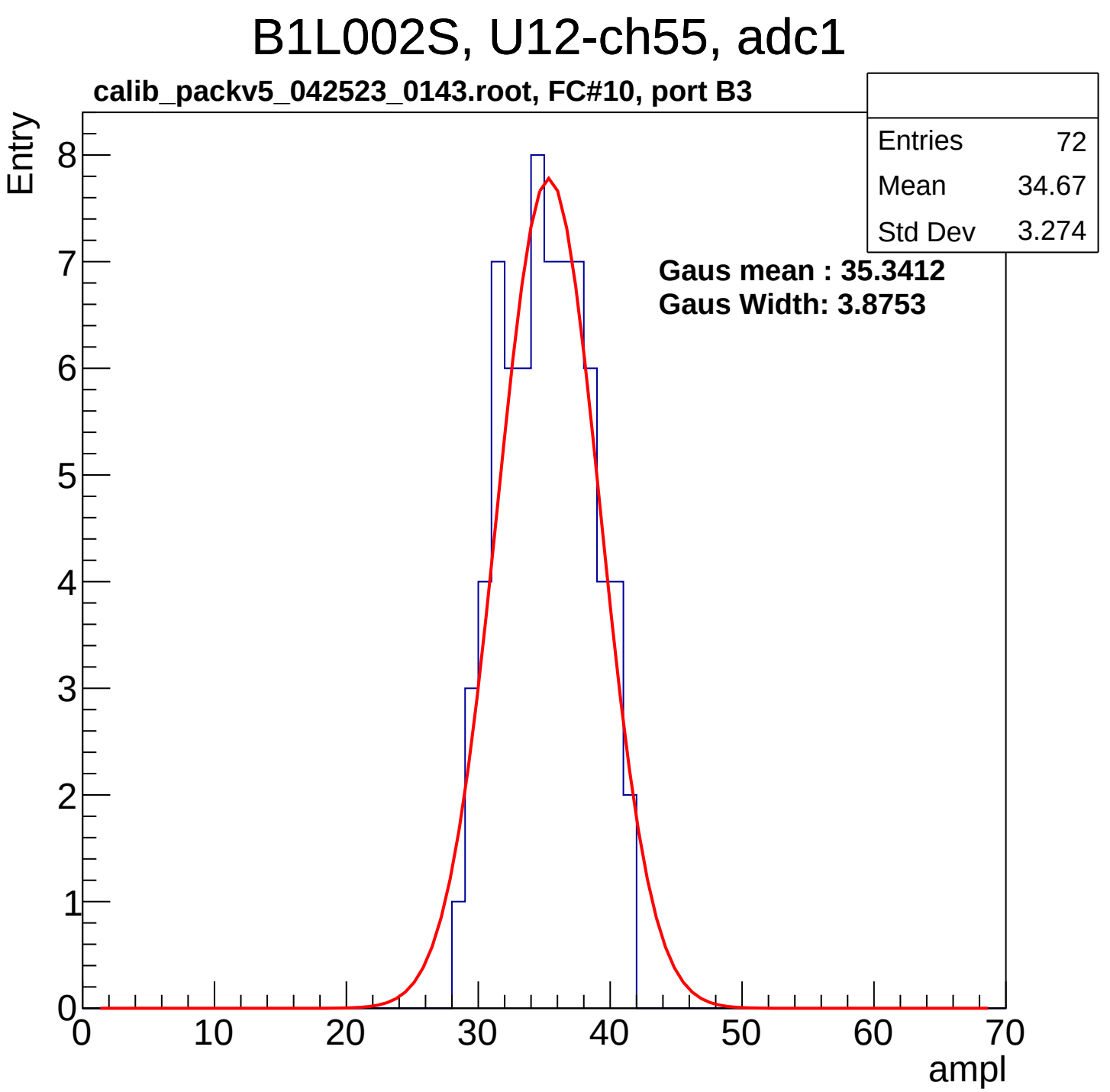
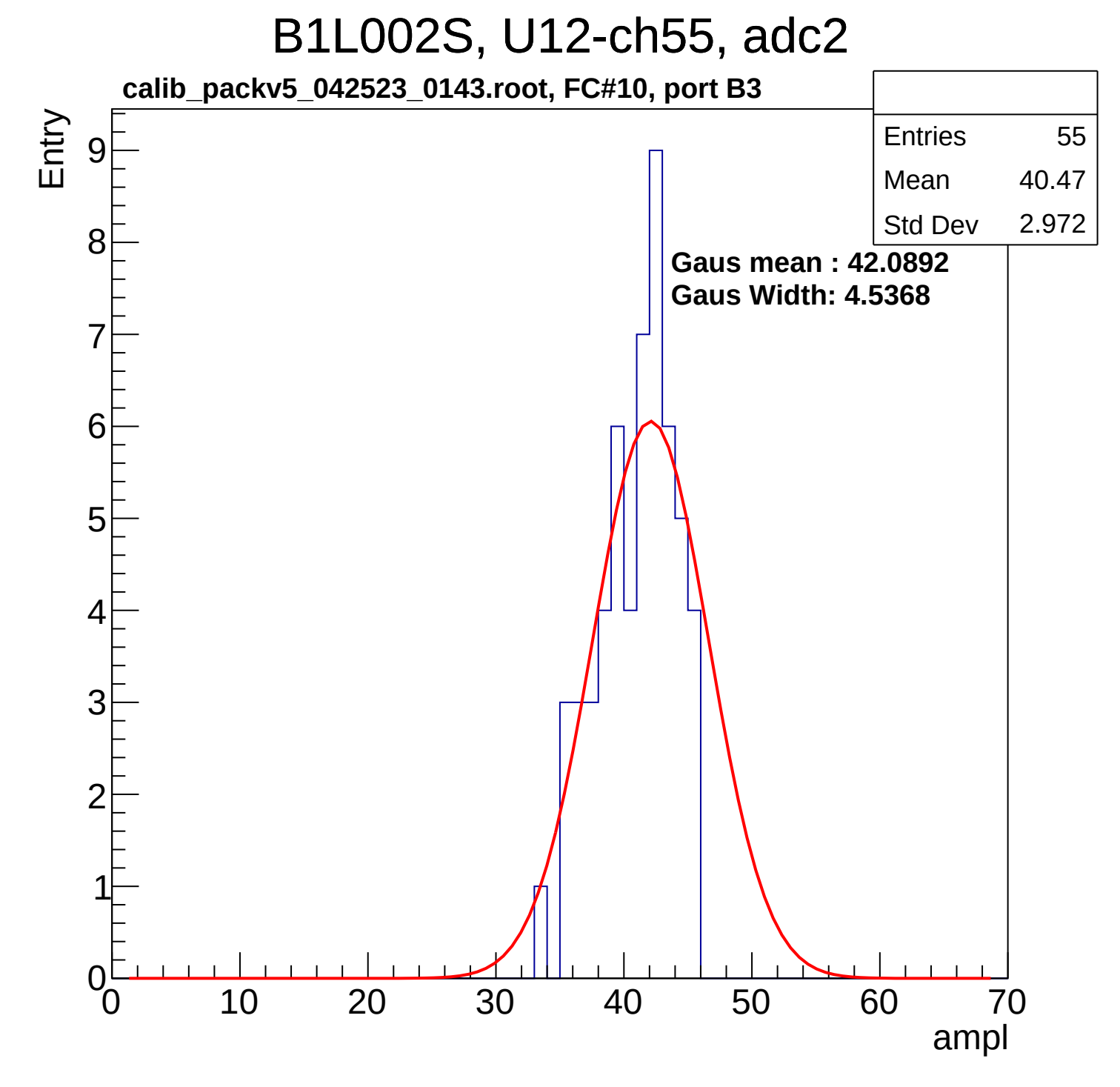

B1L002S, U12-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.67
Std Dev 3.274
Gaus mean : 35.3412
Gaus Width: 3.8753
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.47
Std Dev 2.972
Gaus mean : 42.0892
Gaus Width: 4.5368
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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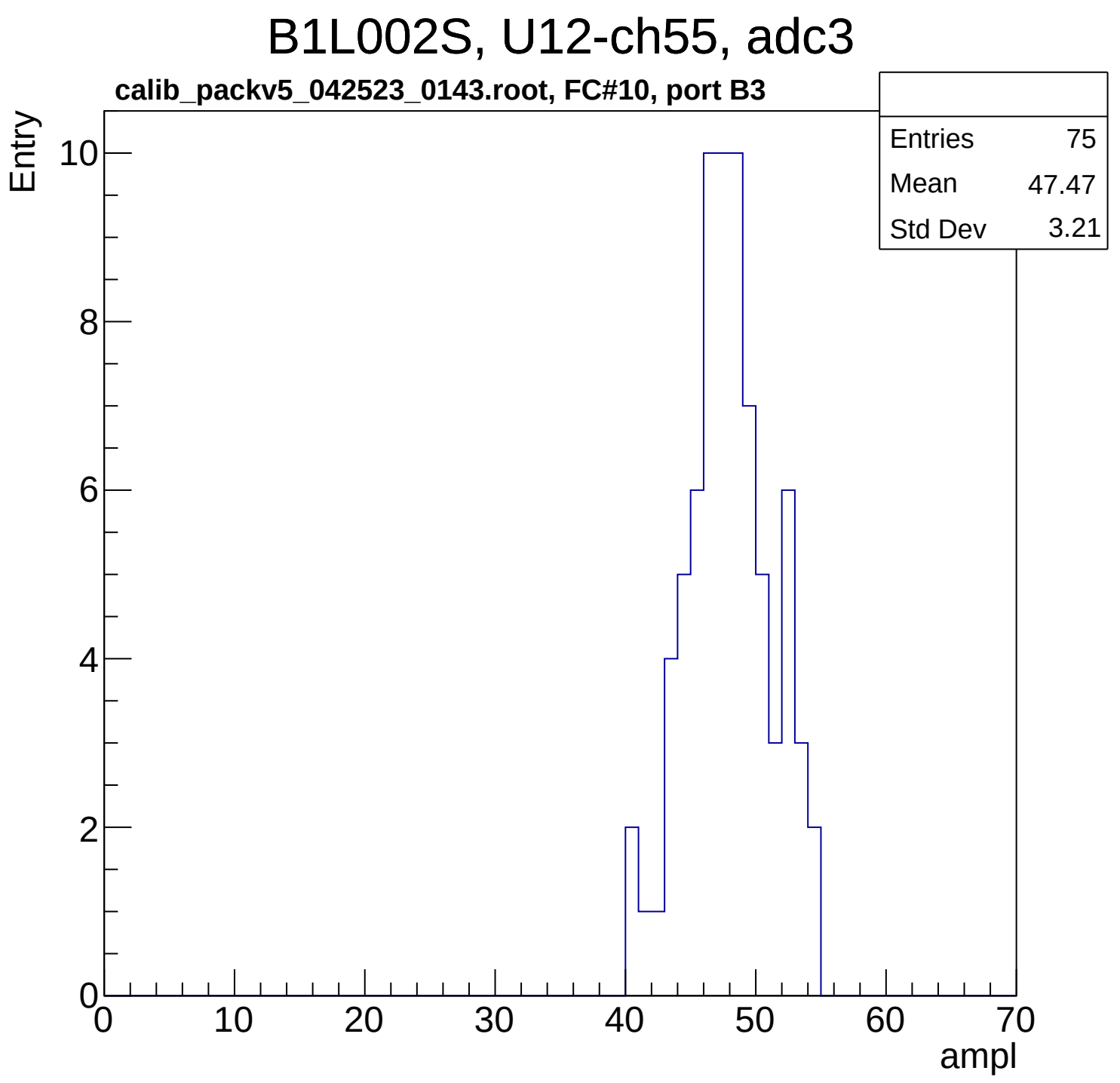
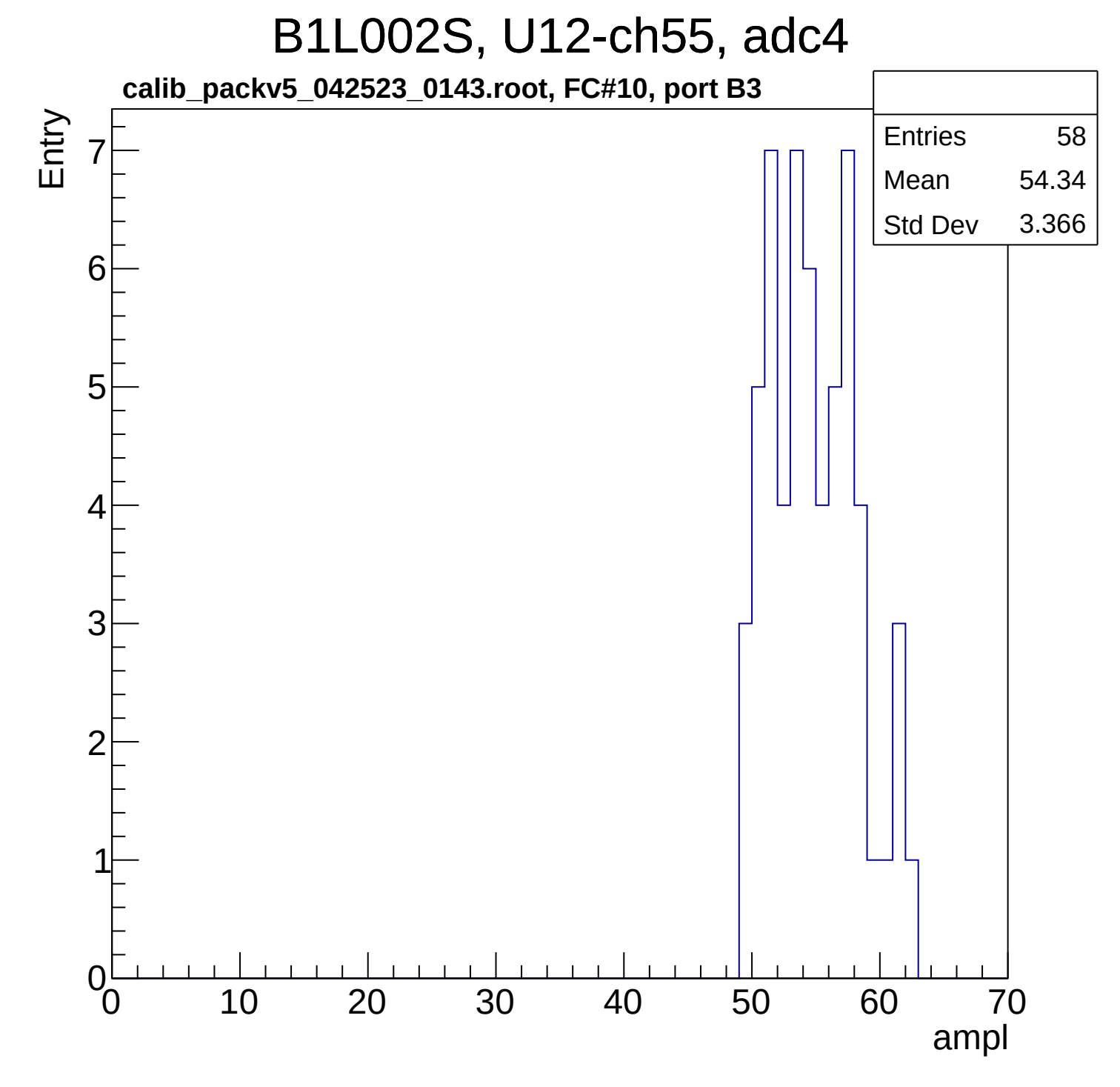

B1L002S, U12-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 47.47
Std Dev 3.21
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 54.34
Std Dev 3.366
Entry
ampl
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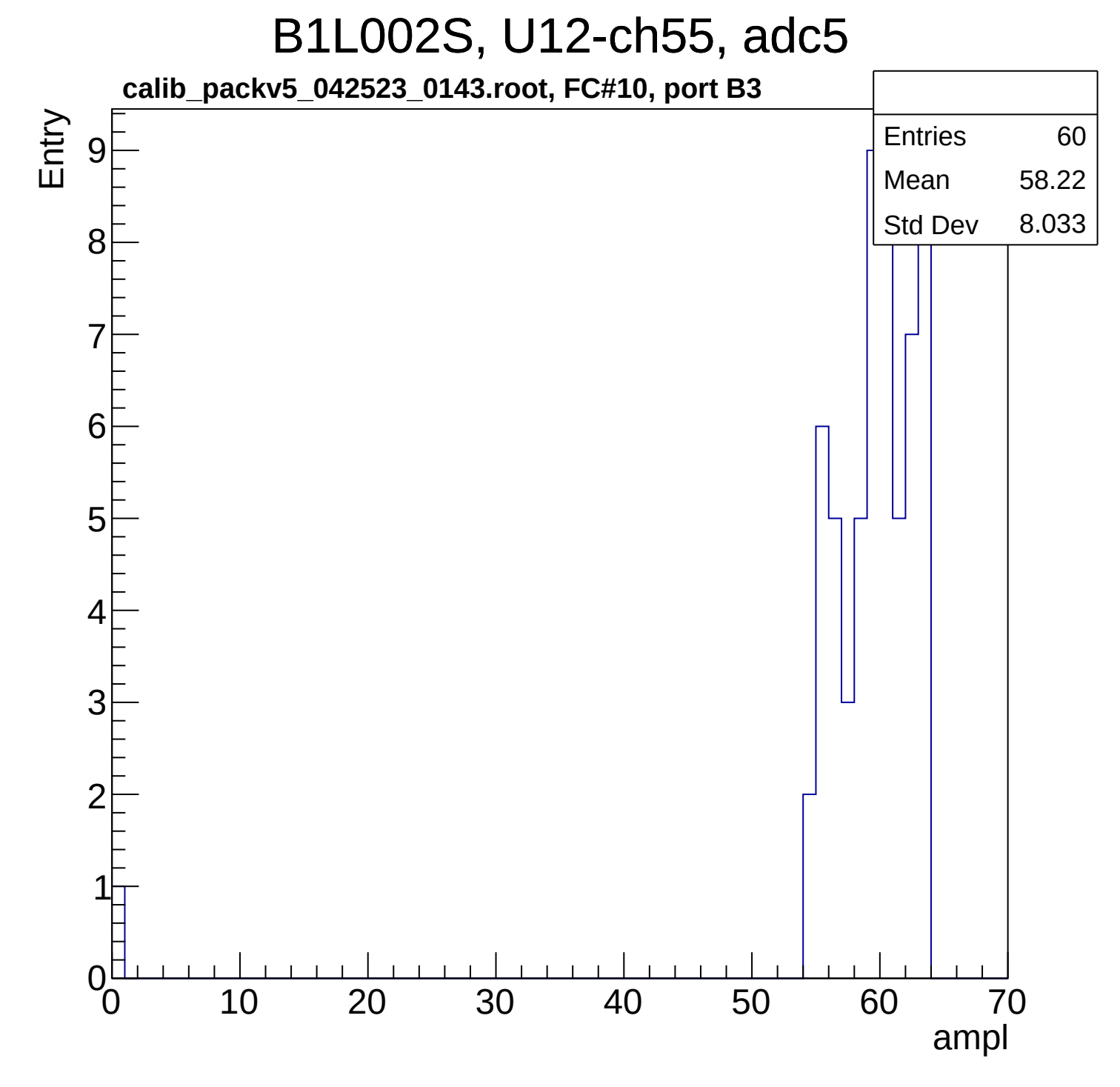
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.22
Std Dev 8.033
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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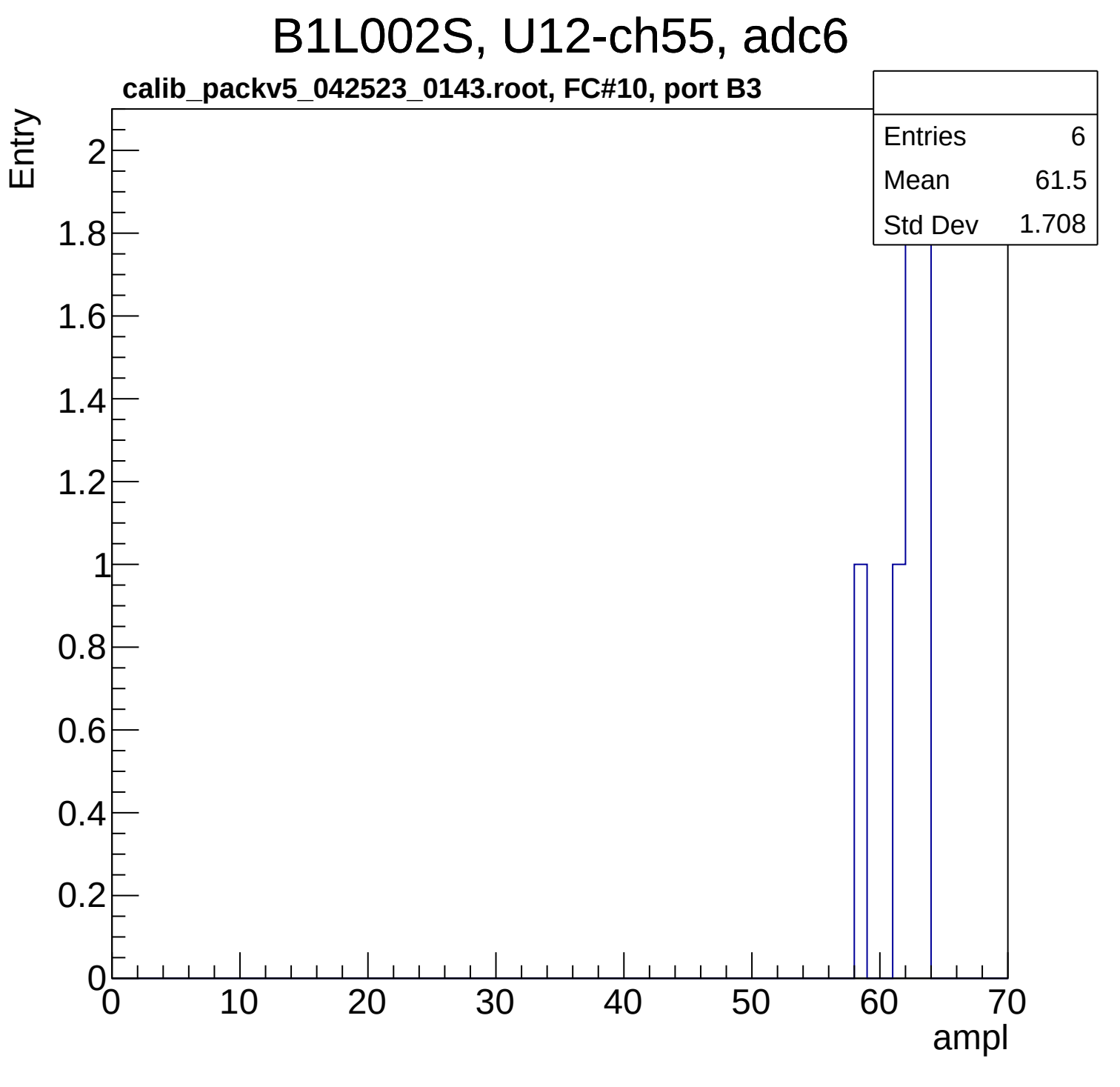
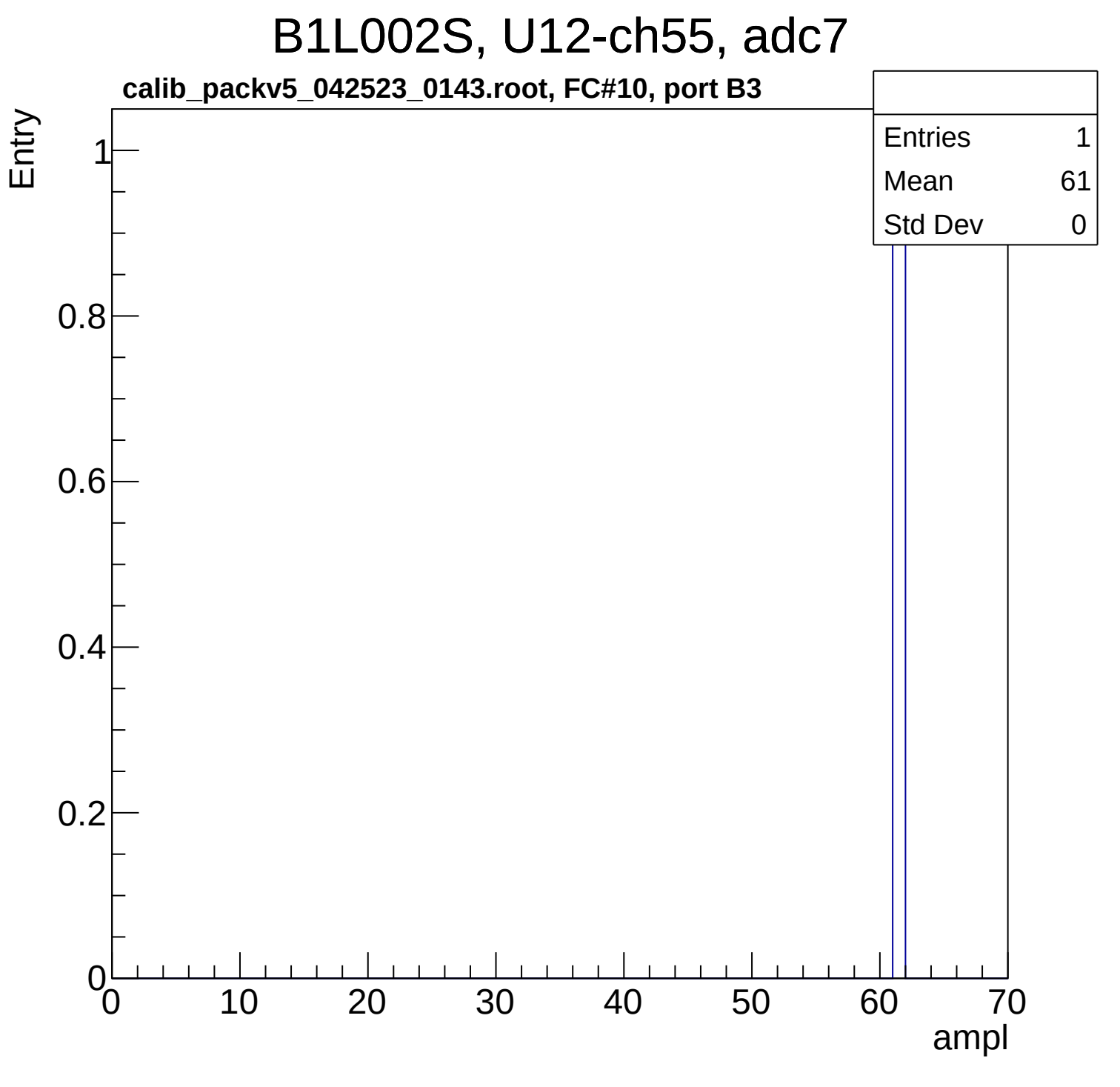
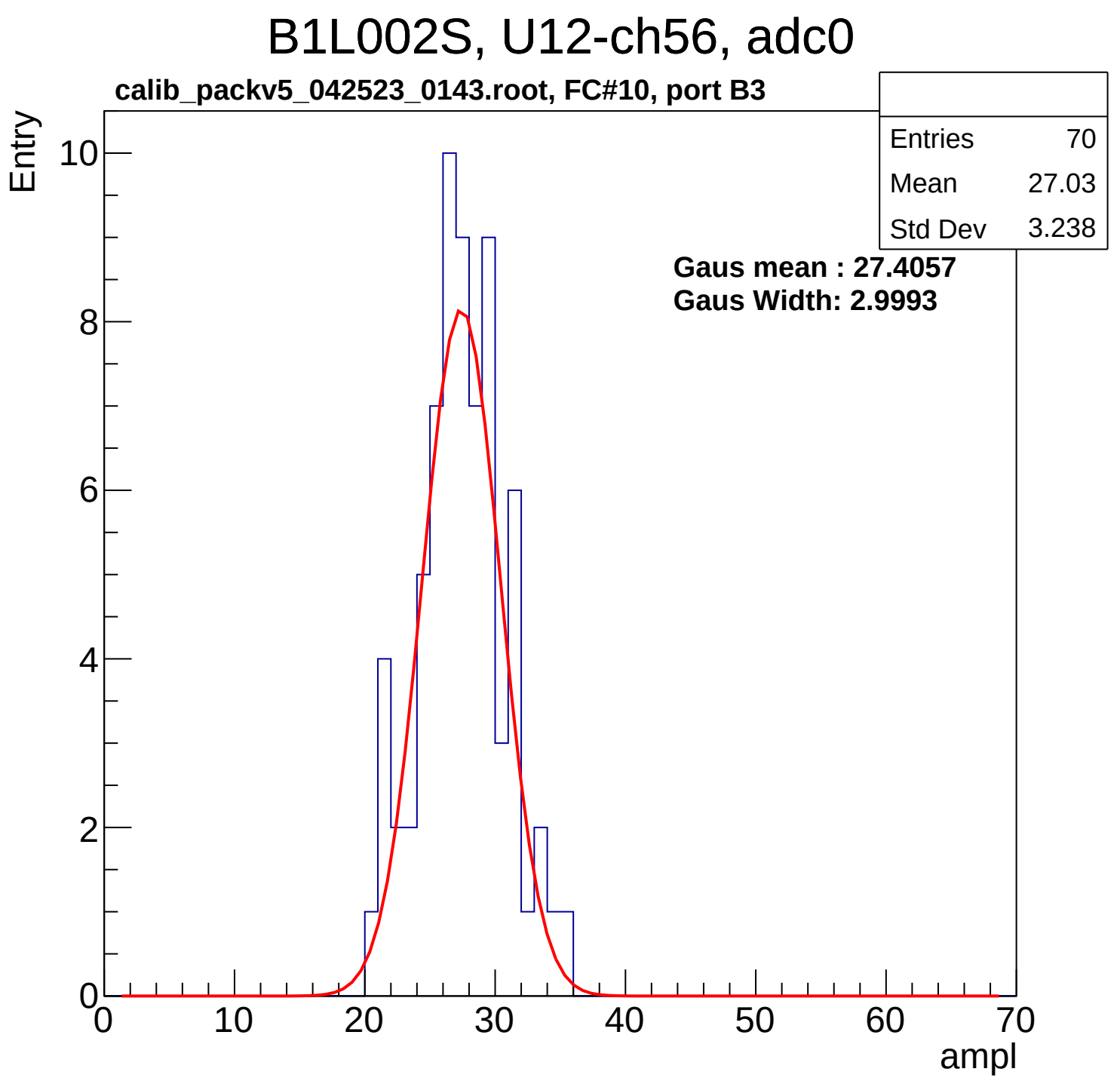
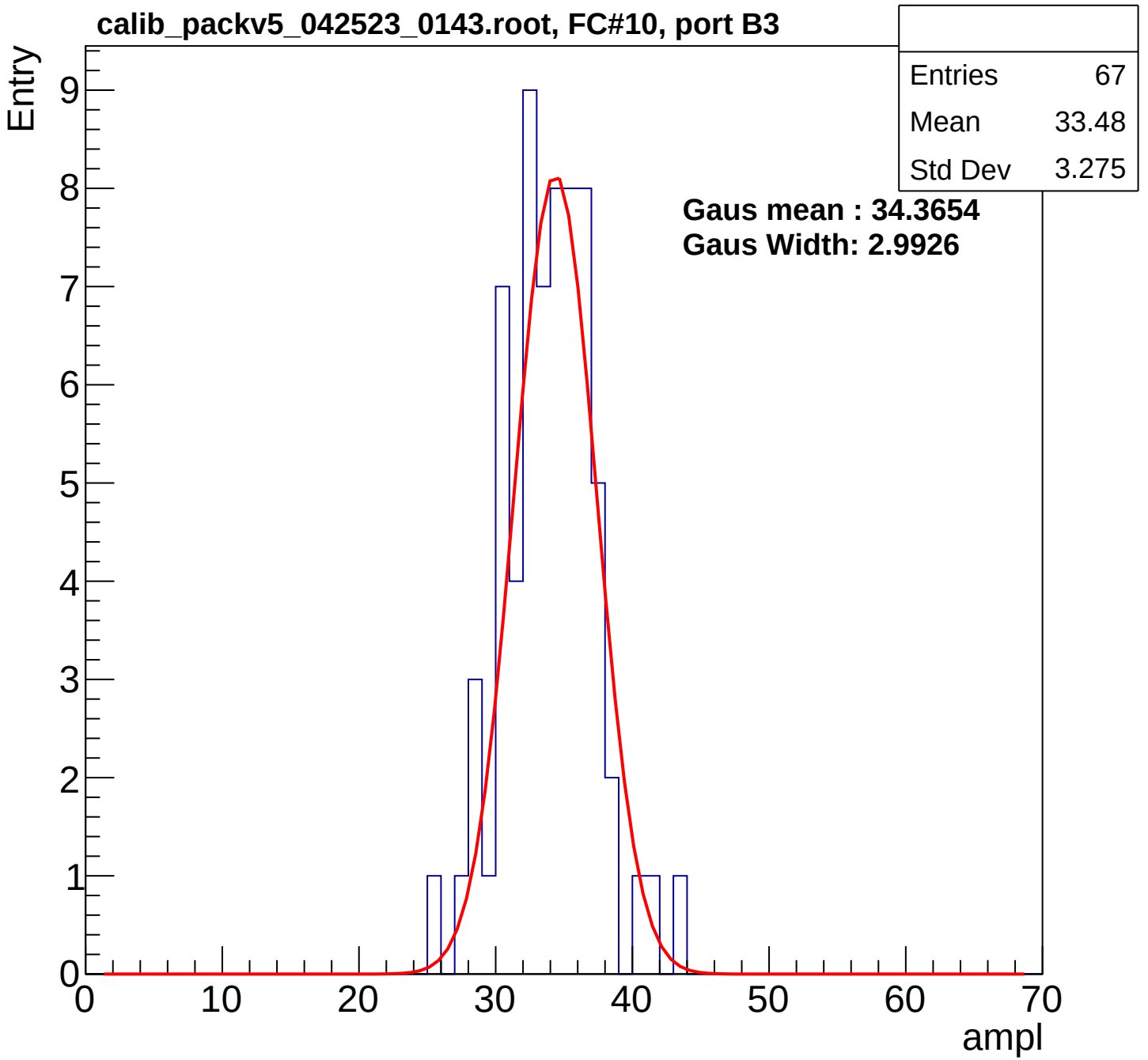
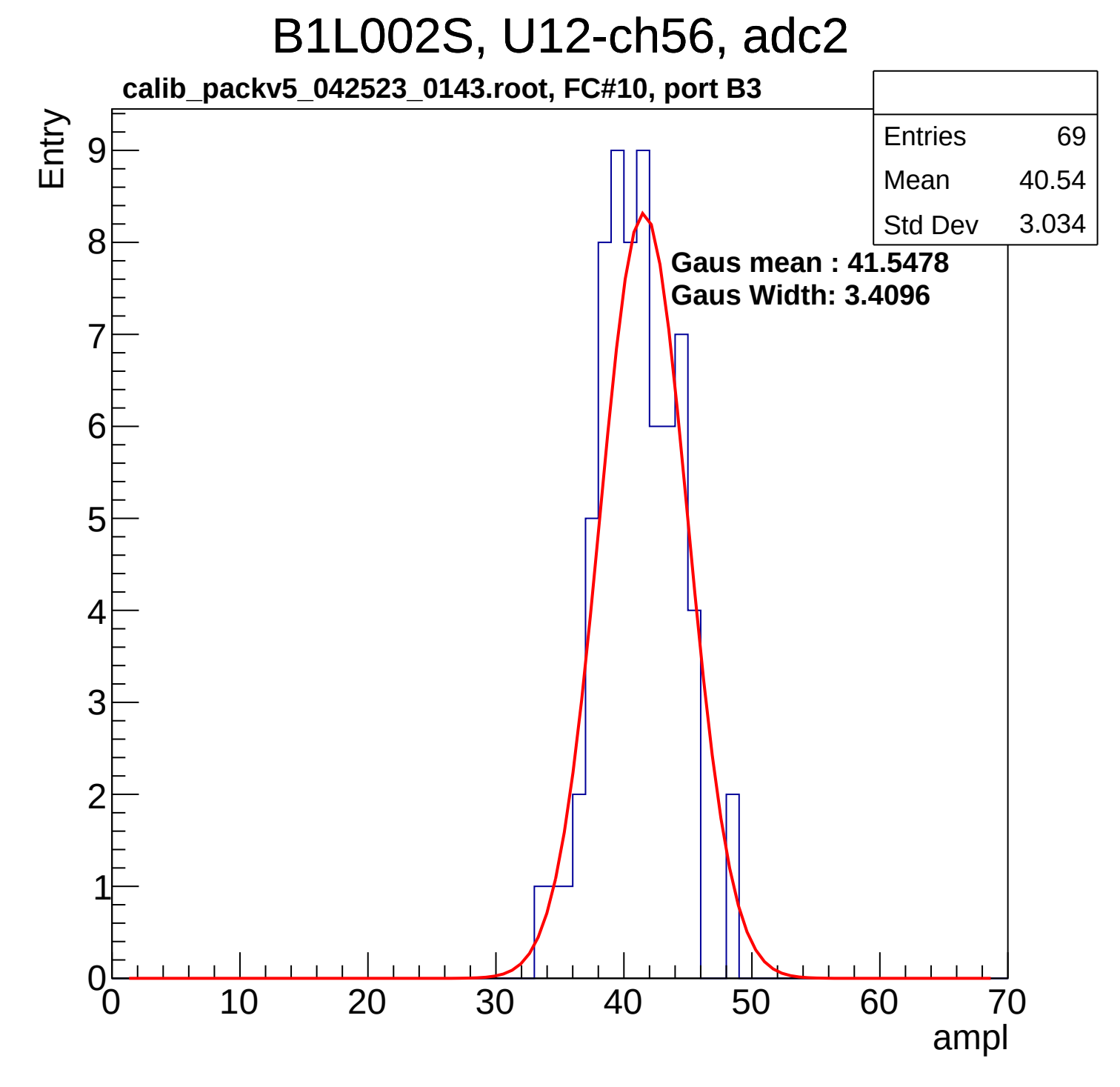

B1L002S, U12-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.5
Std Dev 1.708
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.03
Std Dev 3.238
Gaus mean : 27.4057
Gaus Width: 2.9993
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.48
Std Dev 3.275
Gaus mean : 34.3654
Gaus Width: 2.9926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.54
Std Dev 3.034
Gaus mean : 41.5478
Gaus Width: 3.4096
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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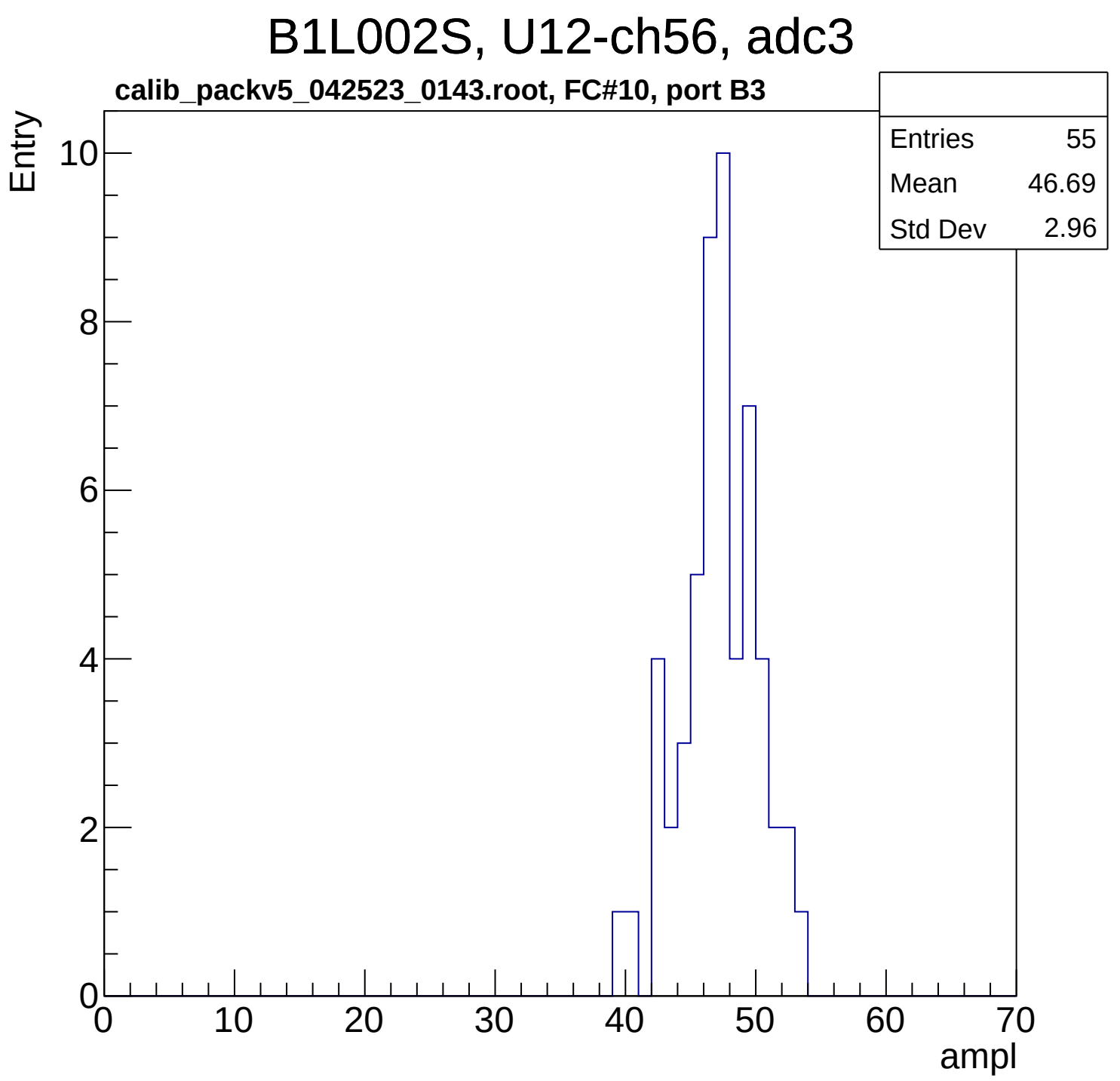
B1L002S, U12-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.69
Std Dev 2.96
Entry
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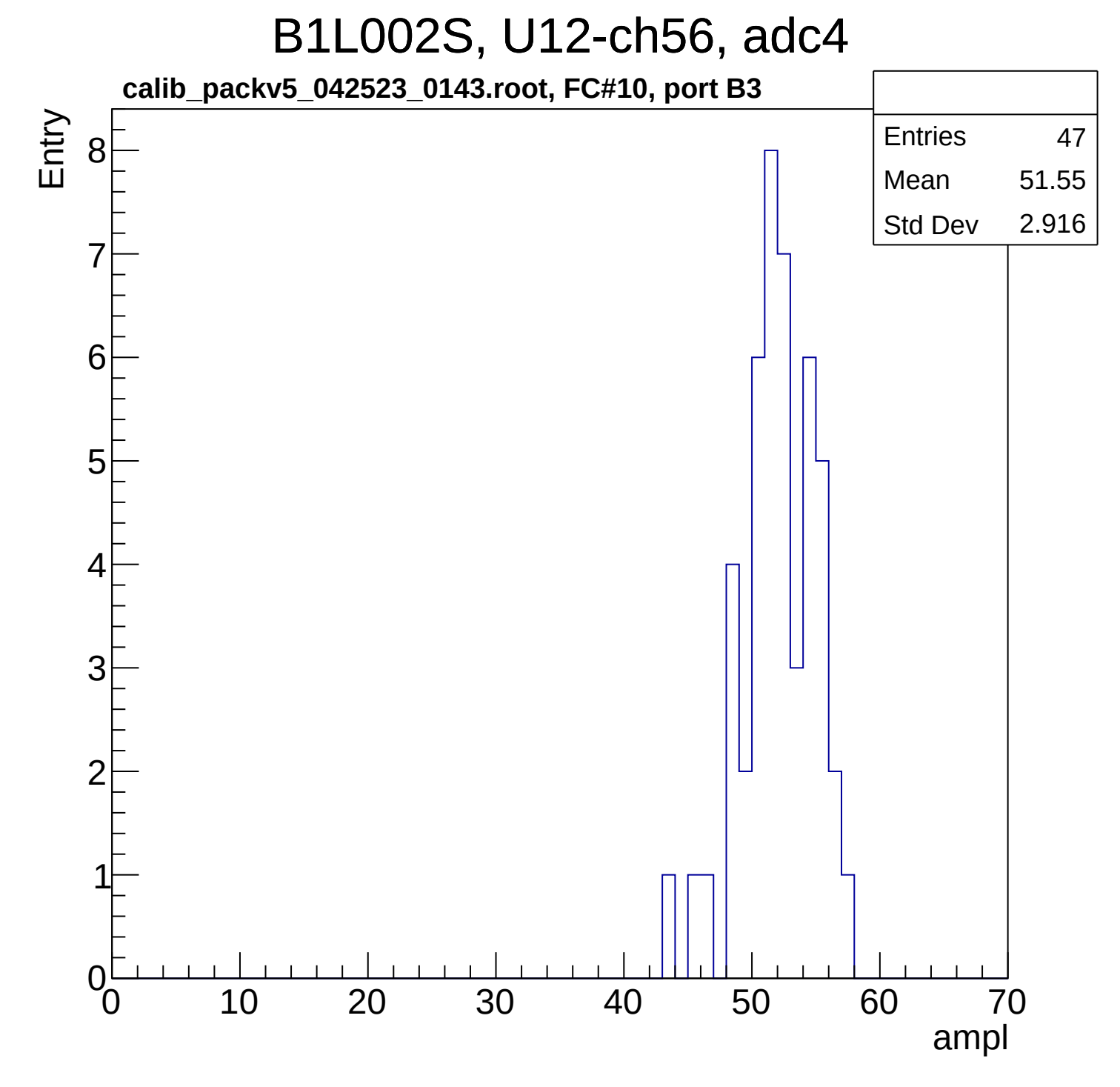
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 51.55
Std Dev 2.916
Entry
ampl
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1
2
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4
5
6
7
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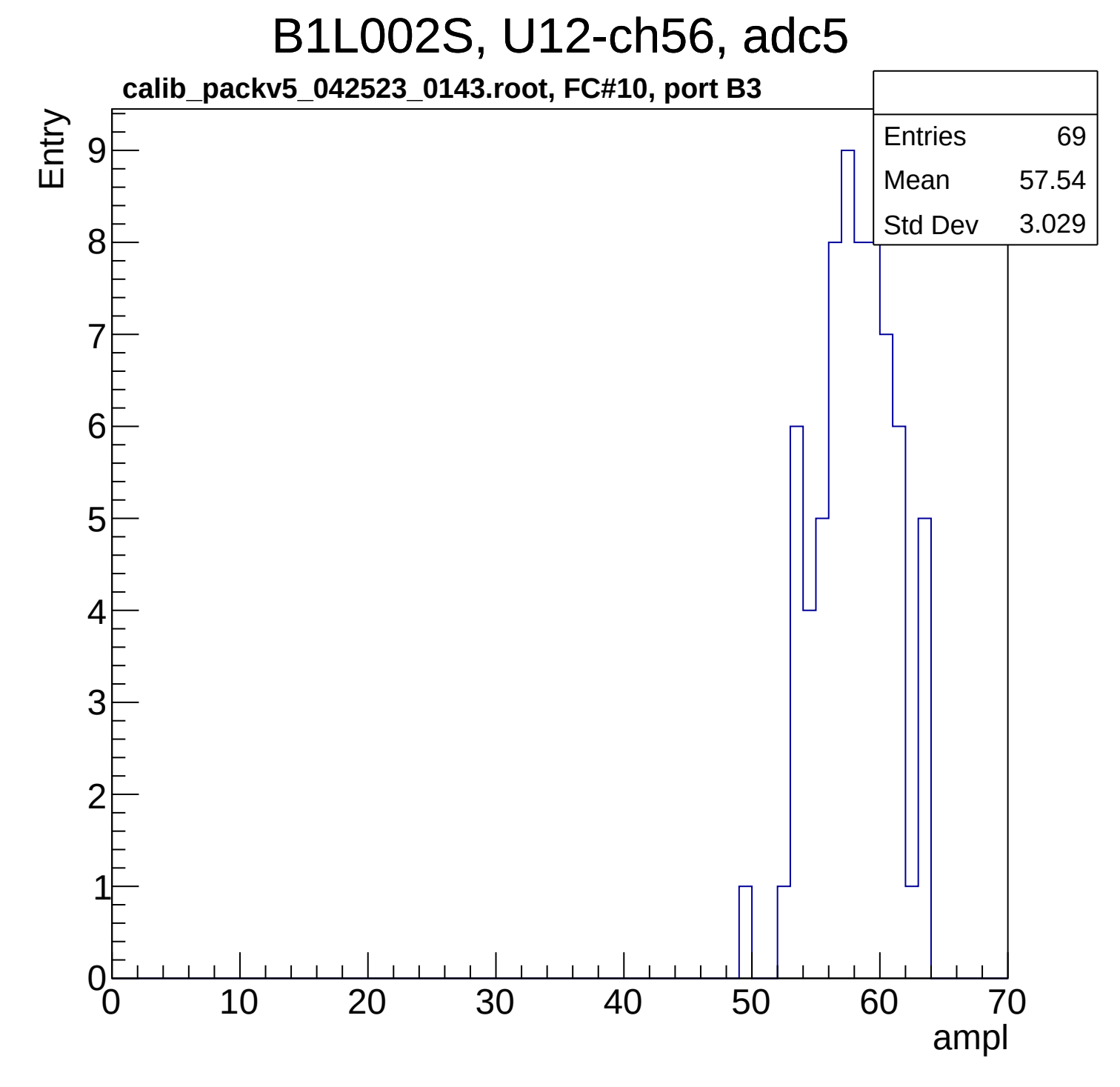
0
10
20
30
40
50
60
70


B1L002S, U12-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.54
Std Dev 3.029
Entry
ampl
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3
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0
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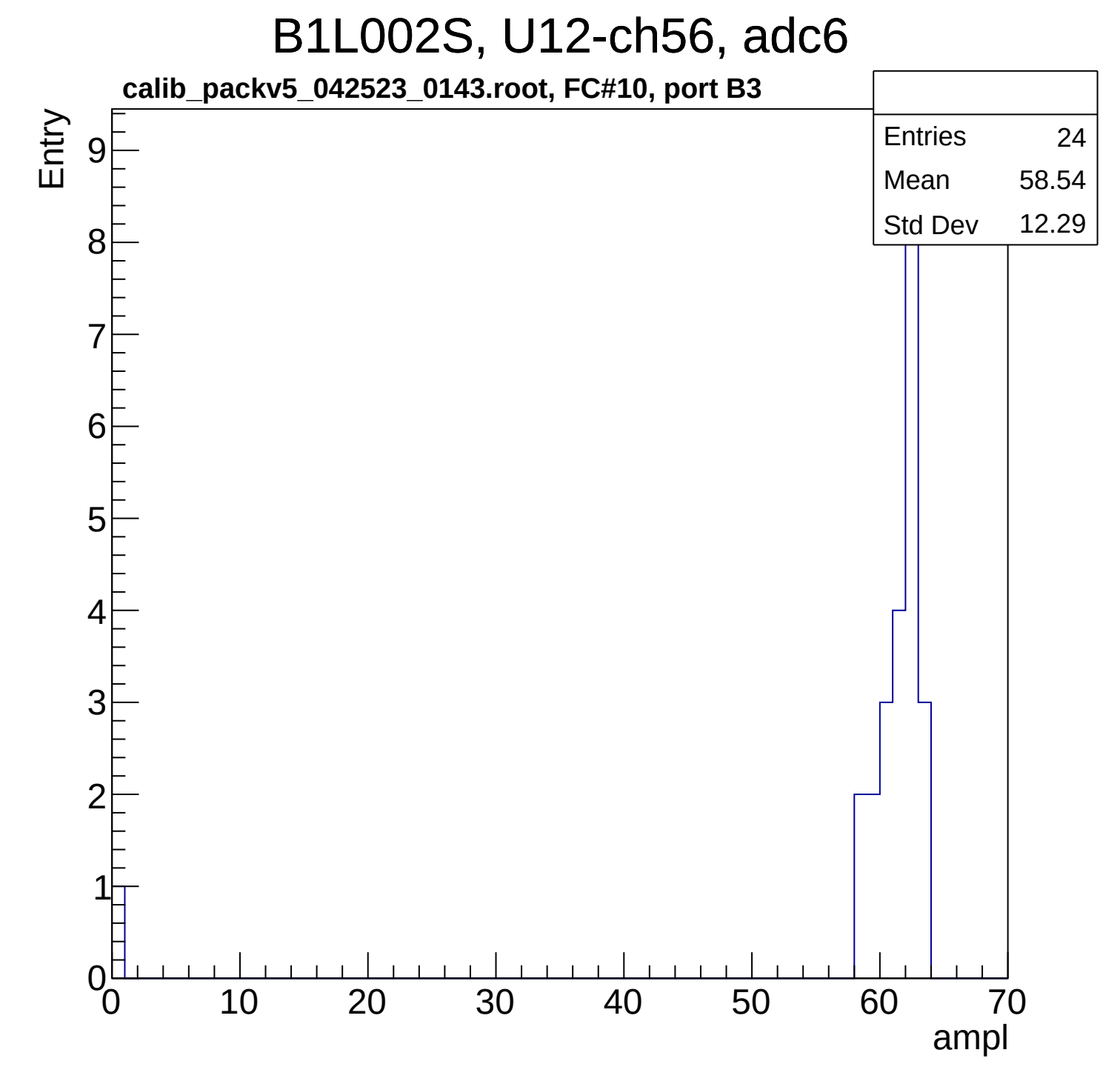
60
70


B1L002S, U12-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.54
Std Dev 12.29
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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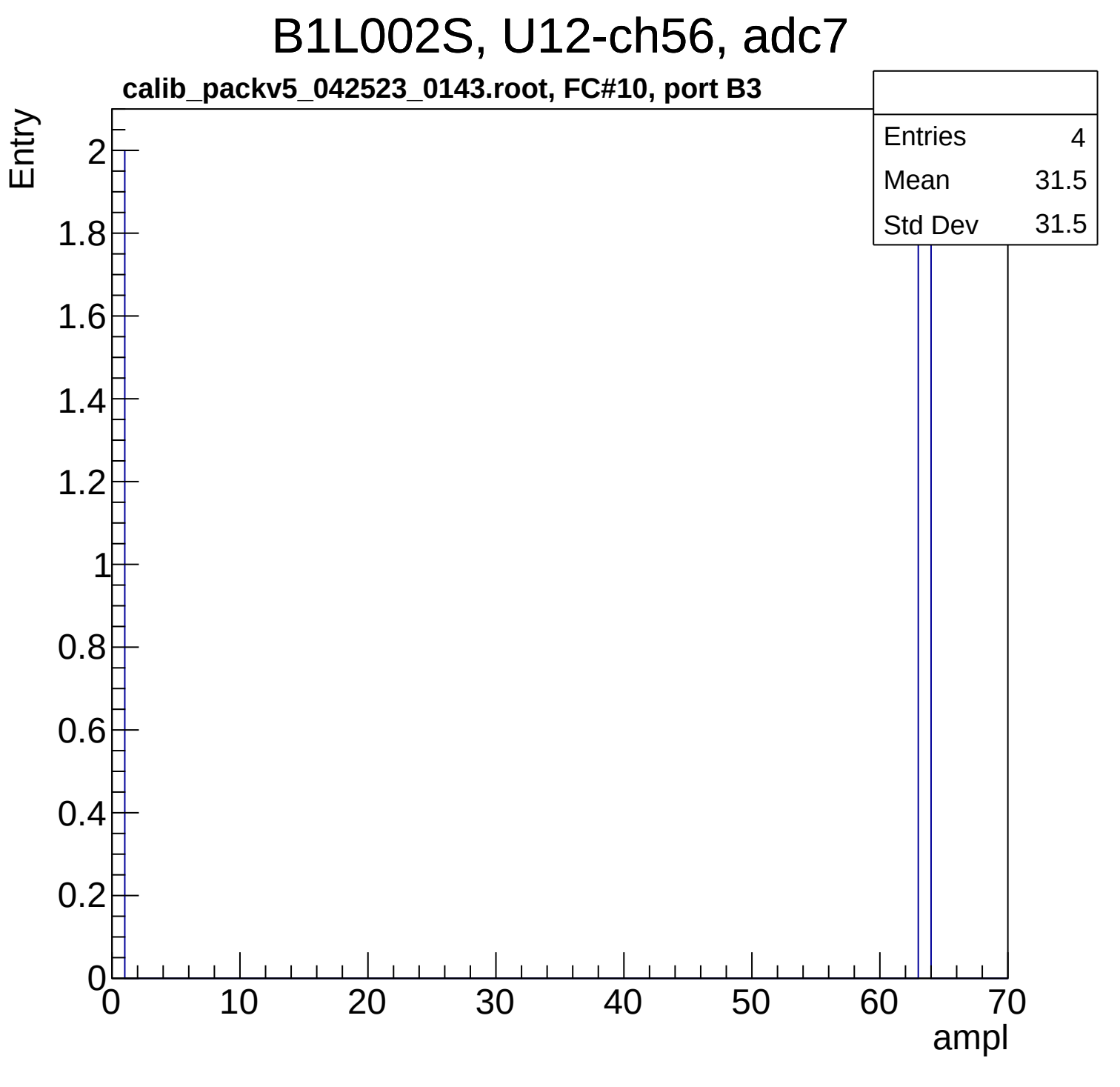
B1L002S, U12-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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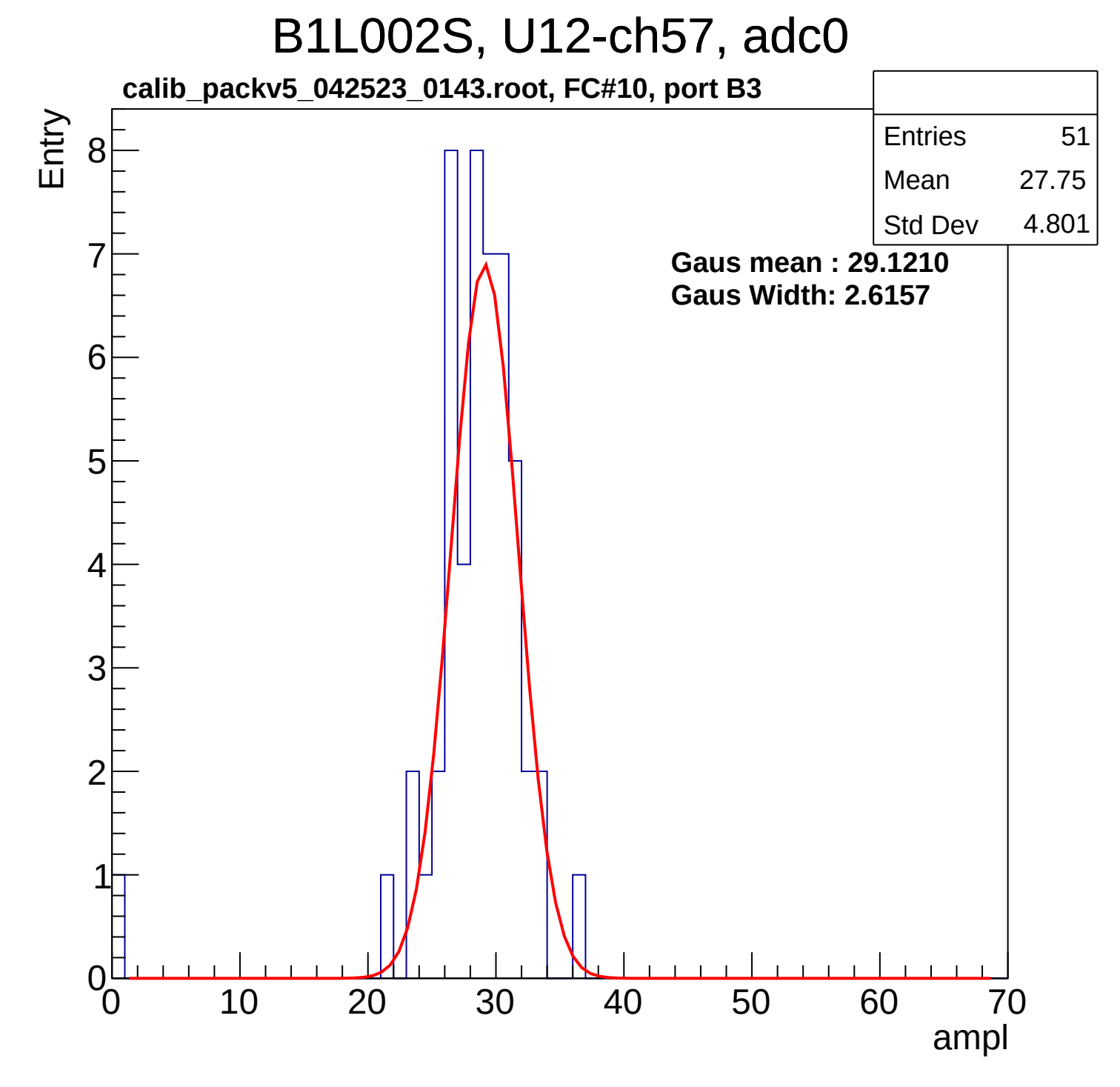
B1L002S, U12-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27.75
Std Dev 4.801
Gaus mean : 29.1210
Gaus Width: 2.6157
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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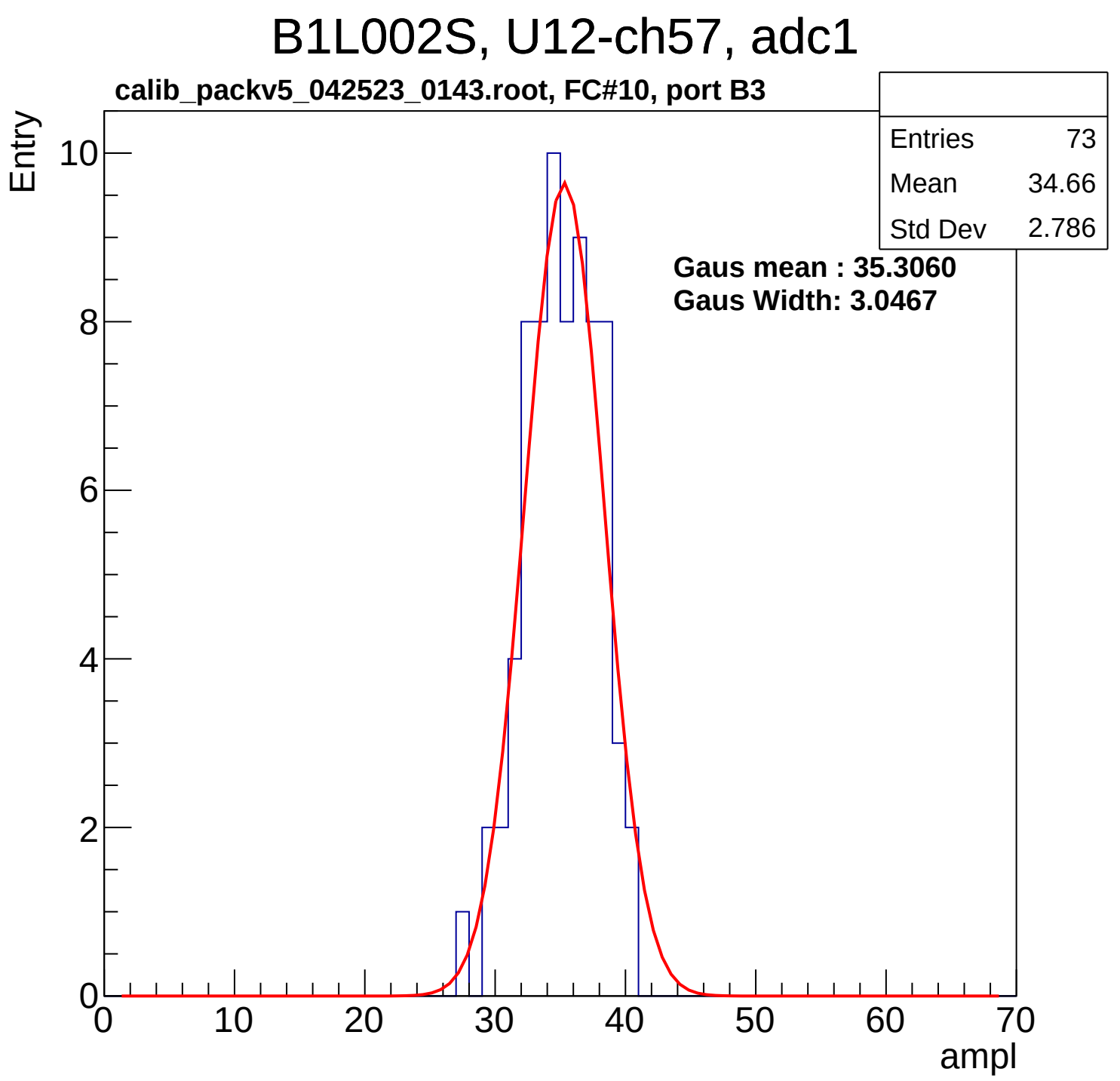
B1L002S, U12-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.66
Std Dev 2.786
Gaus mean : 35.3060
Gaus Width: 3.0467
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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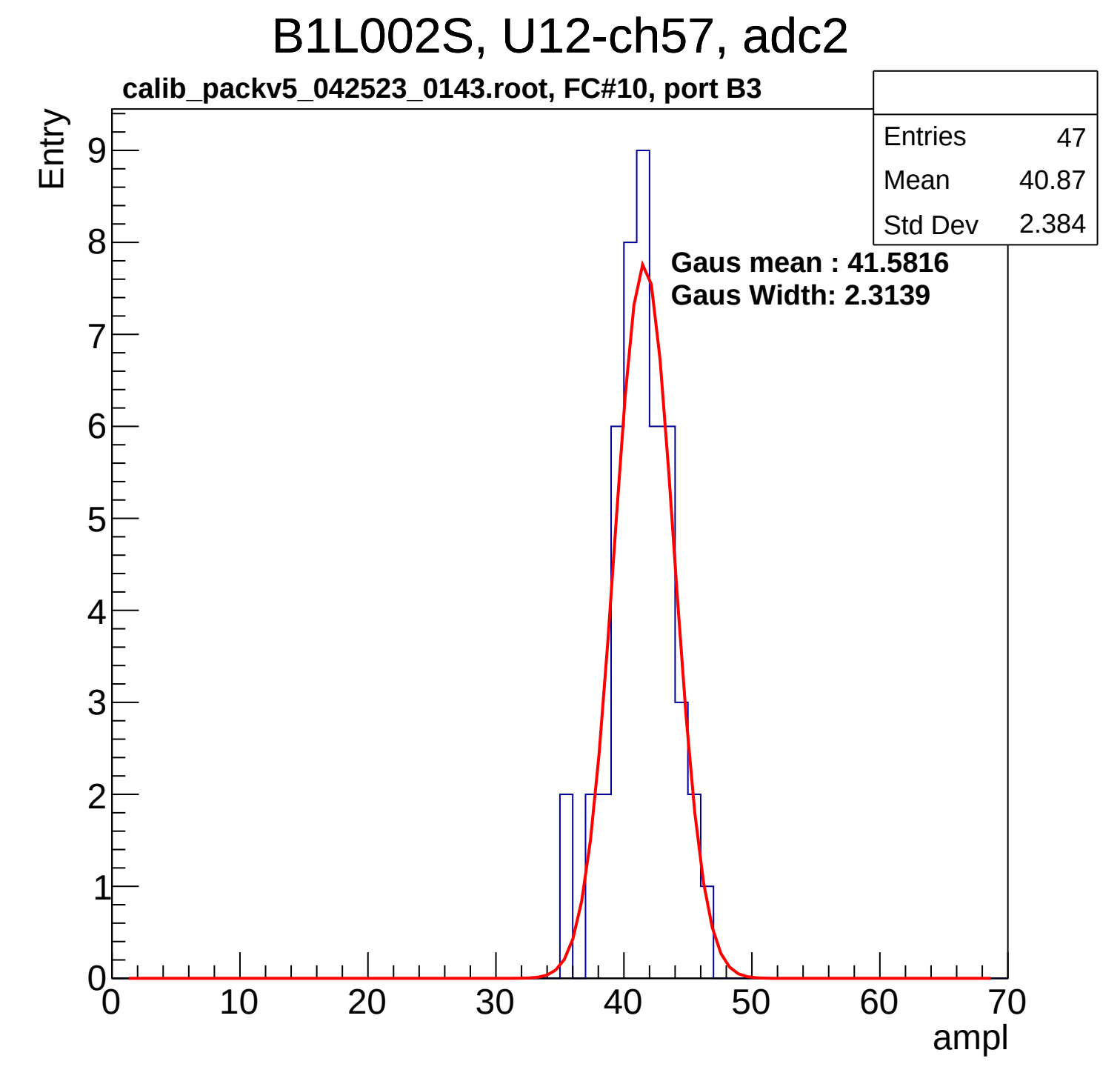
B1L002S, U12-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.87
Std Dev 2.384
Gaus mean : 41.5816
Gaus Width: 2.3139
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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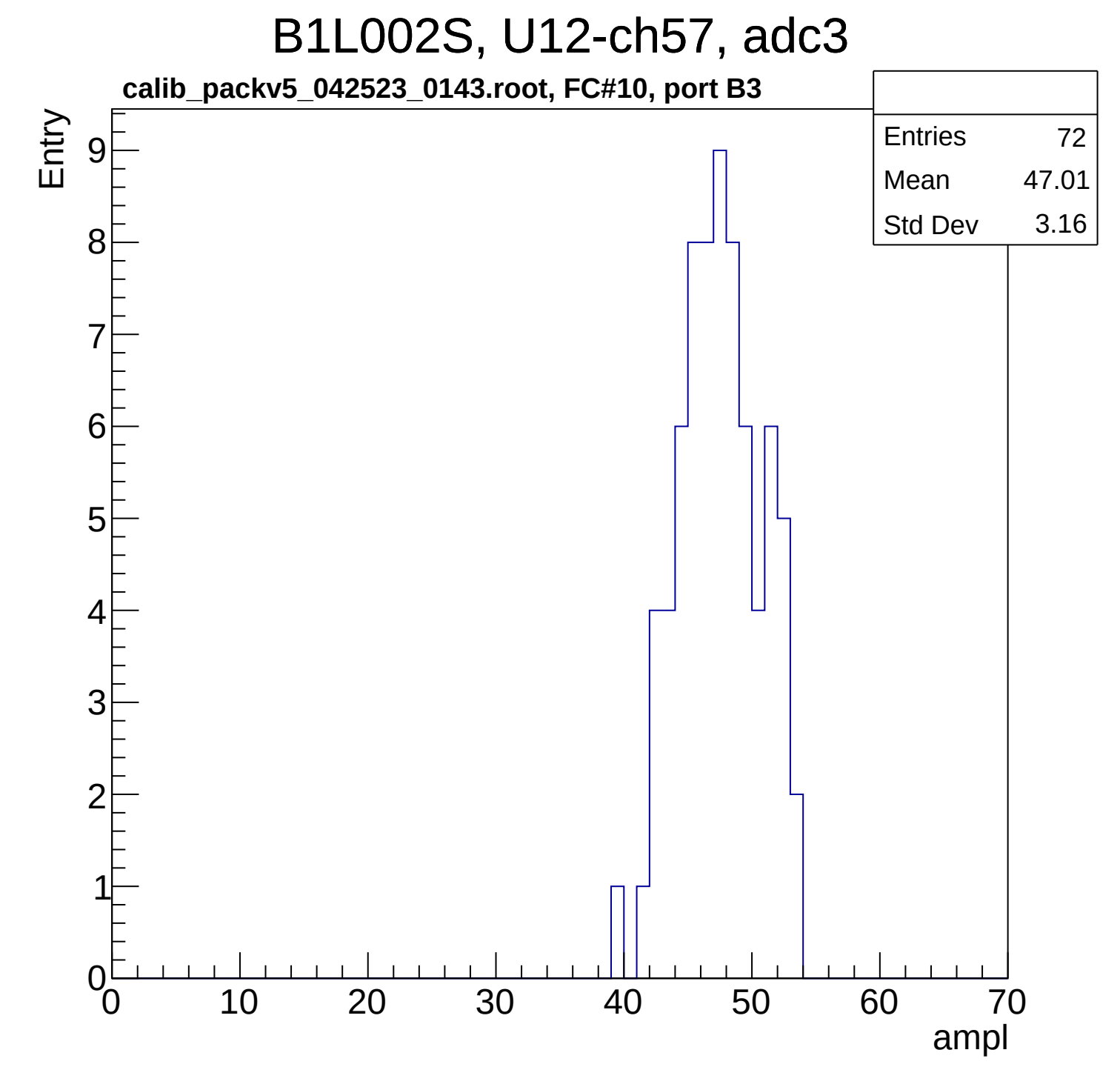
B1L002S, U12-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.01
Std Dev 3.16
Entry
ampl
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2
3
4
5
6
7
8
9
0
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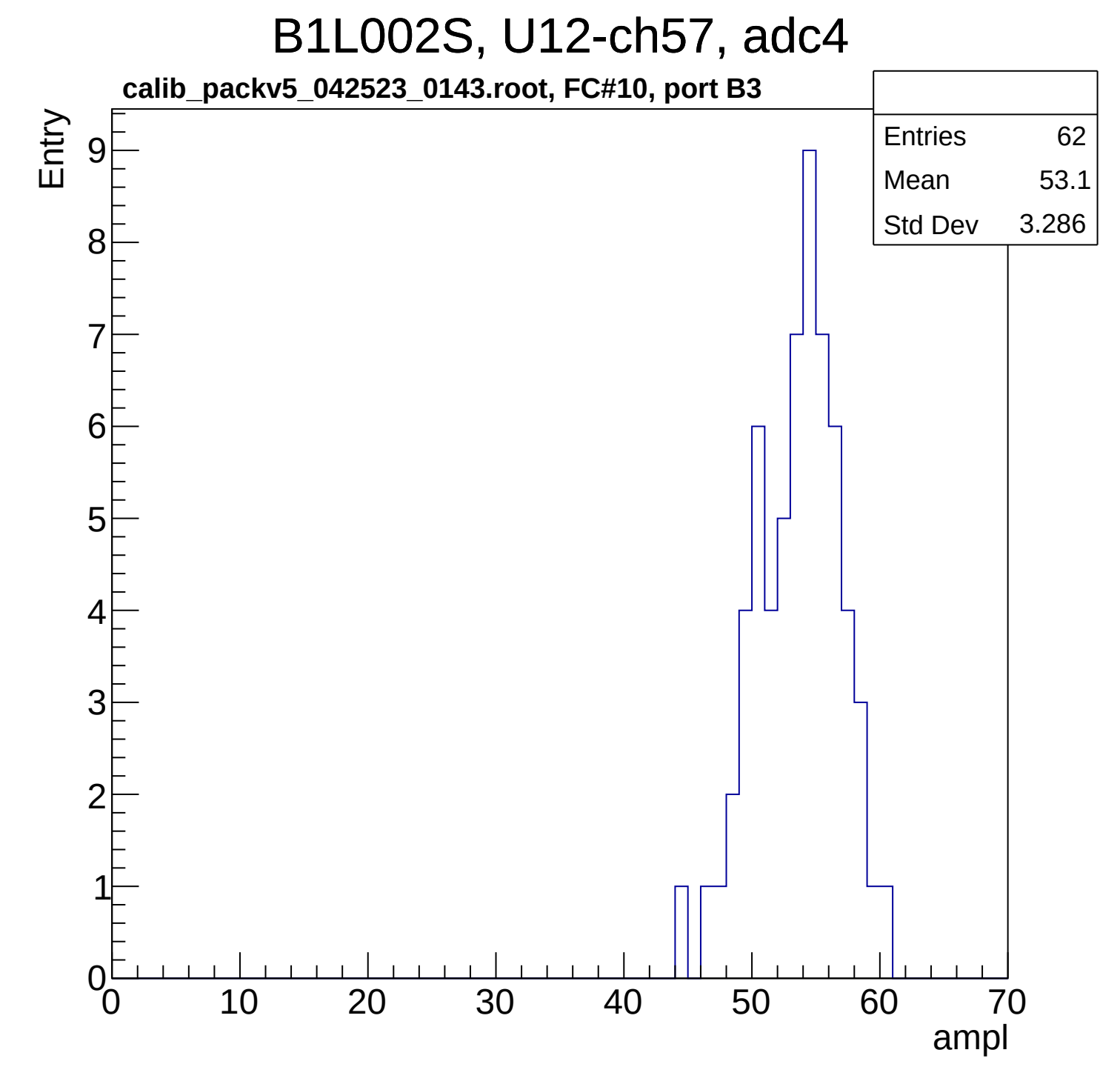
B1L002S, U12-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.1
Std Dev 3.286
Entry
ampl
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4
5
6
7
8
9
0
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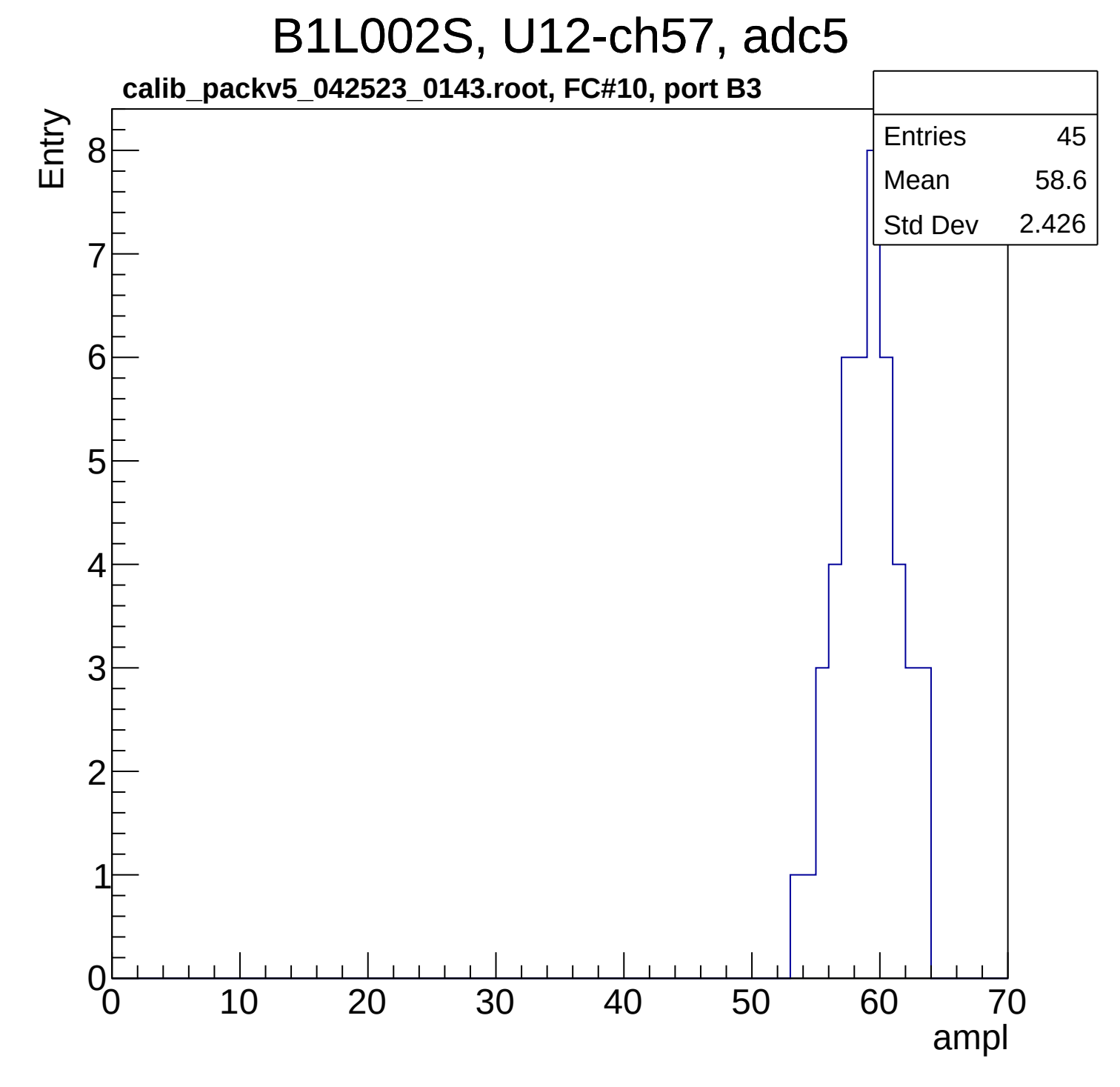
B1L002S, U12-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.6
Std Dev 2.426
Entry
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8
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30
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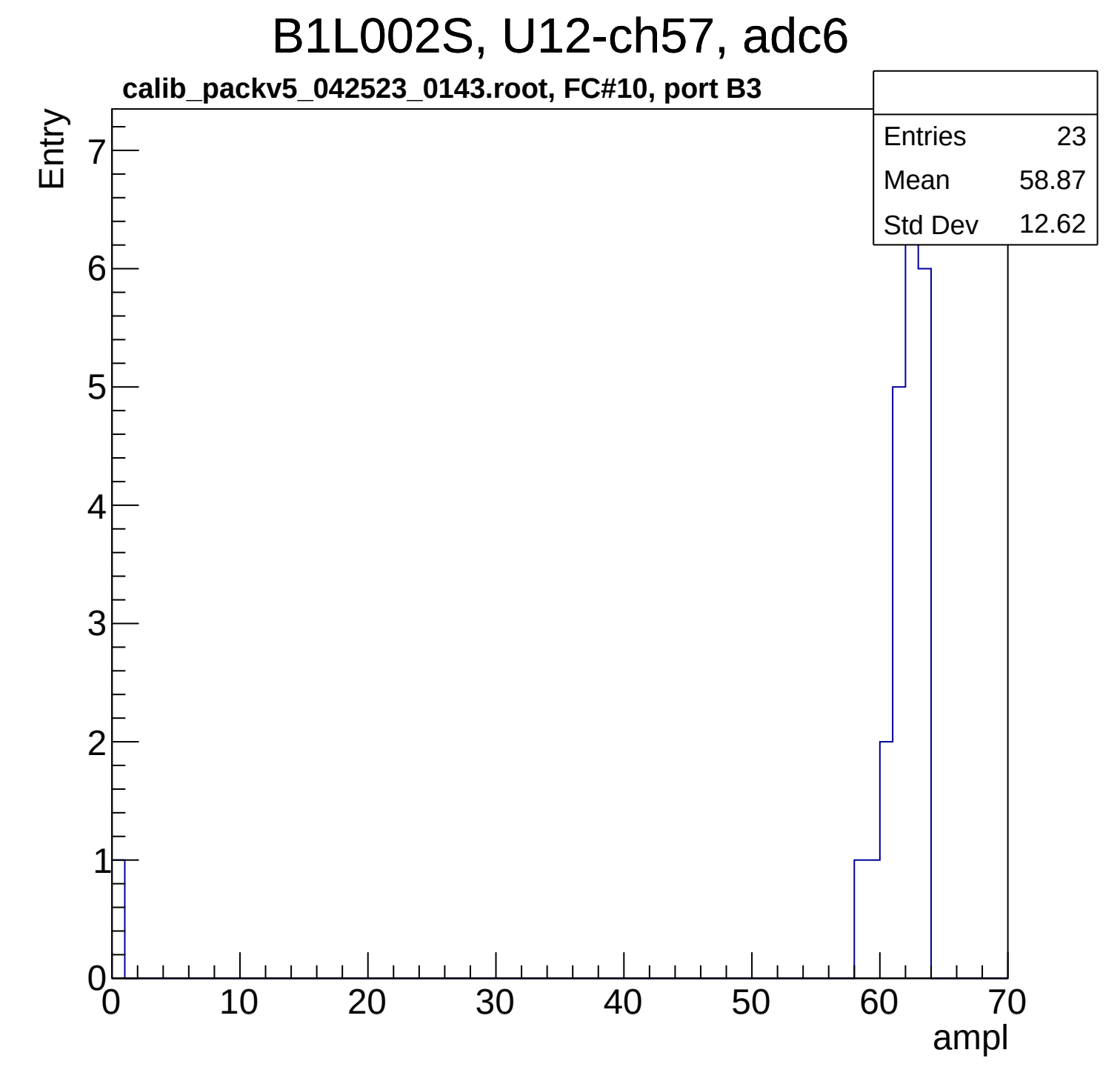
50
60
70
ampl


B1L002S, U12-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.87
Std Dev 12.62
Entry
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5
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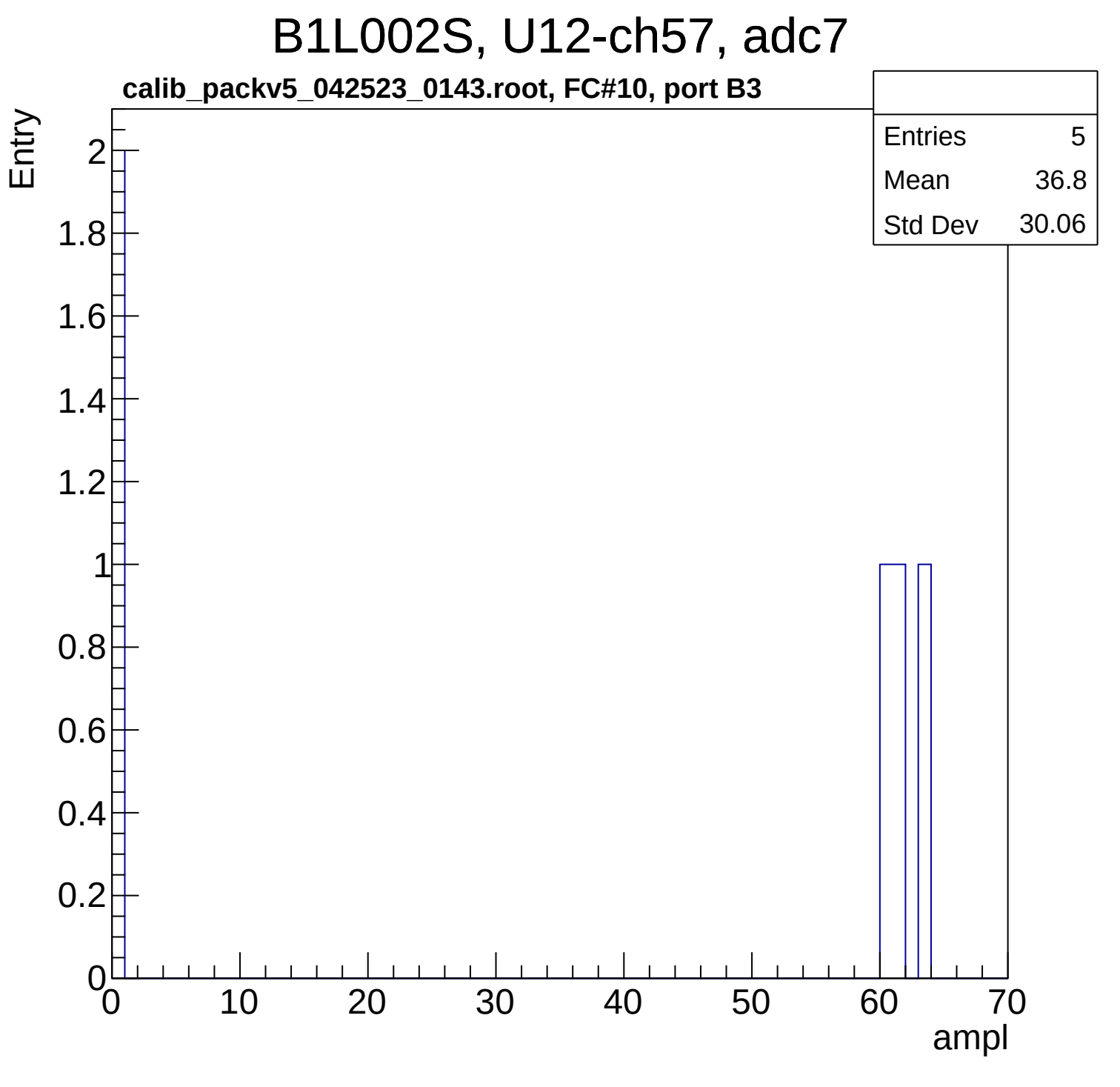
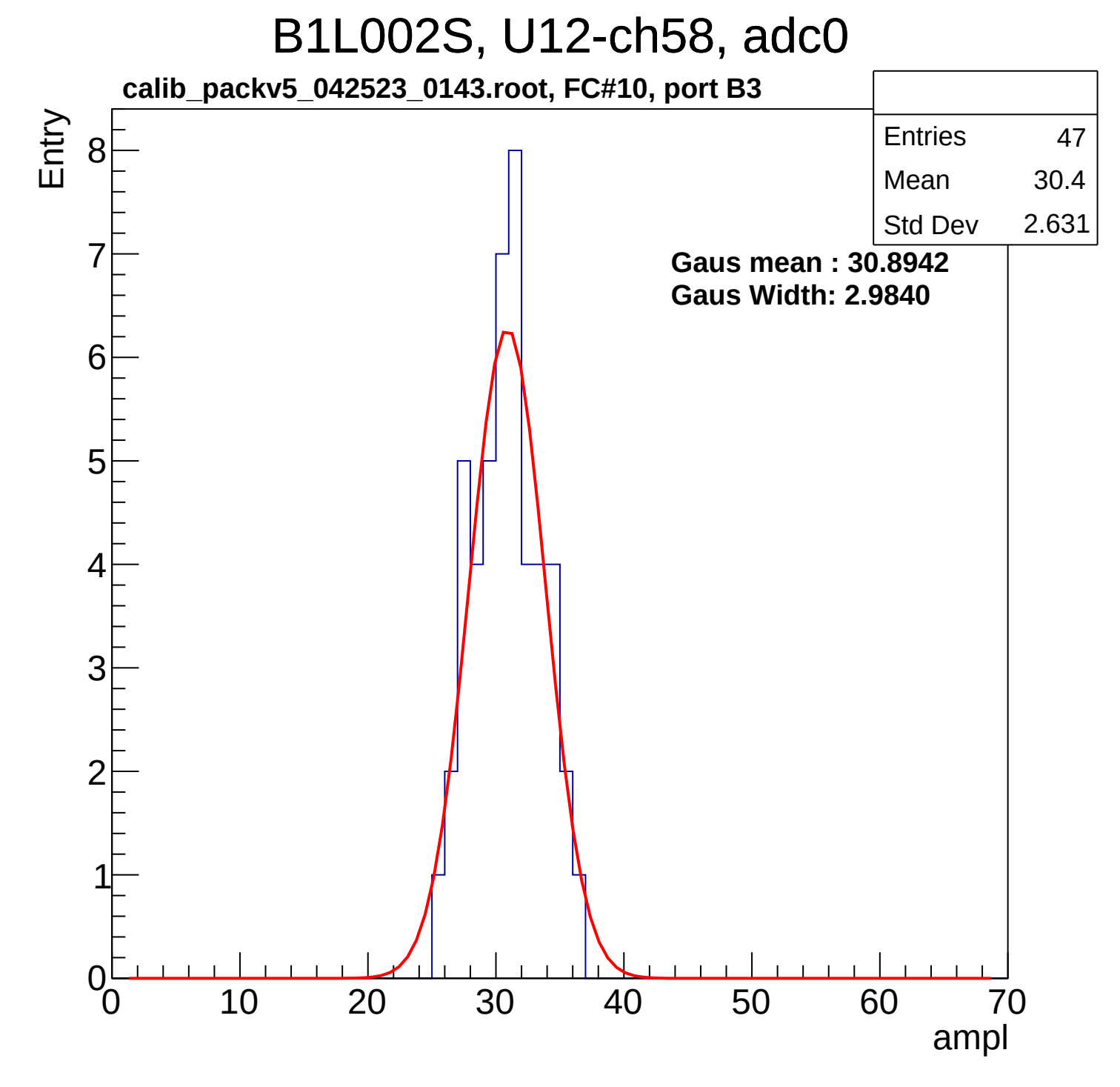
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 36.8
Std Dev 30.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 30.4
Std Dev 2.631
Gaus mean : 30.8942
Gaus Width: 2.9840
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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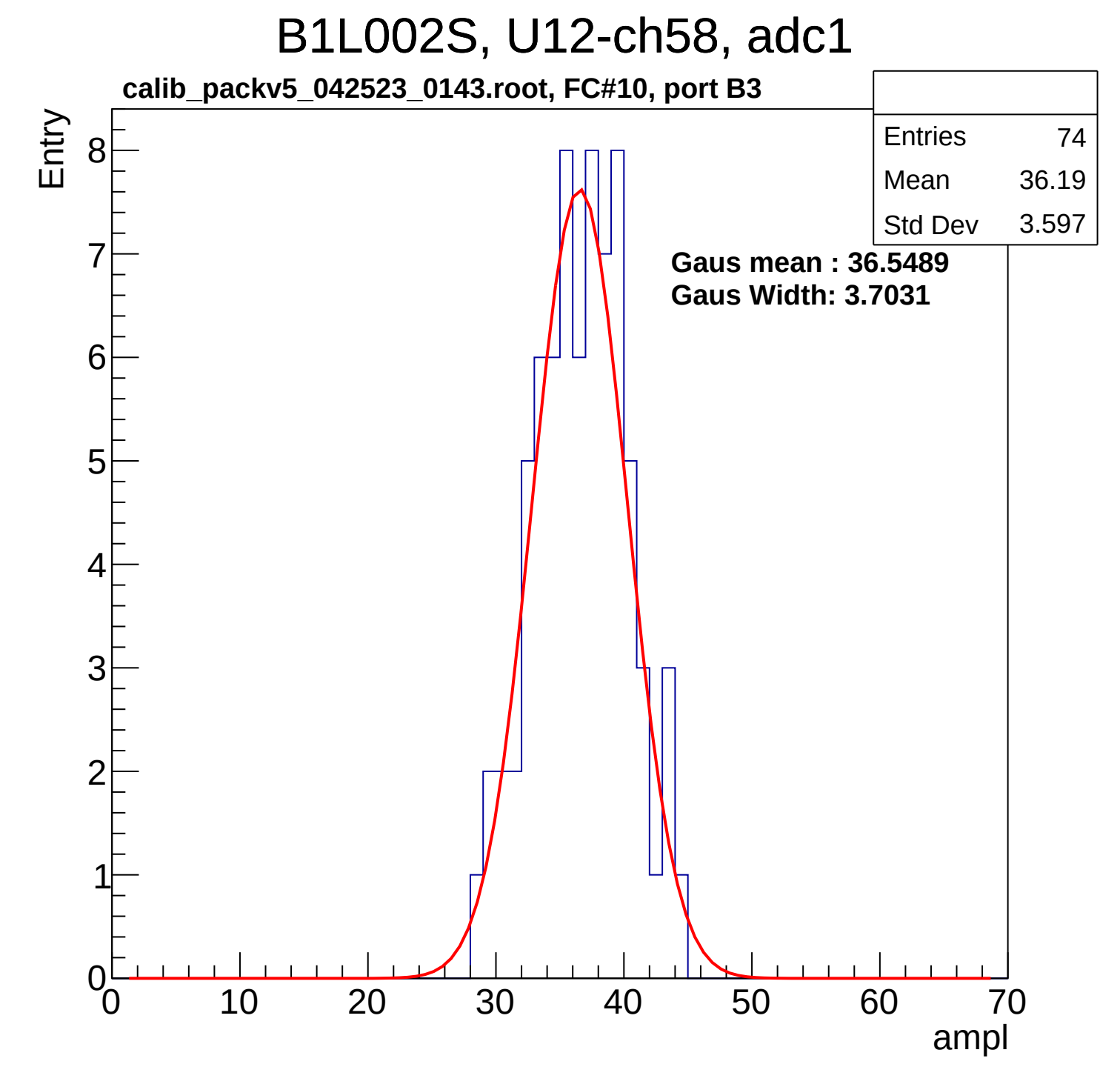
B1L002S, U12-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 36.19
Std Dev 3.597
Gaus mean : 36.5489
Gaus Width: 3.7031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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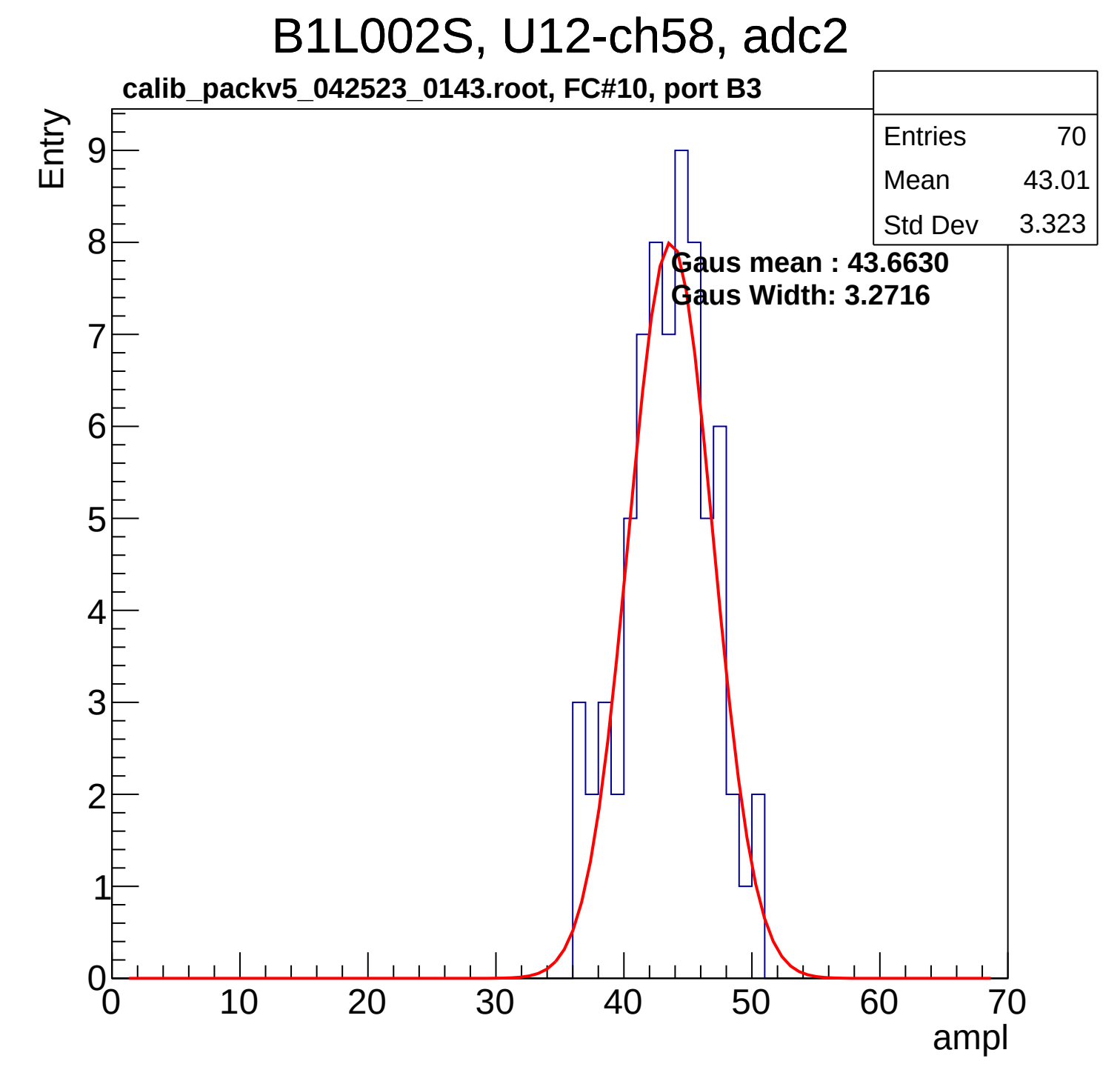
B1L002S, U12-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 43.01
Std Dev 3.323
Gaus mean : 43.6630
Gaus Width: 3.2716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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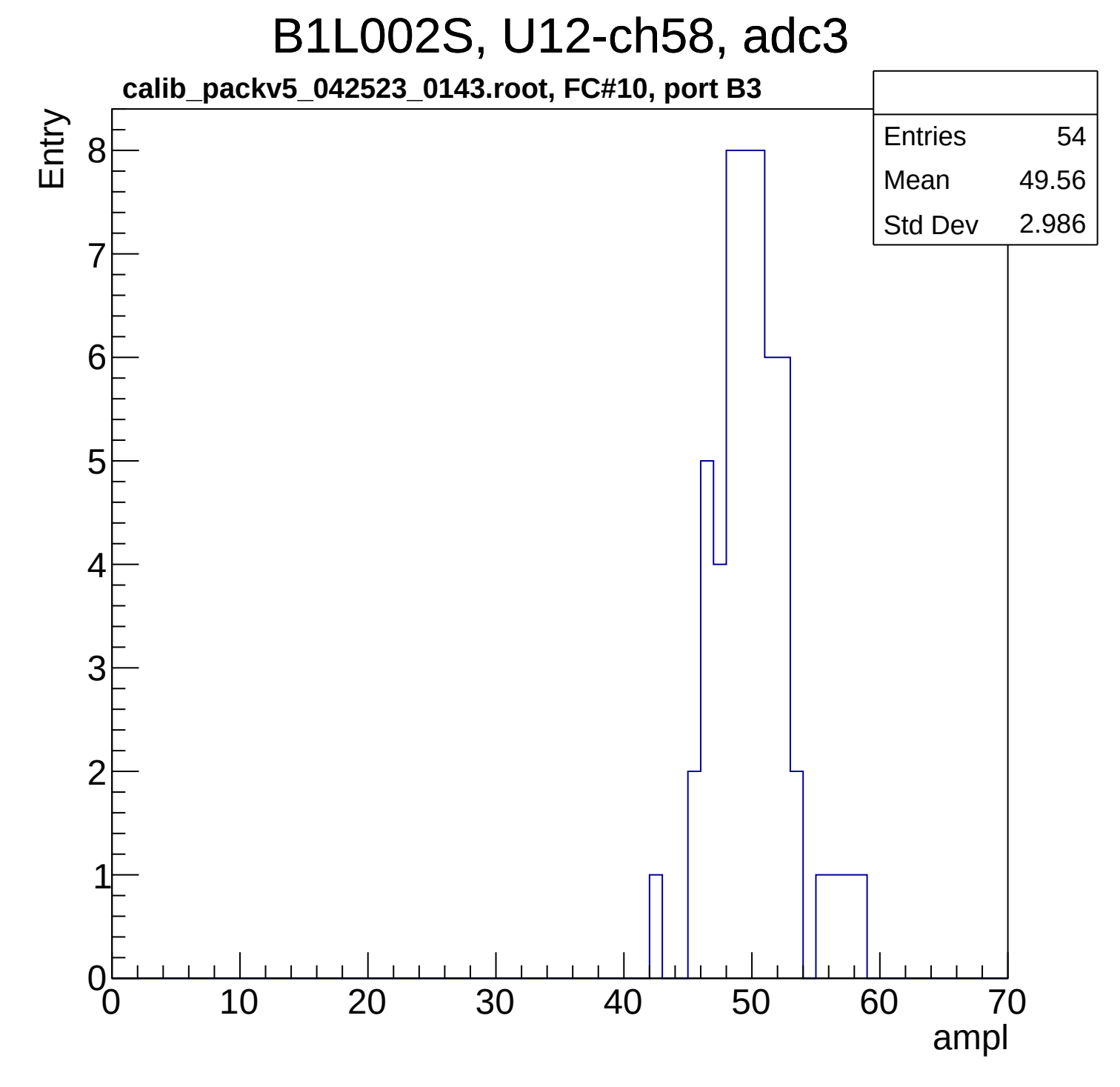
B1L002S, U12-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 49.56
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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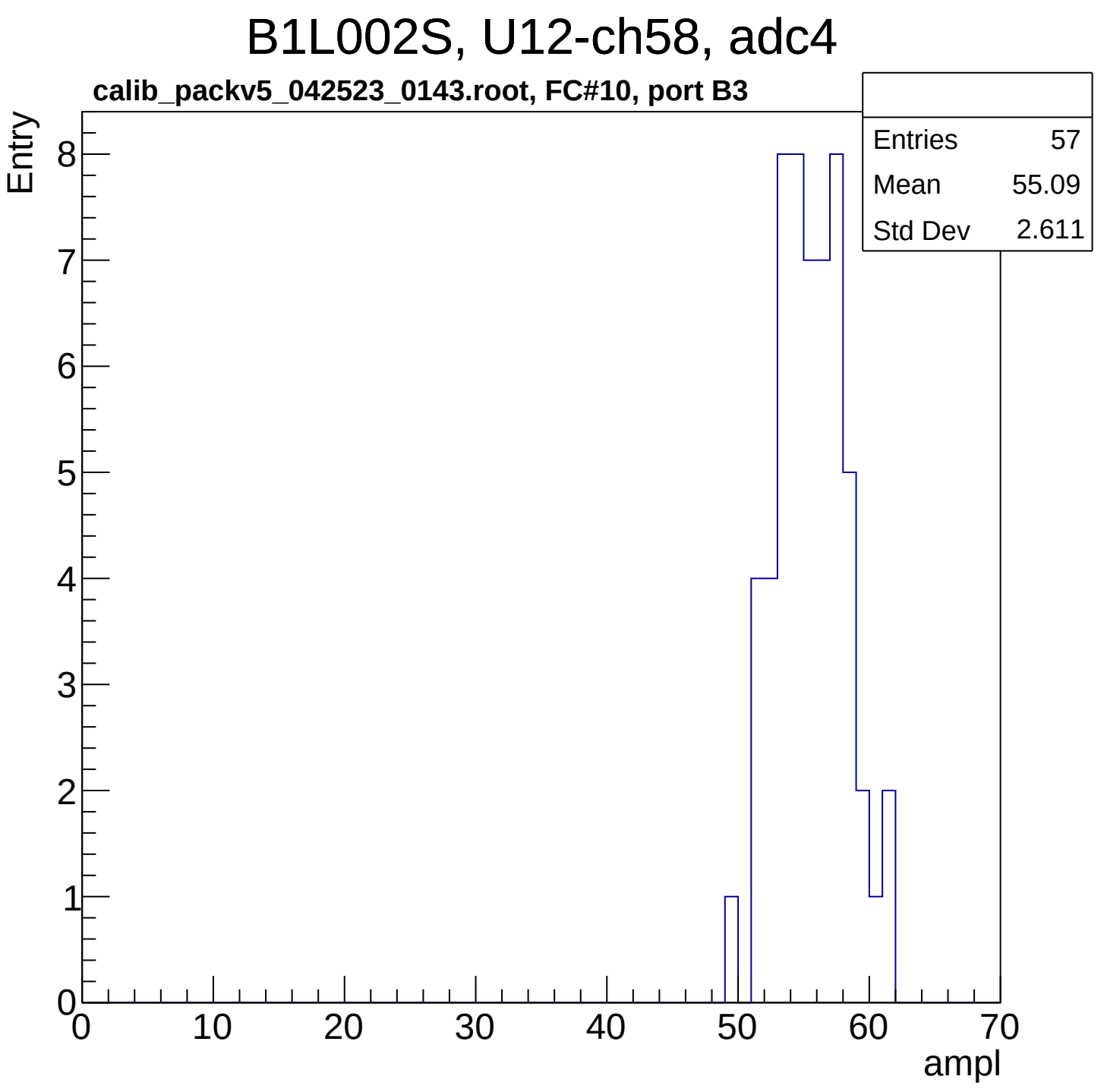
B1L002S, U12-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 55.09
Std Dev 2.611
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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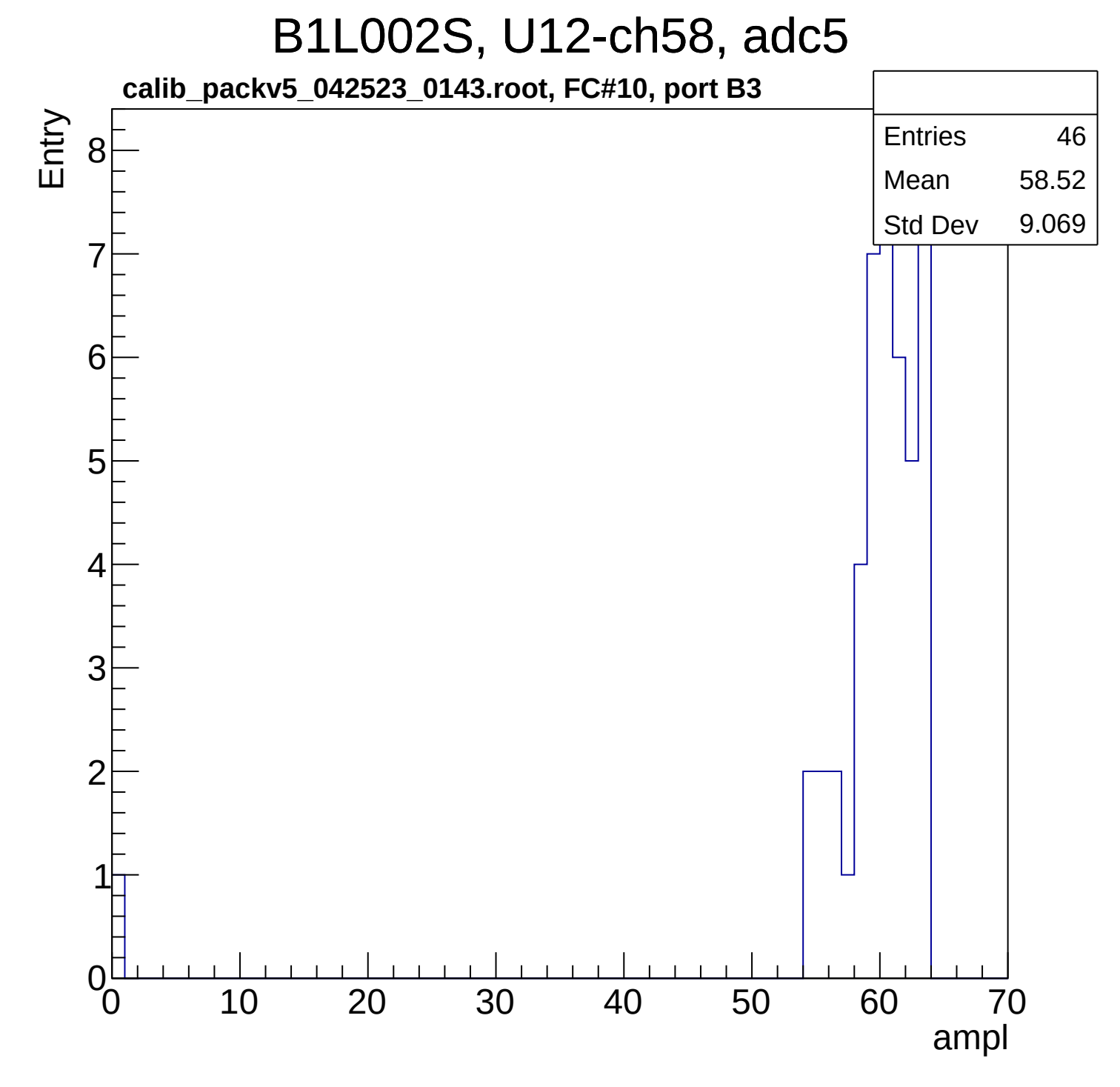
B1L002S, U12-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.52
Std Dev 9.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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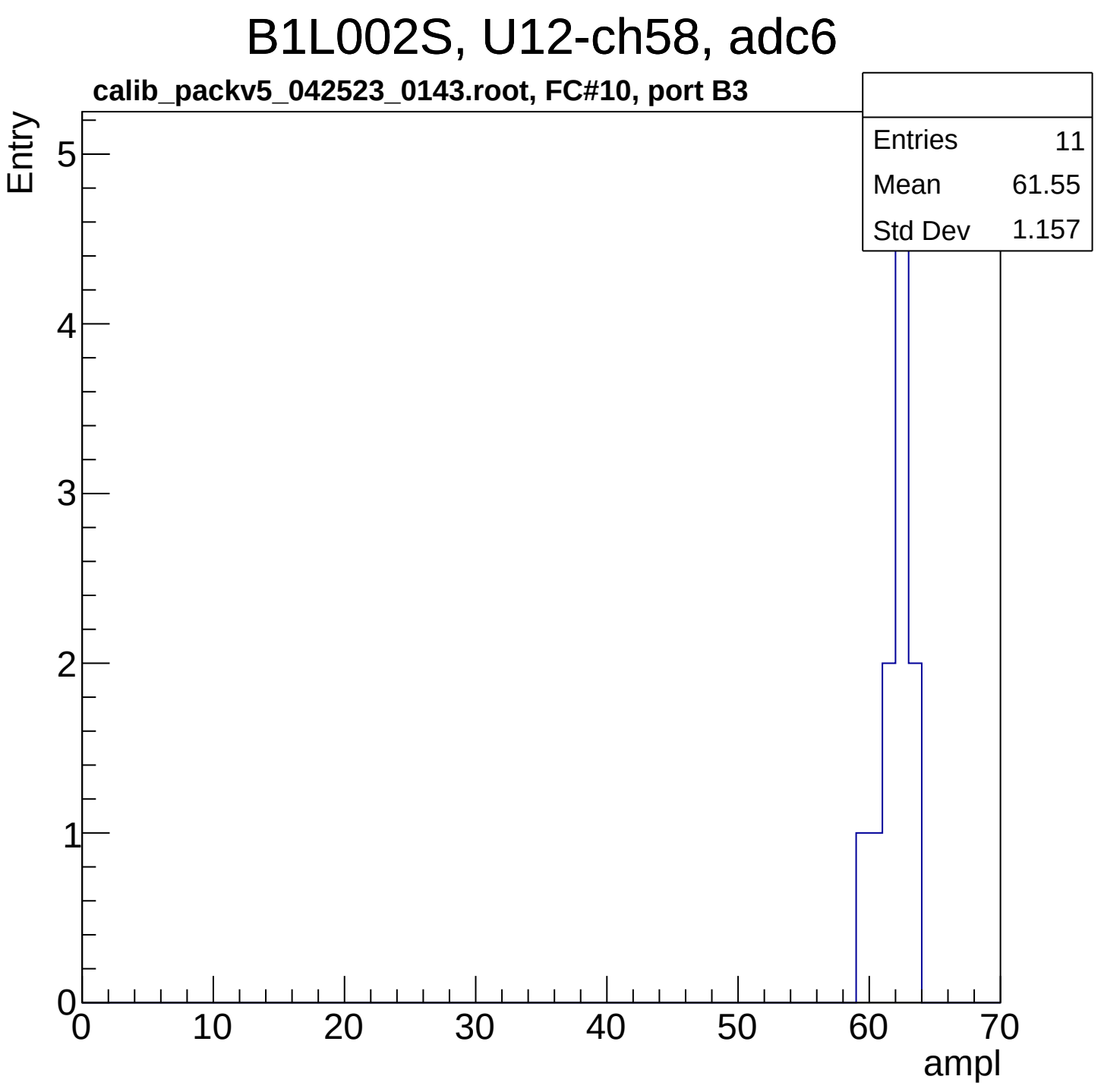
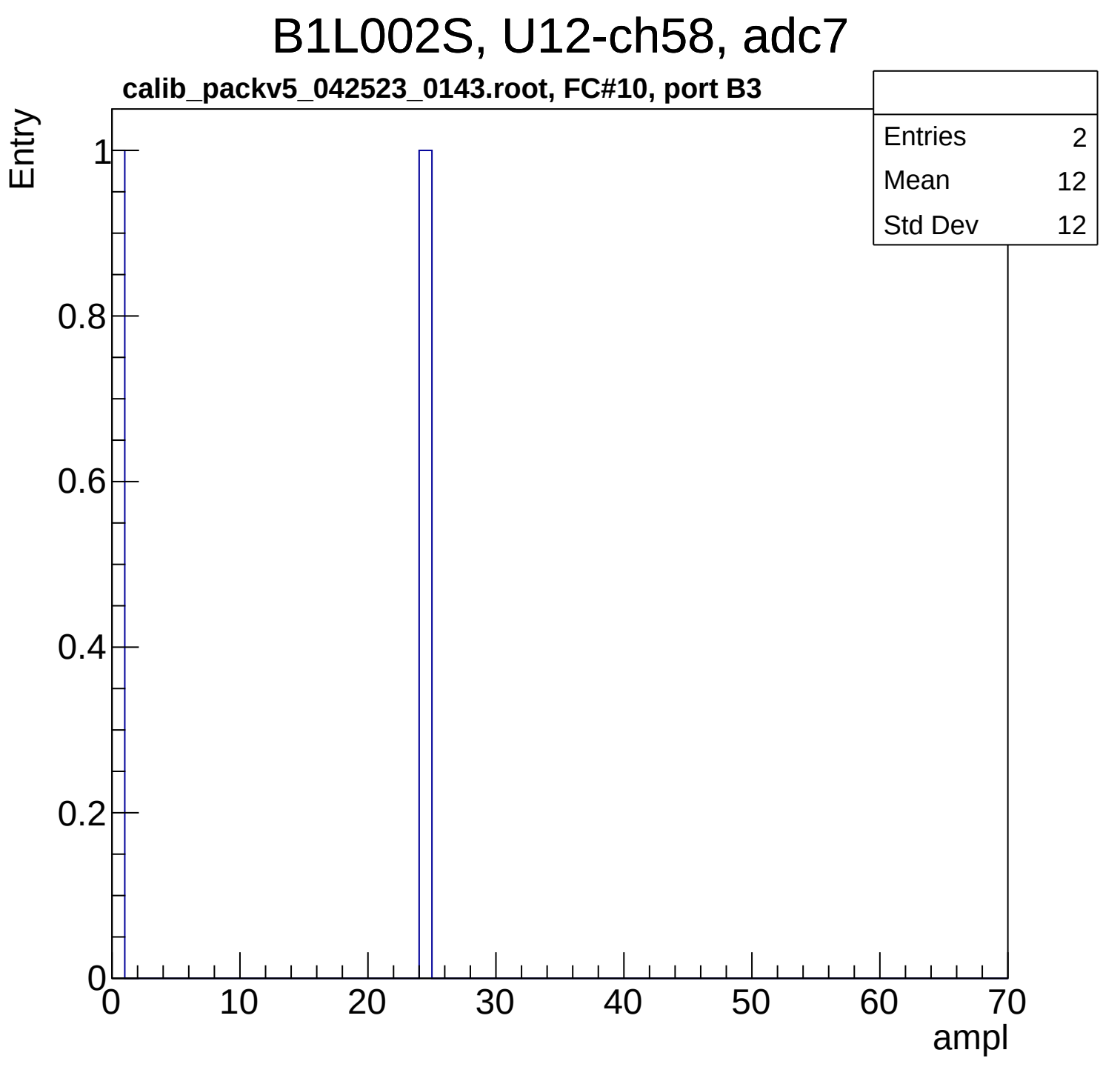
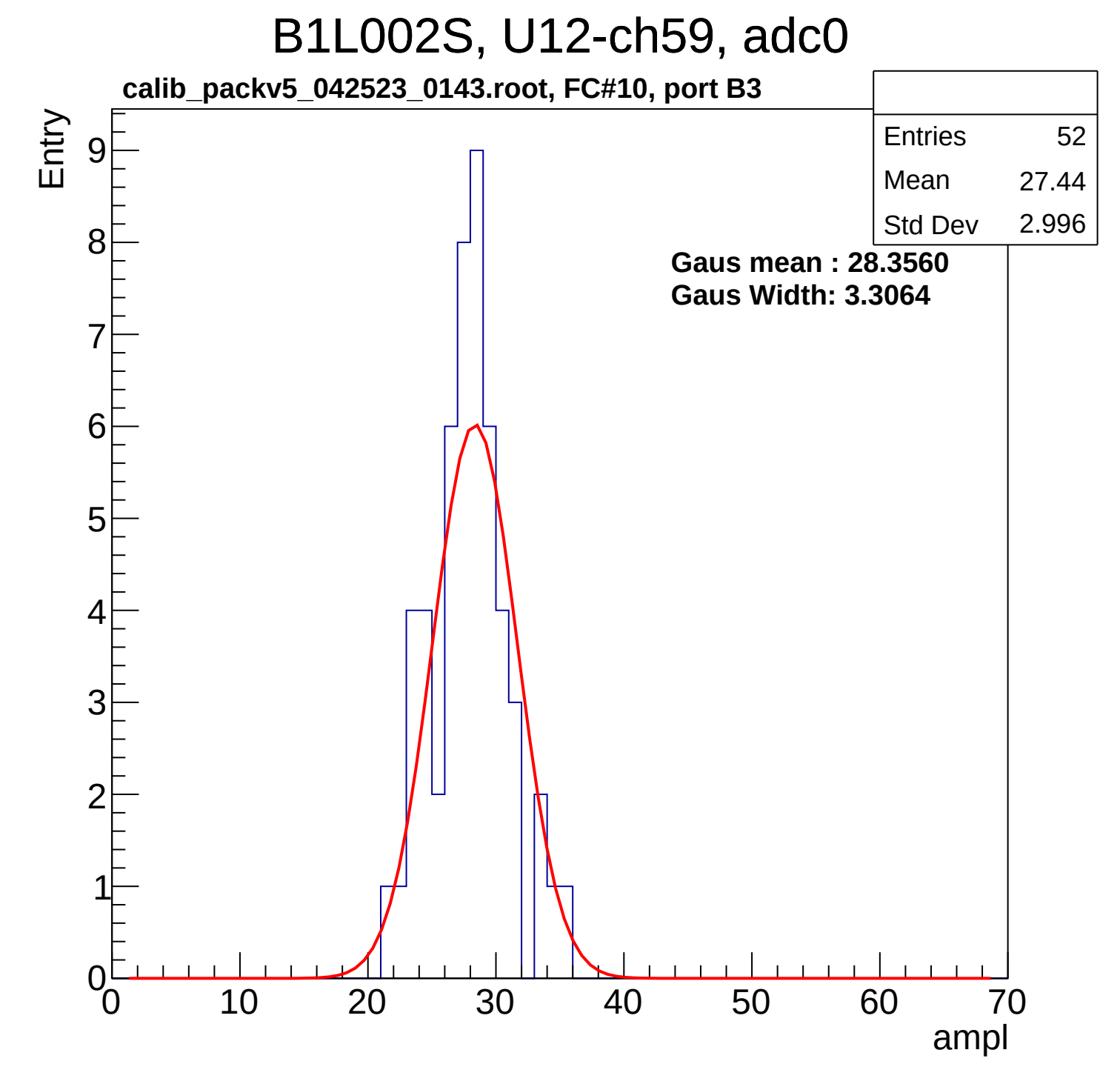
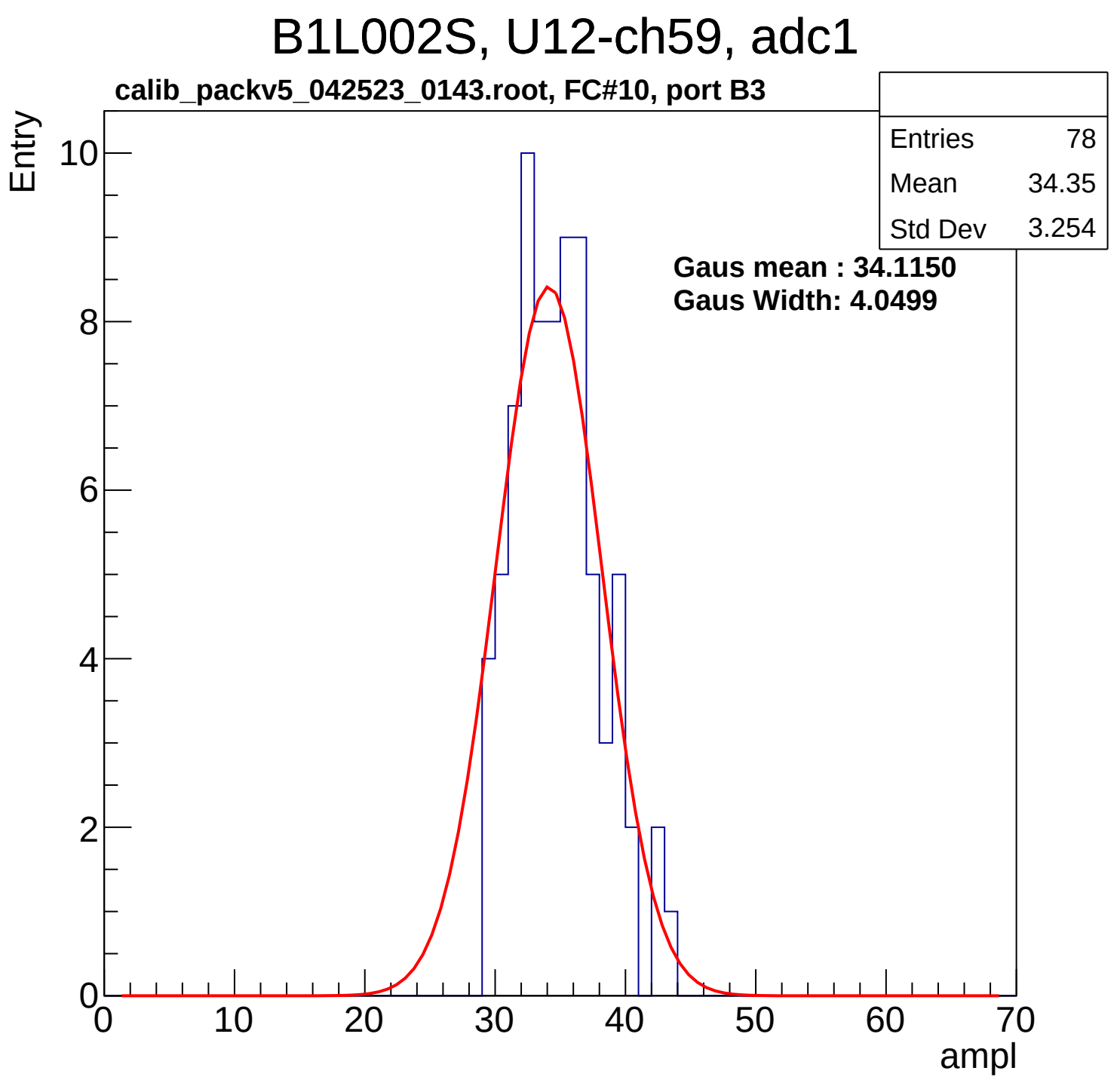

B1L002S, U12-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.55
Std Dev 1.157
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.44
Std Dev 2.996
Gaus mean : 28.3560
Gaus Width: 3.3064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 34.35
Std Dev 3.254
Gaus mean : 34.1150
Gaus Width: 4.0499
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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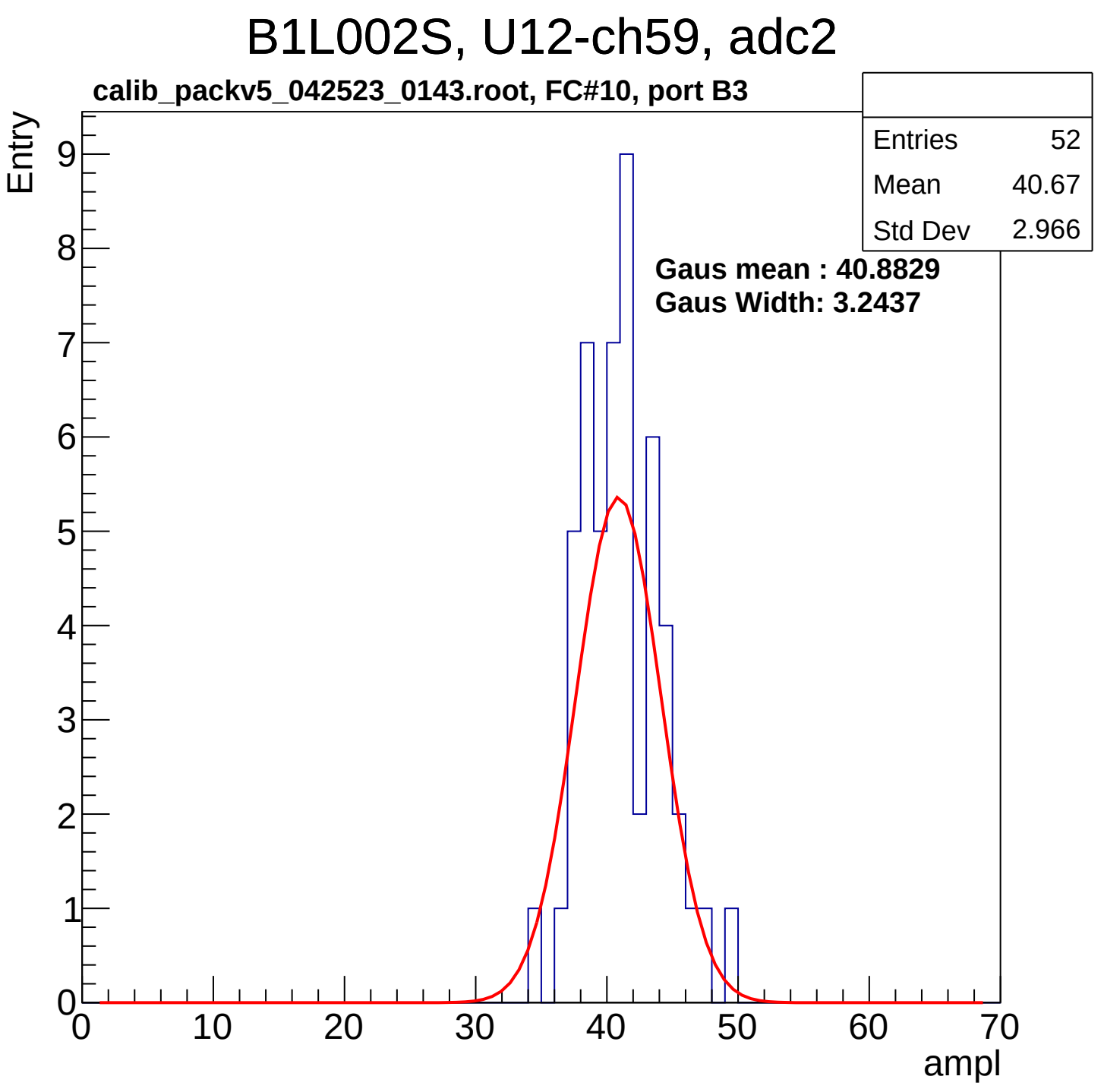
B1L002S, U12-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.67
Std Dev 2.966
Gaus mean : 40.8829
Gaus Width: 3.2437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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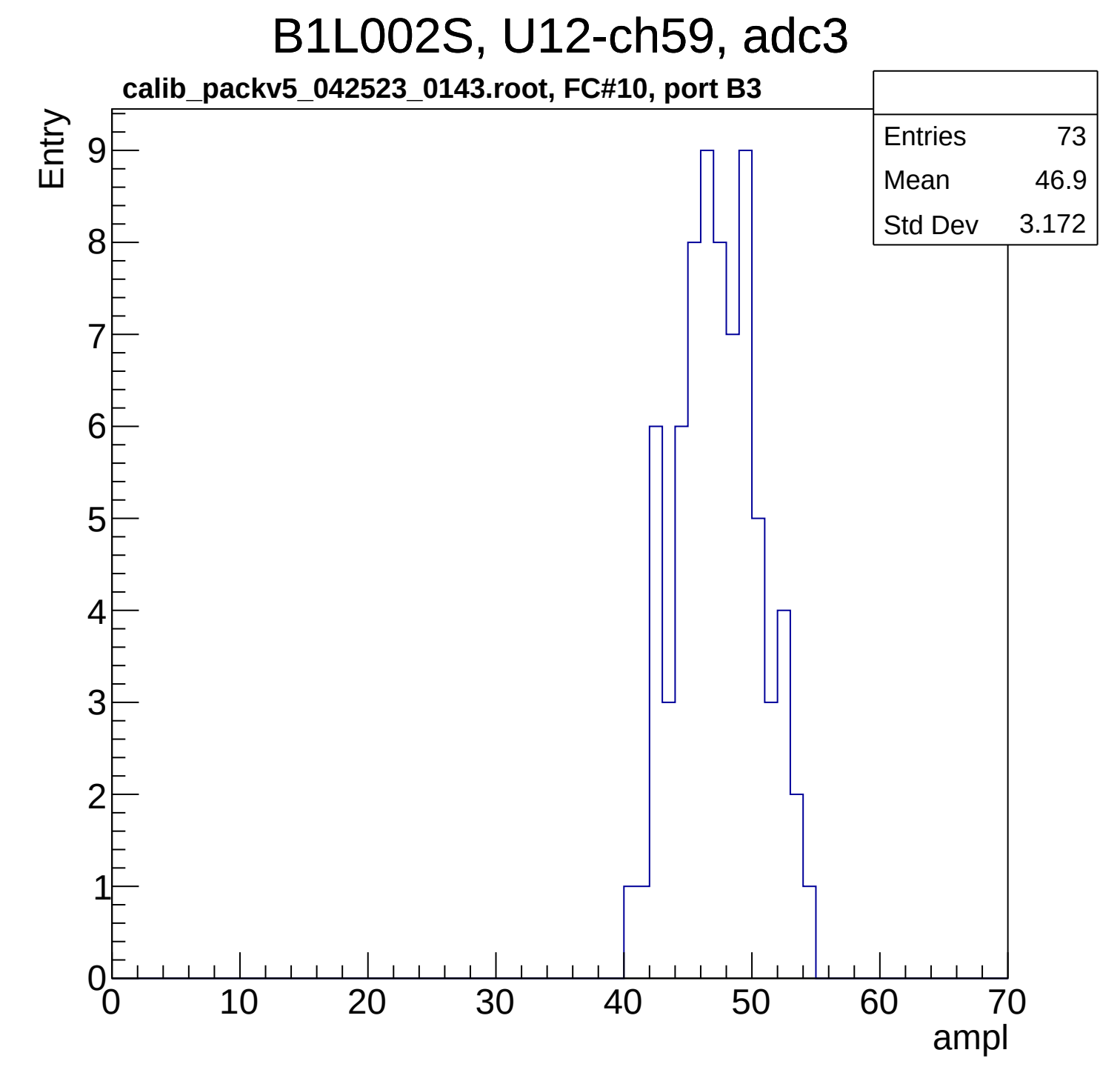
B1L002S, U12-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 46.9
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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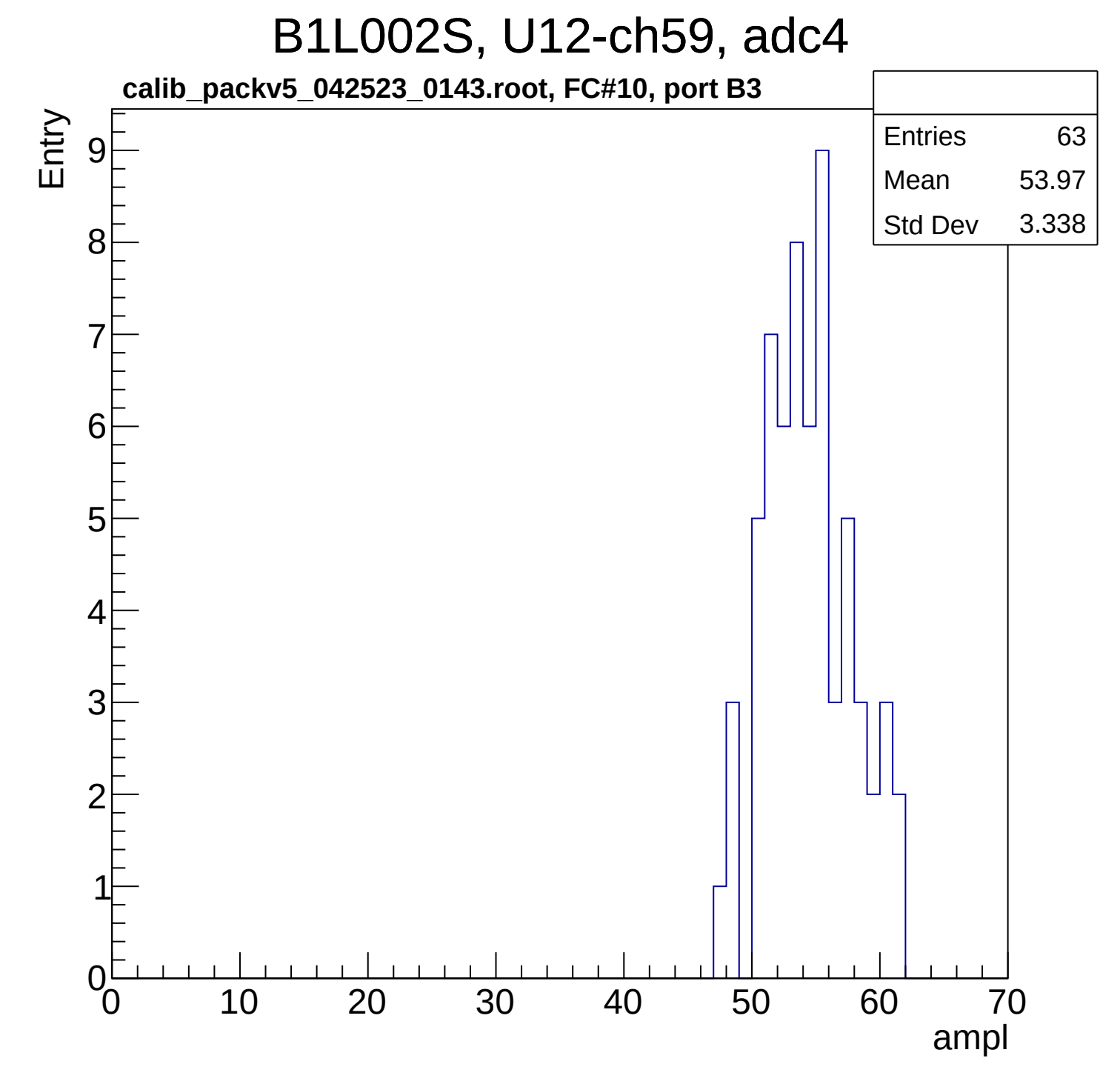
B1L002S, U12-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.97
Std Dev 3.338
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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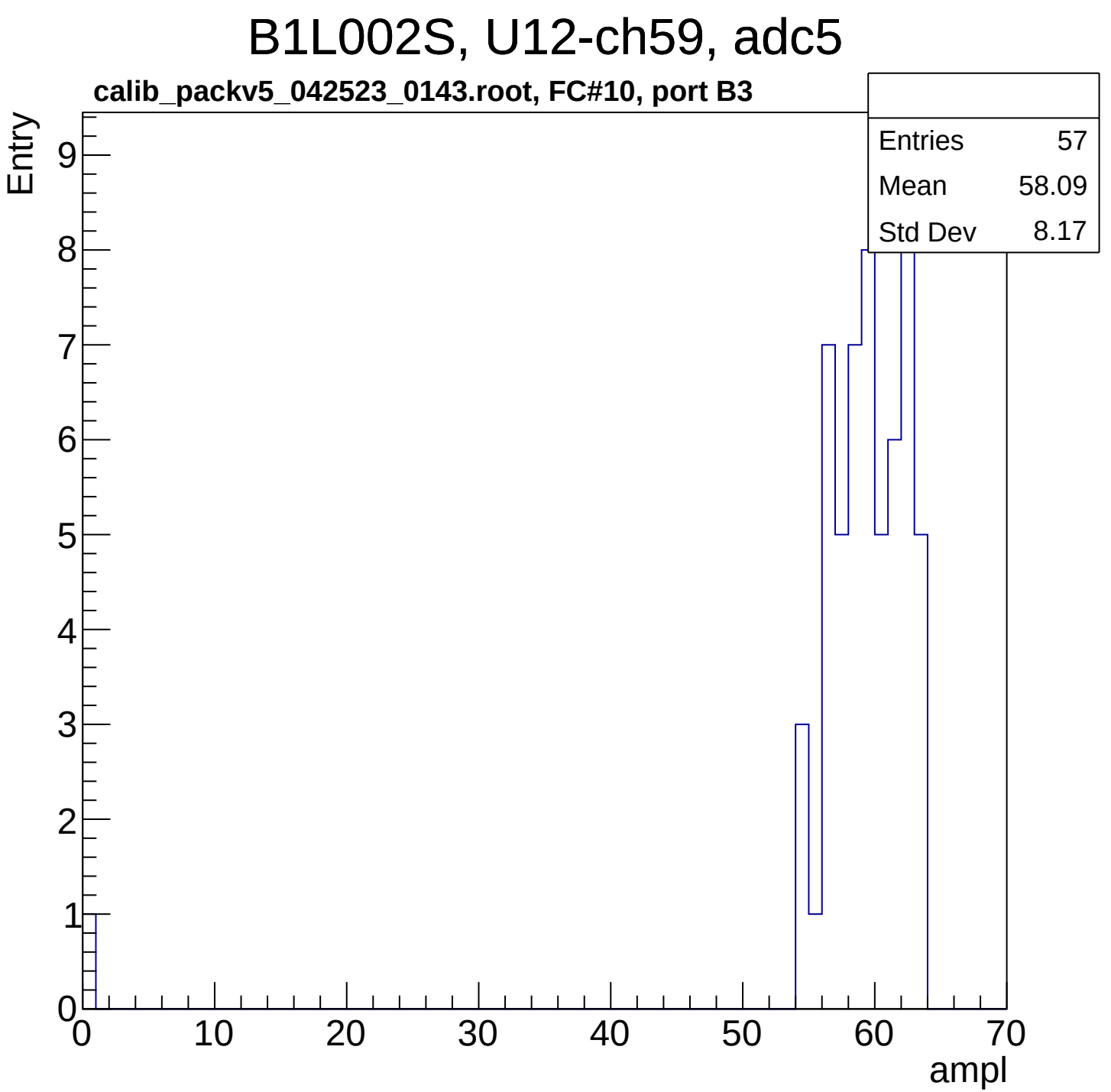
B1L002S, U12-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.09
Std Dev 8.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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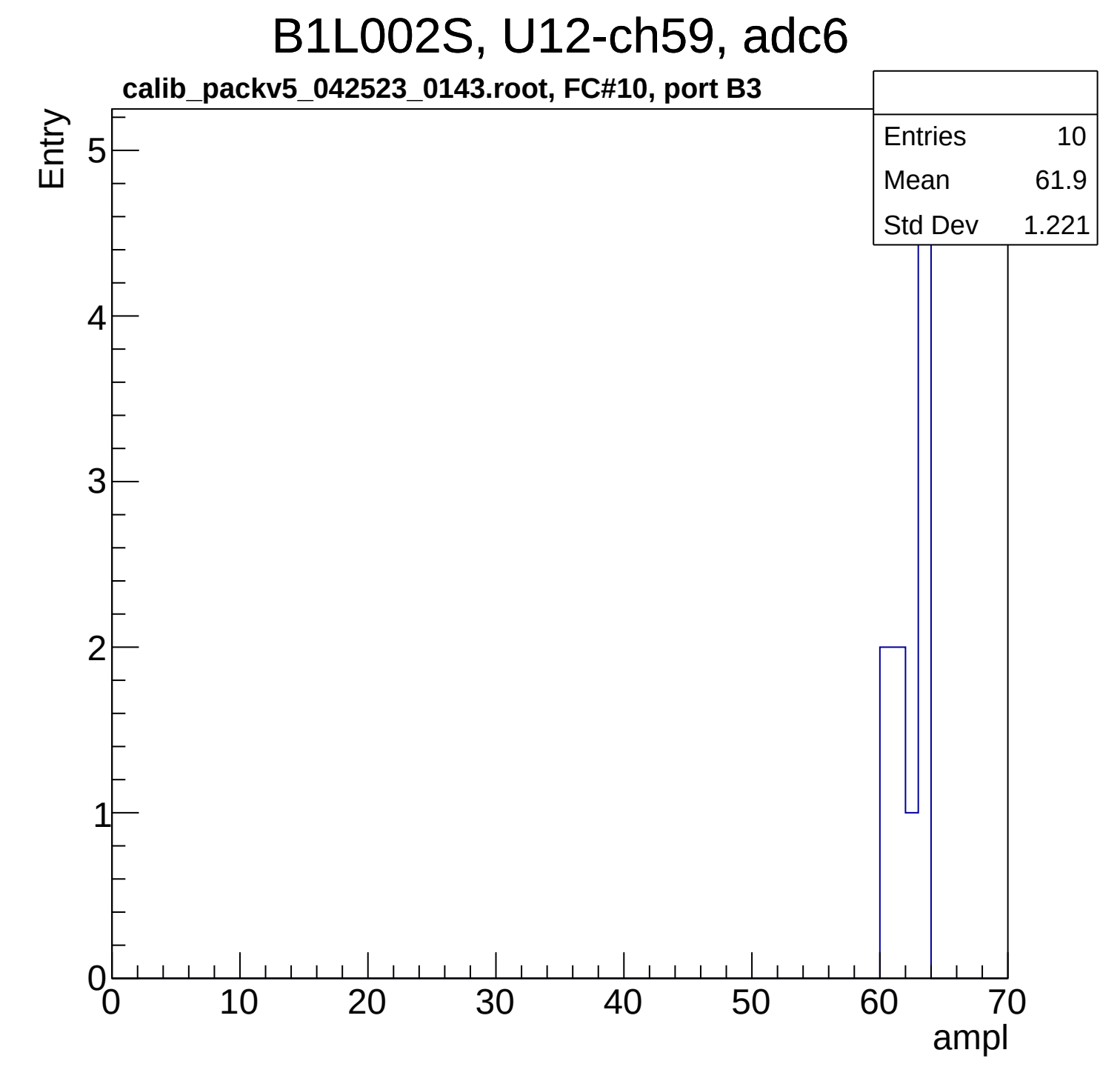
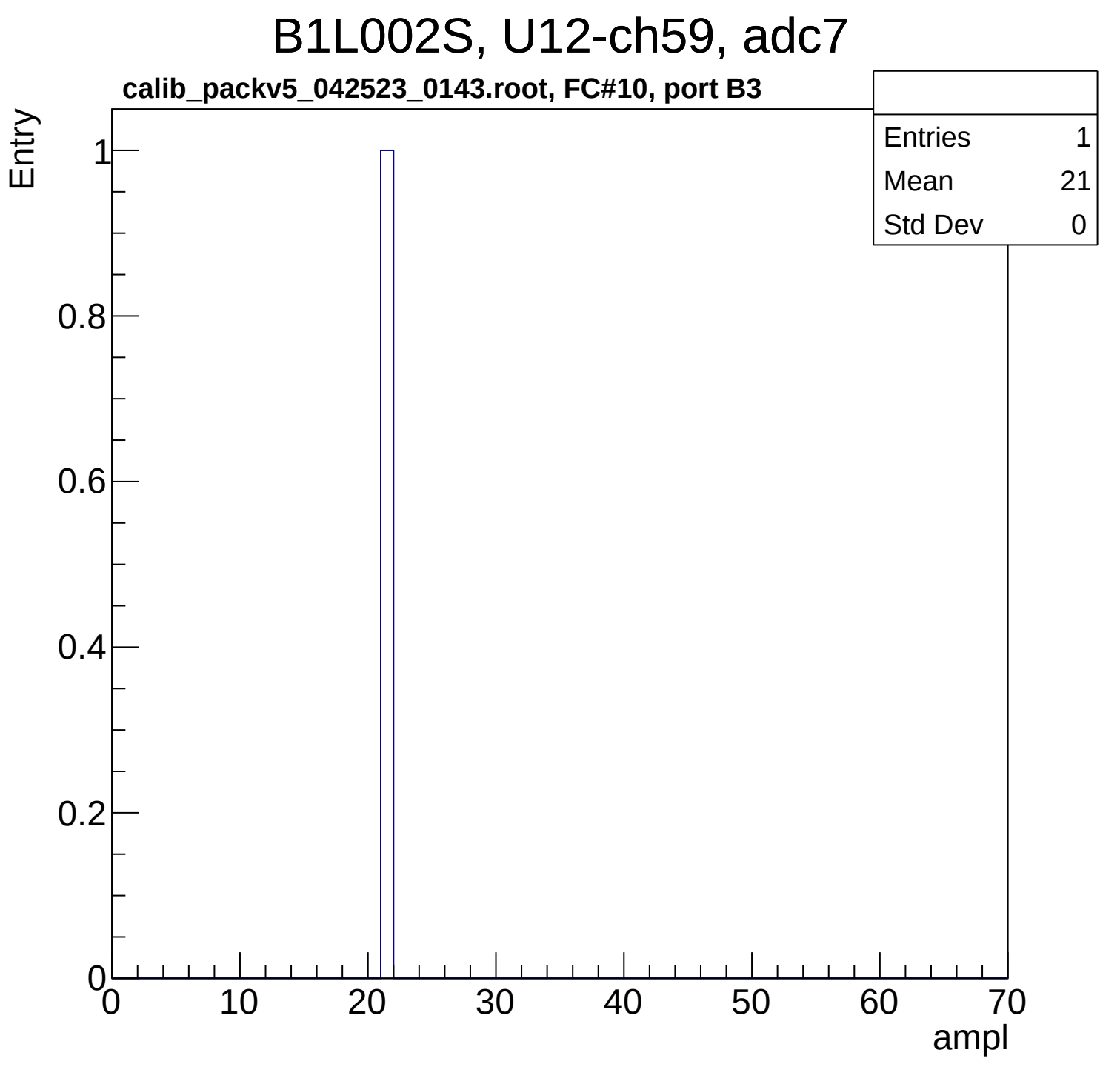
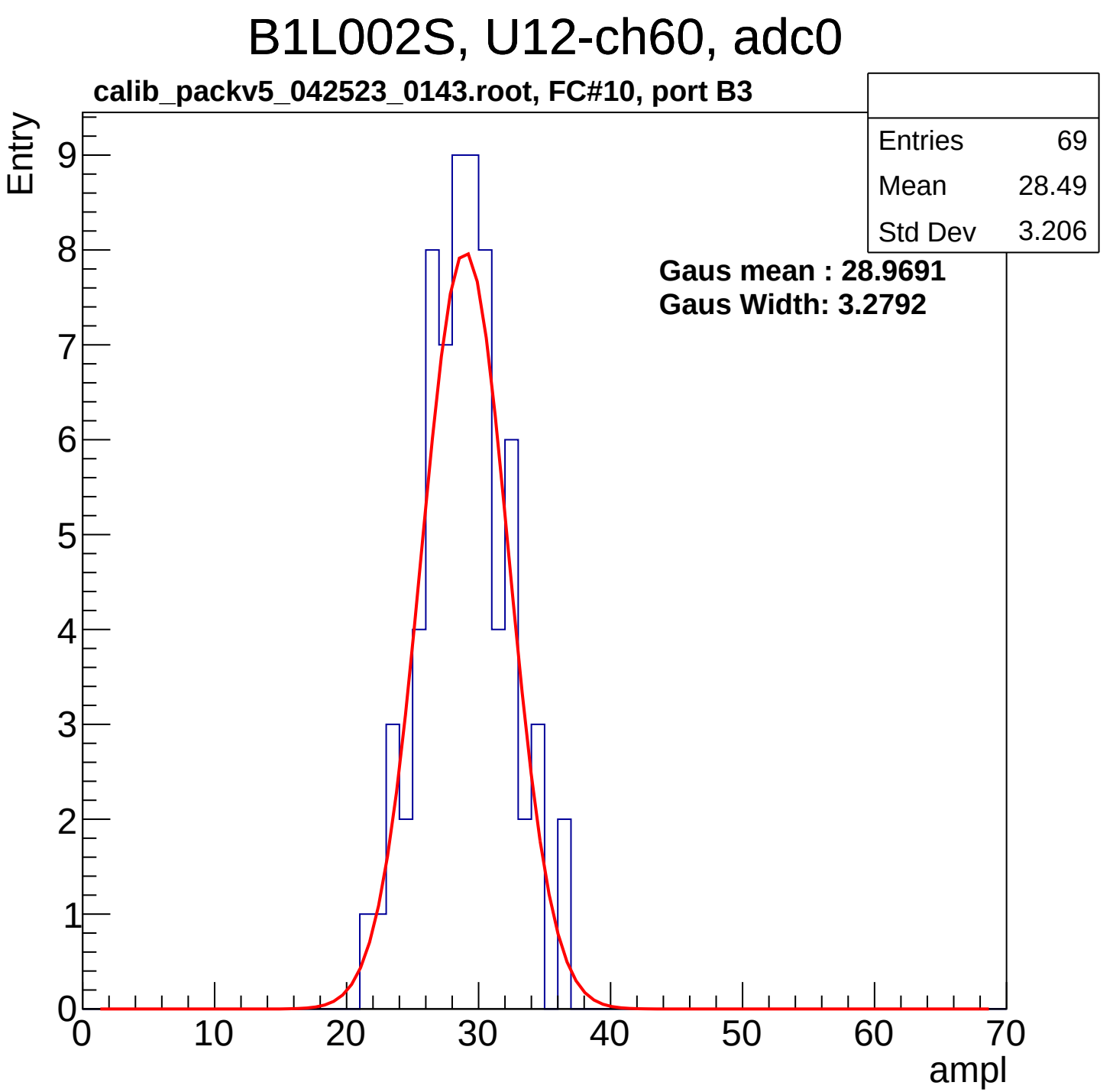
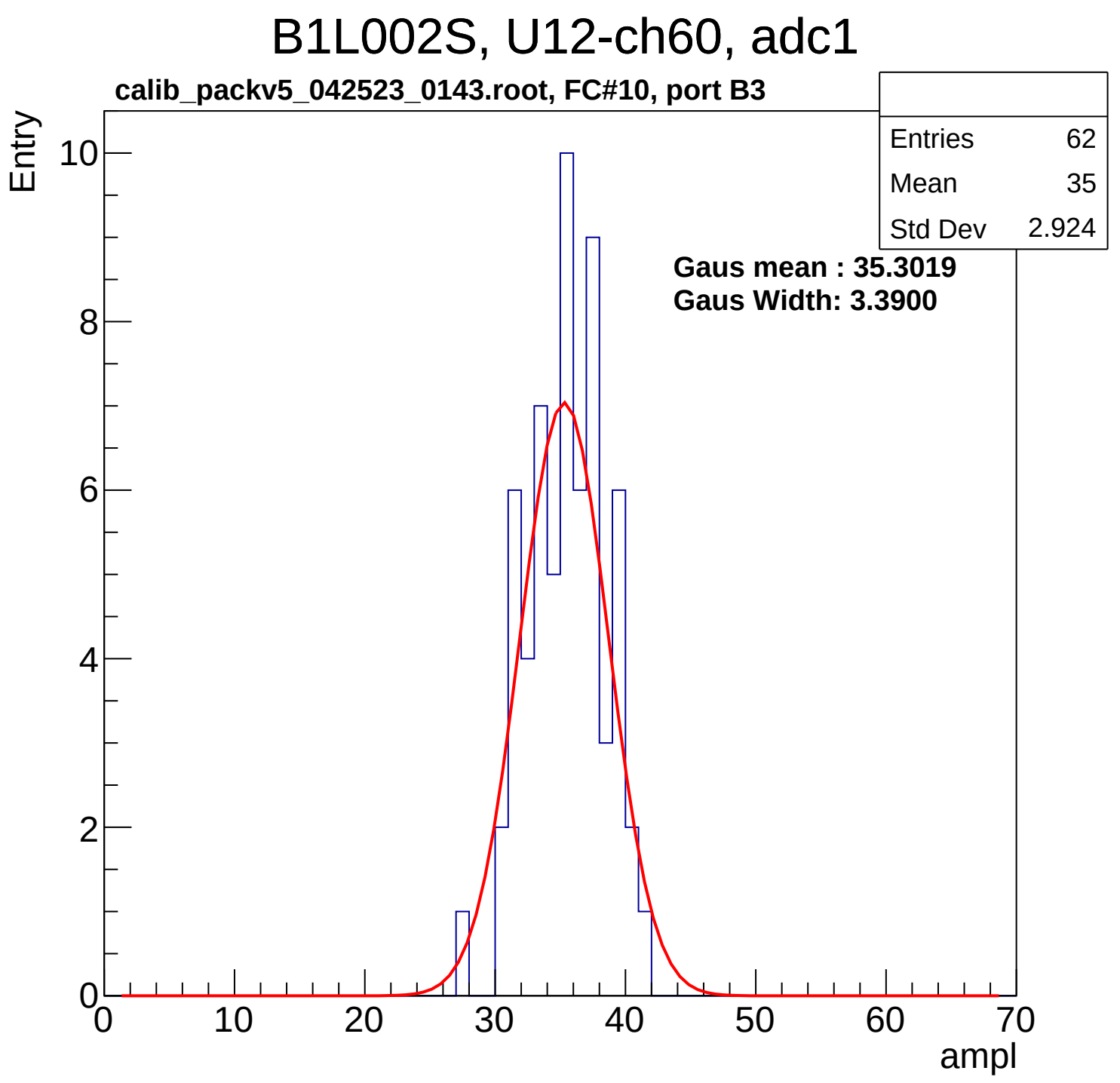
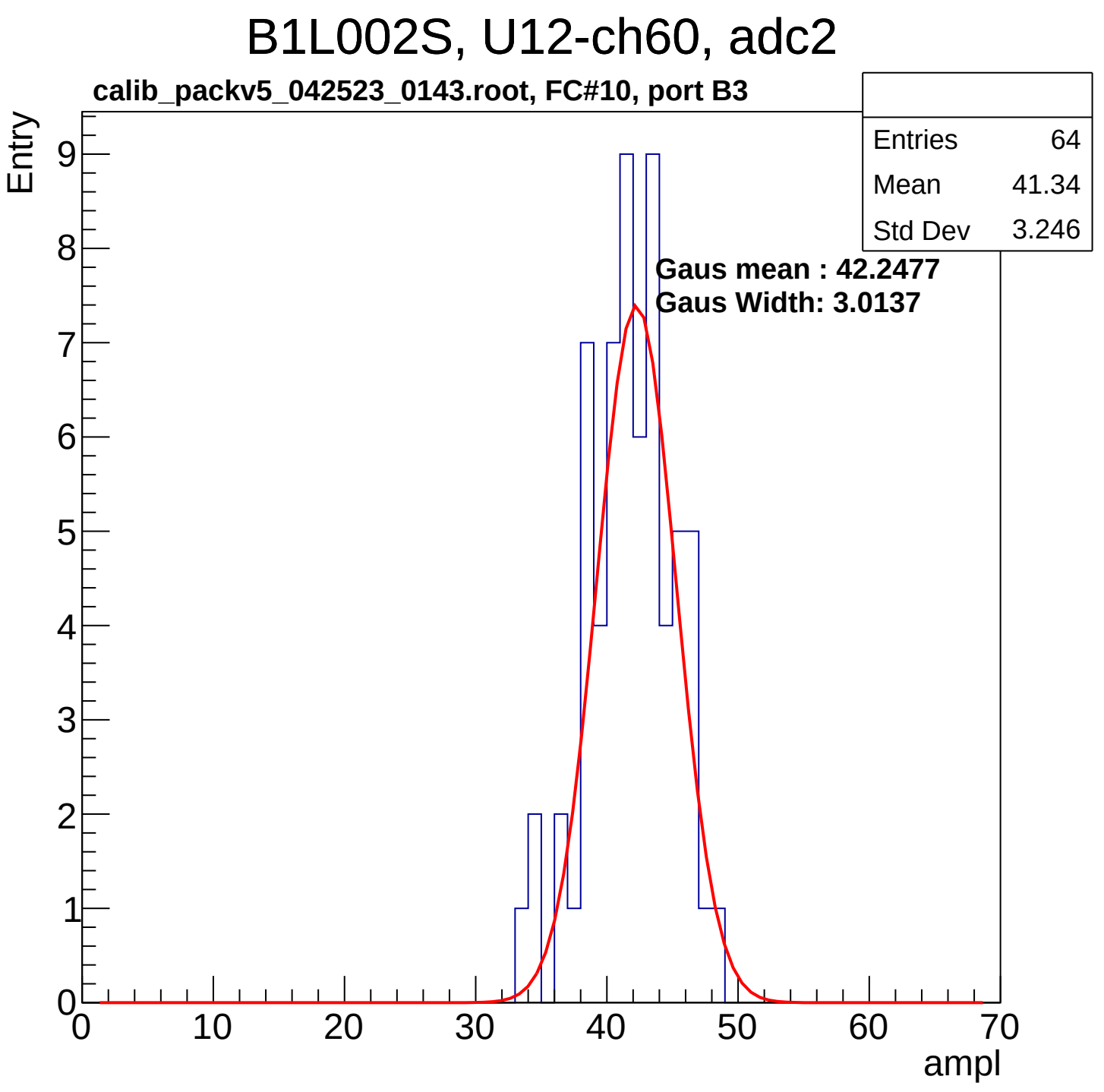

B1L002S, U12-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.9
Std Dev 1.221
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.49
Std Dev 3.206
Gaus mean : 28.9691
Gaus Width: 3.2792
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 35
Std Dev 2.924
Gaus mean : 35.3019
Gaus Width: 3.3900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.34
Std Dev 3.246
Gaus mean : 42.2477
Gaus Width: 3.0137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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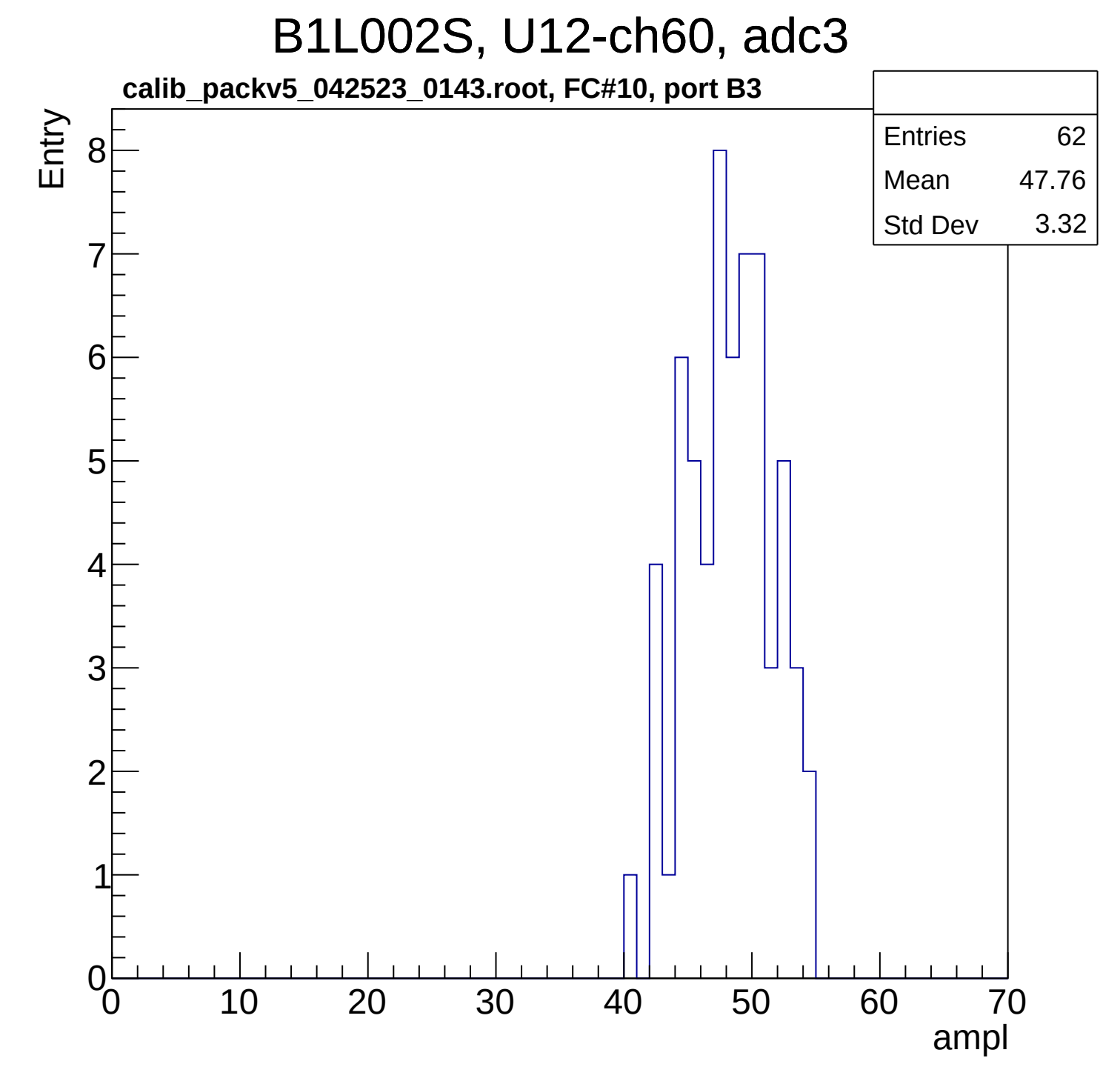
B1L002S, U12-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.76
Std Dev 3.32
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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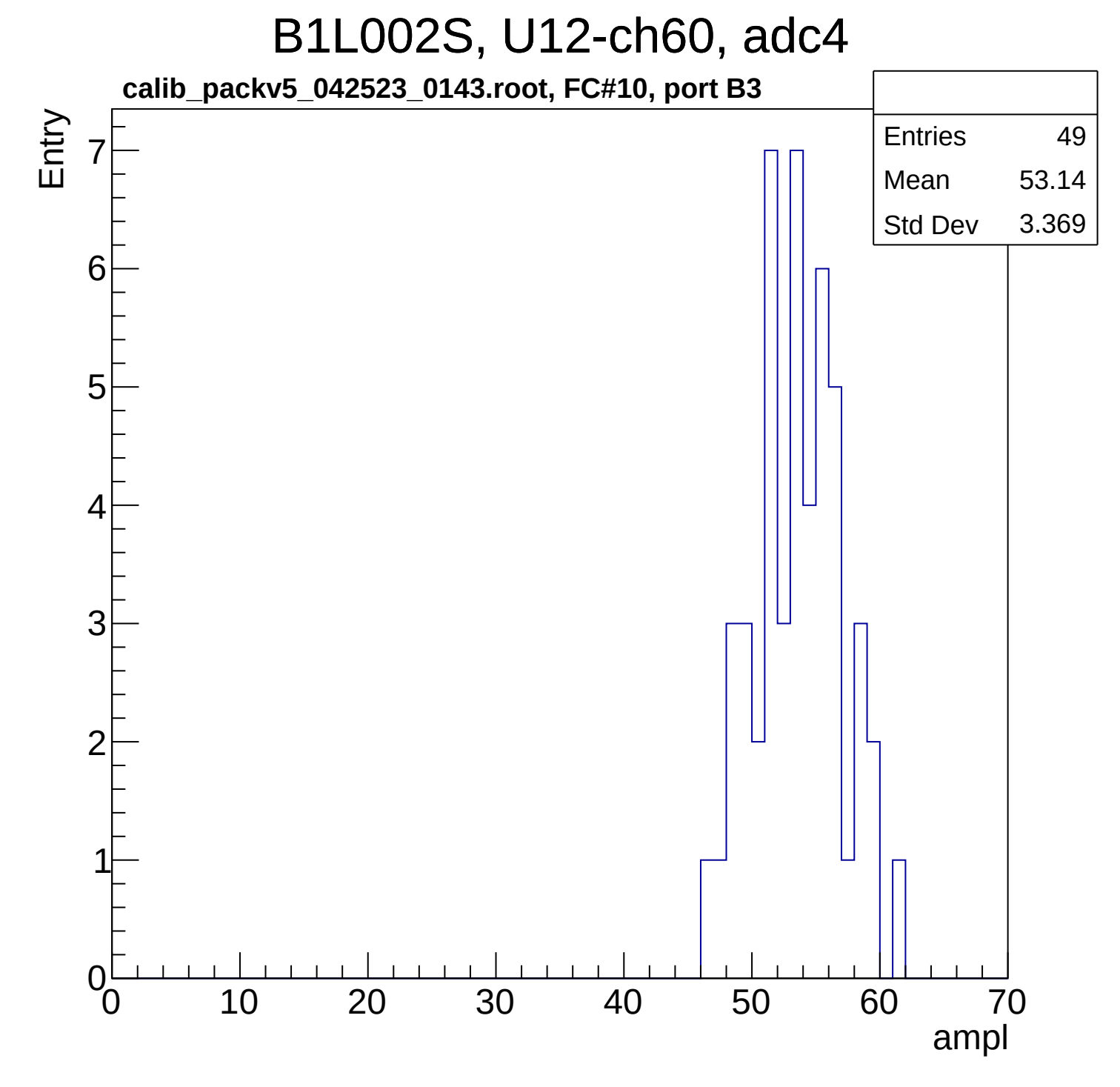
B1L002S, U12-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.14
Std Dev 3.369
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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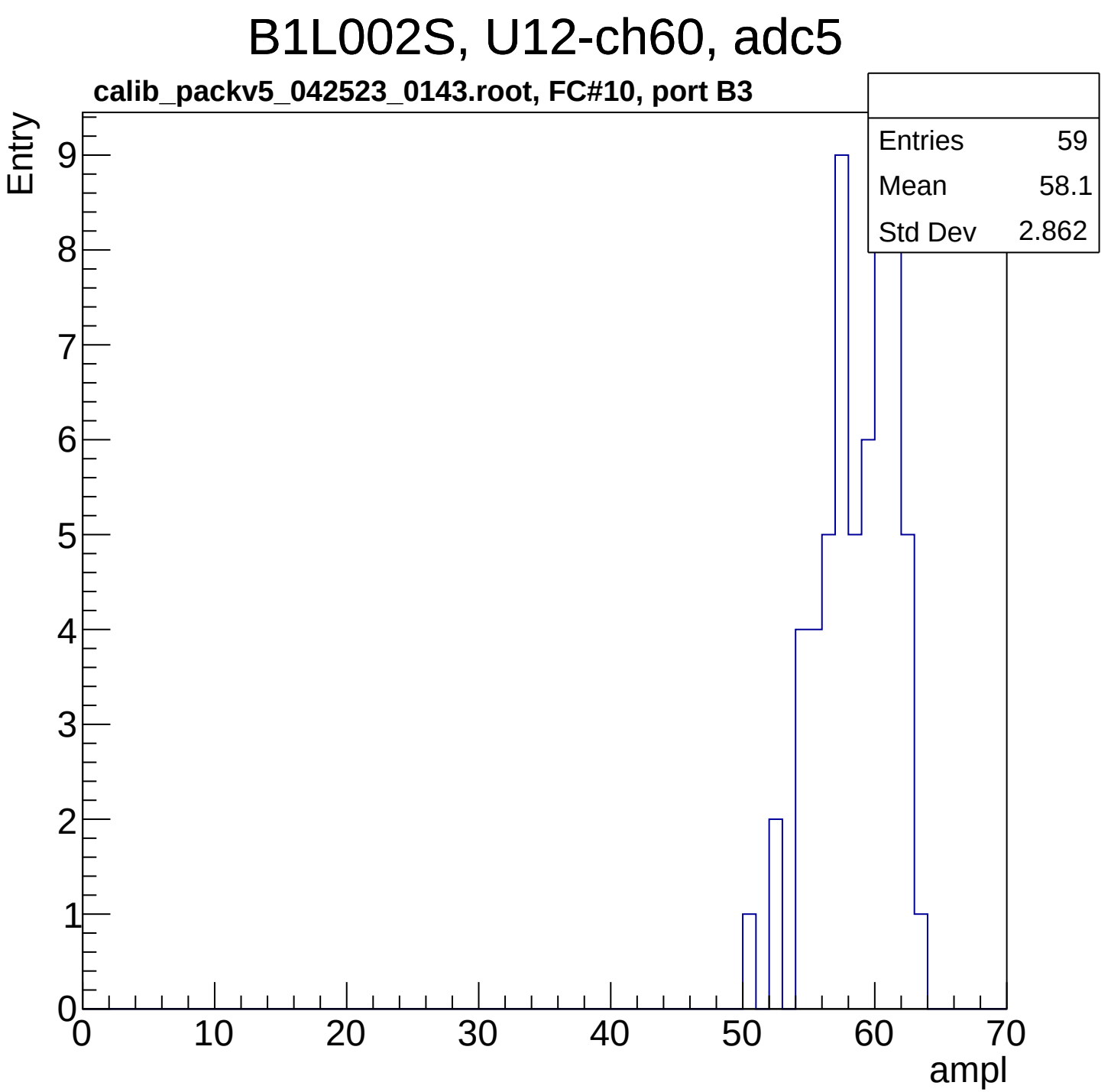
B1L002S, U12-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.1
Std Dev 2.862
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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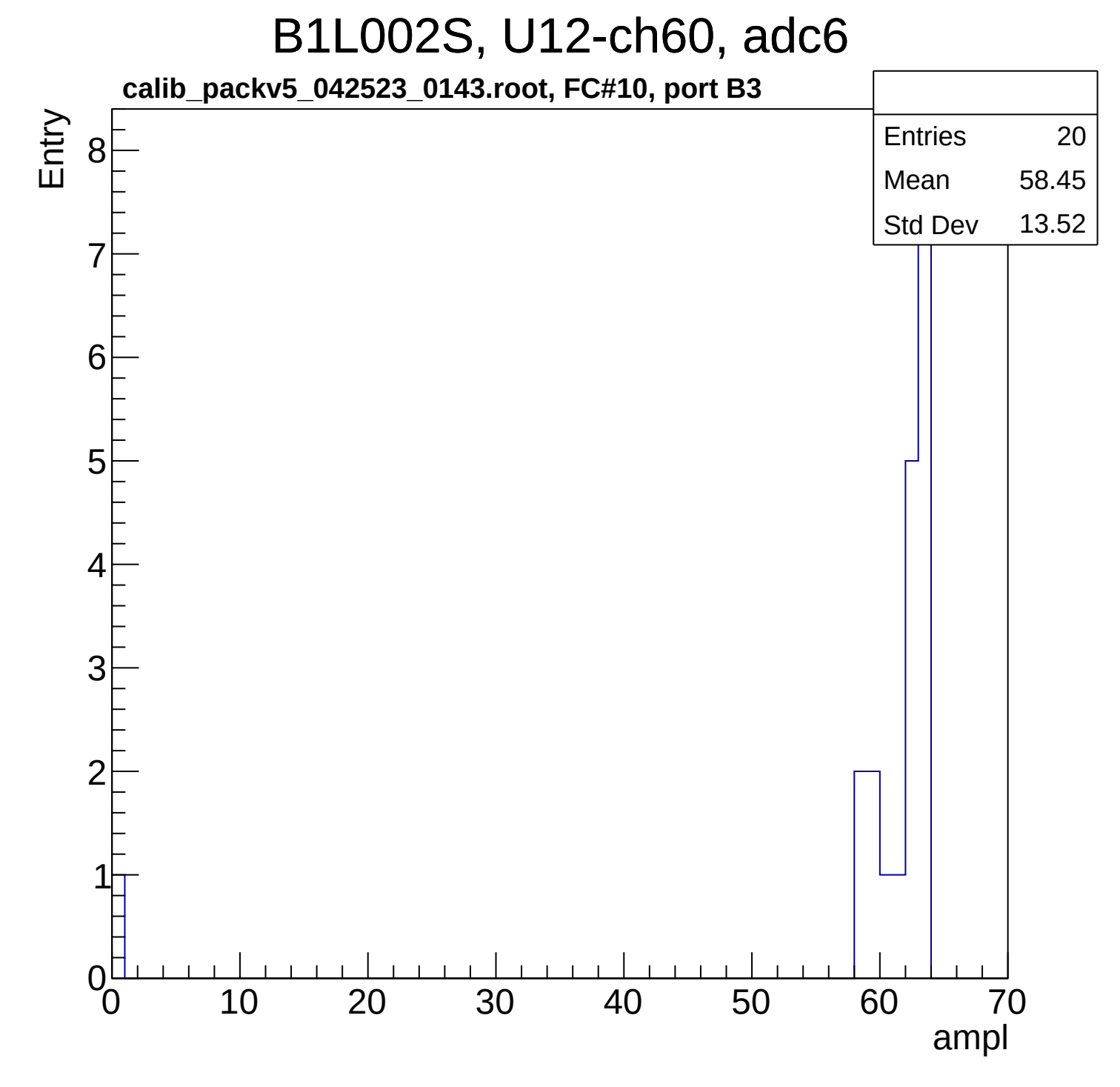
B1L002S, U12-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.45
Std Dev 13.52
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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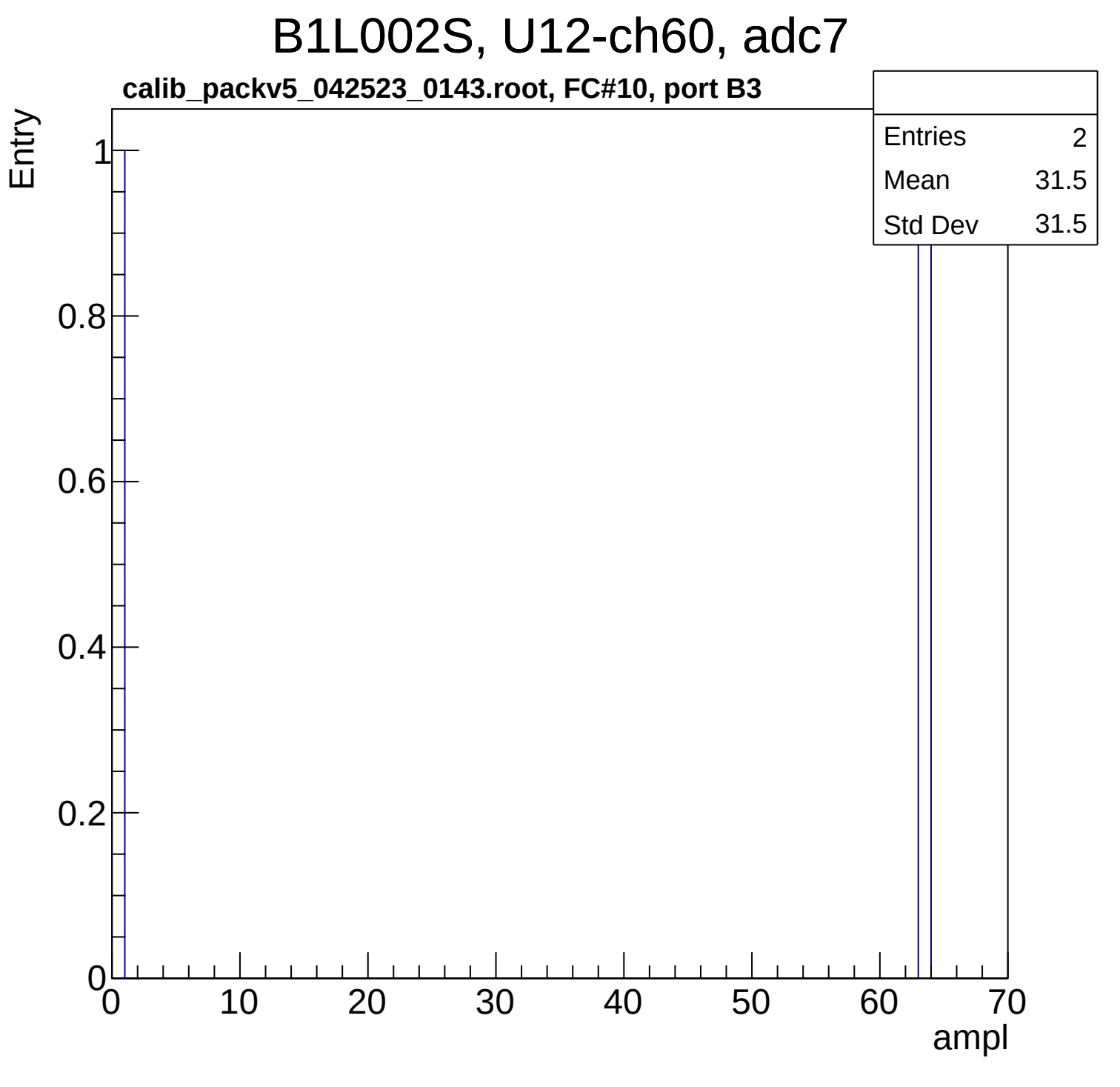
B1L002S, U12-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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0
10
20
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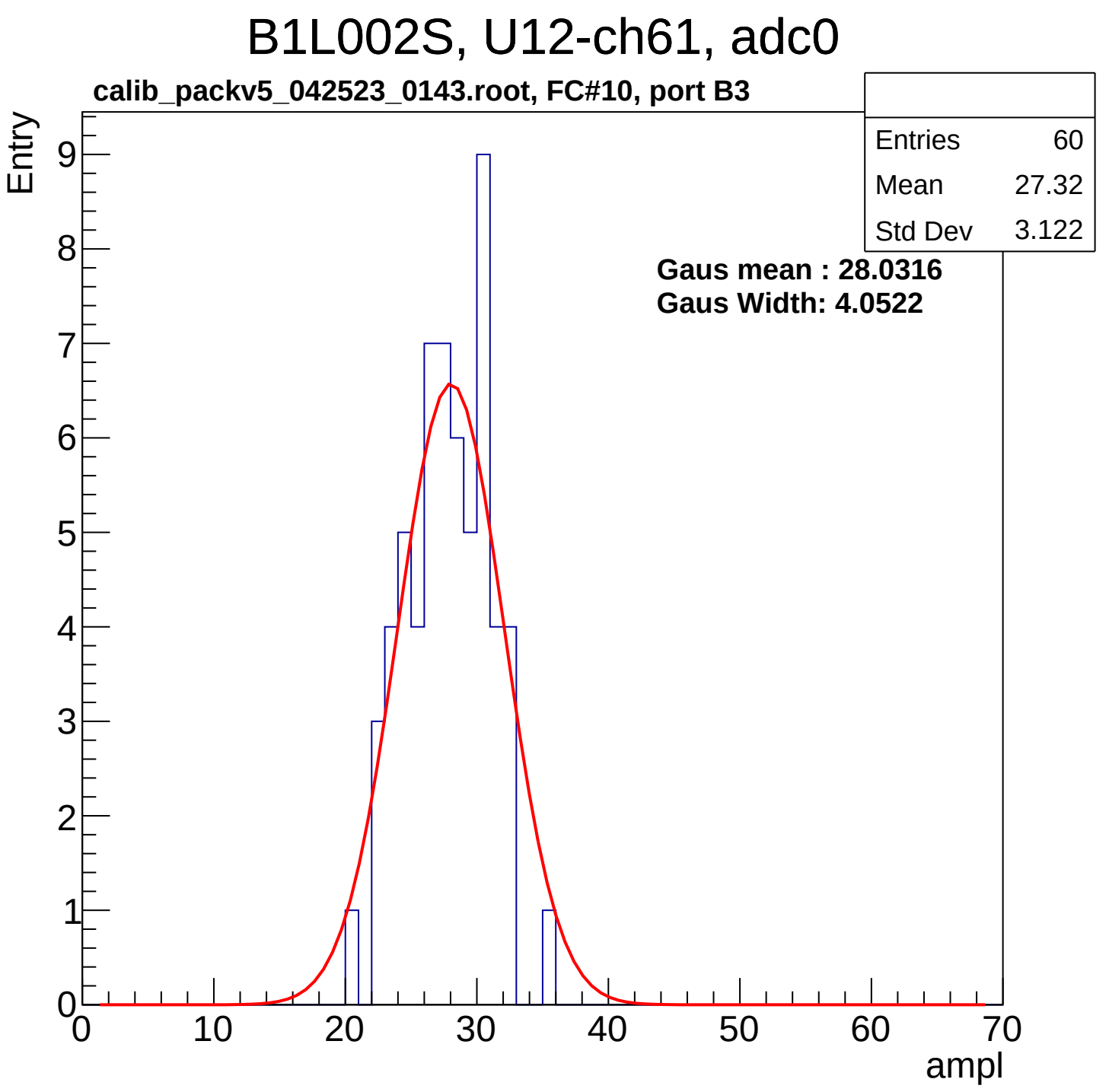
40
50
60
70
ampl


B1L002S, U12-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.32
Std Dev 3.122
Gaus mean : 28.0316
Gaus Width: 4.0522
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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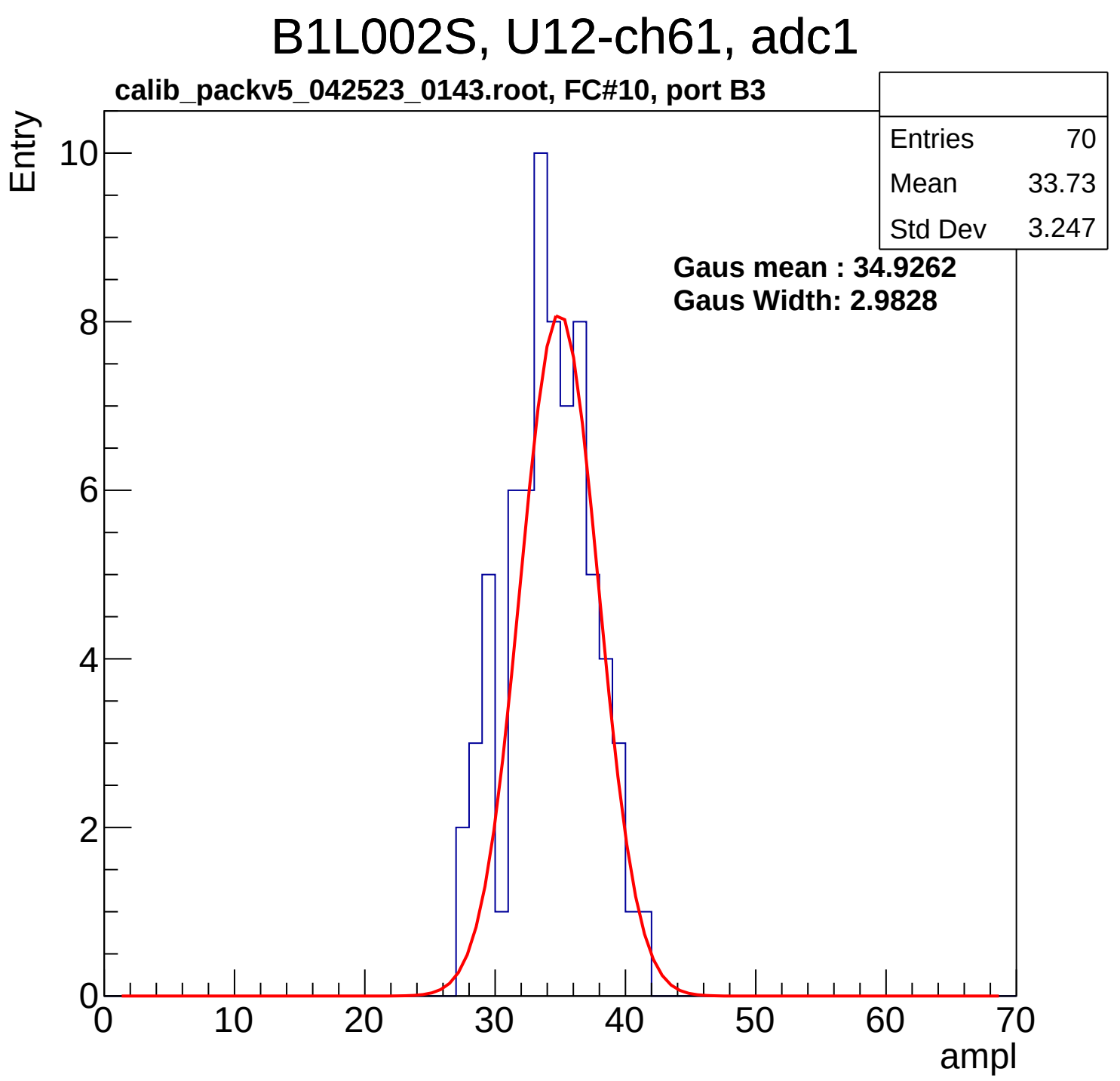
B1L002S, U12-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.73
Std Dev 3.247
Gaus mean : 34.9262
Gaus Width: 2.9828
Entry
ampl
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4
6
8
10
0
10
20
30
40
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60
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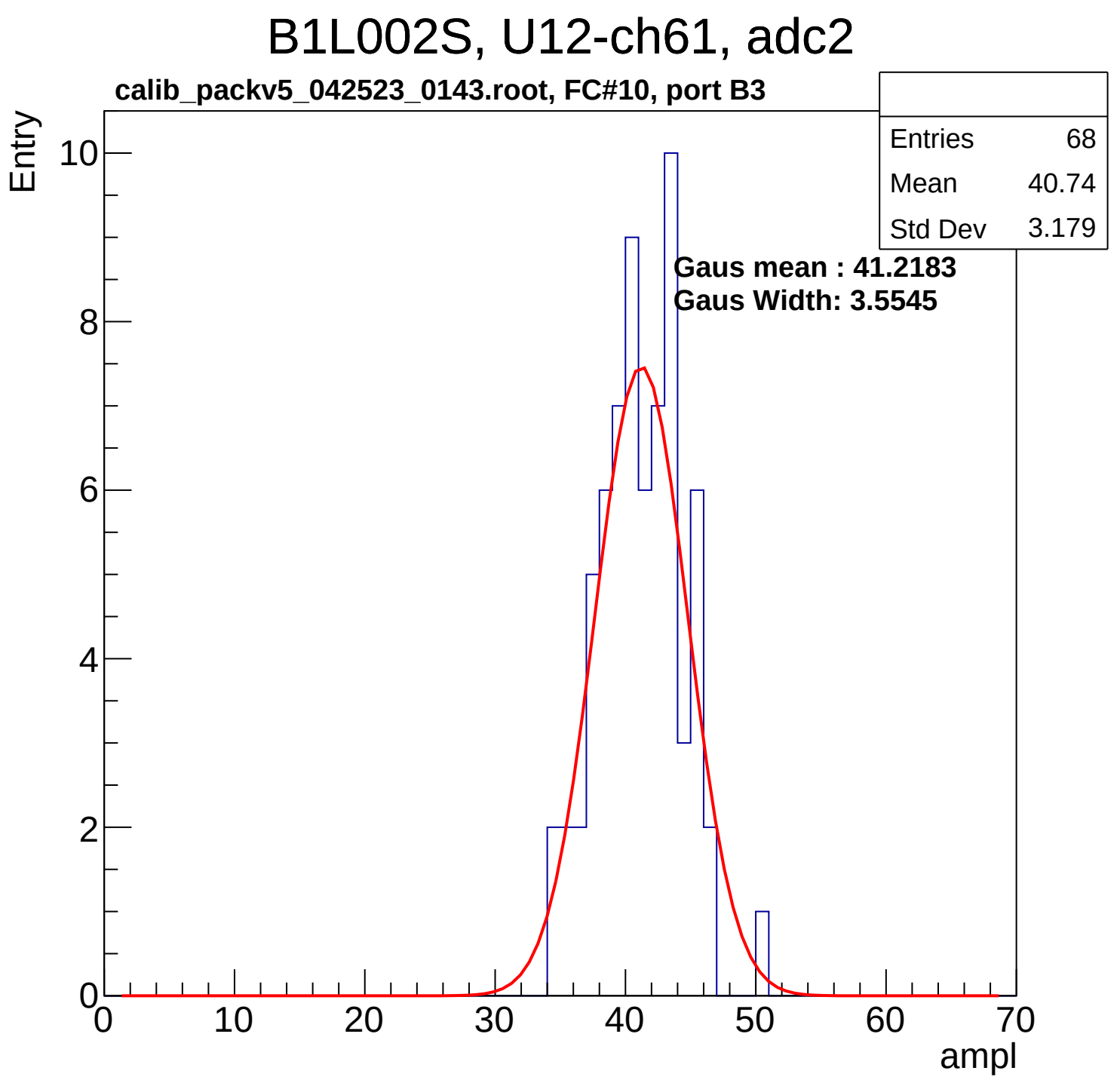
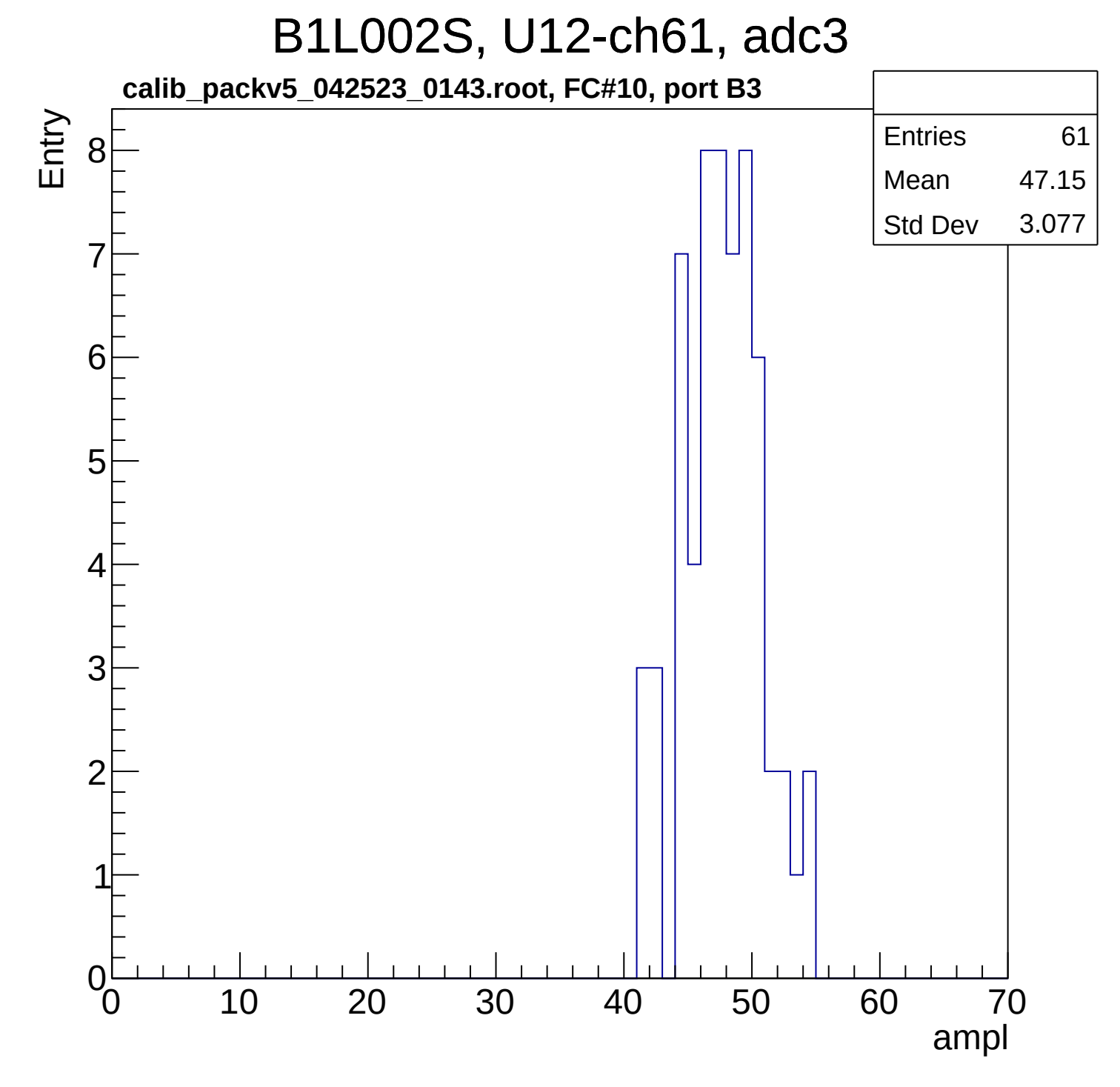

B1L002S, U12-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.74
Std Dev 3.179
Gaus mean : 41.2183
Gaus Width: 3.5545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.15
Std Dev 3.077
Entry
ampl
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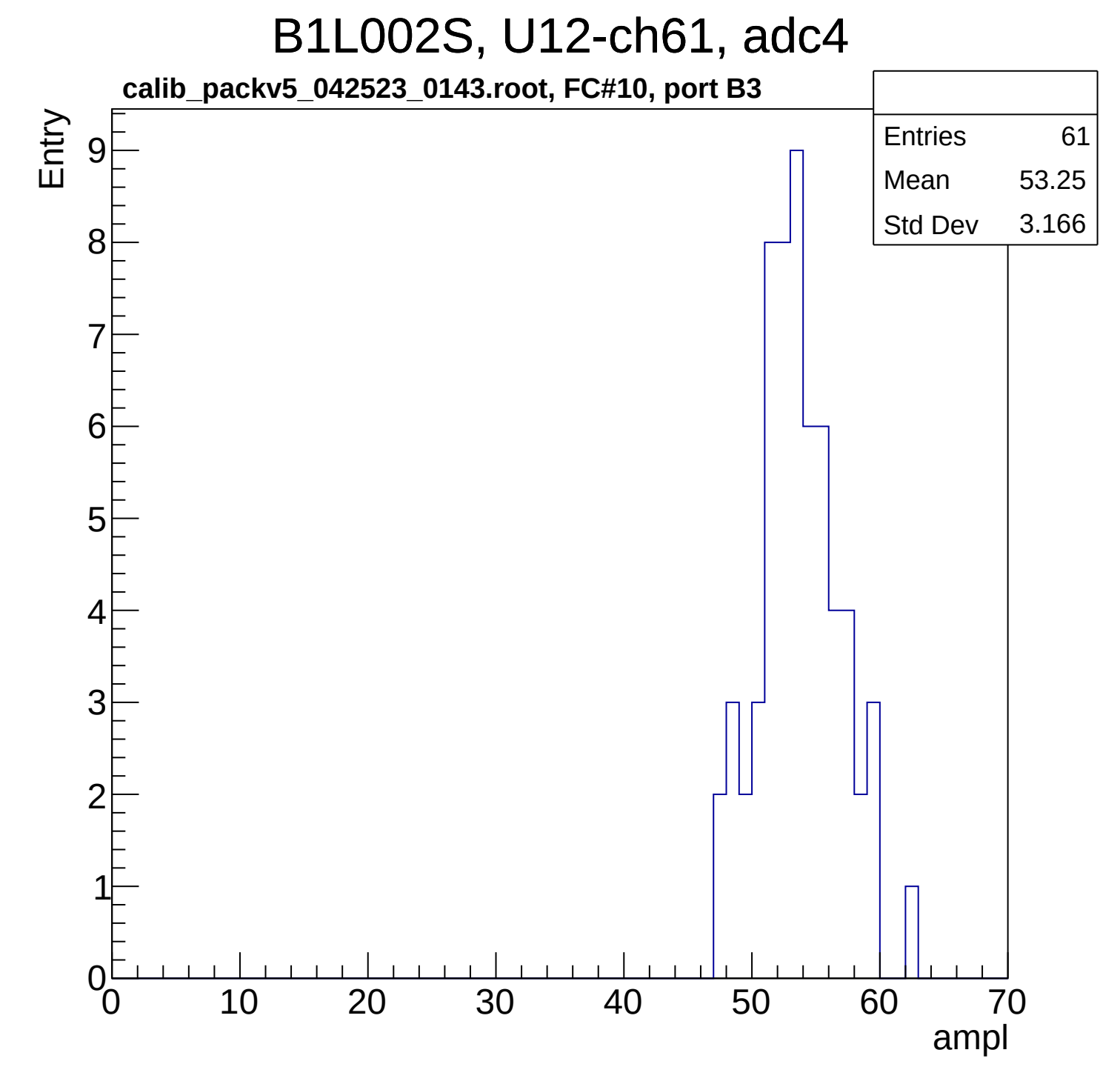
B1L002S, U12-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.25
Std Dev 3.166
Entry
ampl
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3
4
5
6
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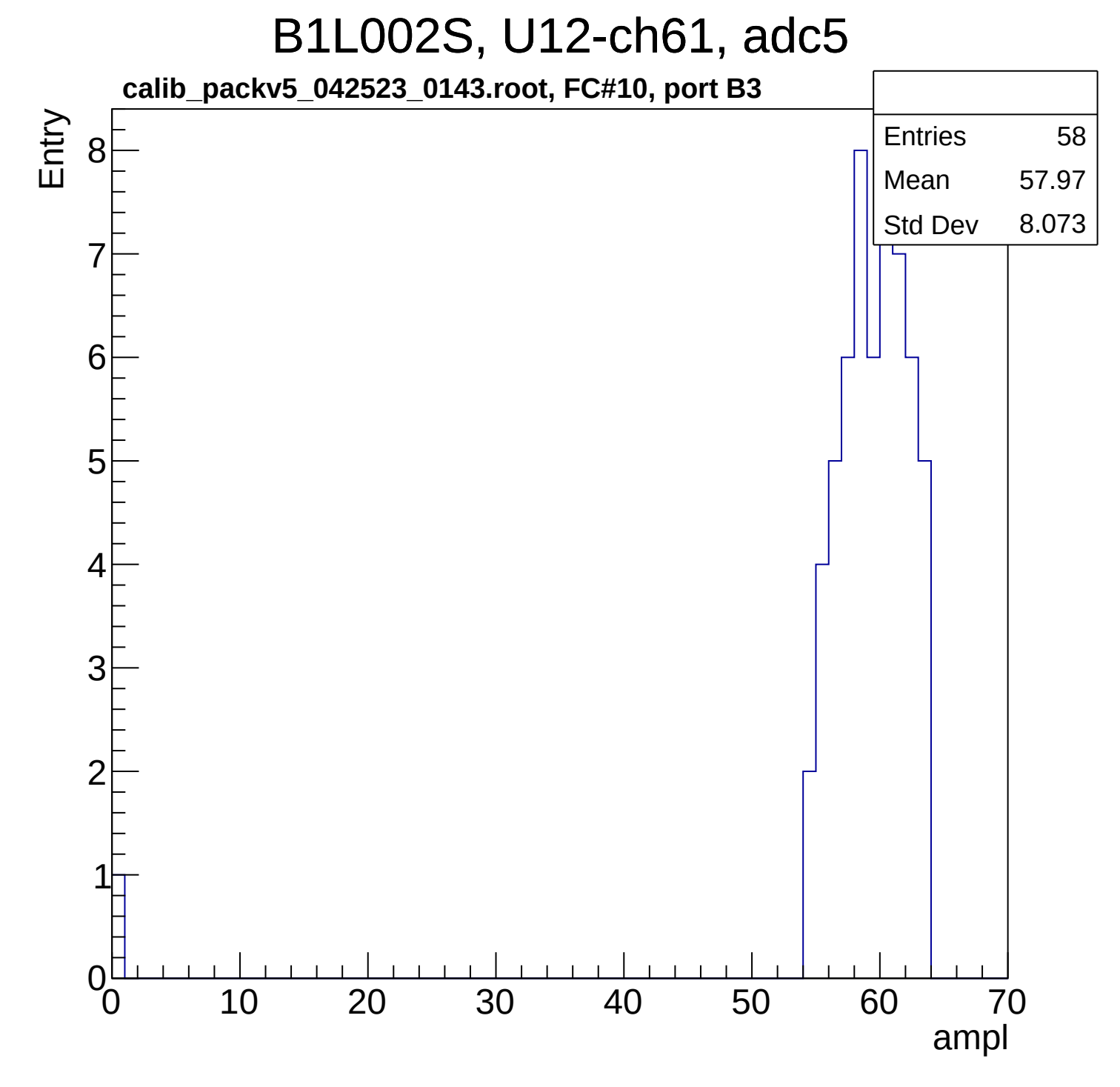
B1L002S, U12-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.97
Std Dev 8.073
Entry
ampl
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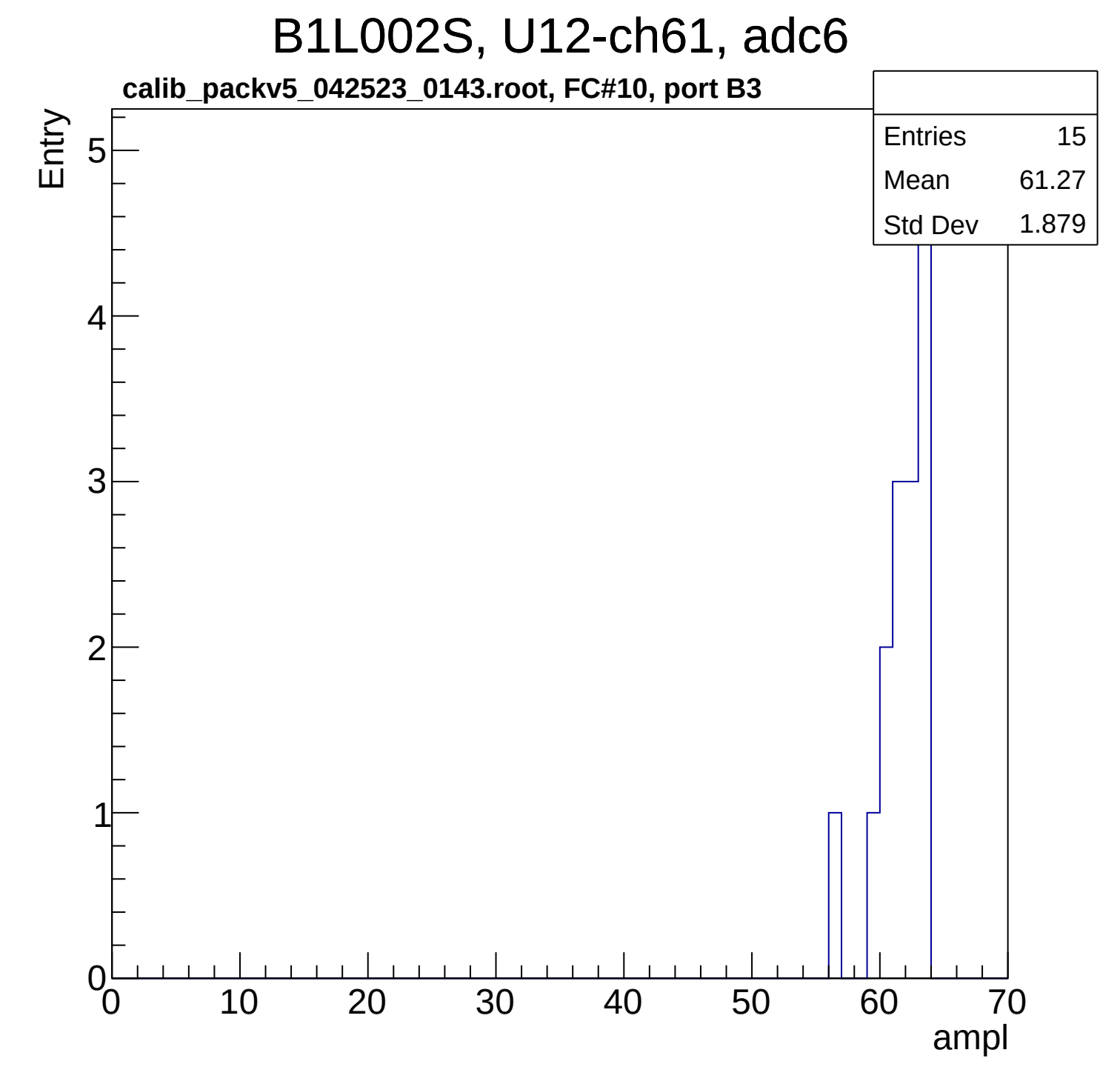
B1L002S, U12-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.27
Std Dev 1.879
Entry
0
1
2
3
4
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0
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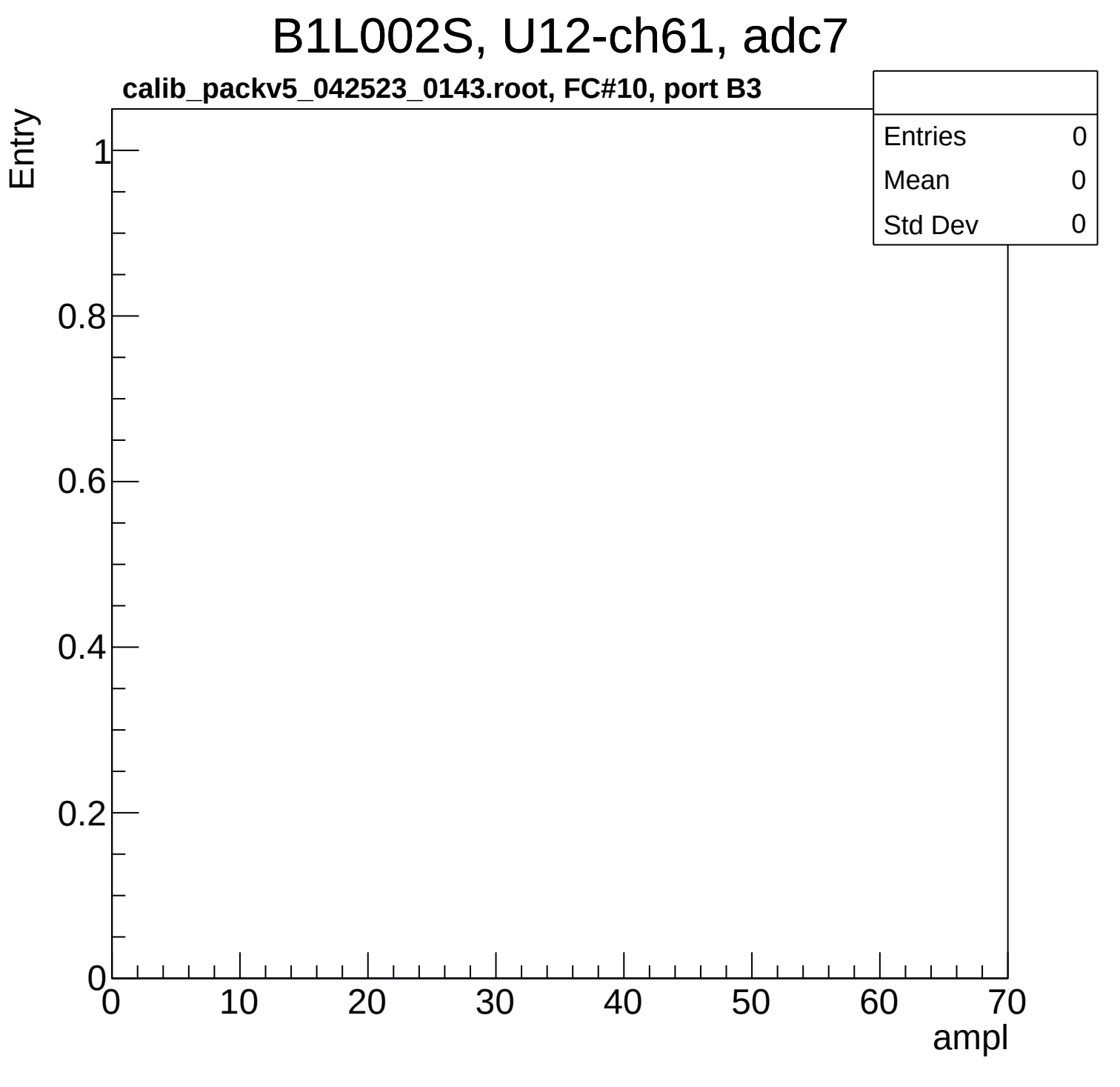
70
ampl


B1L002S, U12-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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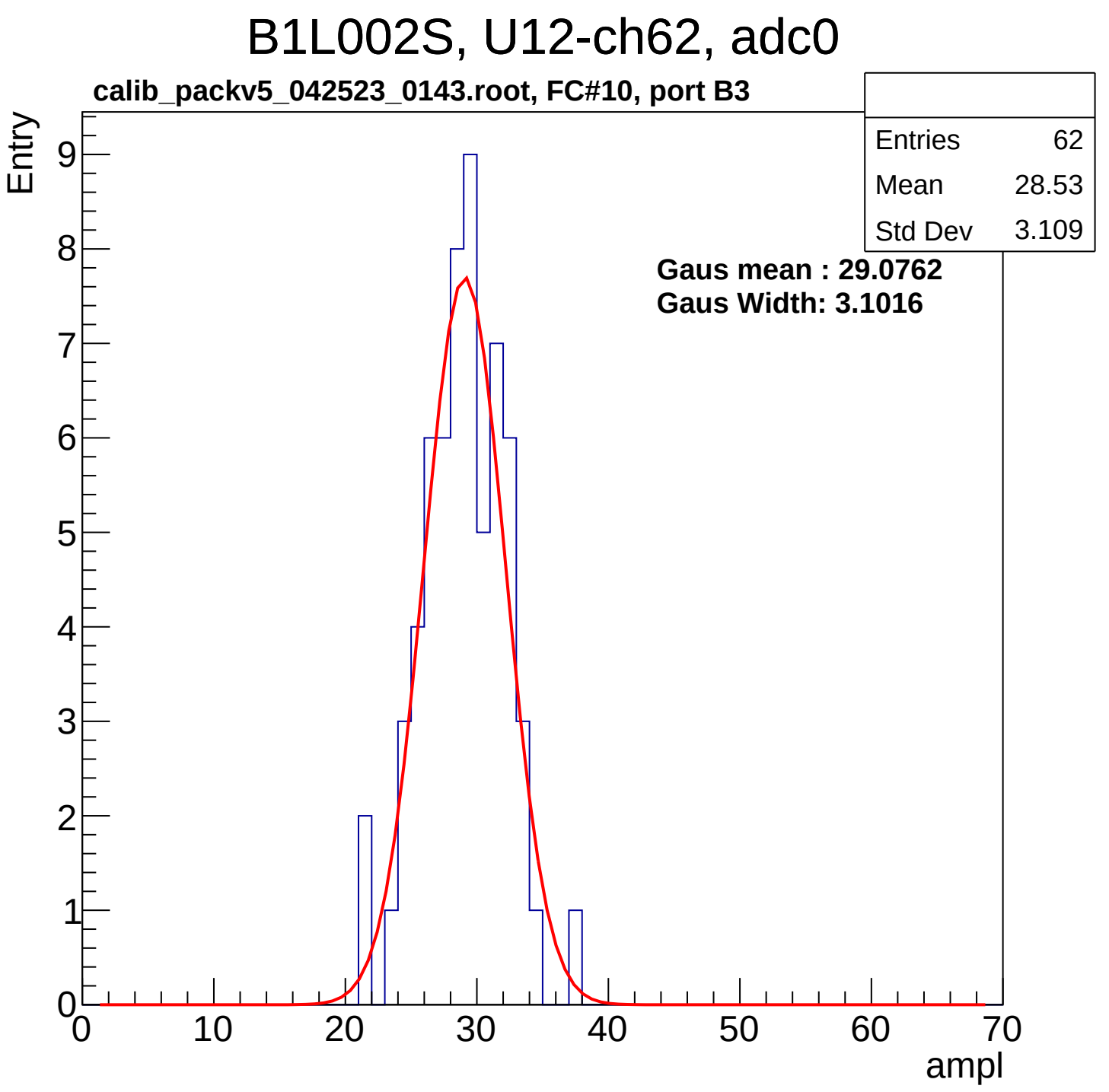
50
60
70
ampl


B1L002S, U12-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.53
Std Dev 3.109
Gaus mean : 29.0762
Gaus Width: 3.1016
Entry
ampl
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2
3
4
5
6
7
8
9
0
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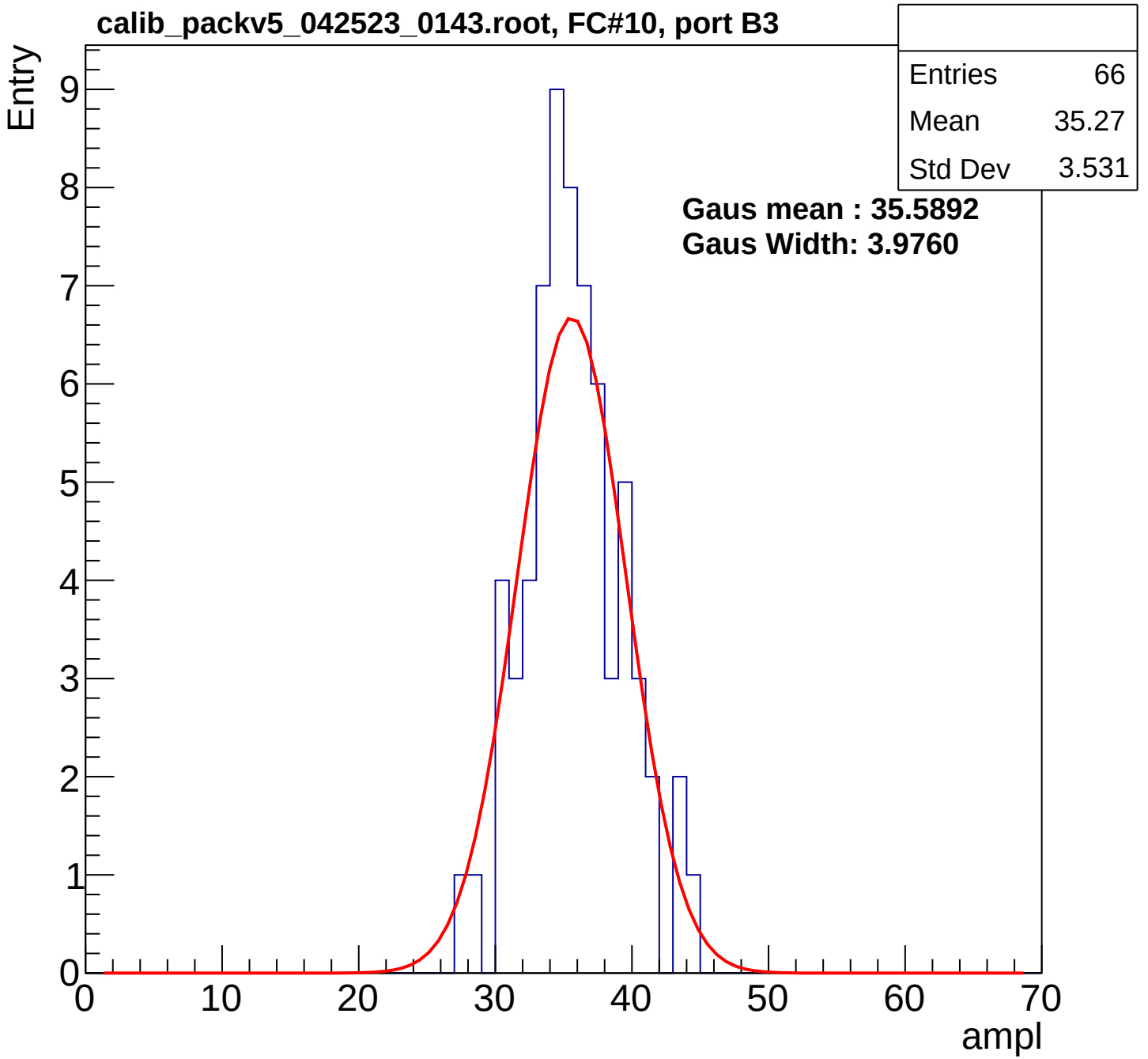
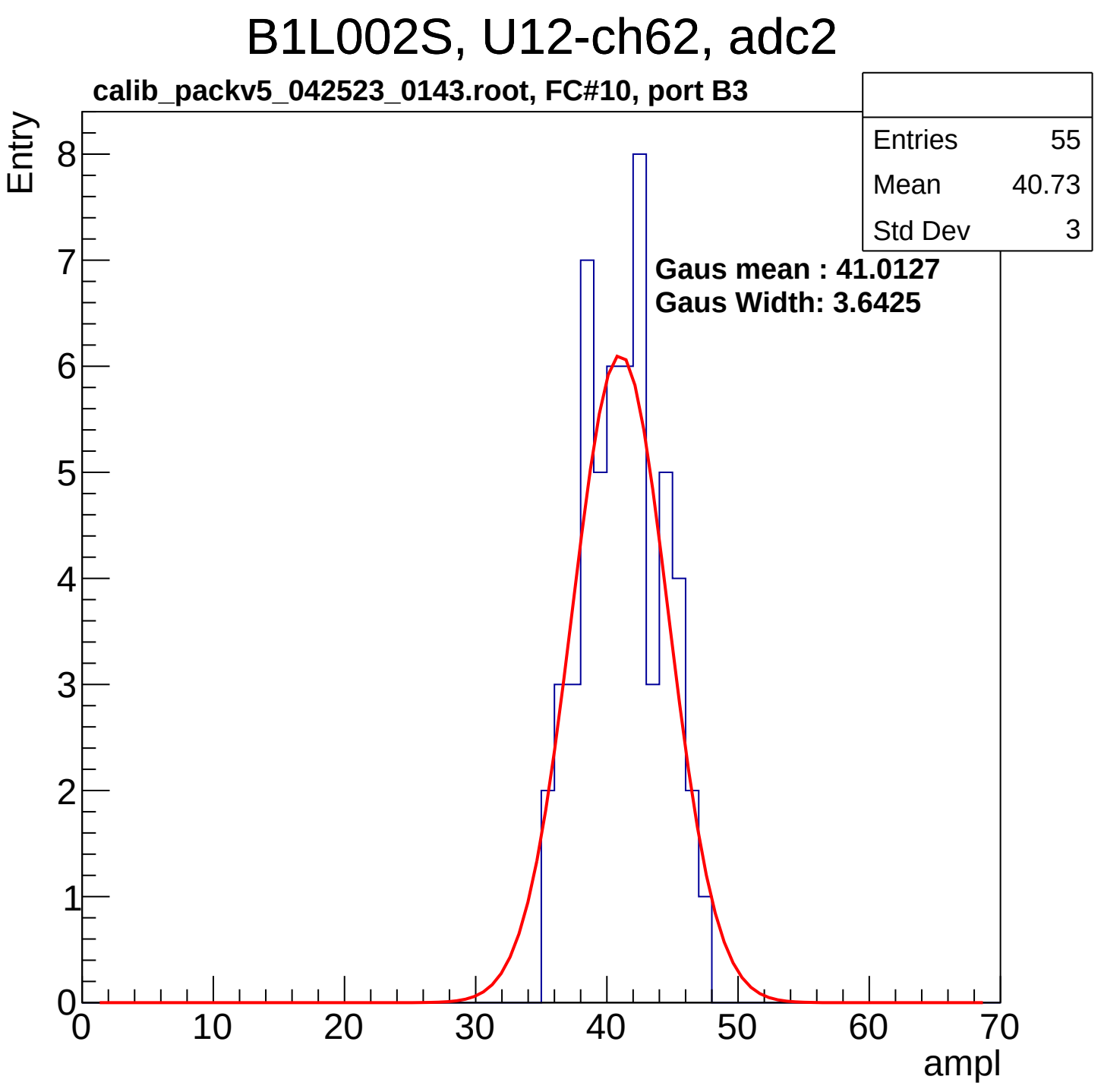

B1L002S, U12-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 35.27
Std Dev 3.531
Gaus mean : 35.5892
Gaus Width: 3.9760
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.73
Std Dev 3
Gaus mean : 41.0127
Gaus Width: 3.6425
Entry
ampl
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1
2
3
4
5
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7
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0
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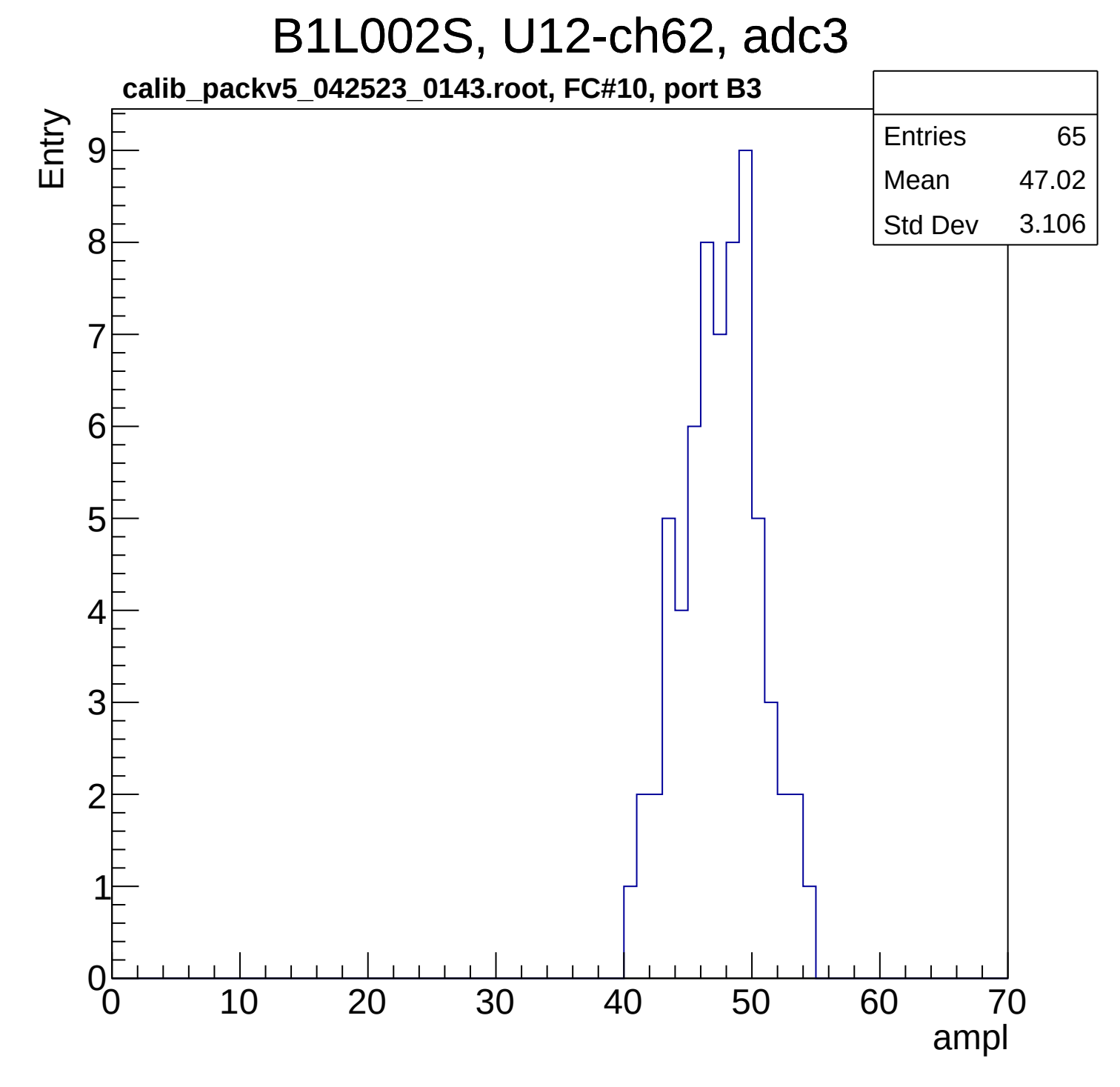
B1L002S, U12-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.02
Std Dev 3.106
Entry
ampl
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2
3
4
5
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0
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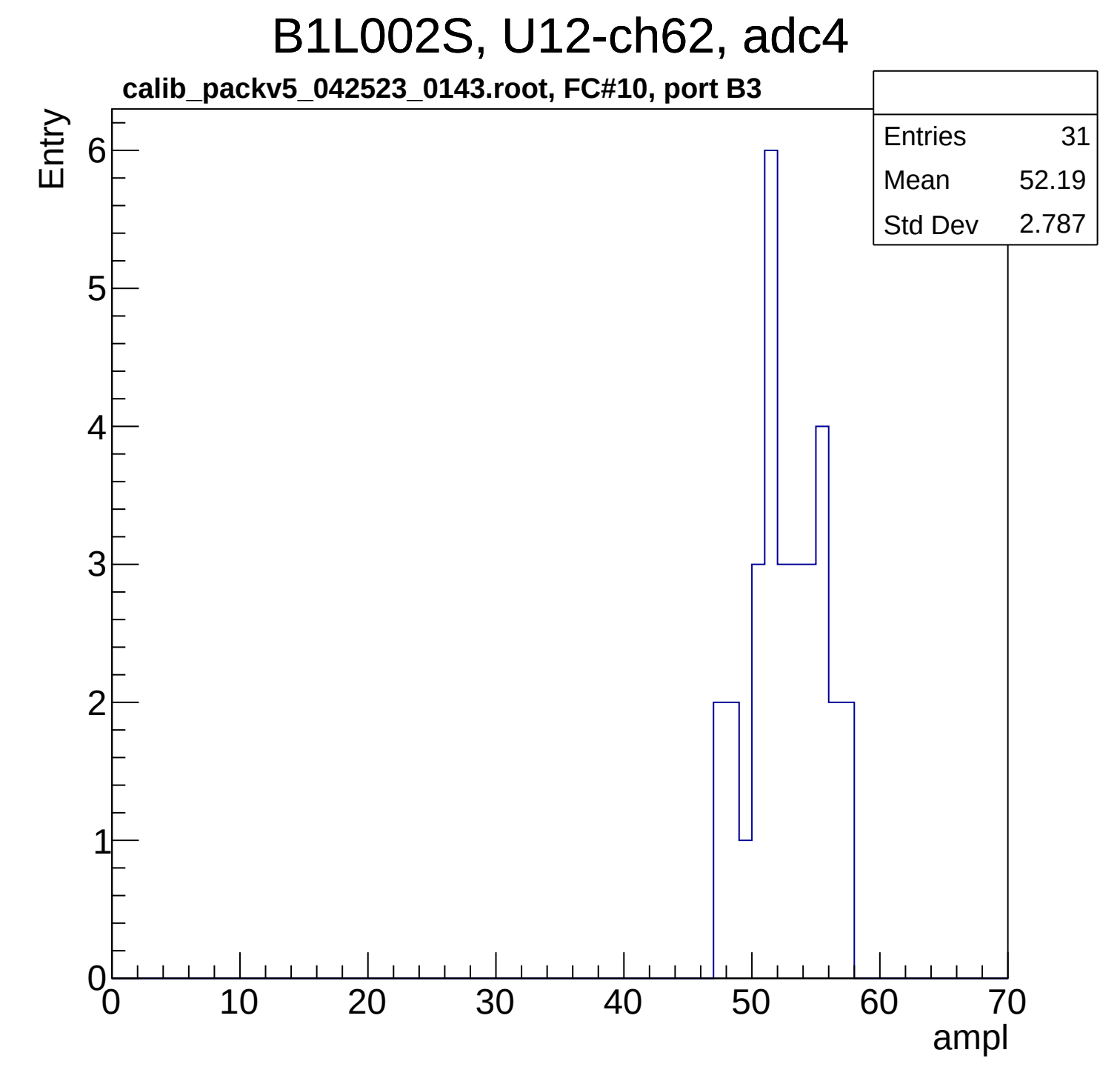
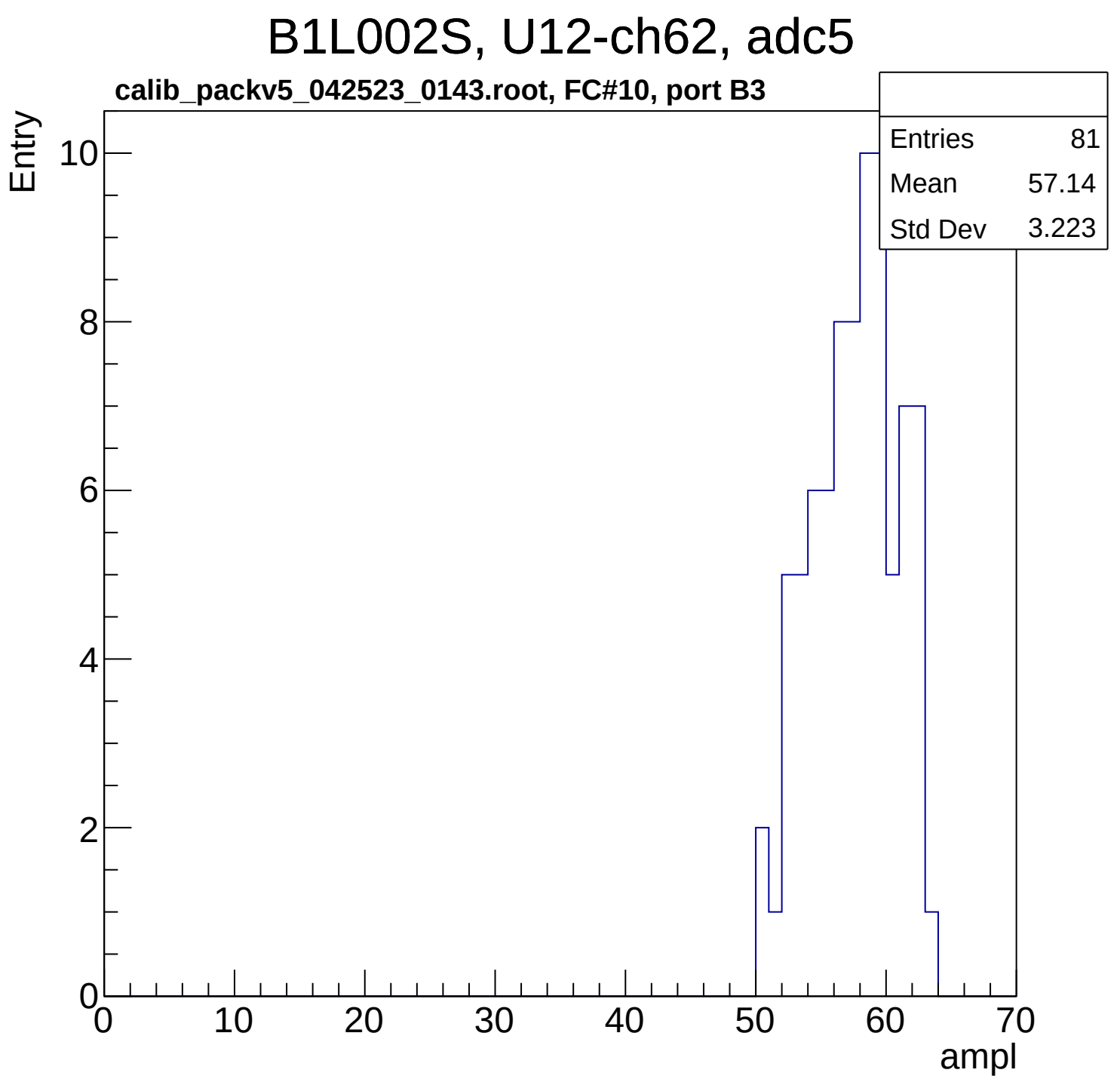

B1L002S, U12-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 52.19
Std Dev 2.787
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 57.14
Std Dev 3.223
Entry
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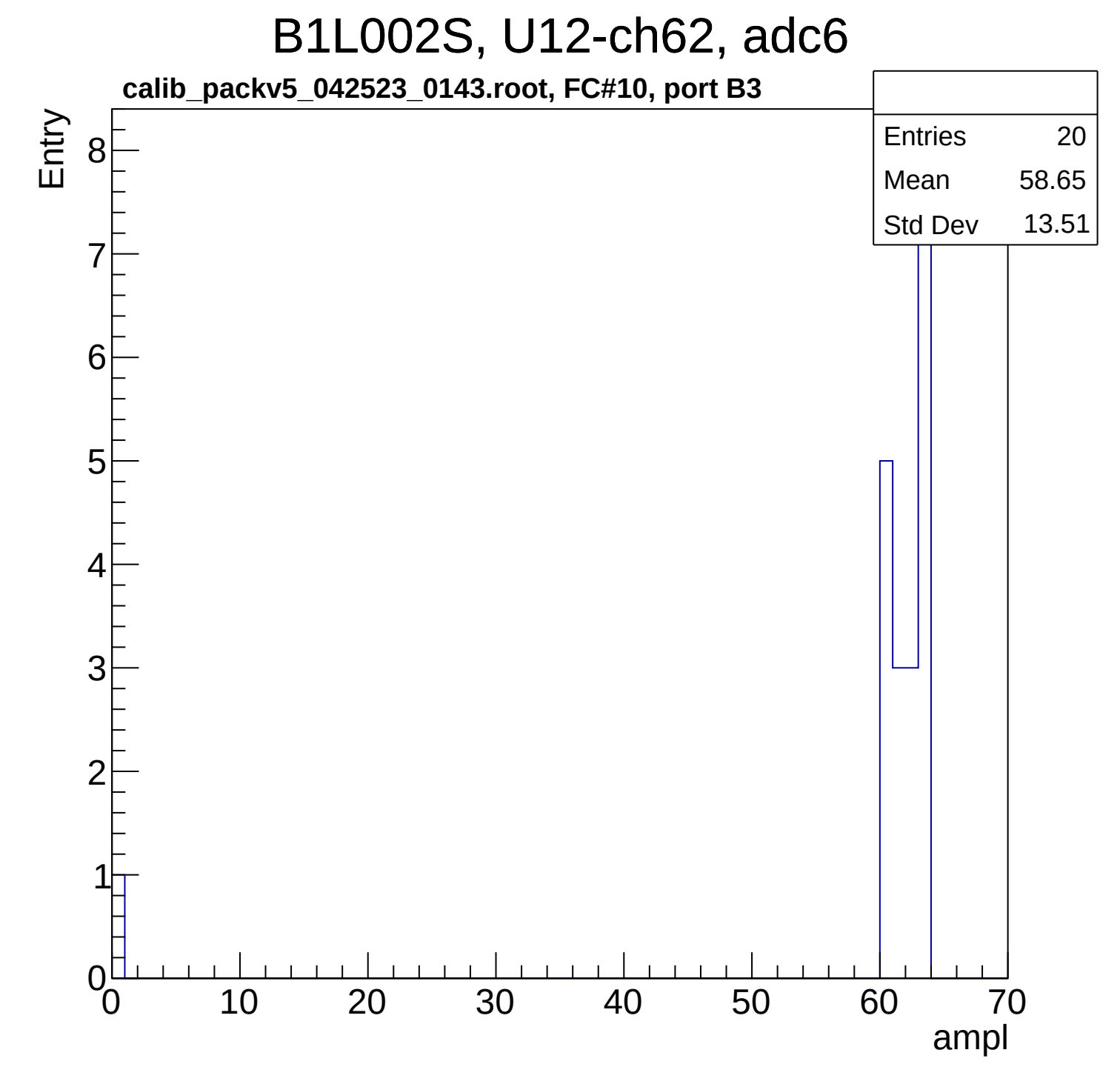
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.65
Std Dev 13.51
Entry
ampl
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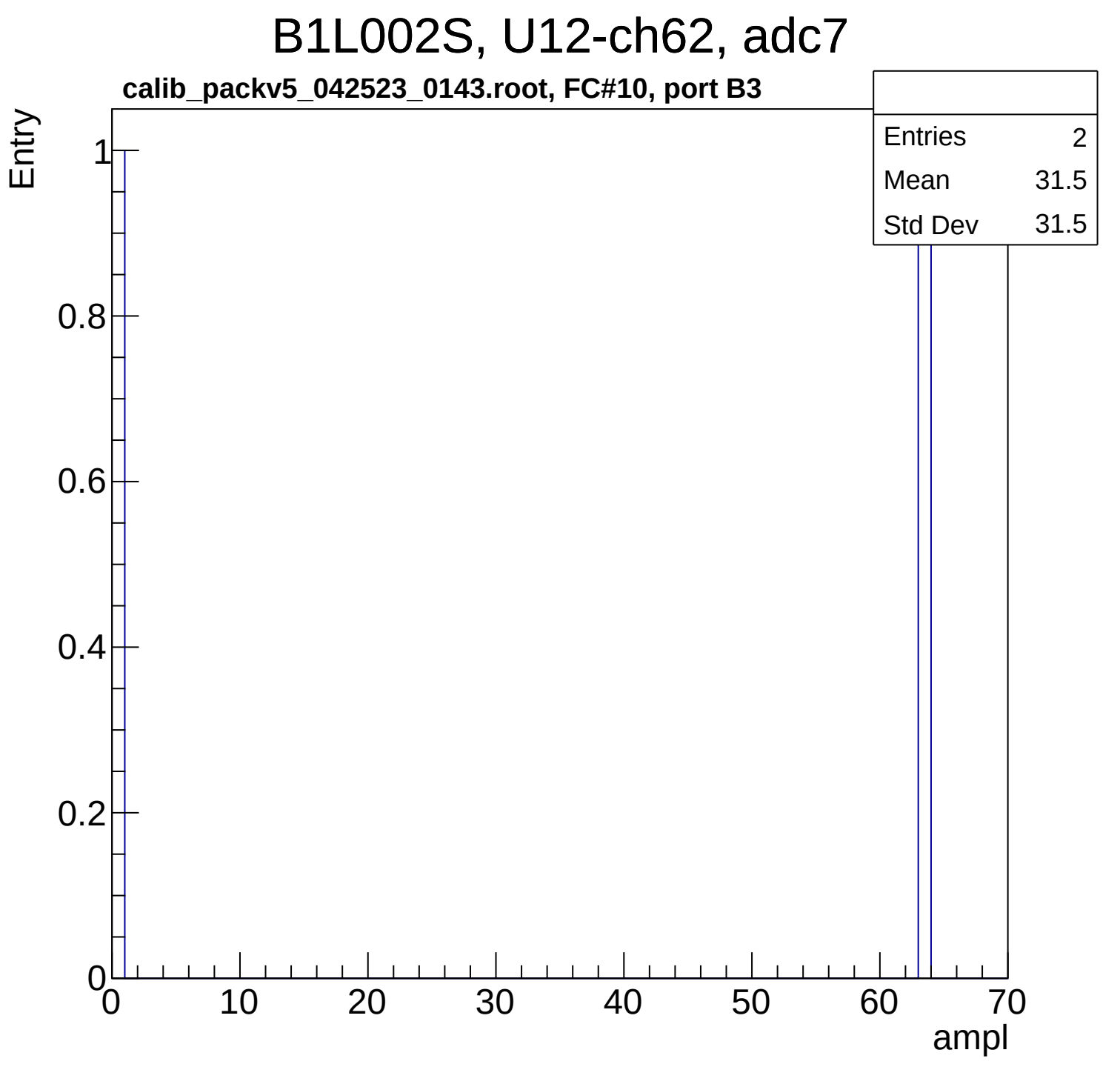
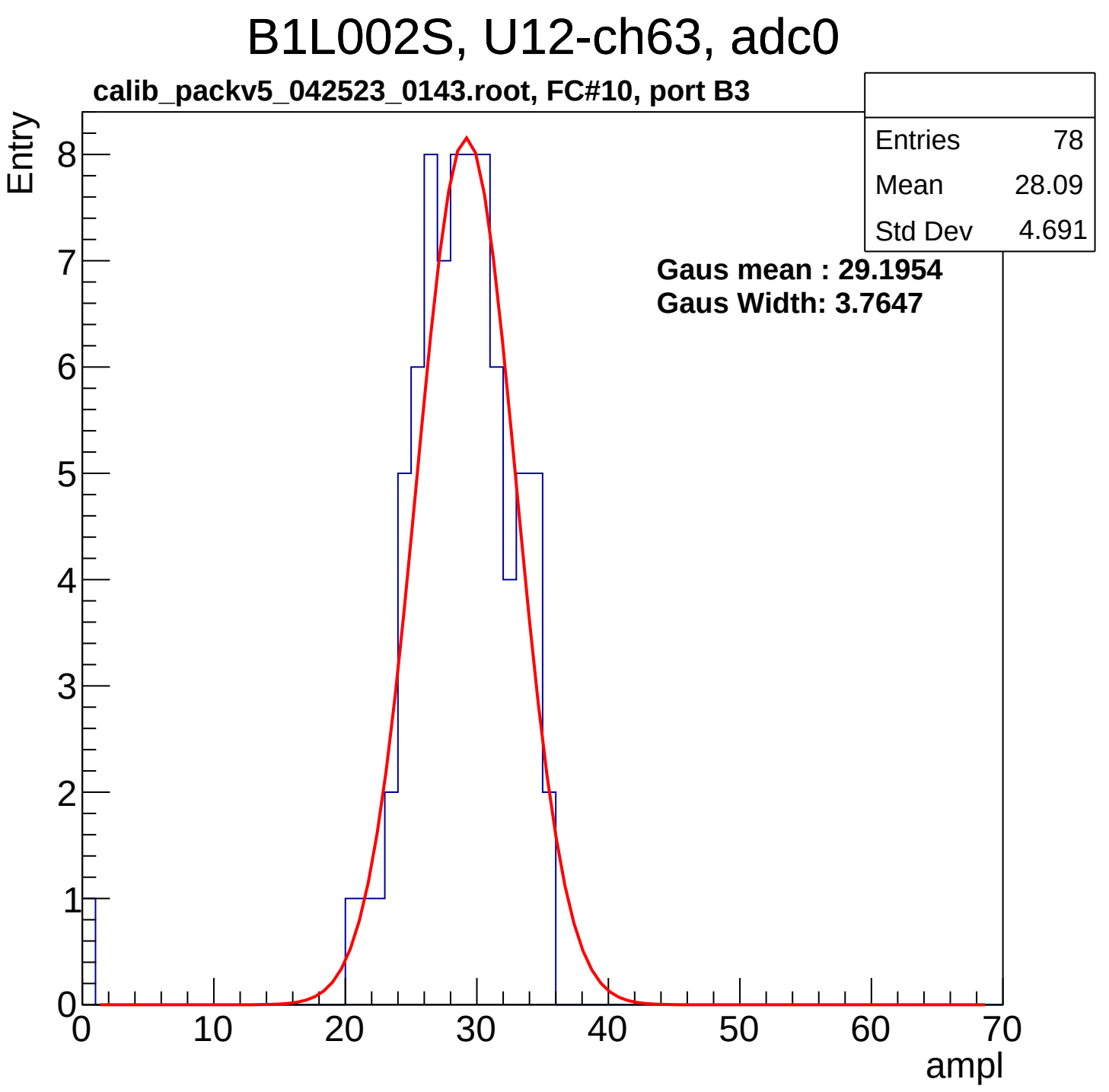
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.09
Std Dev 4.691
Gaus mean : 29.1954
Gaus Width: 3.7647
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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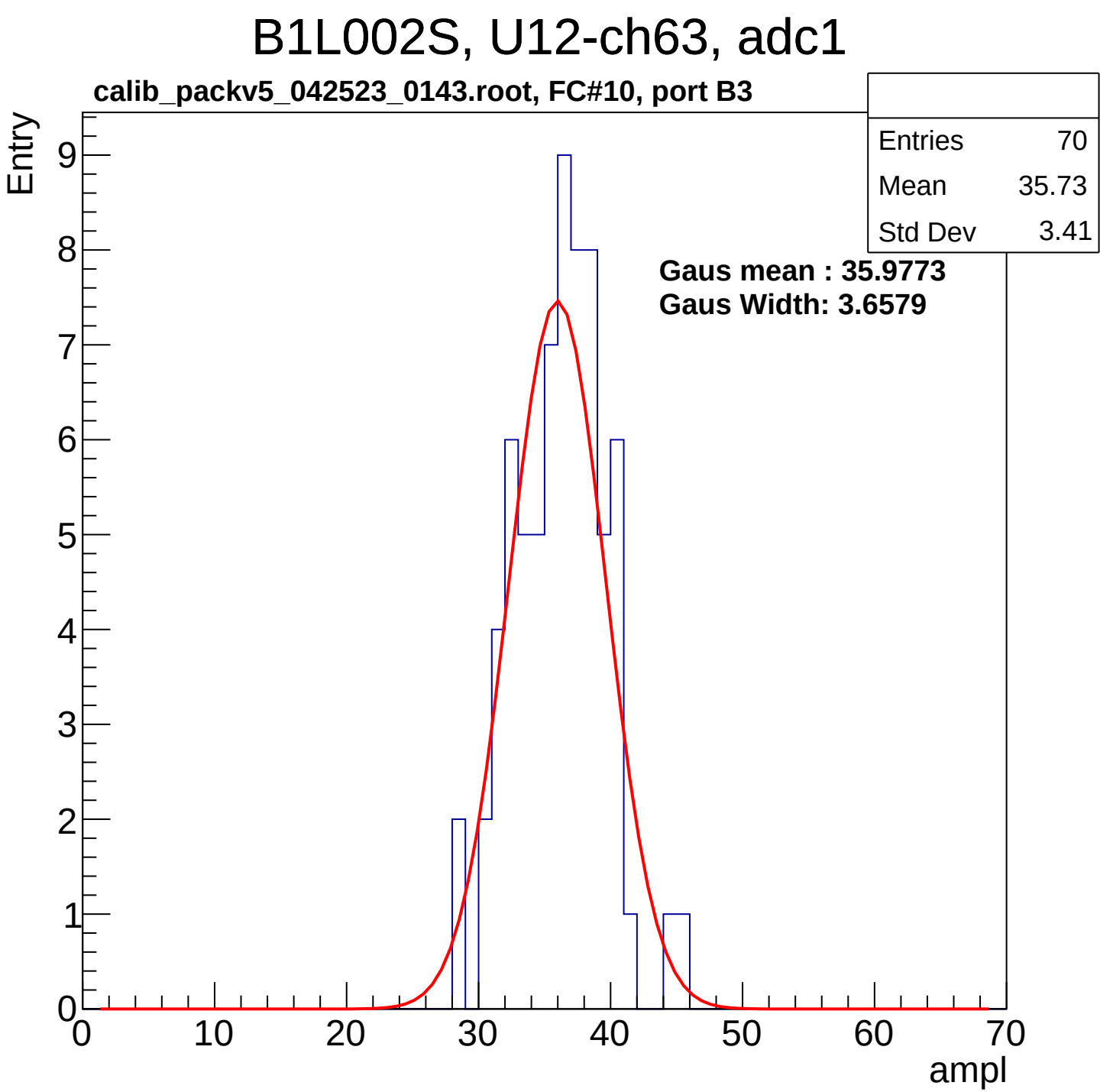
B1L002S, U12-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.73
Std Dev 3.41
Gaus mean : 35.9773
Gaus Width: 3.6579
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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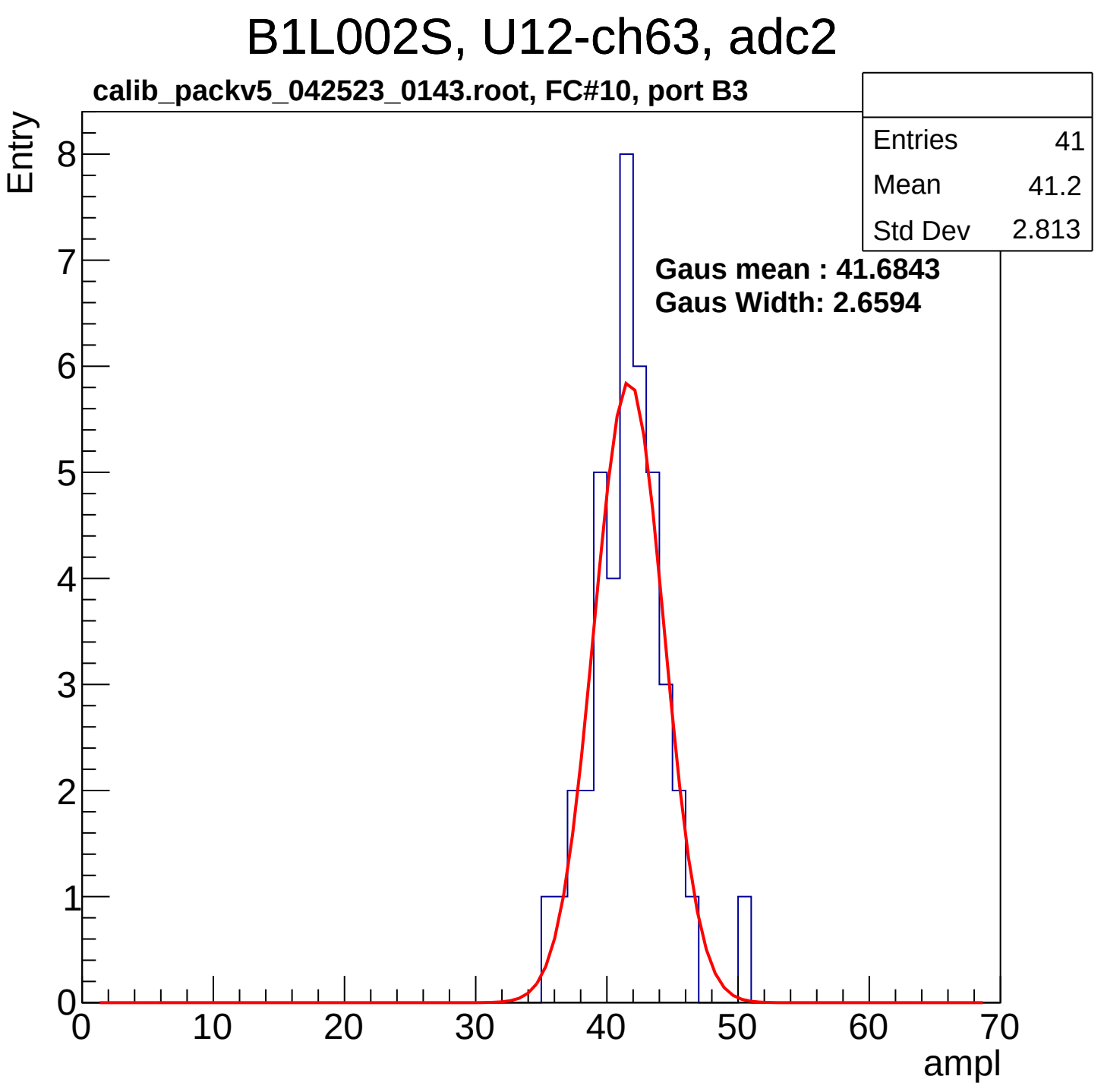
B1L002S, U12-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 41.2
Std Dev 2.813
Gaus mean : 41.6843
Gaus Width: 2.6594
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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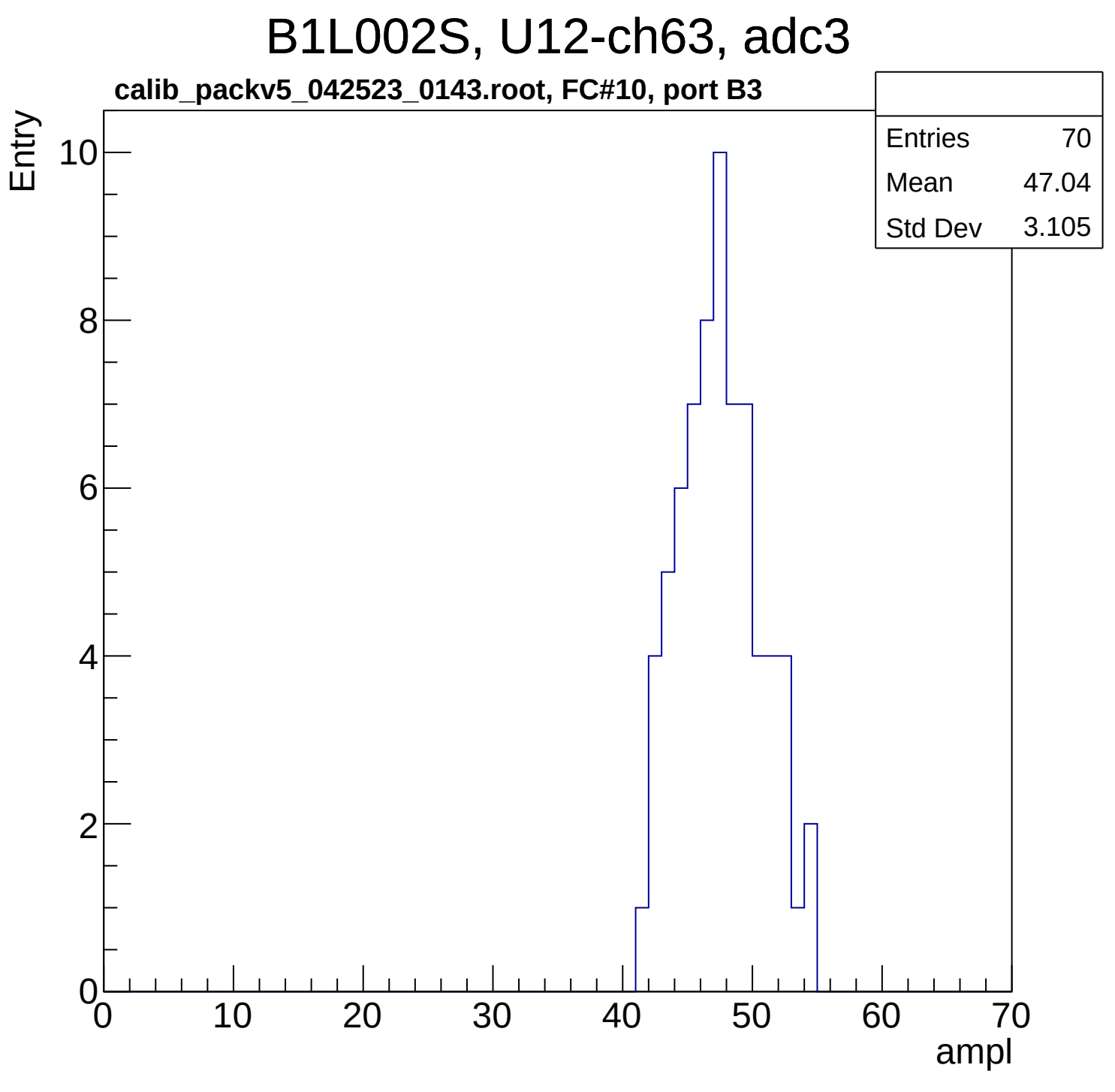
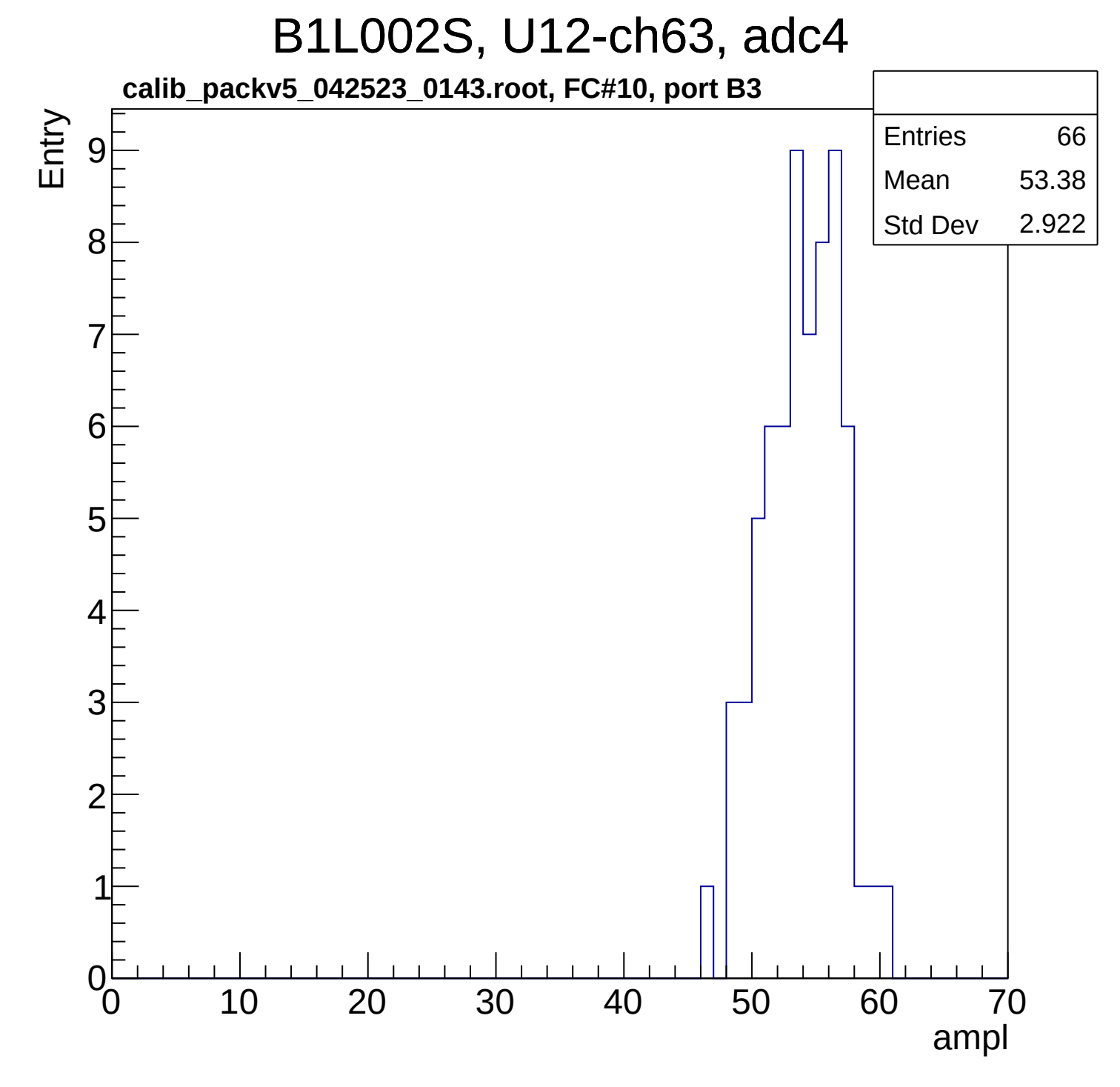

B1L002S, U12-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.04
Std Dev 3.105
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 53.38
Std Dev 2.922
Entry
ampl
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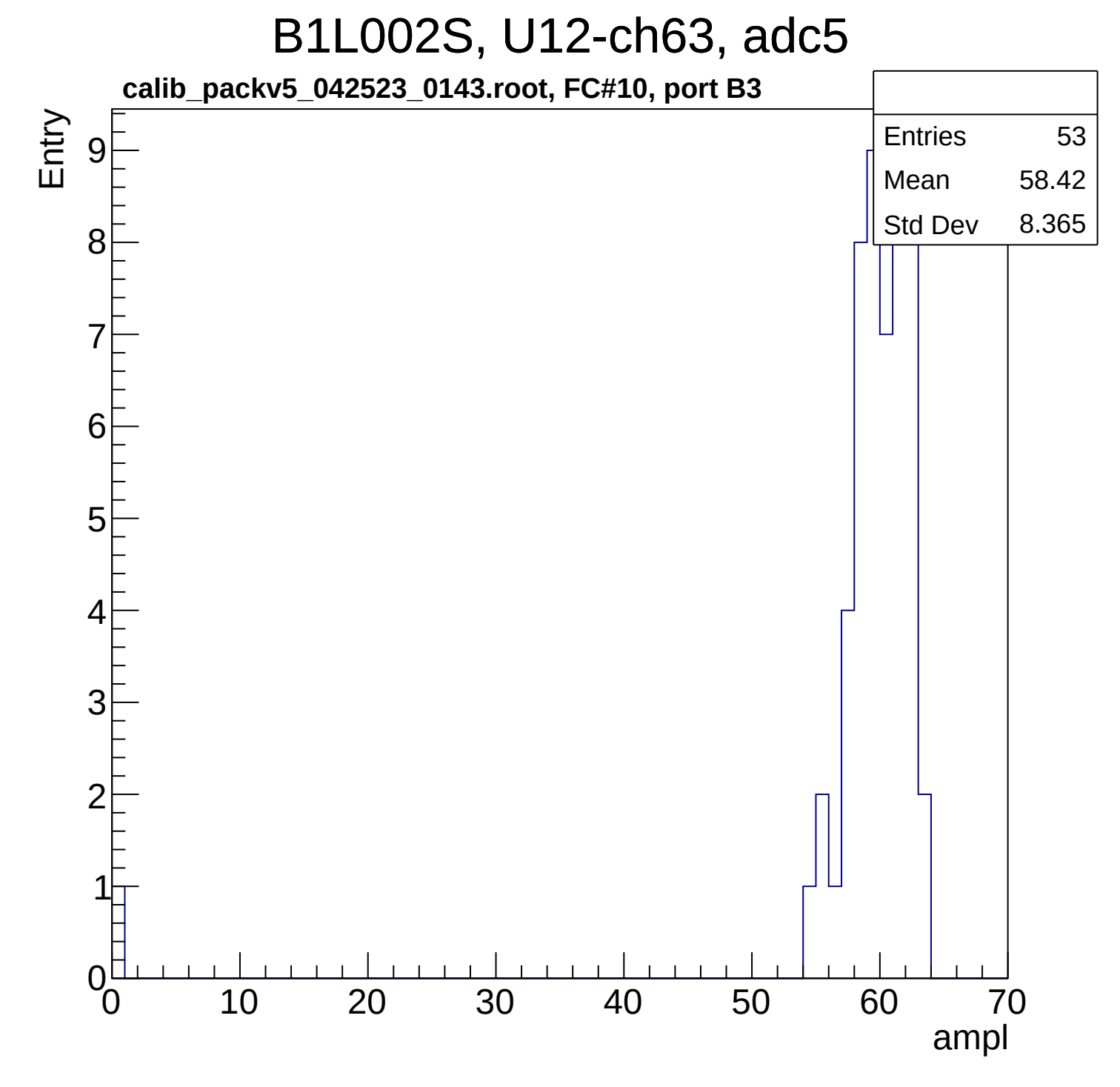
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.42
Std Dev 8.365
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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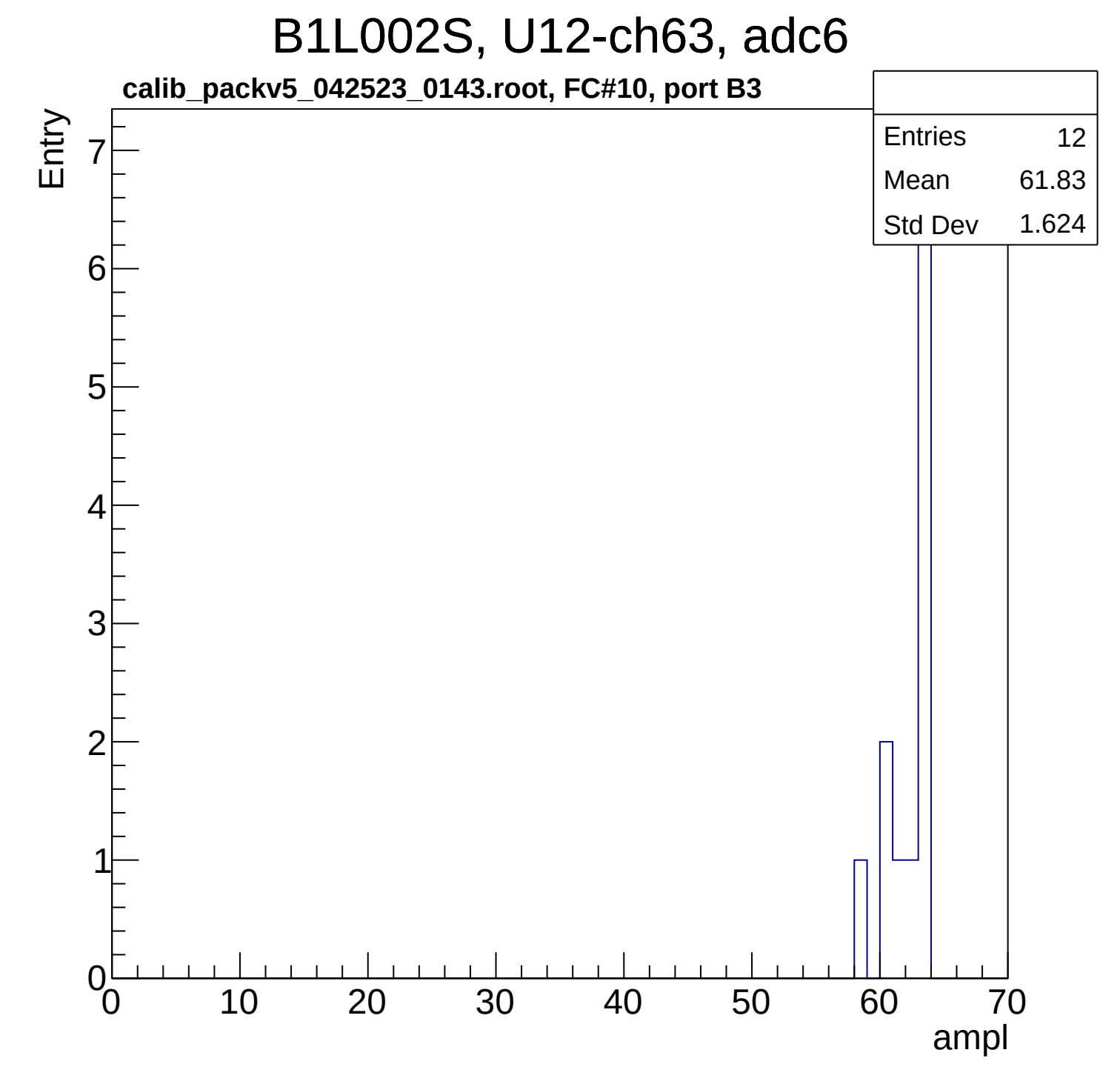
B1L002S, U12-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.83
Std Dev 1.624
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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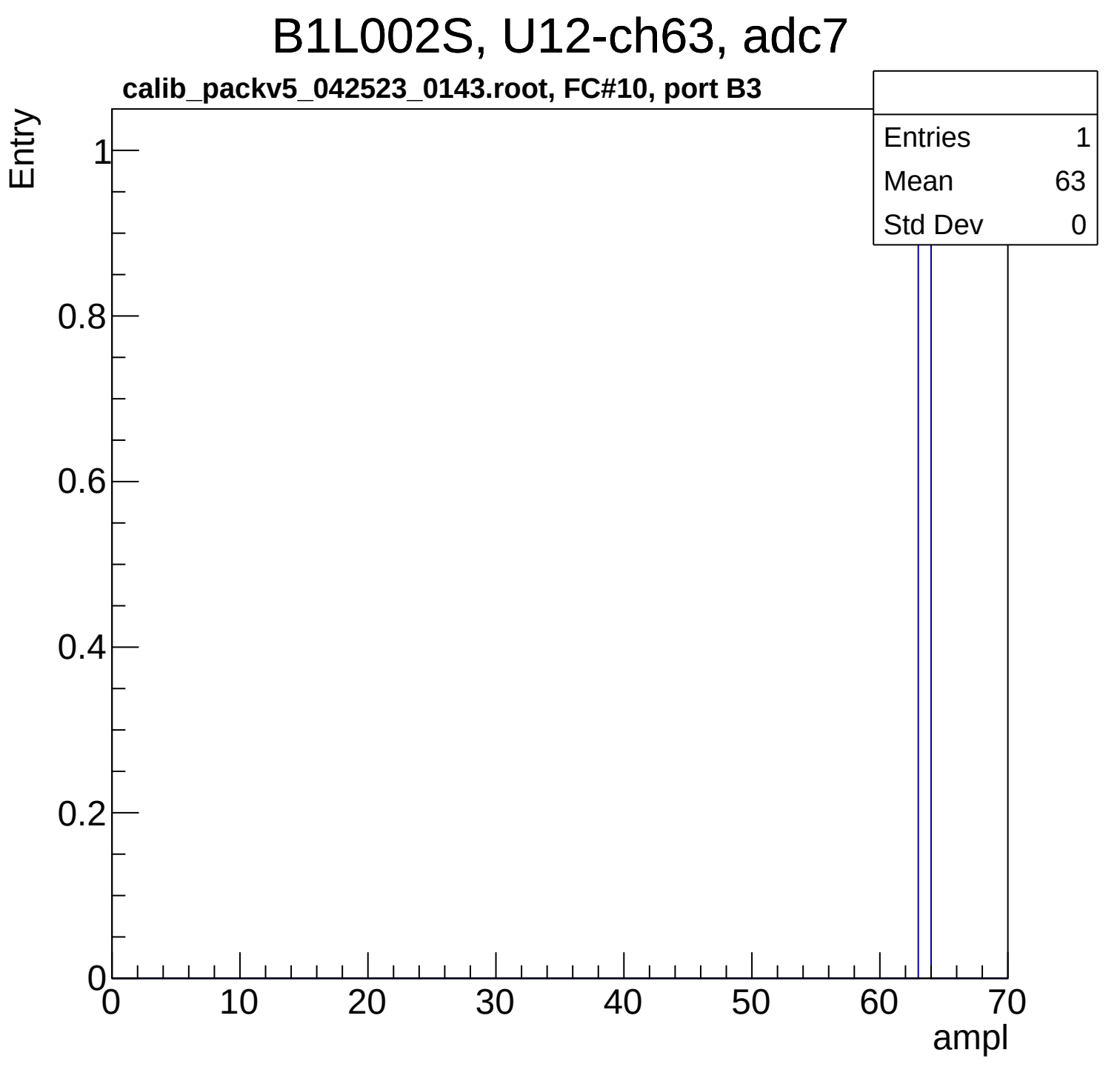
B1L002S, U12-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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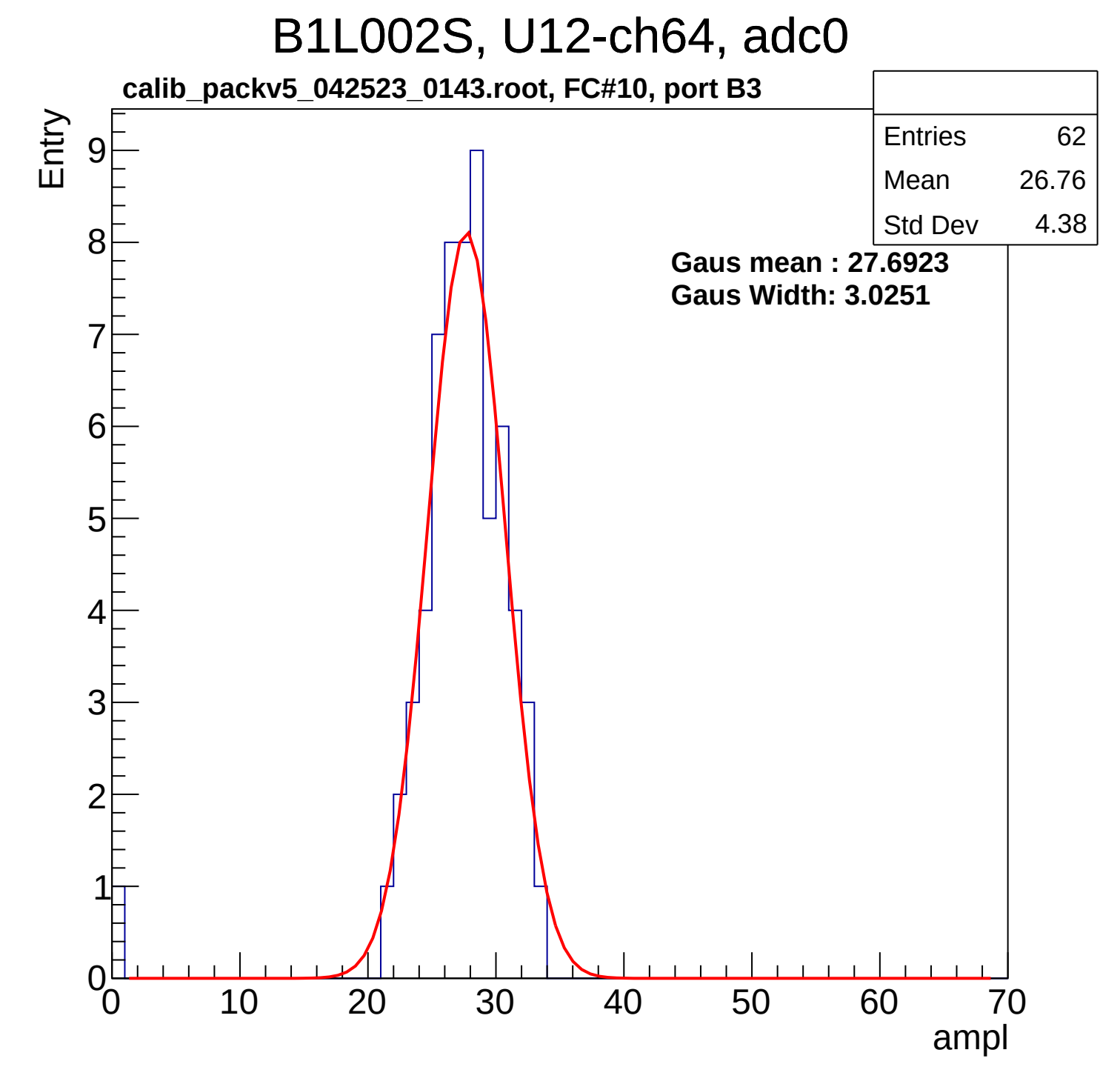
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.76
Std Dev 4.38
Gaus mean : 27.6923
Gaus Width: 3.0251
Entry
ampl
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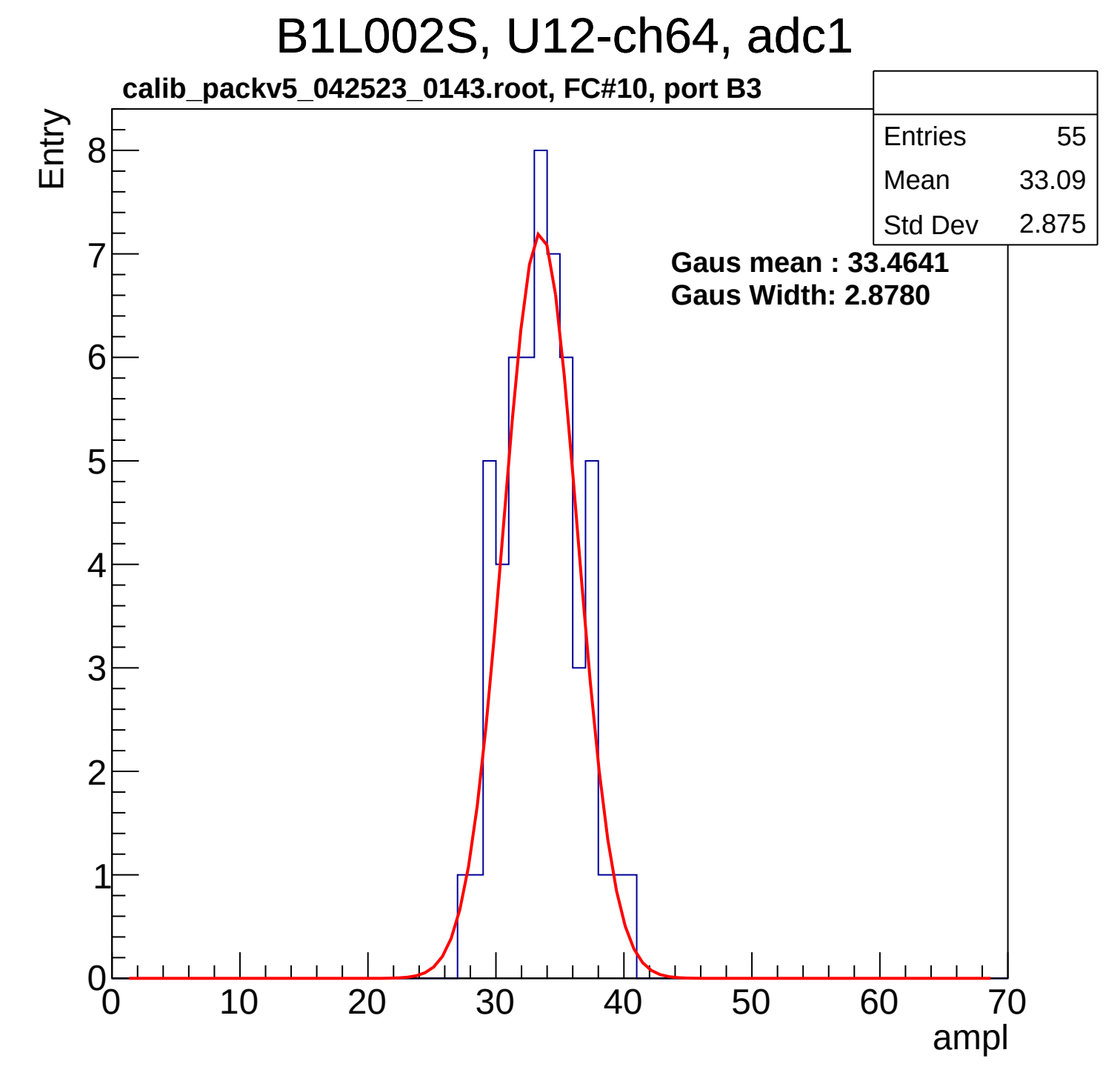
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 33.09
Std Dev 2.875
Gaus mean : 33.4641
Gaus Width: 2.8780
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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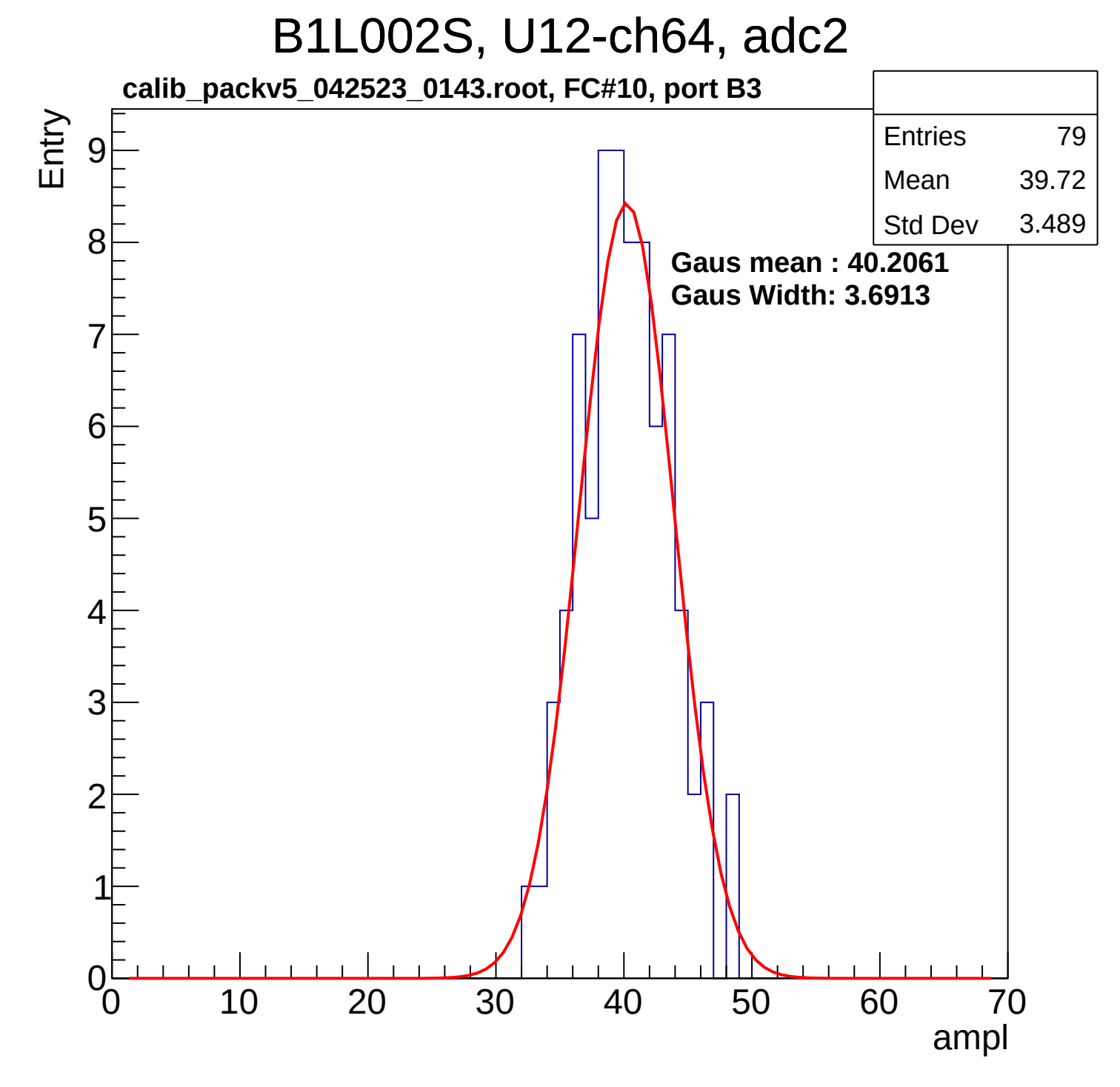
B1L002S, U12-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.72
Std Dev 3.489
Gaus mean : 40.2061
Gaus Width: 3.6913
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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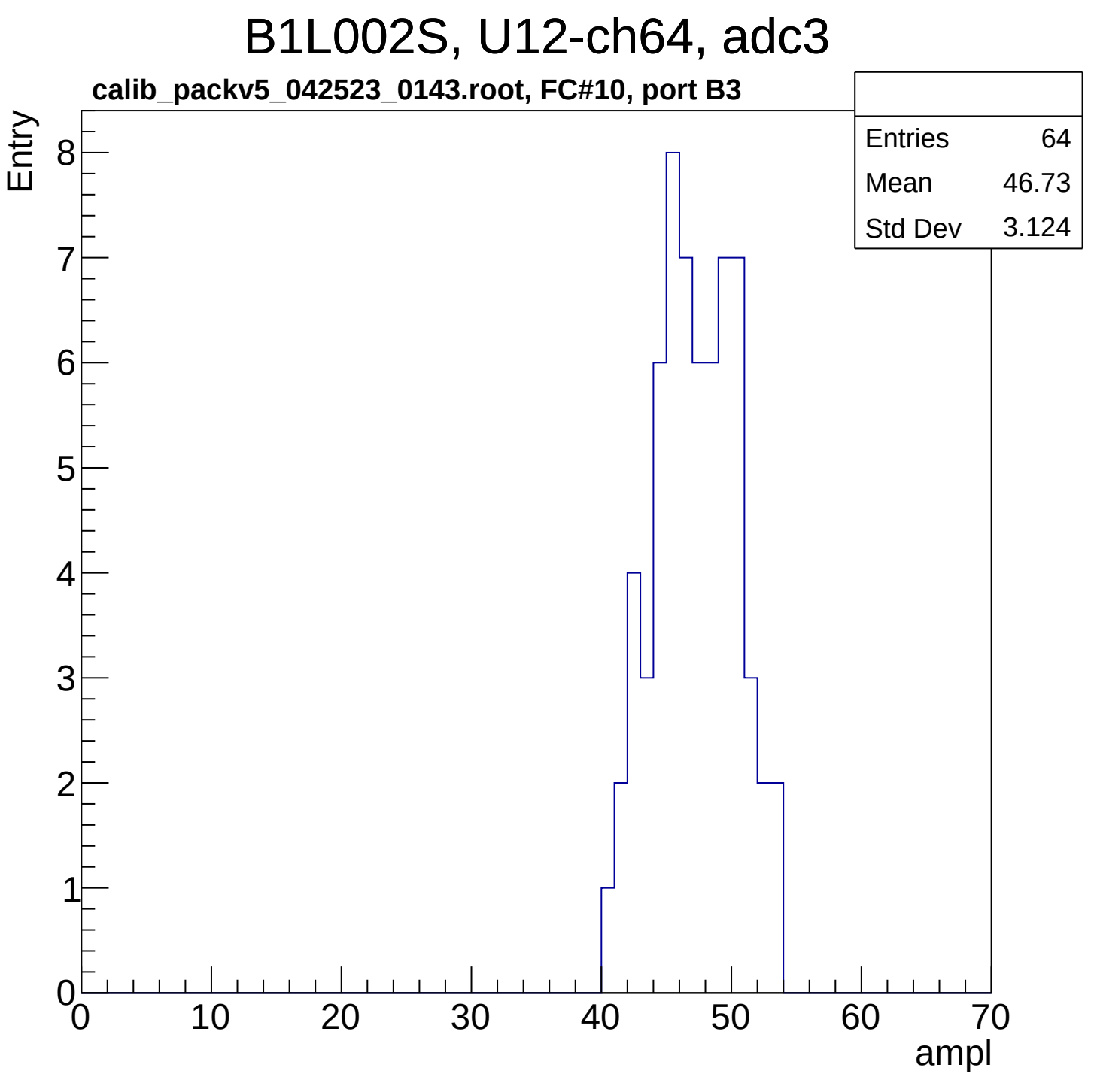
B1L002S, U12-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.73
Std Dev 3.124
Entry
ampl
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4
5
6
7
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0
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60
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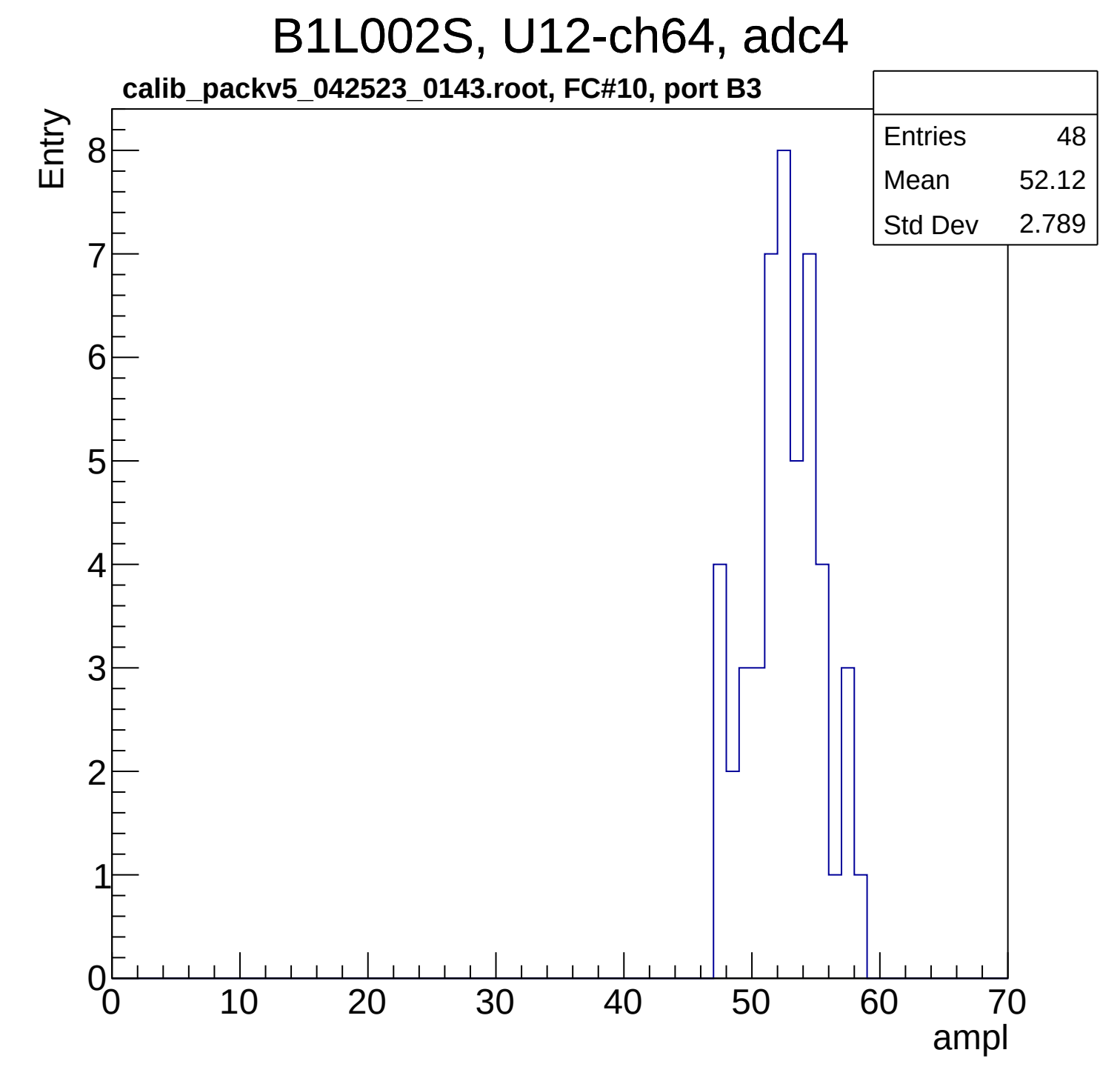
B1L002S, U12-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.12
Std Dev 2.789
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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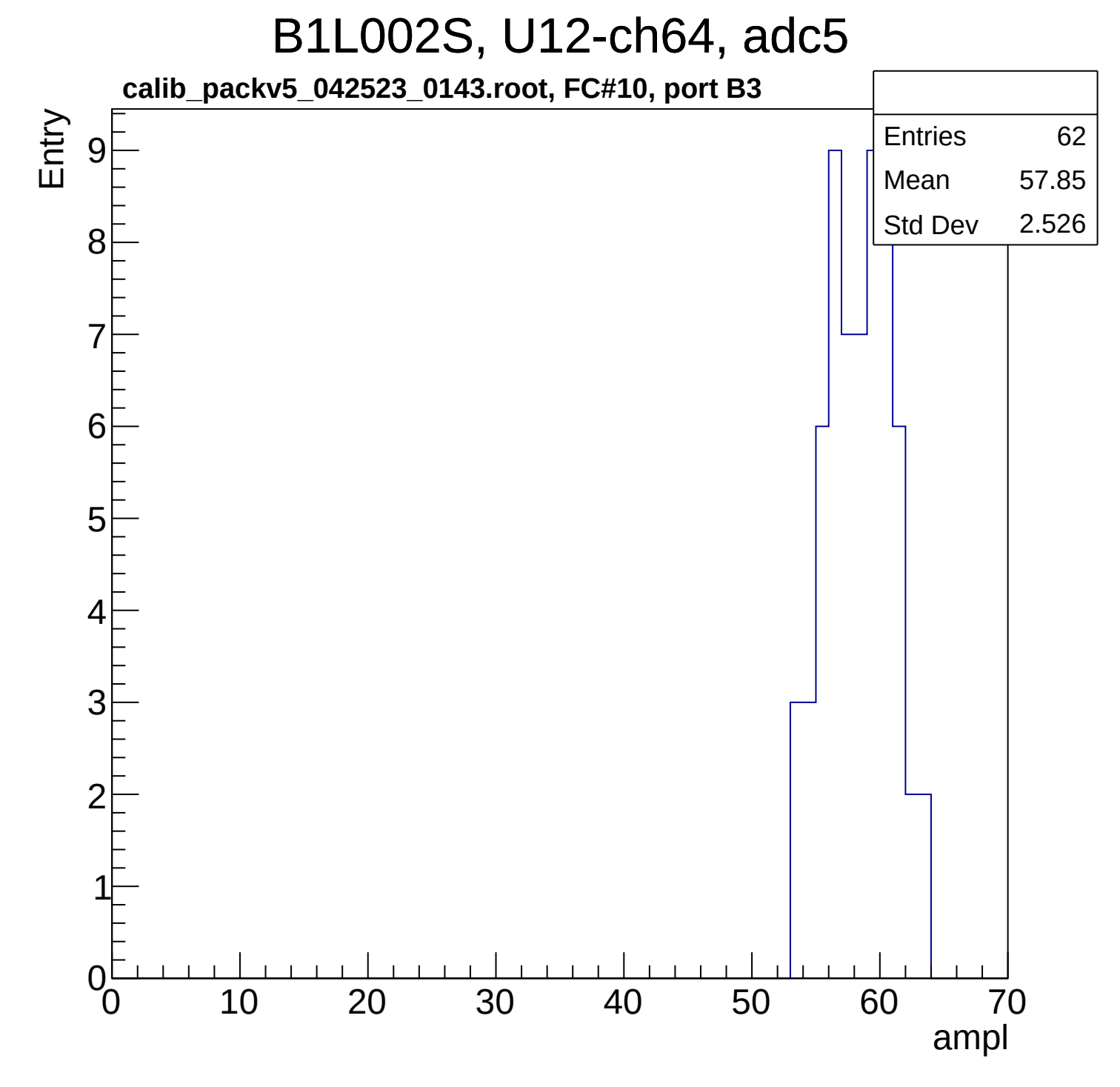
B1L002S, U12-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.85
Std Dev 2.526
Entry
ampl
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3
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6
7
8
9
0
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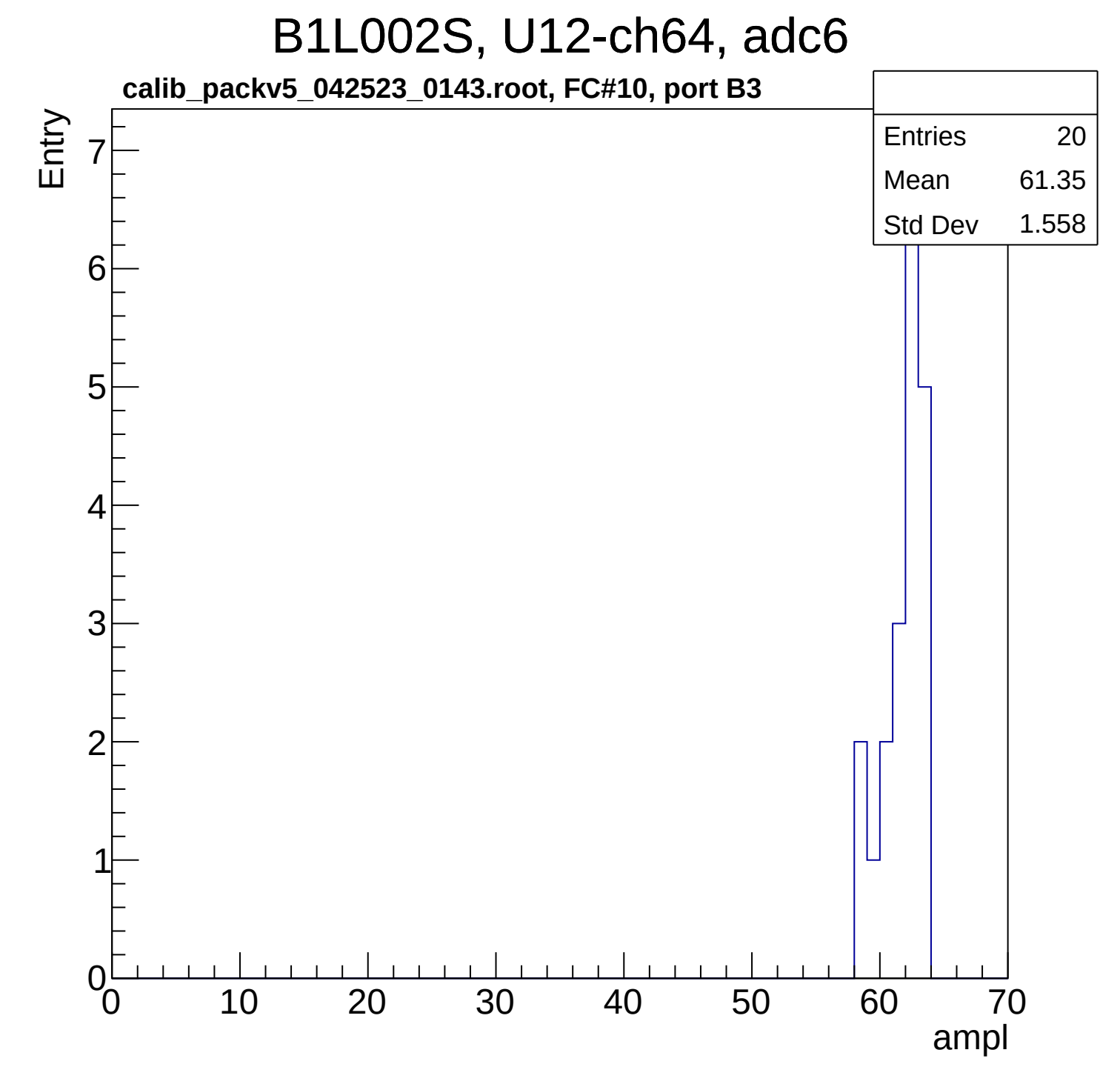
B1L002S, U12-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.35
Std Dev 1.558
Entry
ampl
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2
3
4
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6
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0
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20
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60
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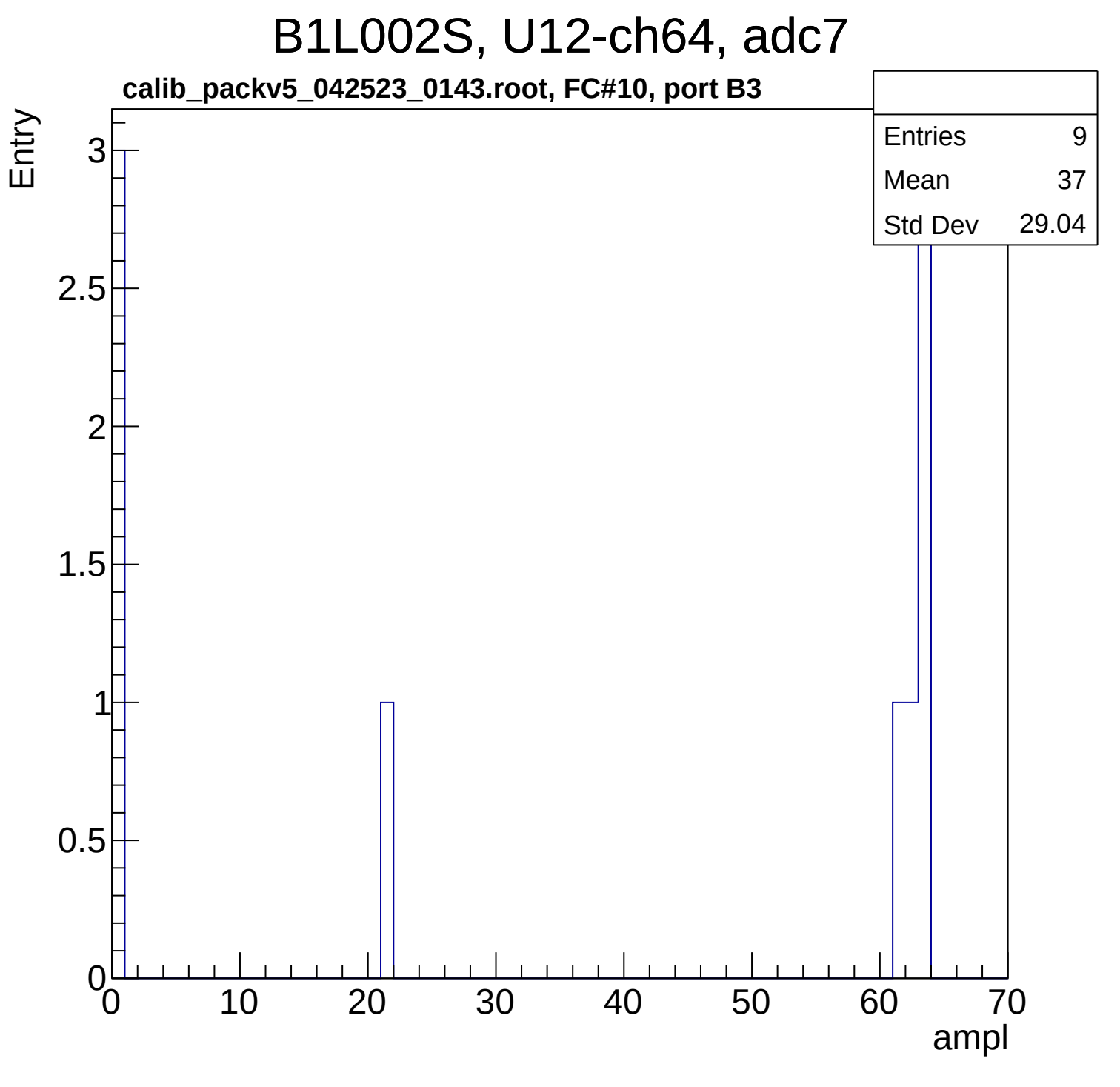
B1L002S, U12-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 37
Std Dev 29.04
Entry
ampl
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1
1.5
2
2.5
3
0
10
20
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40
50
60
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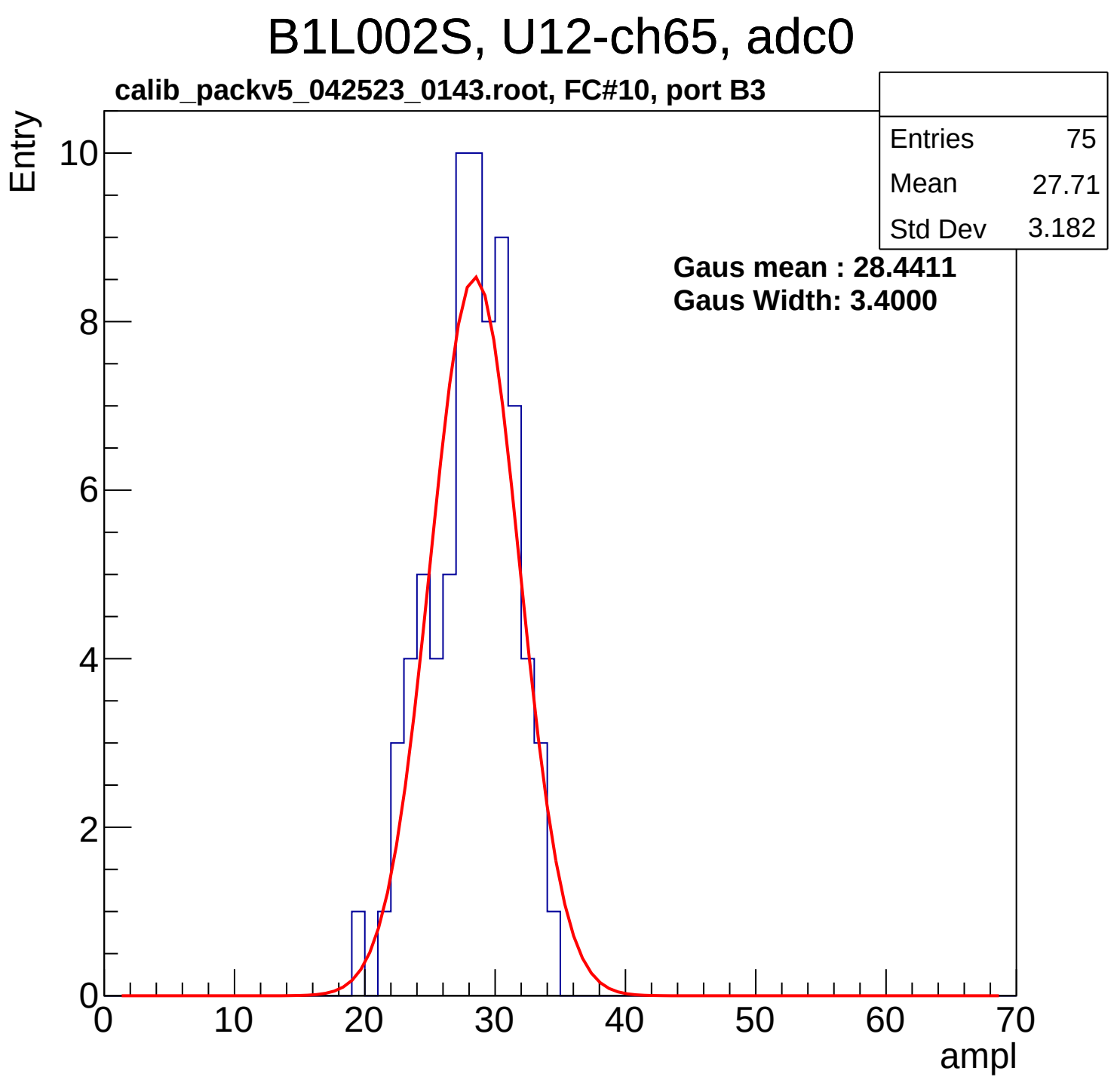
B1L002S, U12-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.71
Std Dev 3.182
Gaus mean : 28.4411
Gaus Width: 3.4000
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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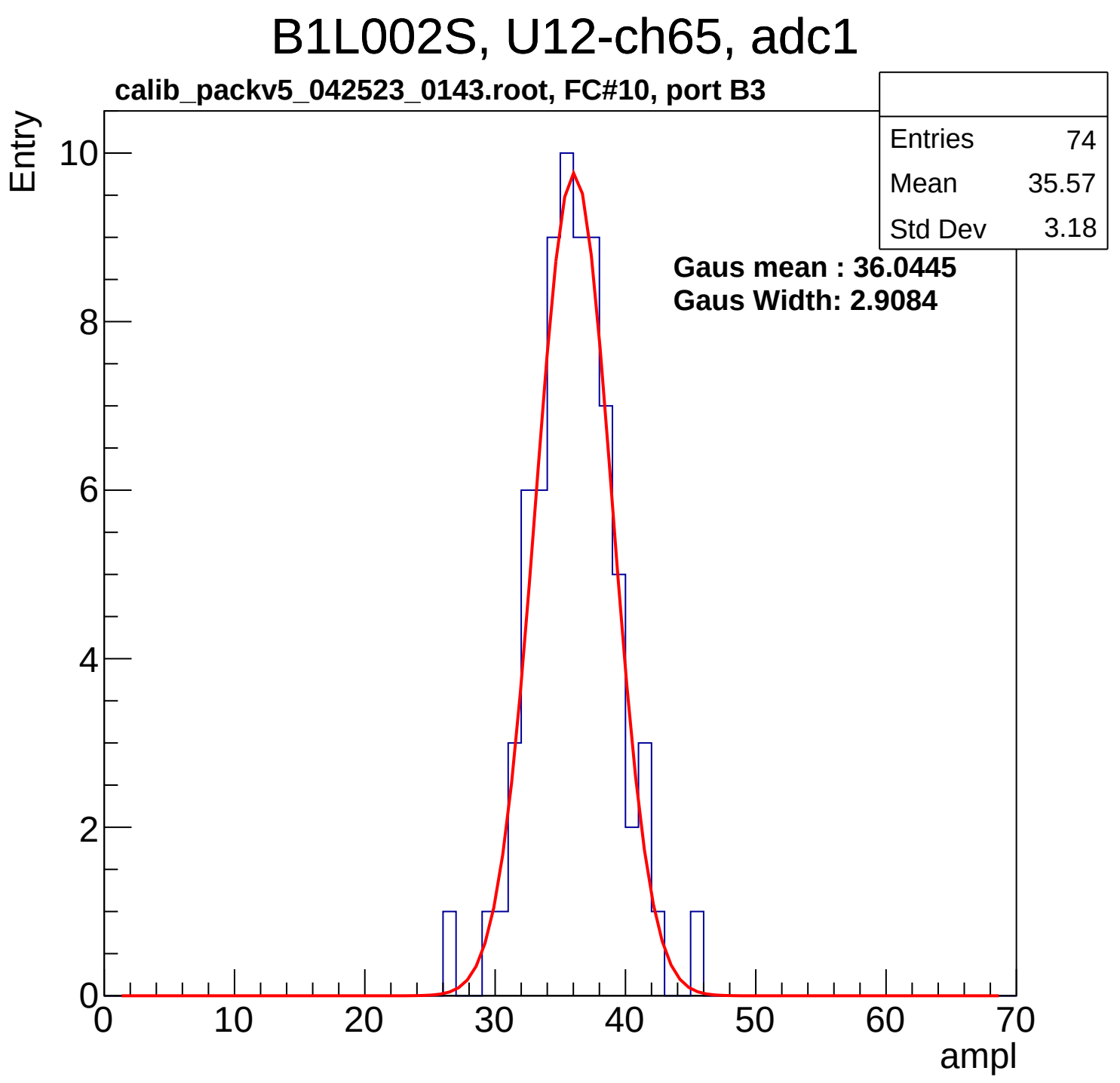
B1L002S, U12-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.57
Std Dev 3.18
Gaus mean : 36.0445
Gaus Width: 2.9084
Entry
ampl
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6
8
10
0
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20
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40
50
60
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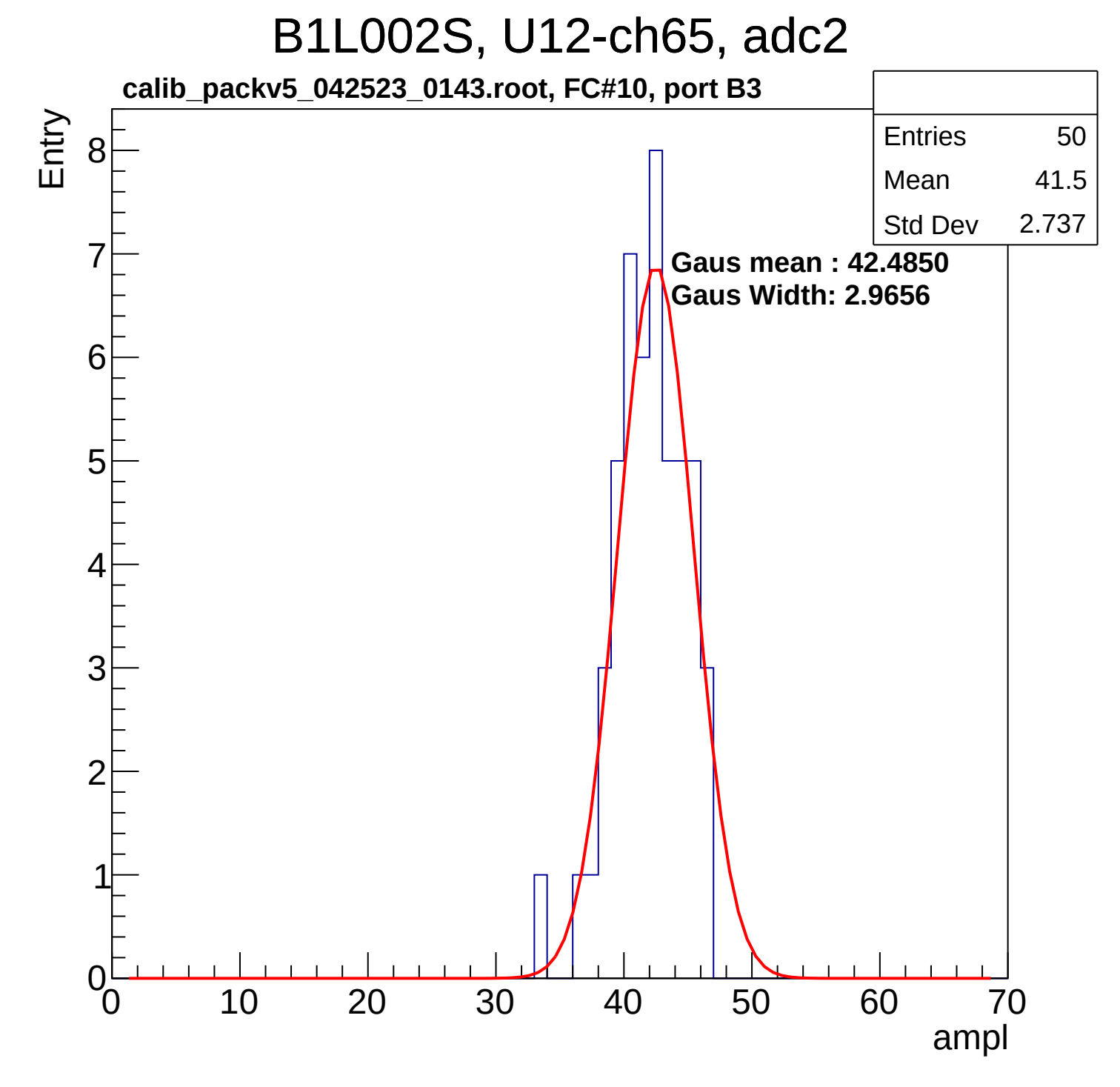
B1L002S, U12-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.5
Std Dev 2.737
Gaus mean : 42.4850
Gaus Width: 2.9656
Entry
ampl
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4
5
6
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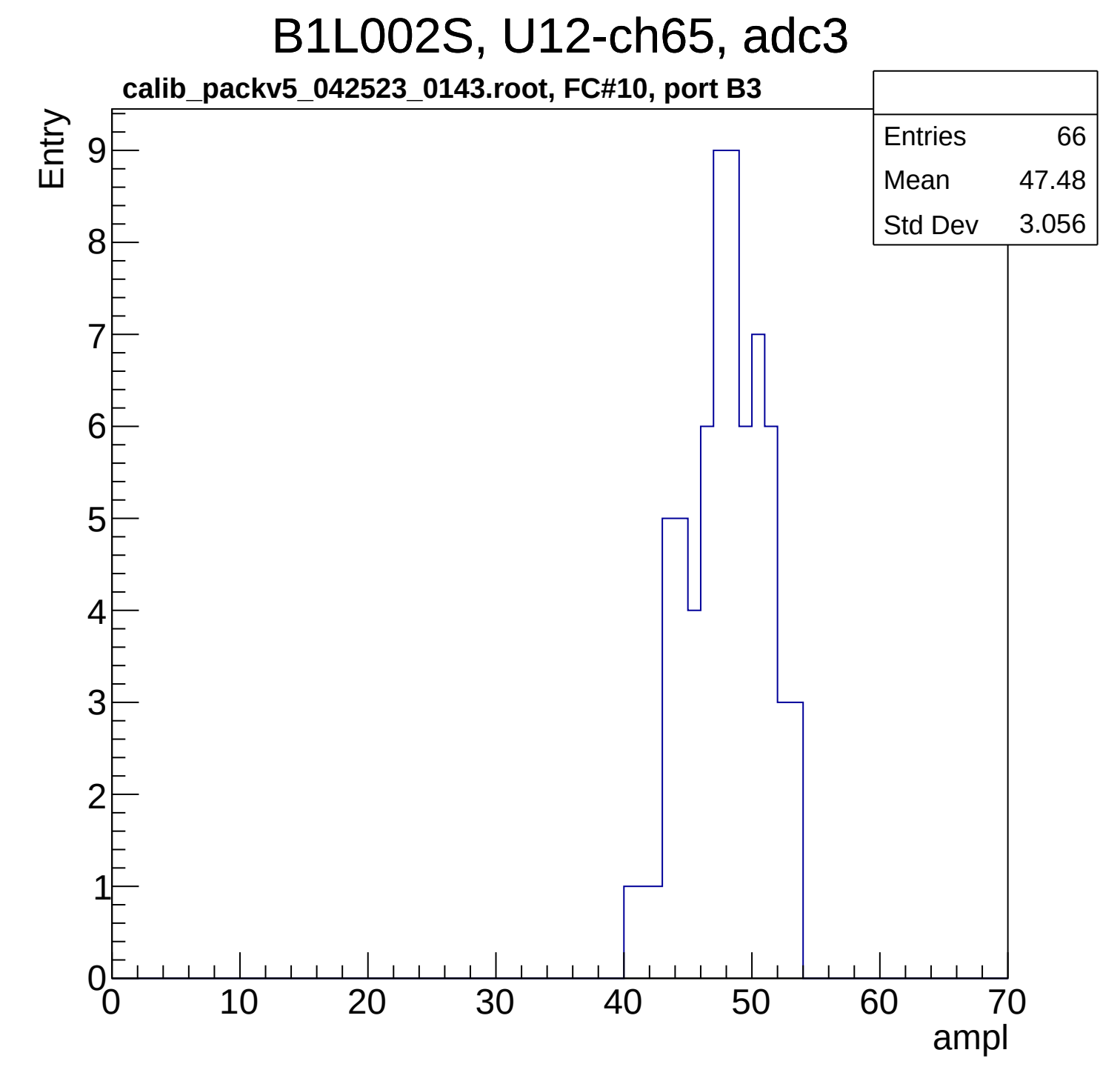
B1L002S, U12-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.48
Std Dev 3.056
Entry
ampl
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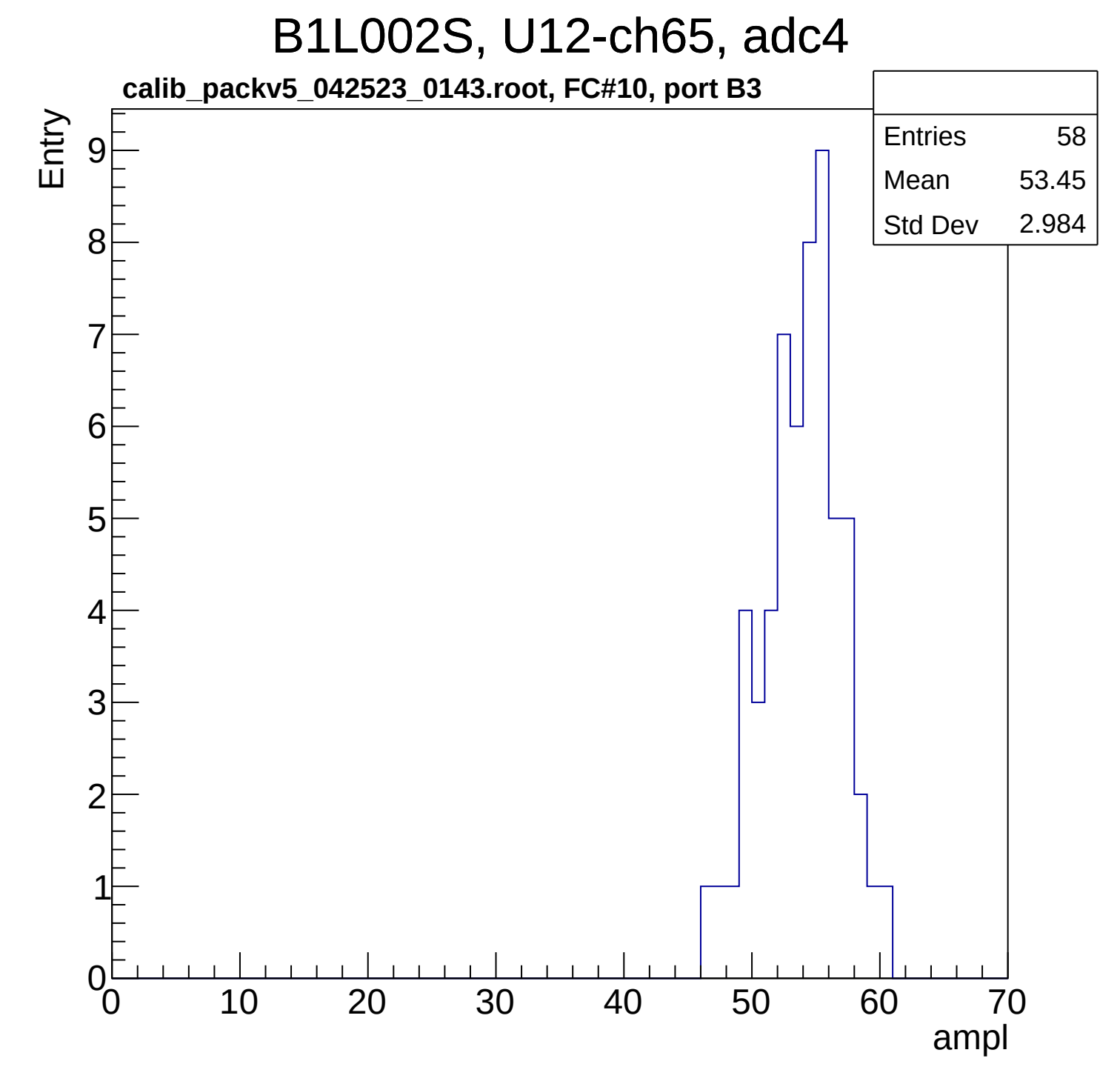
B1L002S, U12-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.45
Std Dev 2.984
Entry
ampl
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2
3
4
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7
8
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0
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20
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40
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60
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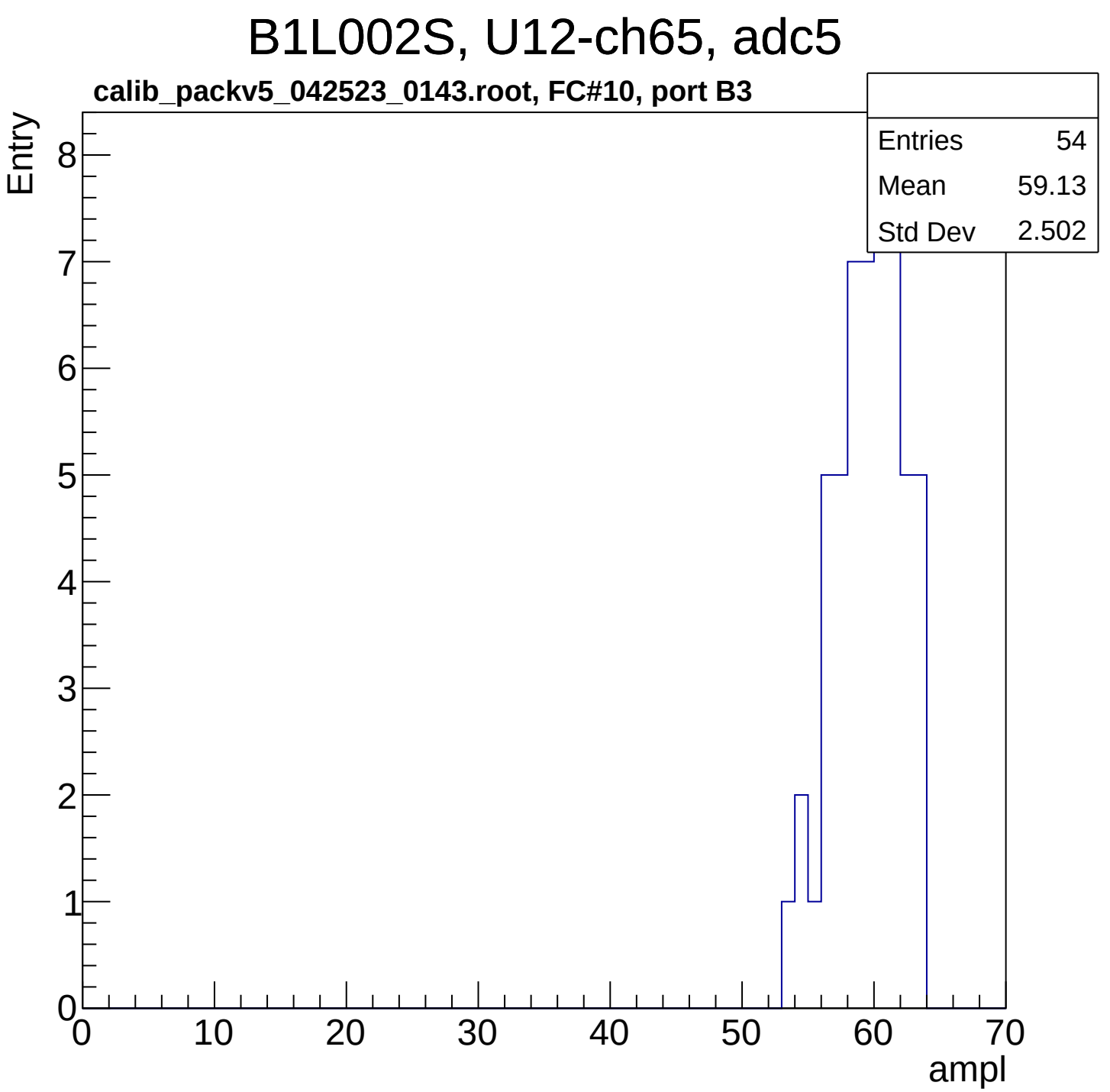
B1L002S, U12-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.13
Std Dev 2.502
Entry
ampl
0
1
2
3
4
5
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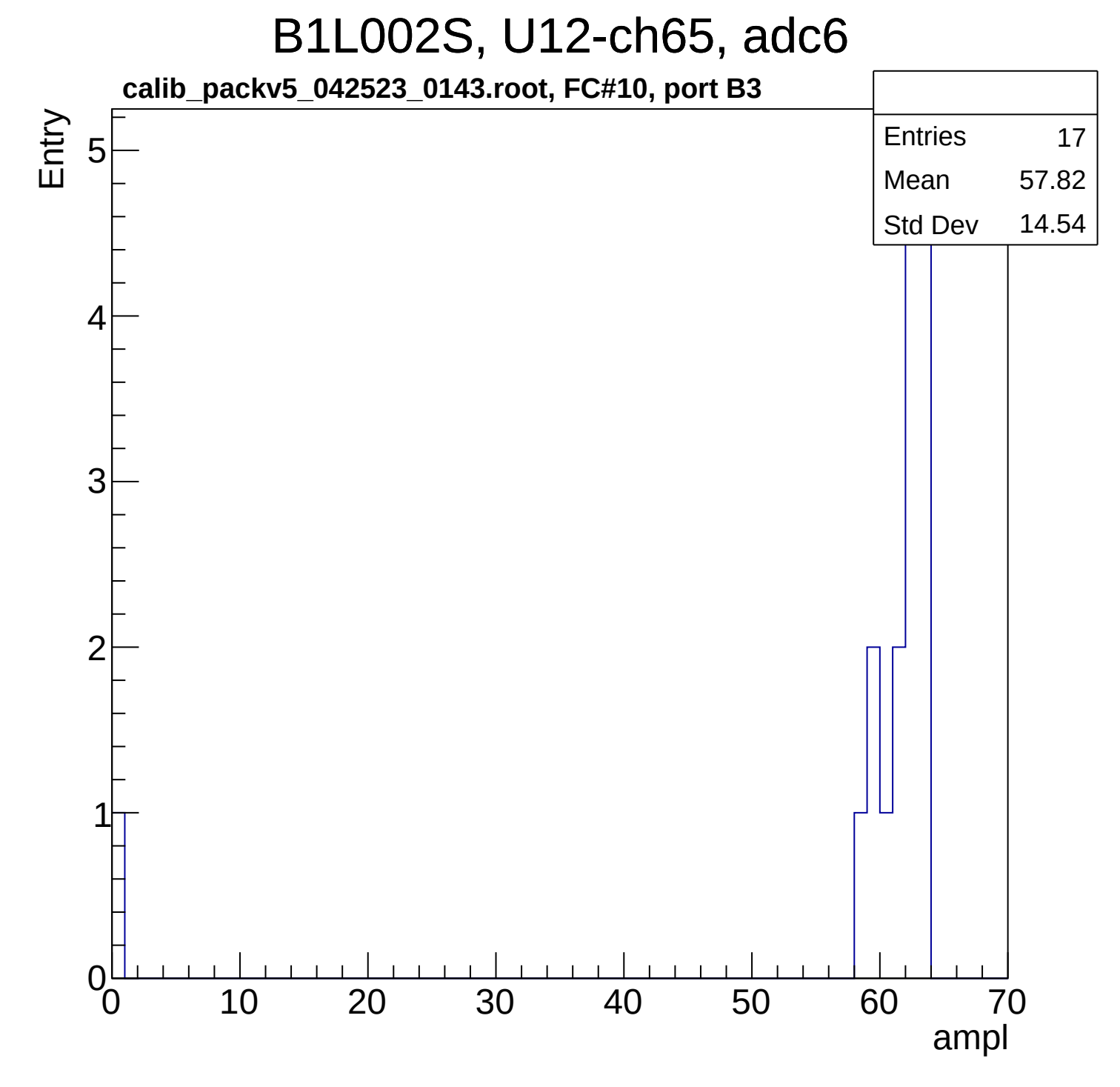
B1L002S, U12-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.82
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
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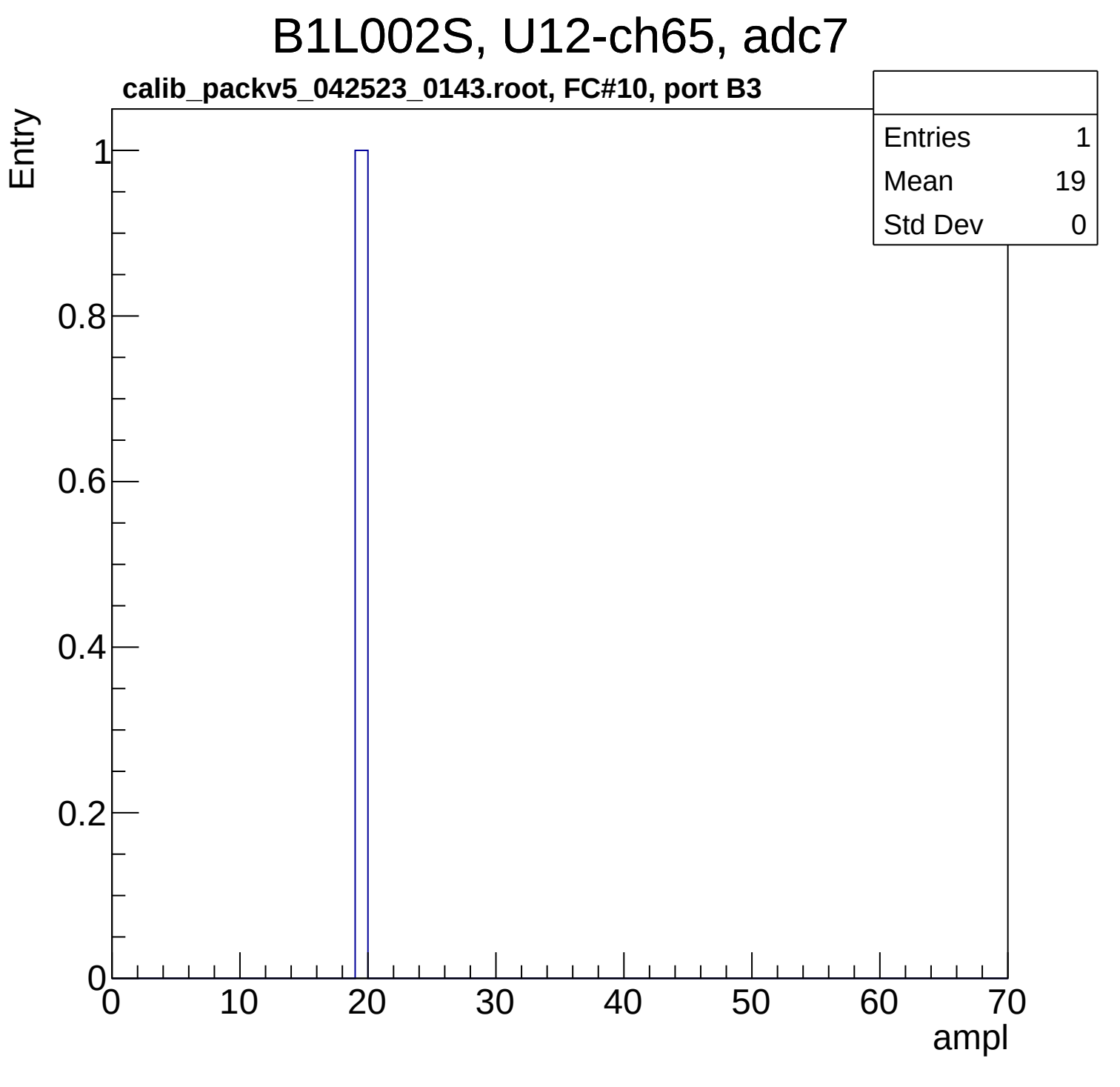
50
60
70
ampl


B1L002S, U12-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
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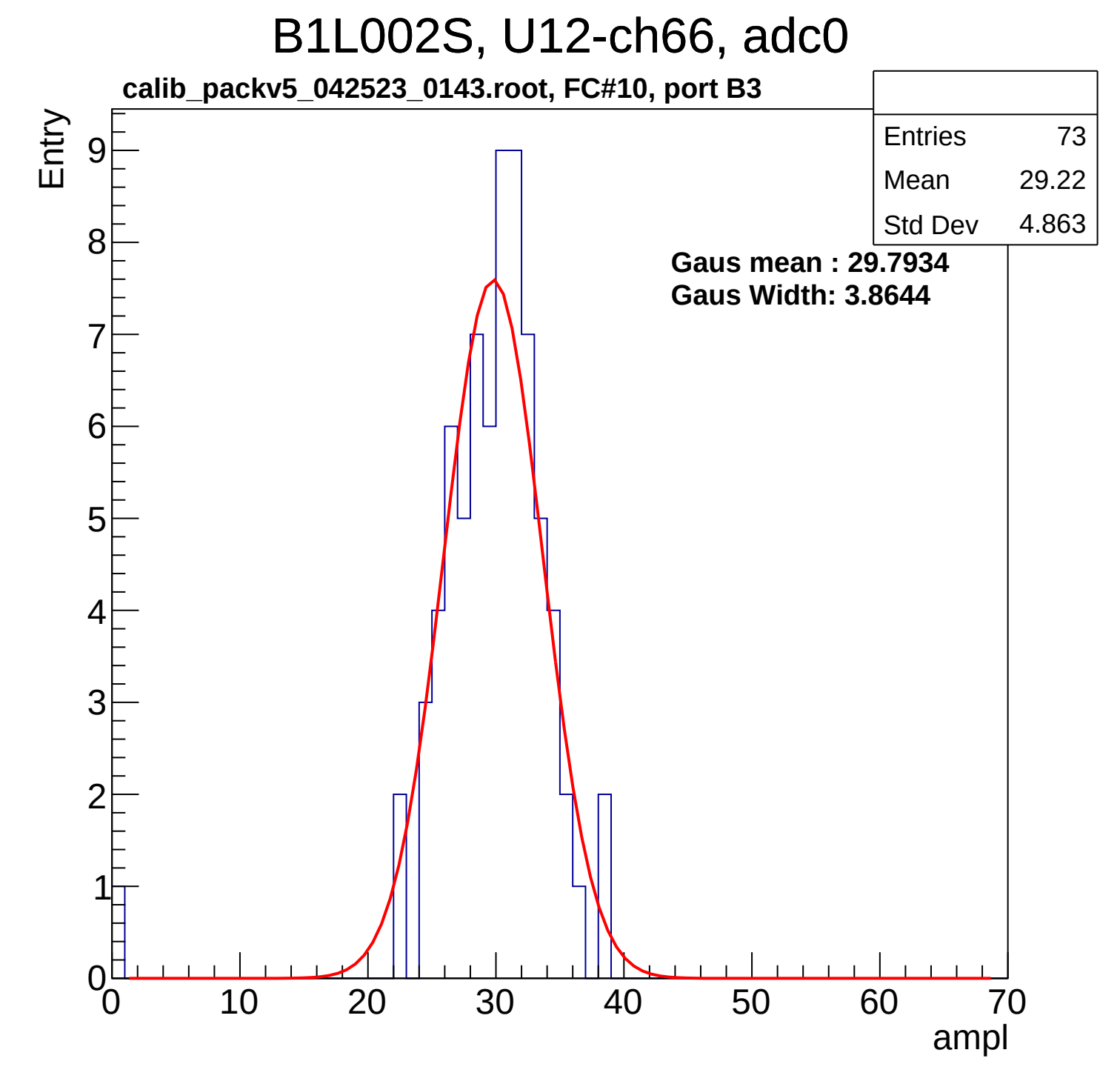
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.22
Std Dev 4.863
Gaus mean : 29.7934
Gaus Width: 3.8644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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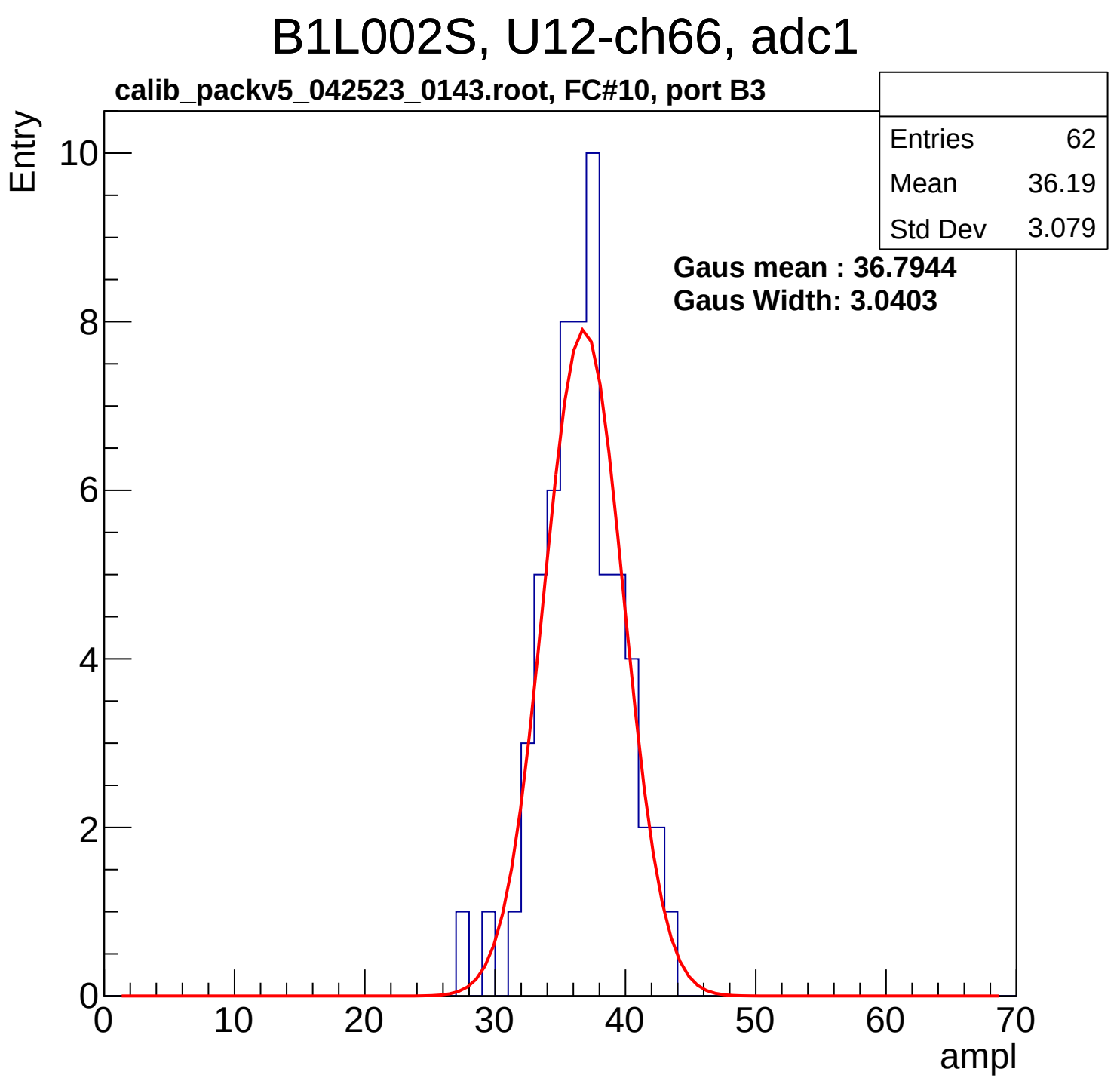
B1L002S, U12-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 36.19
Std Dev 3.079
Gaus mean : 36.7944
Gaus Width: 3.0403
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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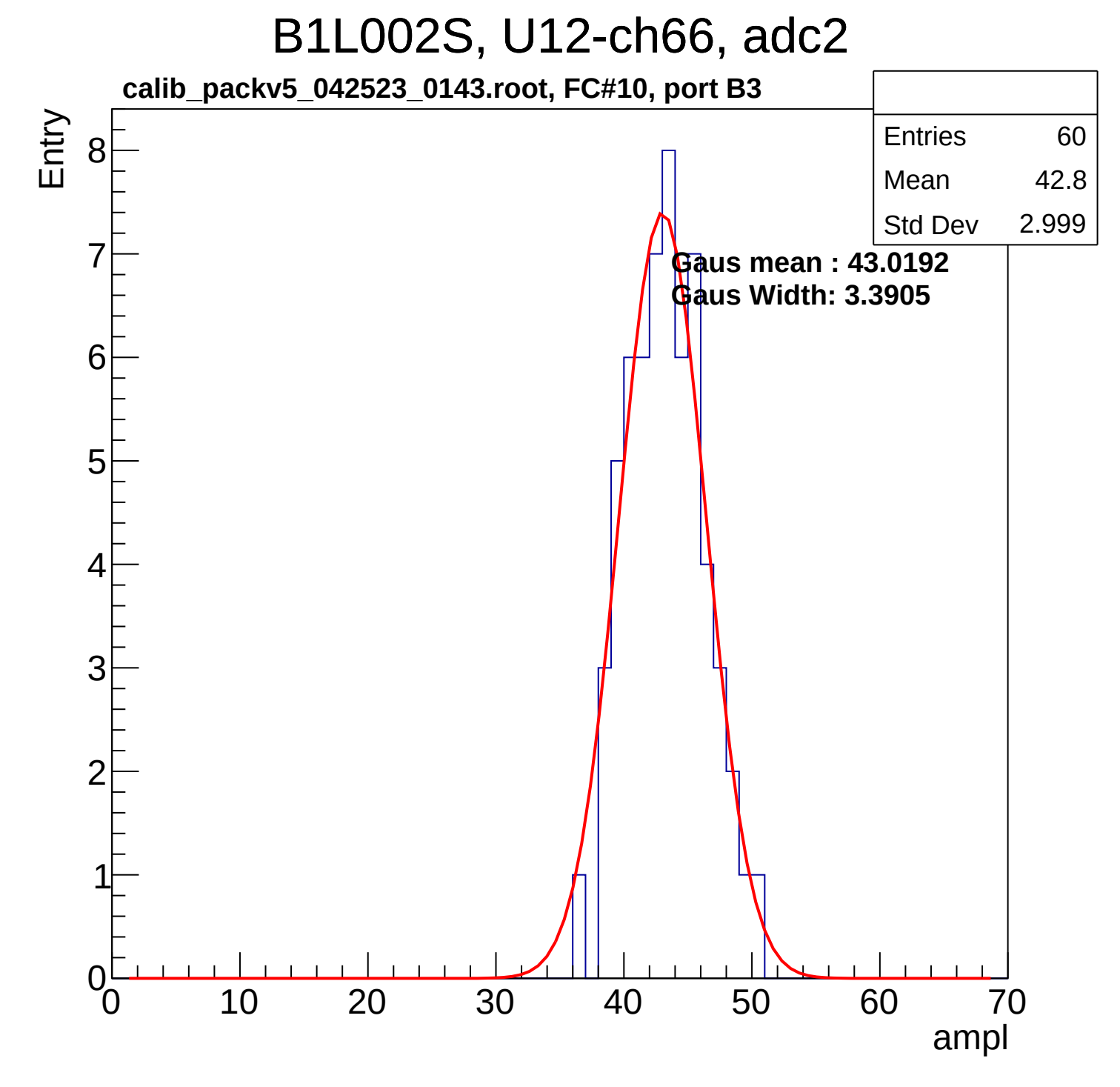
B1L002S, U12-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.8
Std Dev 2.999
Gaus mean : 43.0192
Gaus Width: 3.3905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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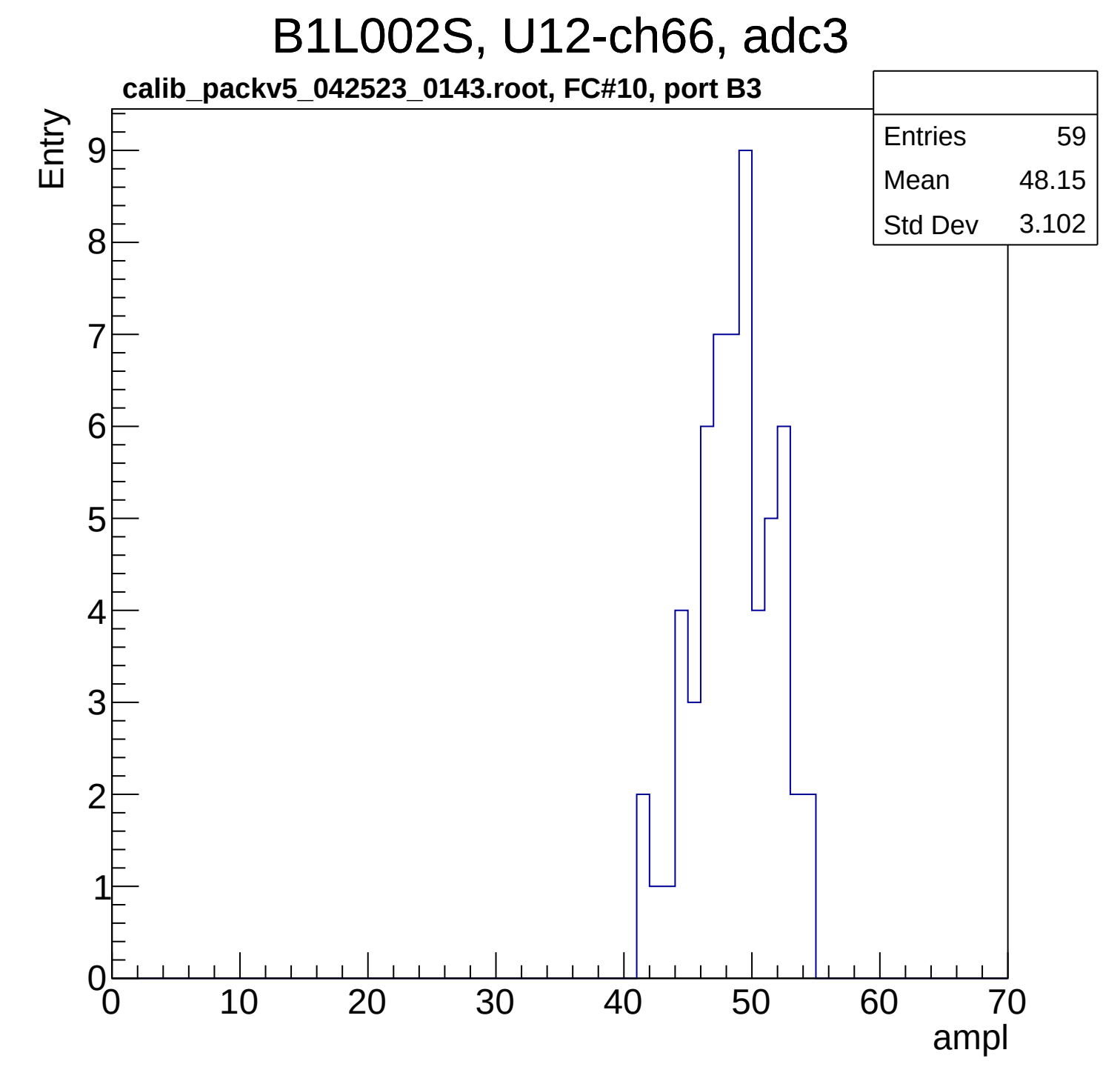
B1L002S, U12-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 48.15
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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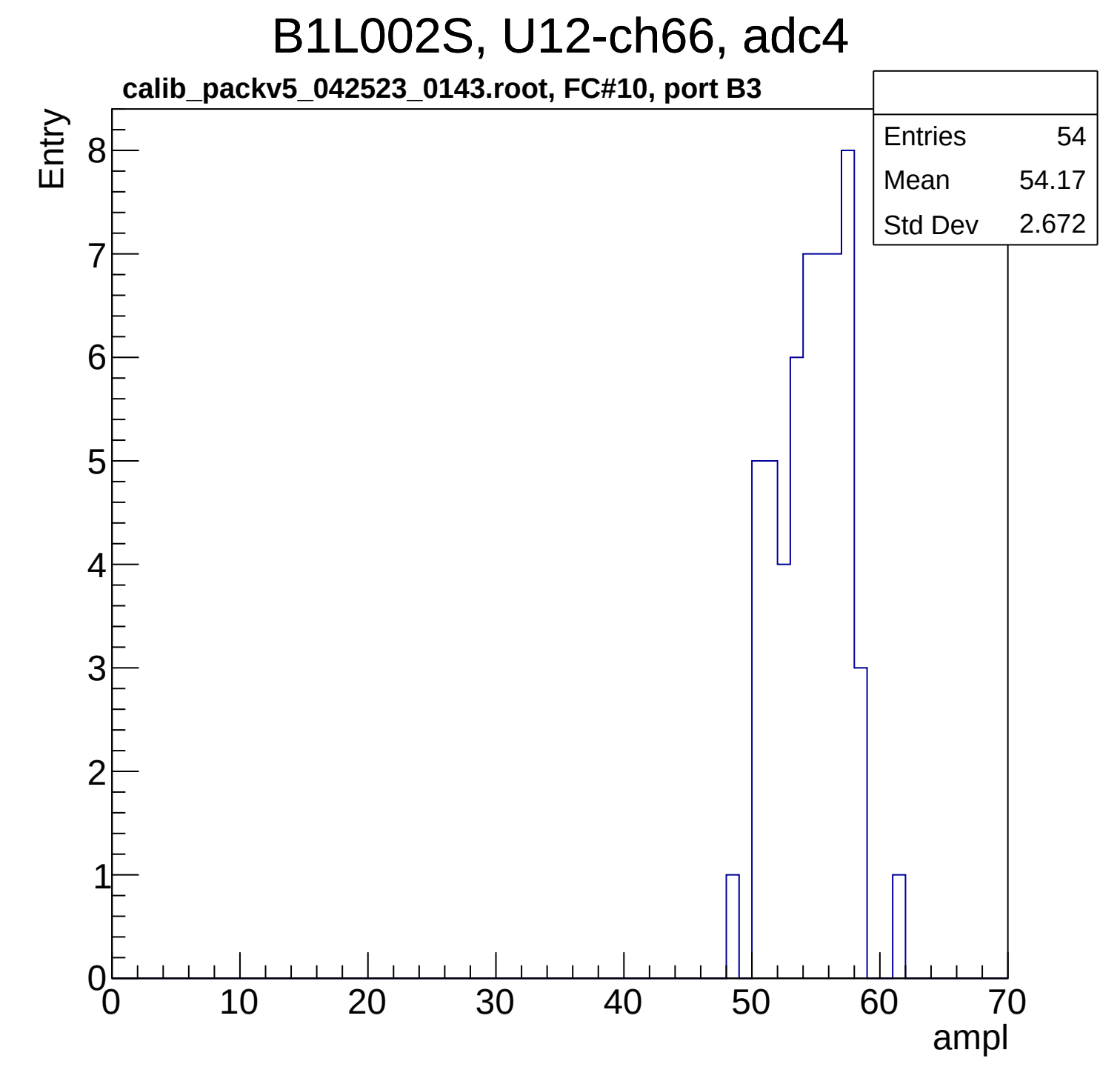
B1L002S, U12-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.17
Std Dev 2.672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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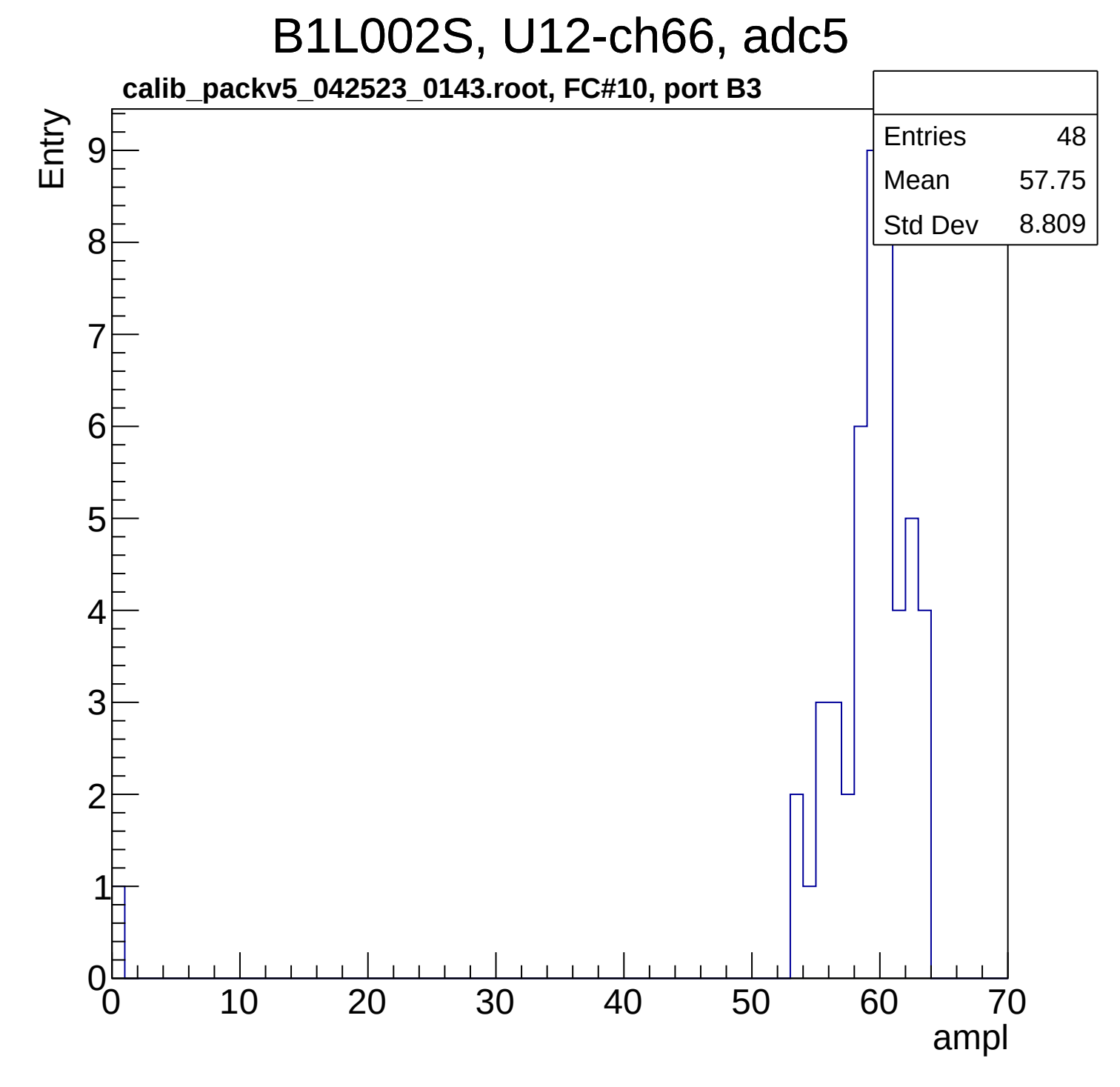
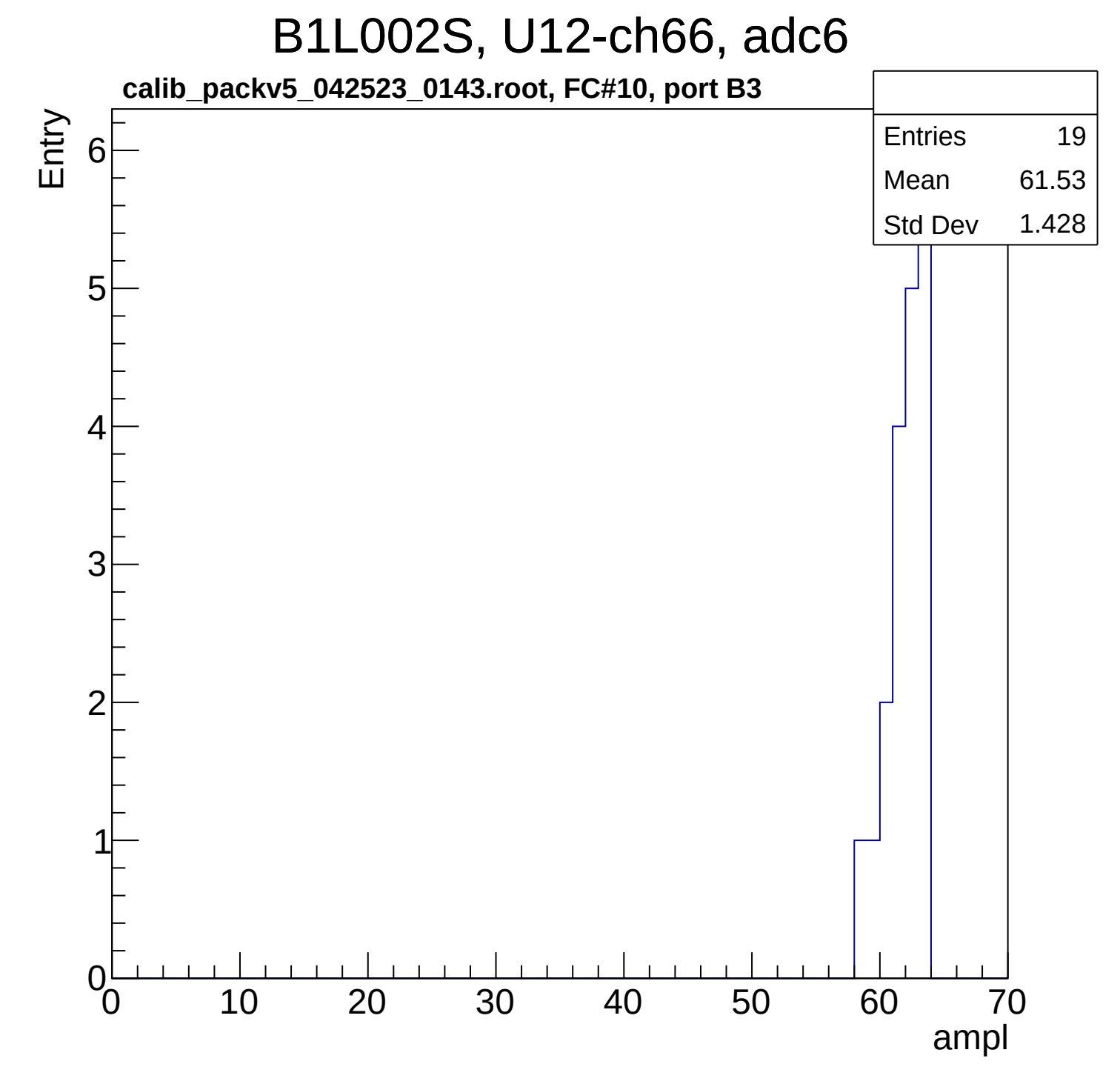
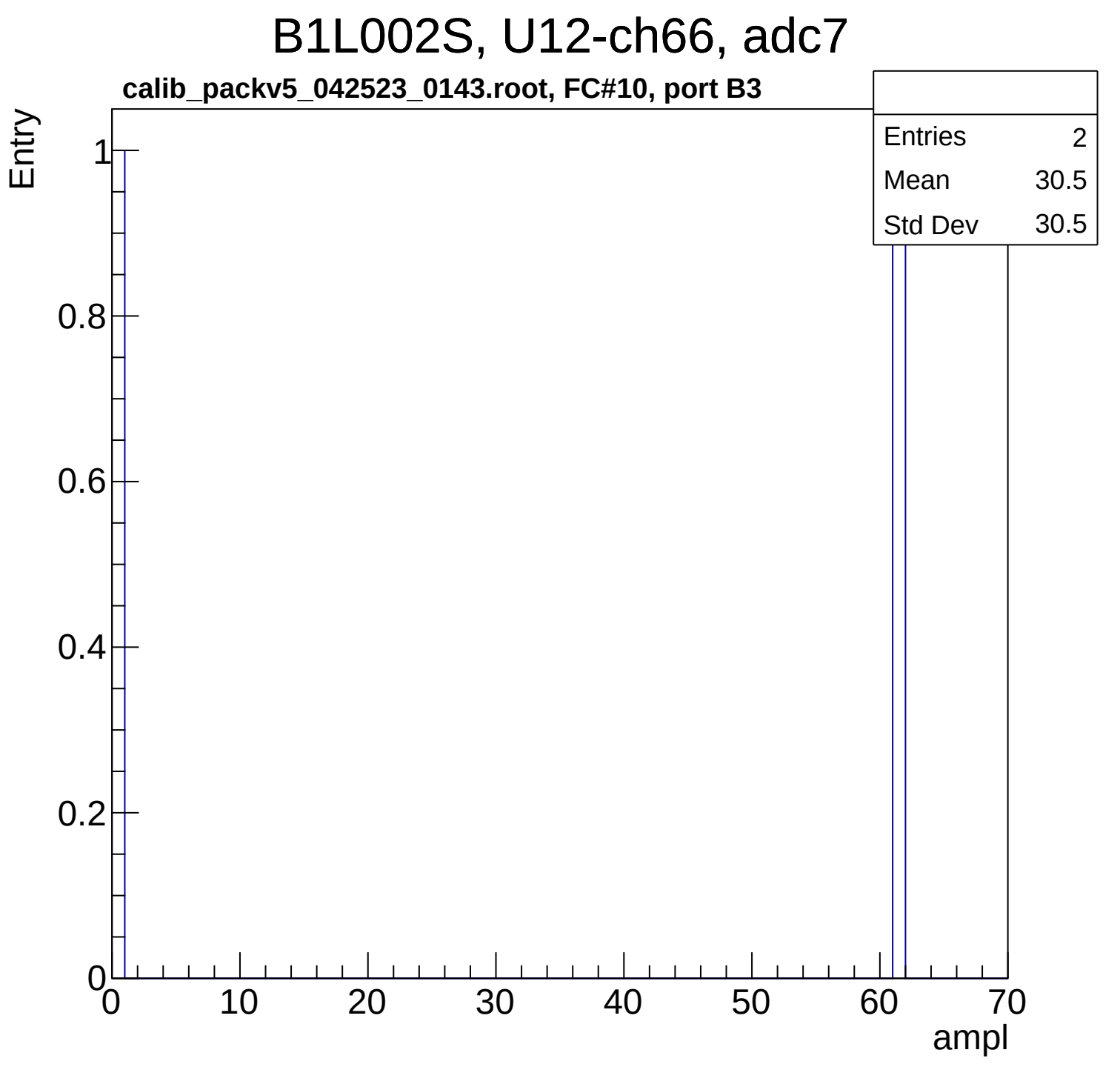
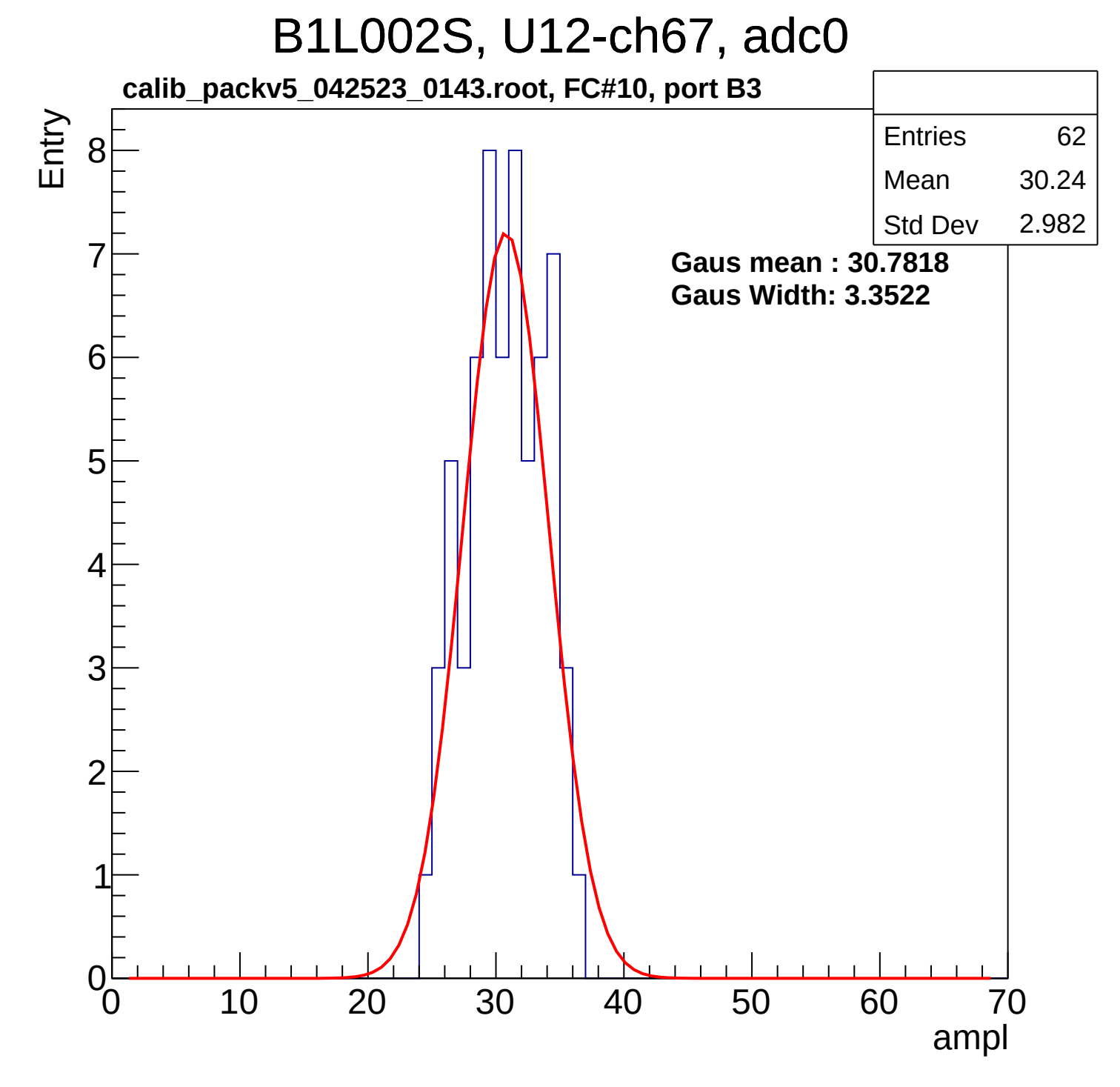

B1L002S, U12-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.75
Std Dev 8.809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.53
Std Dev 1.428
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 30.24
Std Dev 2.982
Gaus mean : 30.7818
Gaus Width: 3.3522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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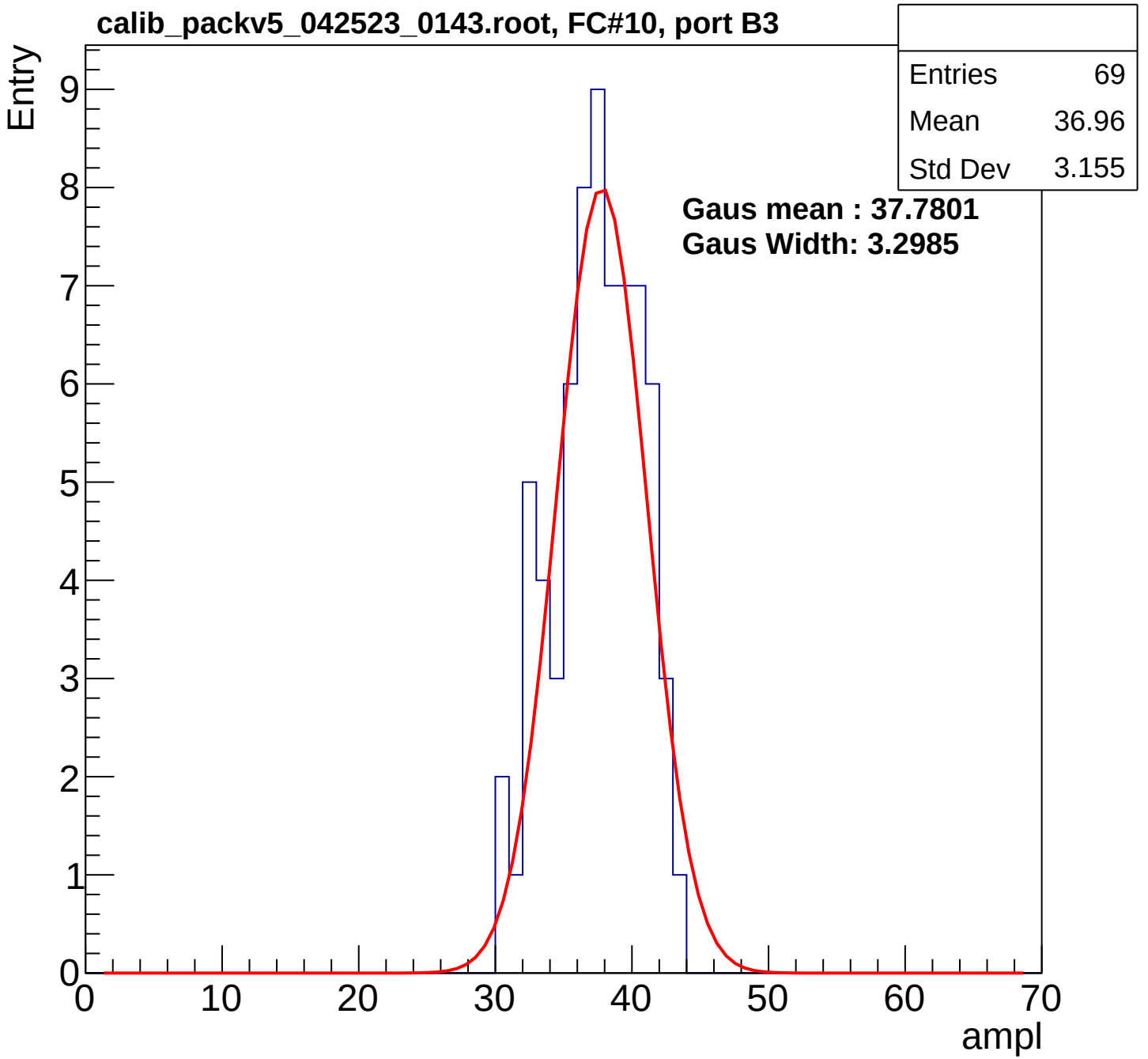
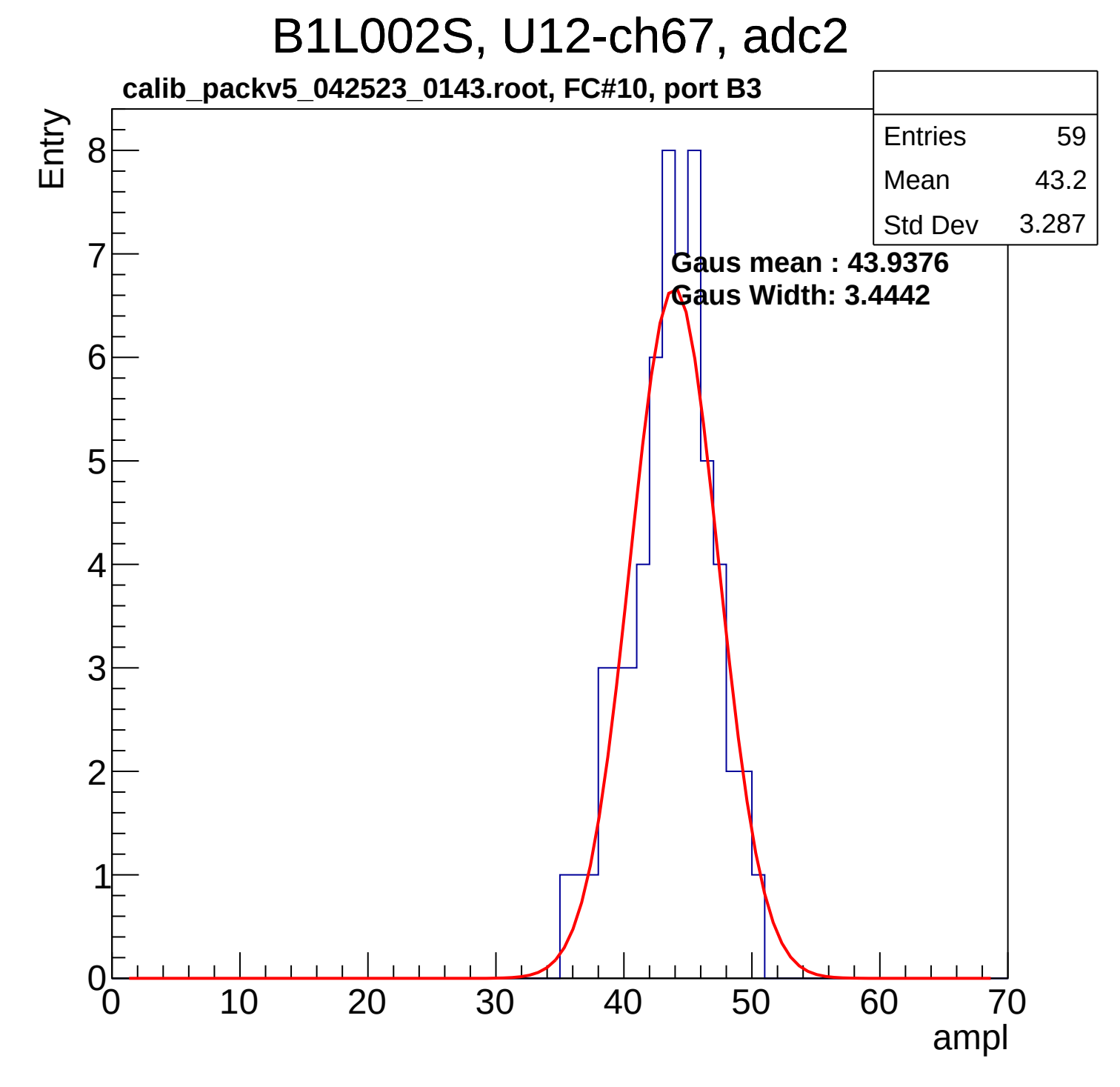
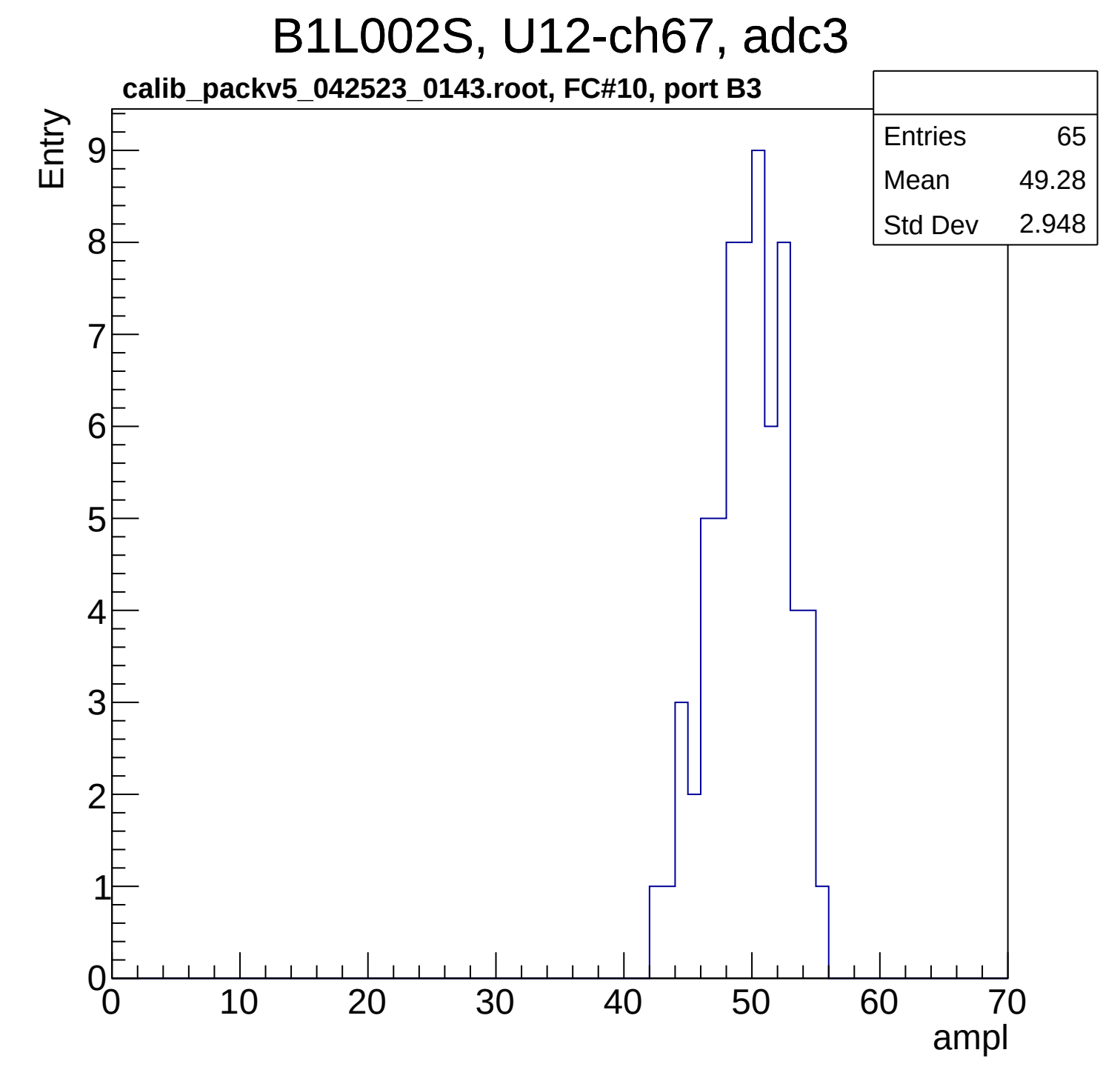

B1L002S, U12-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 36.96
Std Dev 3.155
Gaus mean : 37.7801
Gaus Width: 3.2985
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 43.2
Std Dev 3.287
Gaus mean : 43.9376
Gaus Width: 3.4442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 49.28
Std Dev 2.948
Entry
ampl
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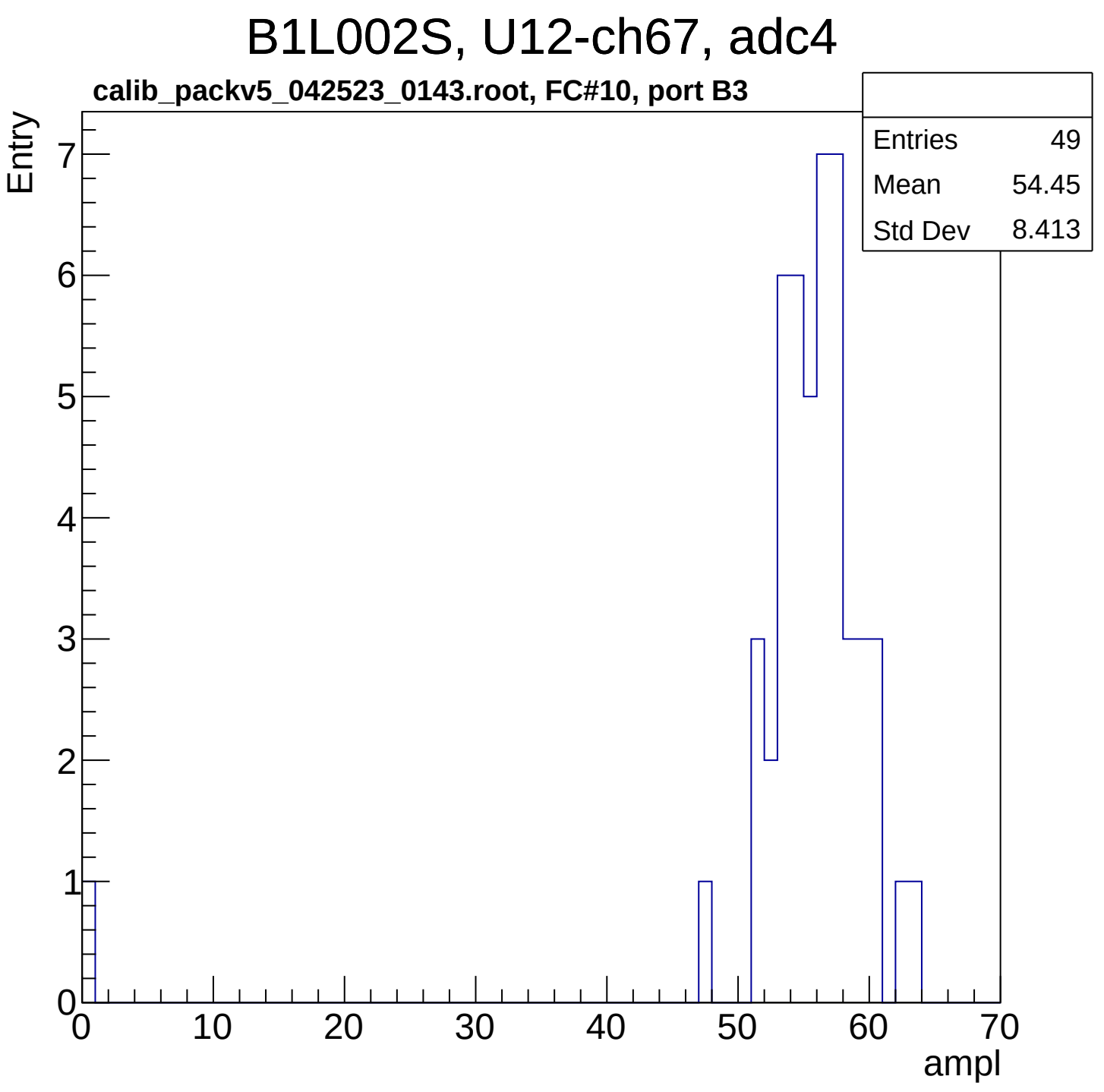
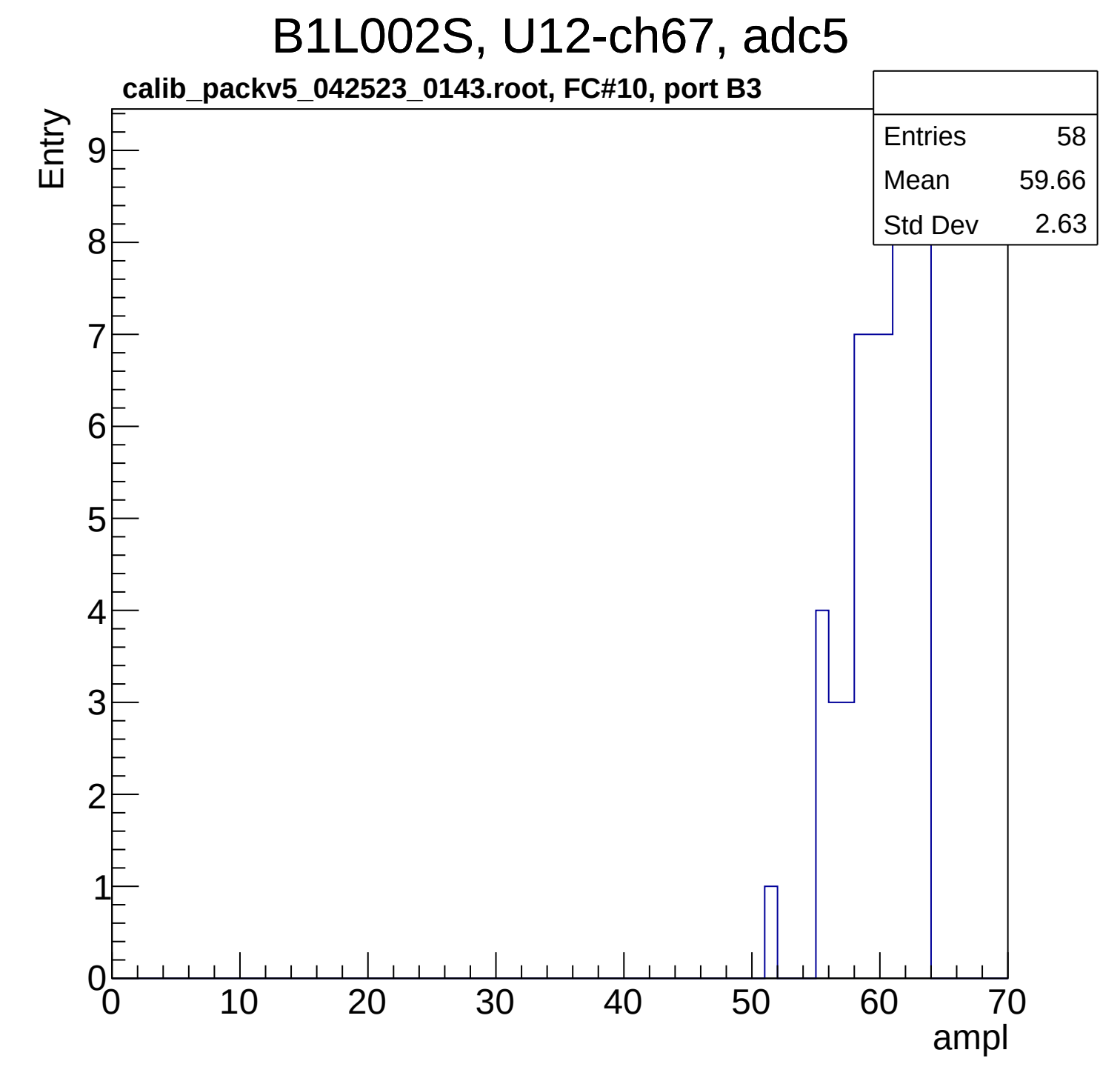
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 54.45
Std Dev 8.413
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 59.66
Std Dev 2.63
Entry
ampl
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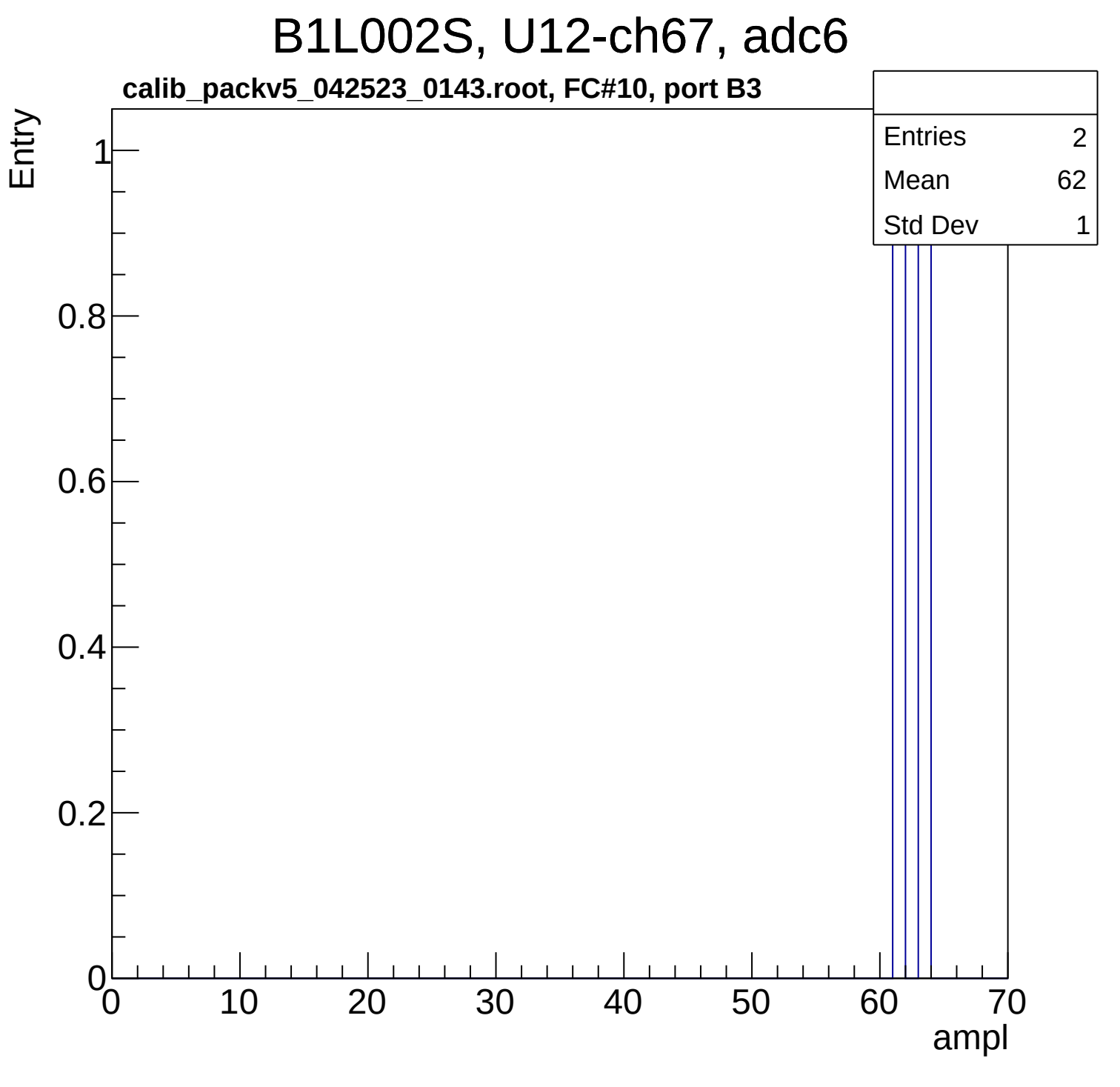
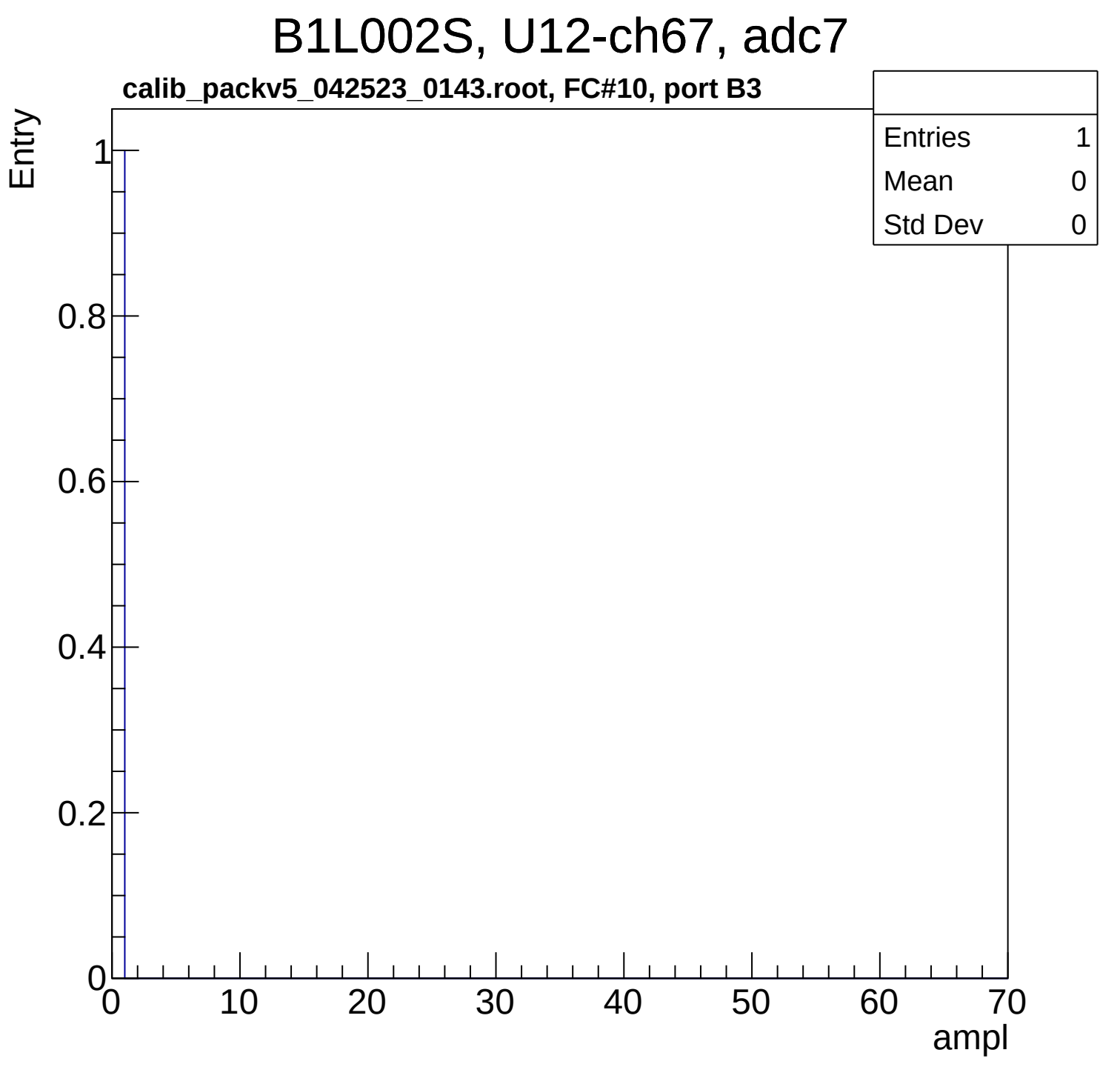
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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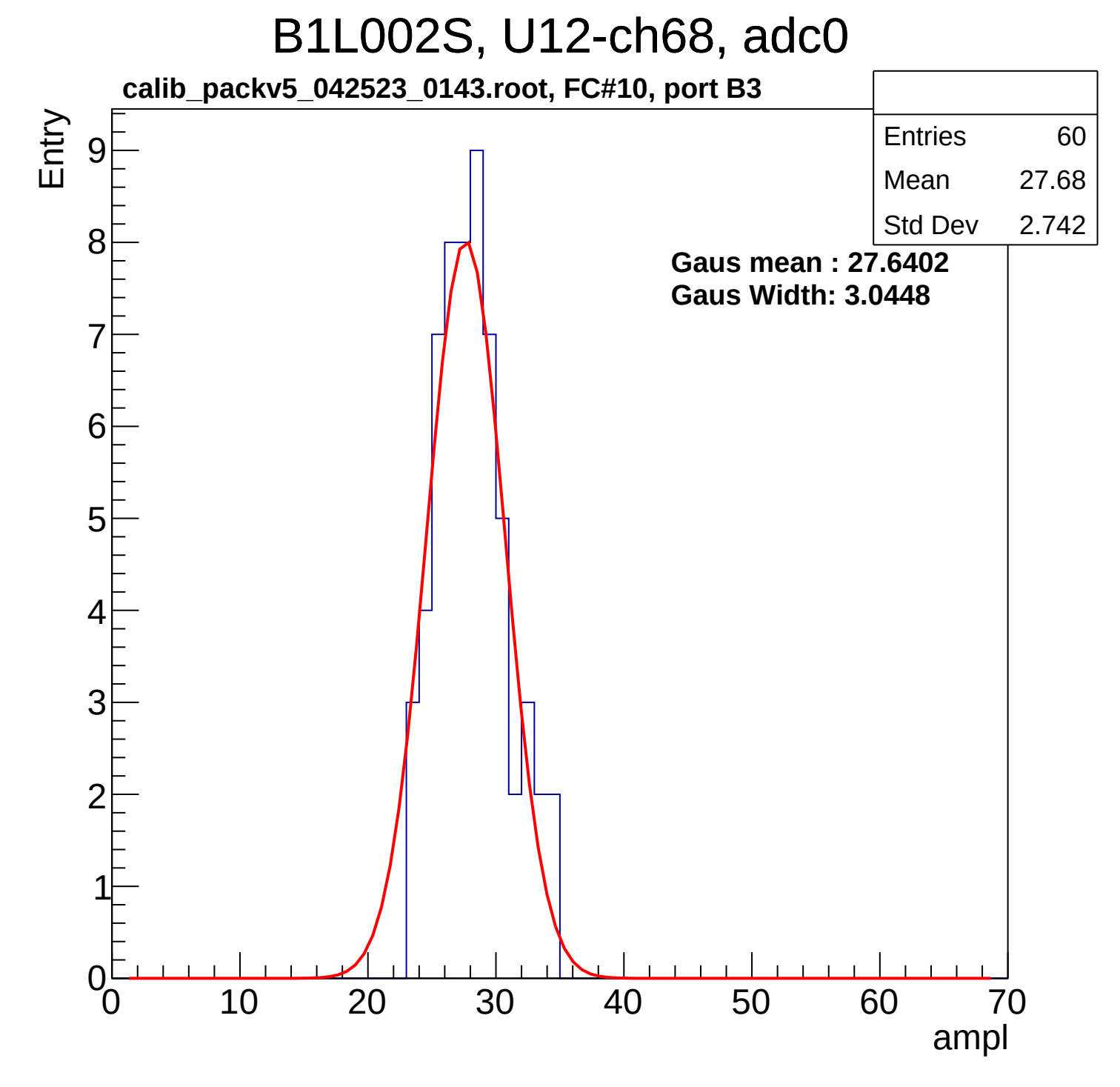
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.68
Std Dev 2.742
Gaus mean : 27.6402
Gaus Width: 3.0448
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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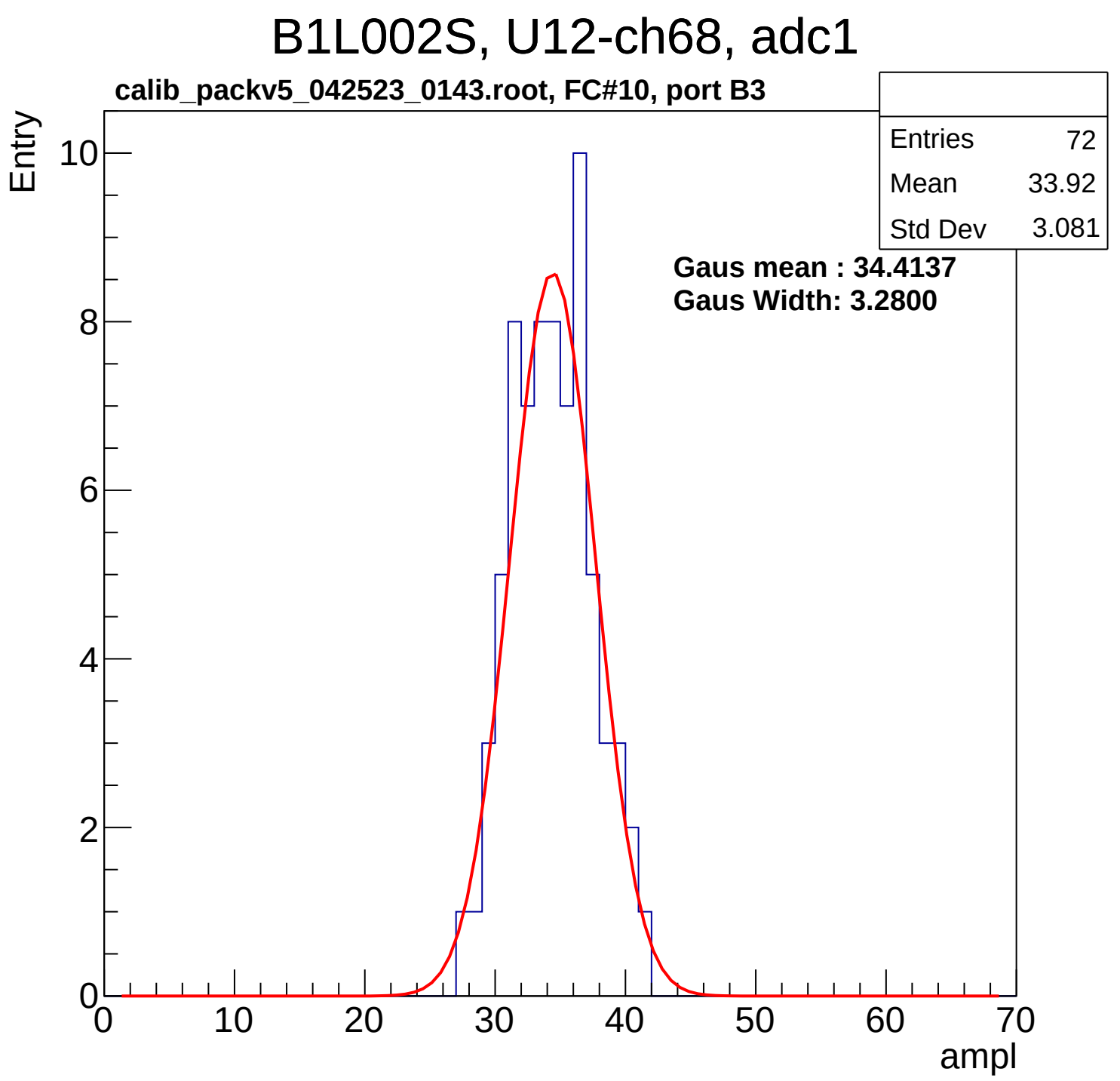
B1L002S, U12-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.92
Std Dev 3.081
Gaus mean : 34.4137
Gaus Width: 3.2800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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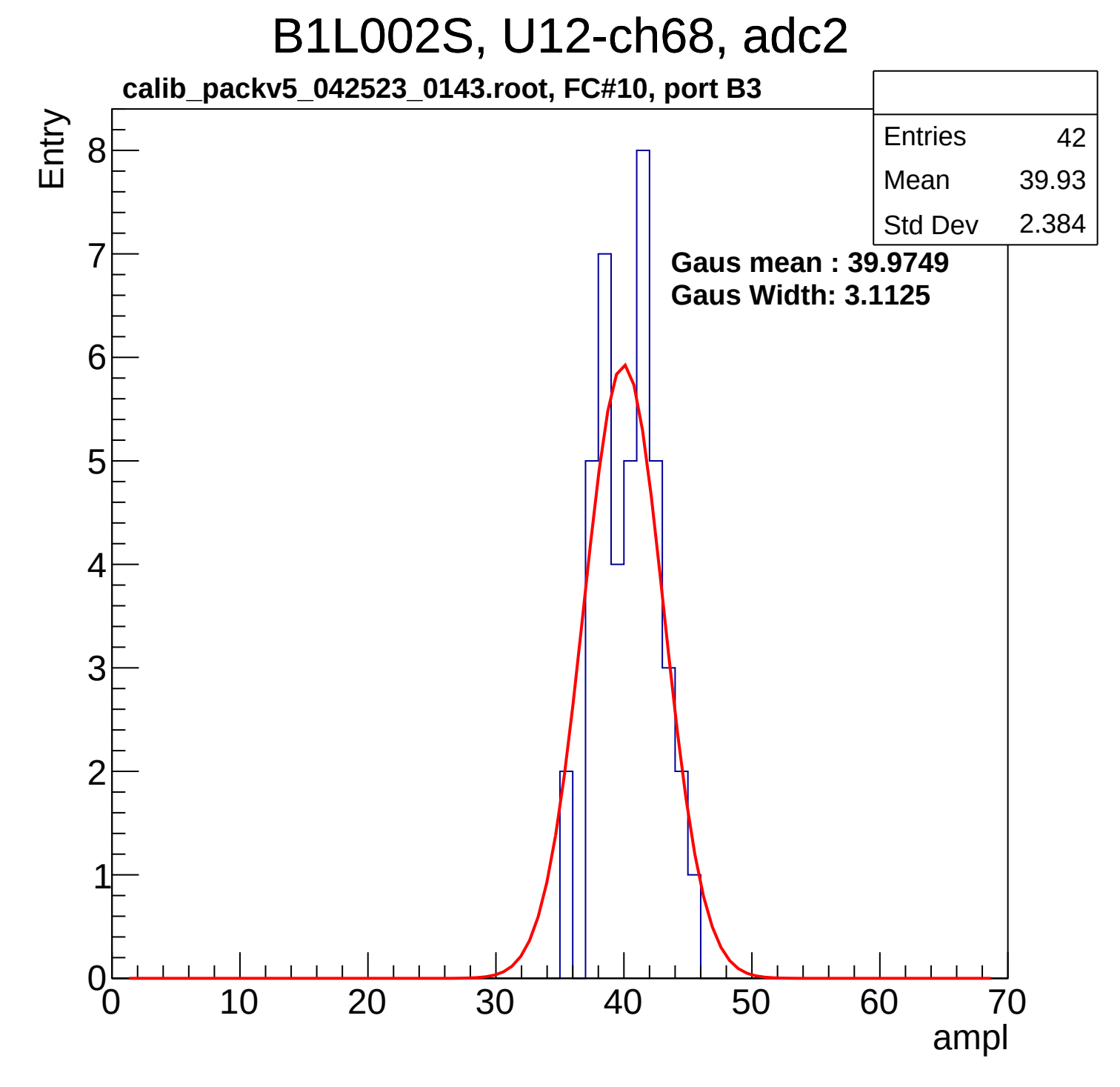
B1L002S, U12-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 39.93
Std Dev 2.384
Gaus mean : 39.9749
Gaus Width: 3.1125
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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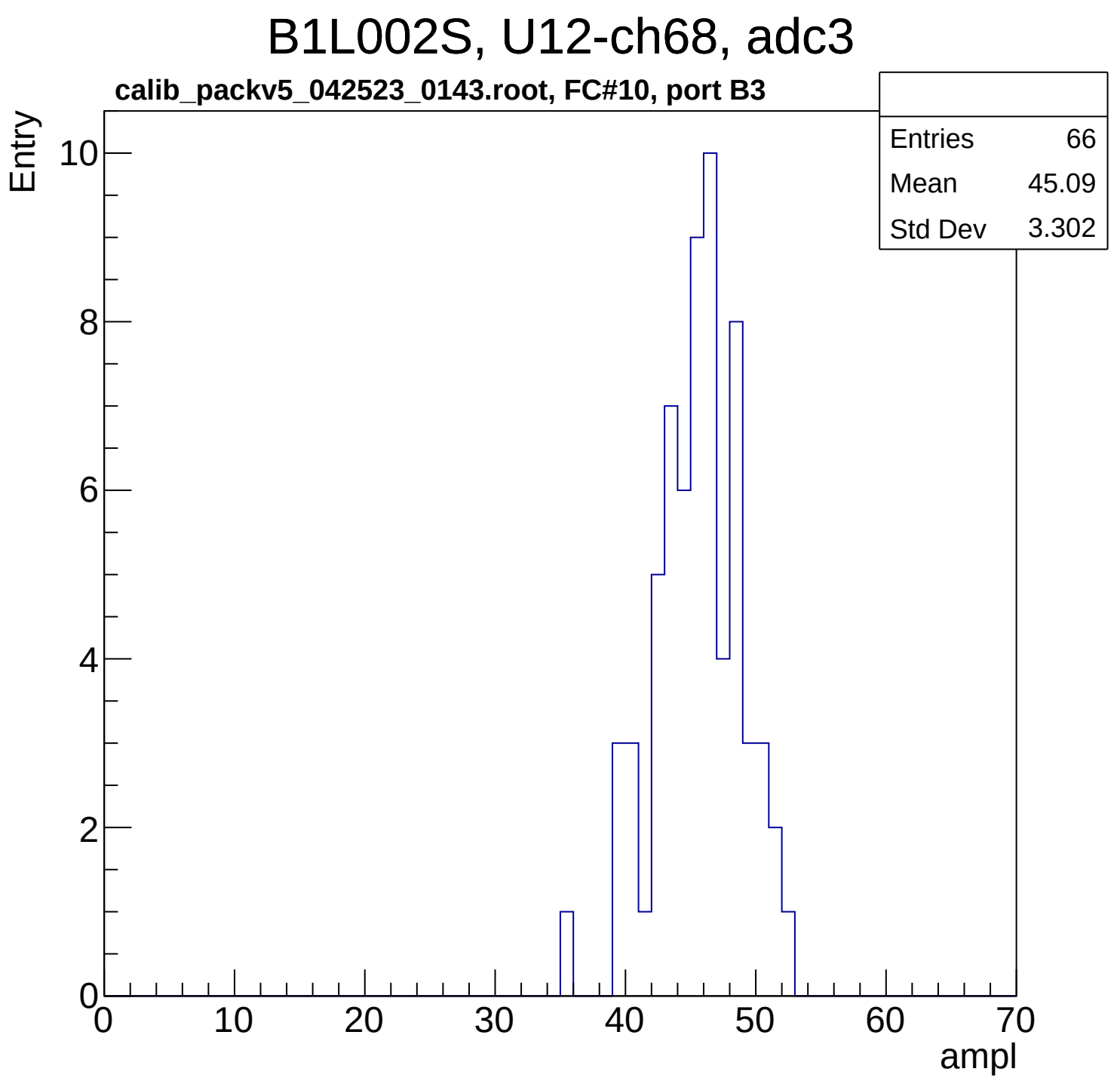
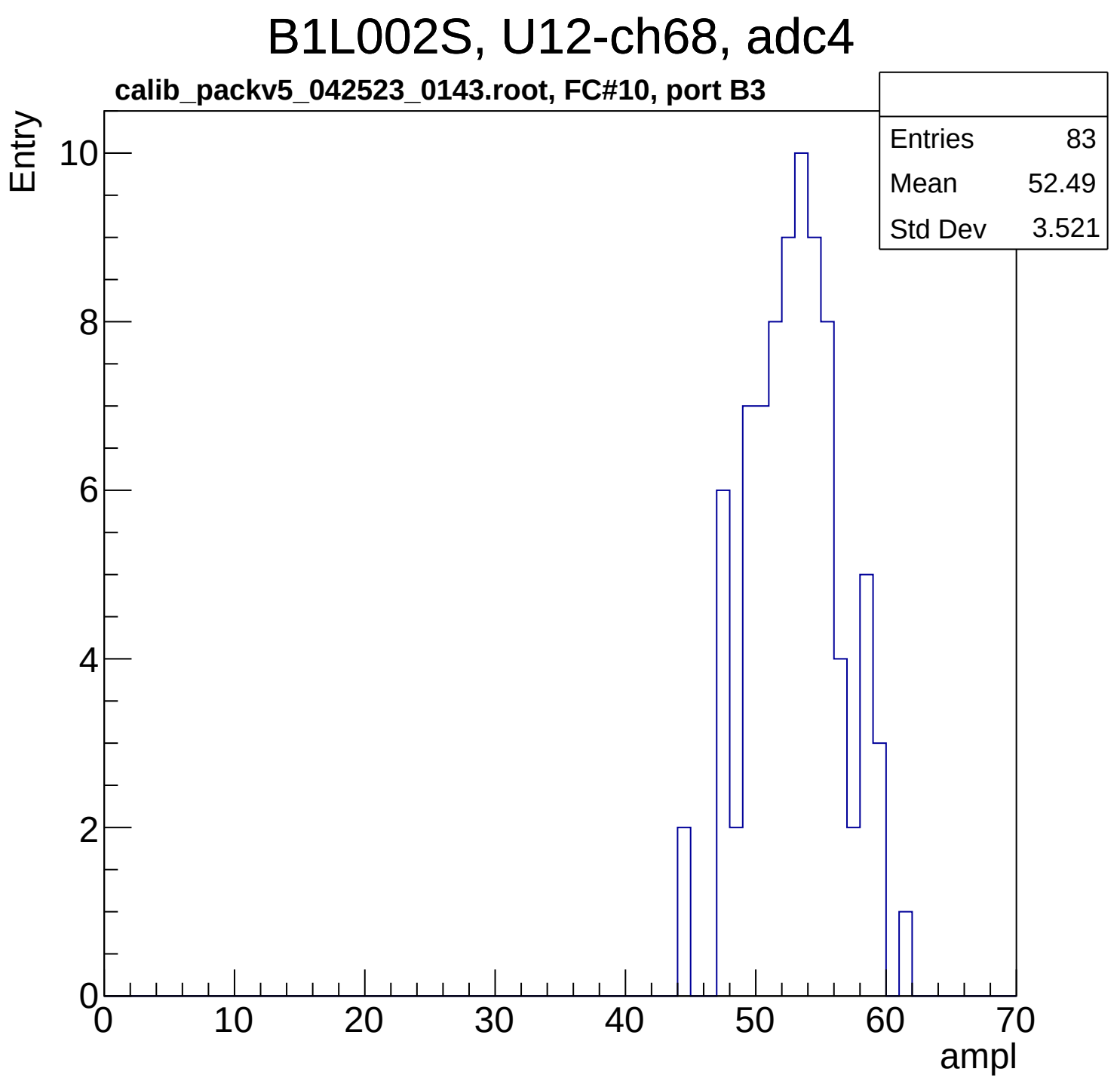
40
50
60
70


B1L002S, U12-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.09
Std Dev 3.302
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 52.49
Std Dev 3.521
Entry
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2
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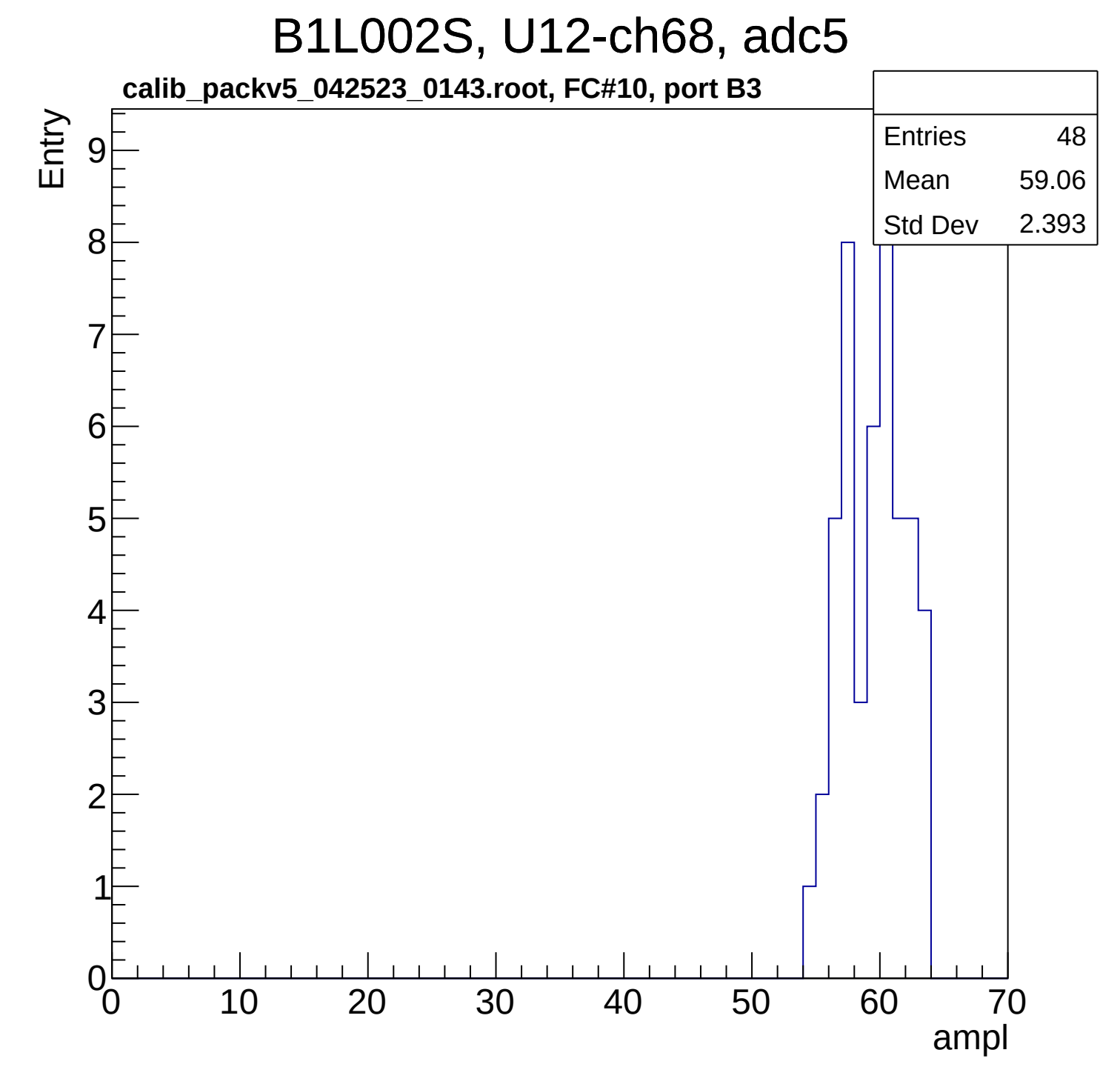
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.06
Std Dev 2.393
Entry
ampl
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3
4
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7
8
9
0
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60
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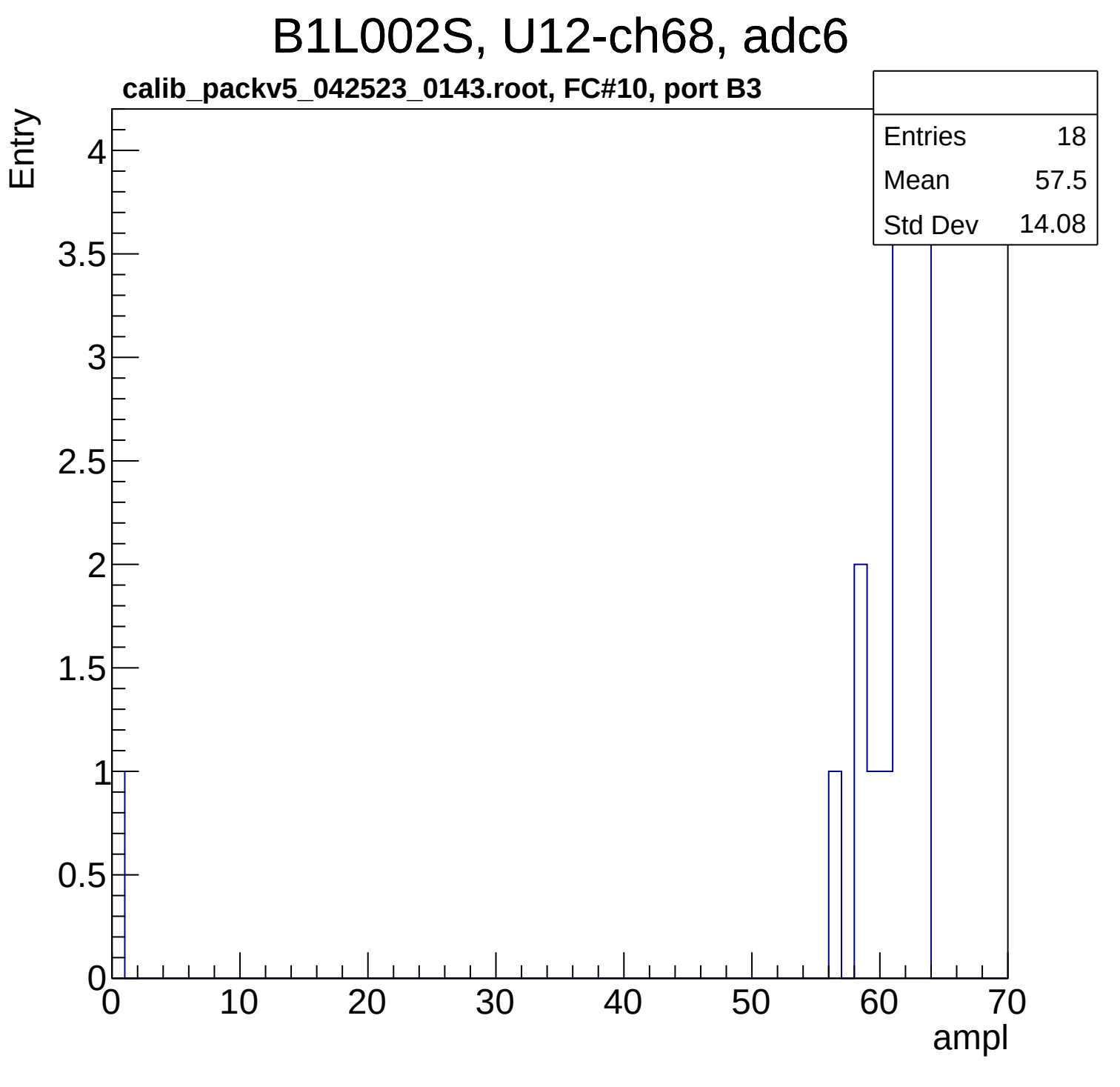
B1L002S, U12-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.5
Std Dev 14.08
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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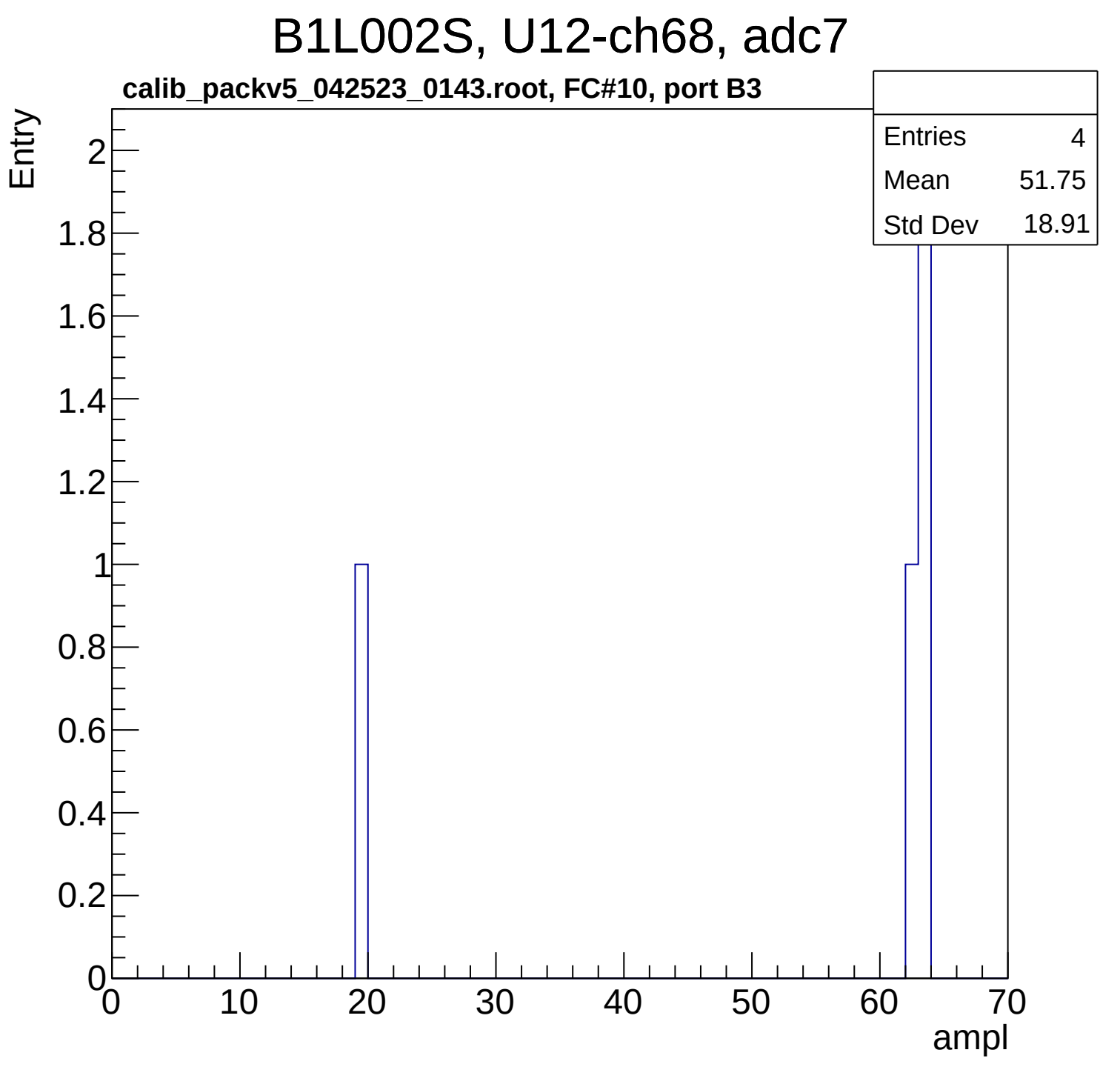
B1L002S, U12-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.75
Std Dev 18.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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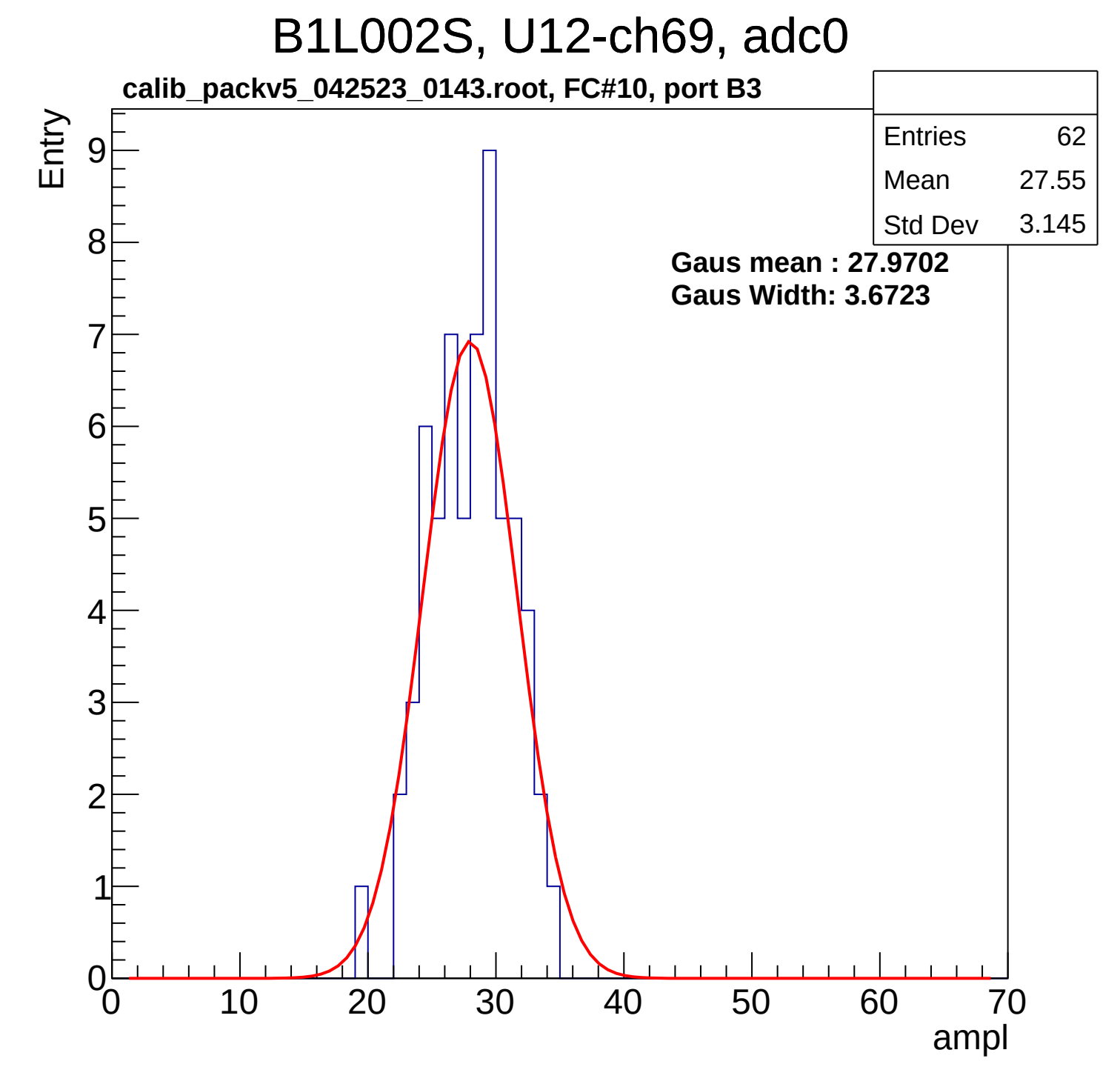
B1L002S, U12-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.55
Std Dev 3.145
Gaus mean : 27.9702
Gaus Width: 3.6723
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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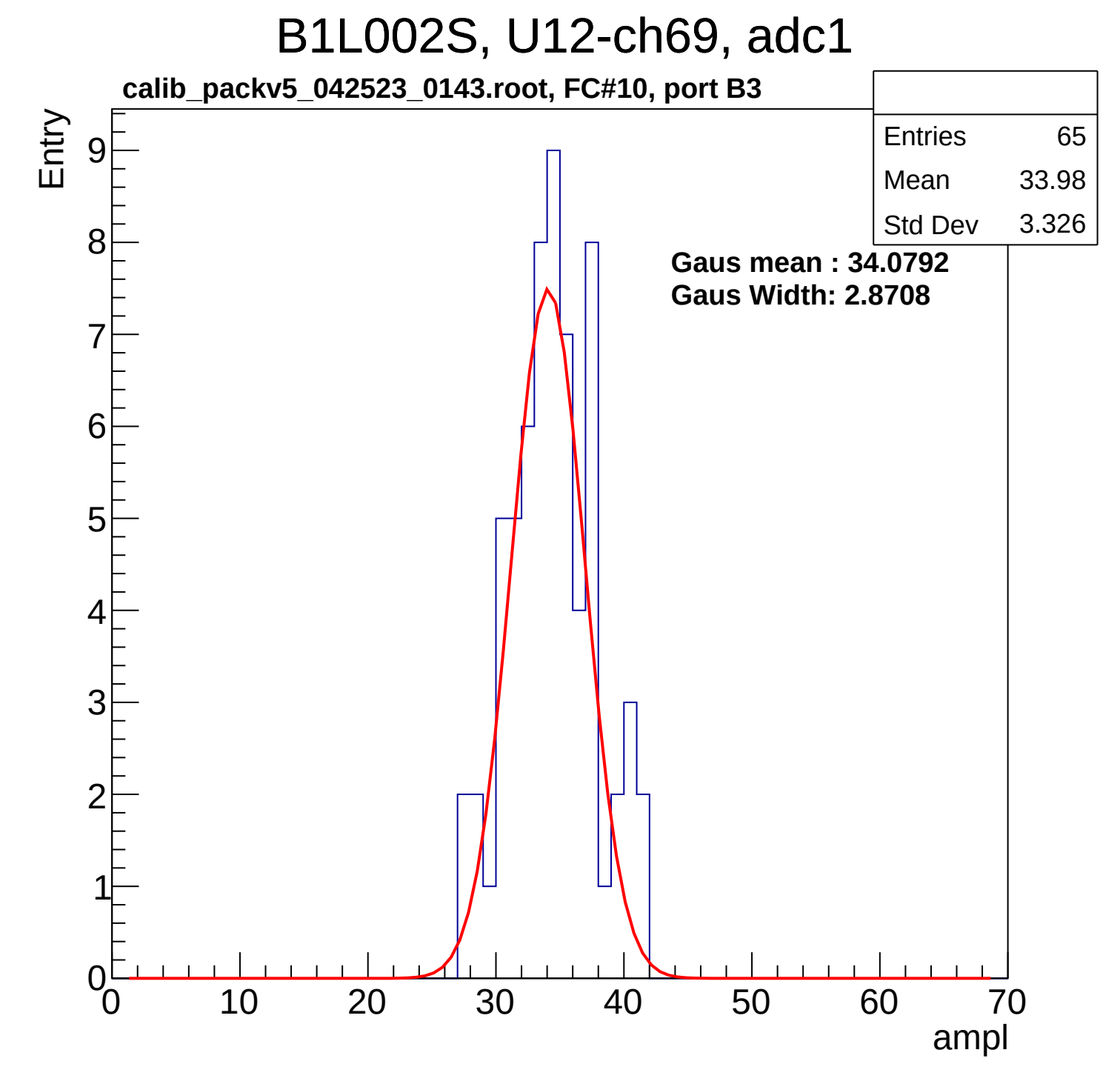
B1L002S, U12-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.98
Std Dev 3.326
Gaus mean : 34.0792
Gaus Width: 2.8708
Entry
ampl
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2
3
4
5
6
7
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0
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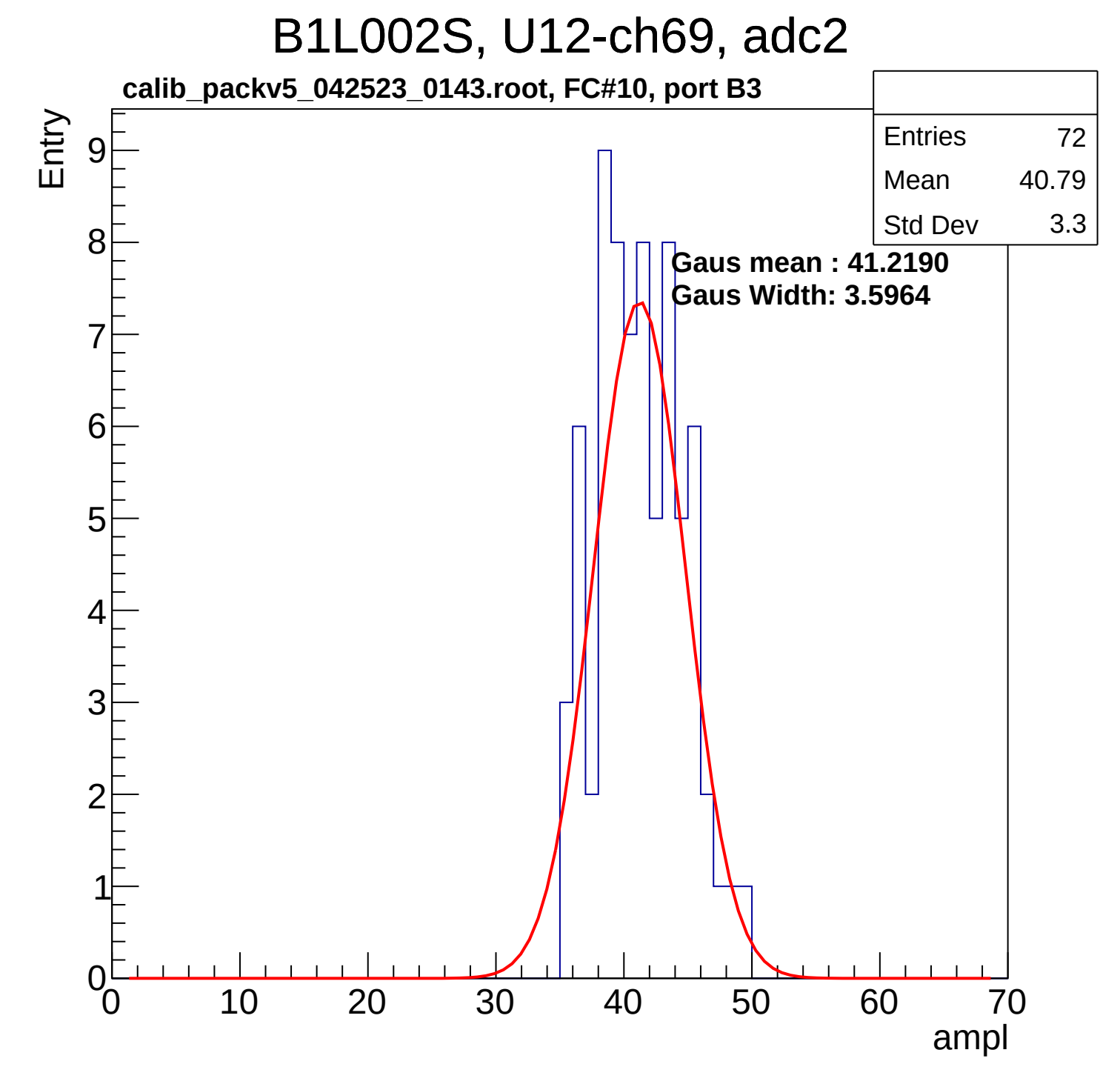
B1L002S, U12-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 40.79
Std Dev 3.3
Gaus mean : 41.2190
Gaus Width: 3.5964
Entry
ampl
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2
3
4
5
6
7
8
9
0
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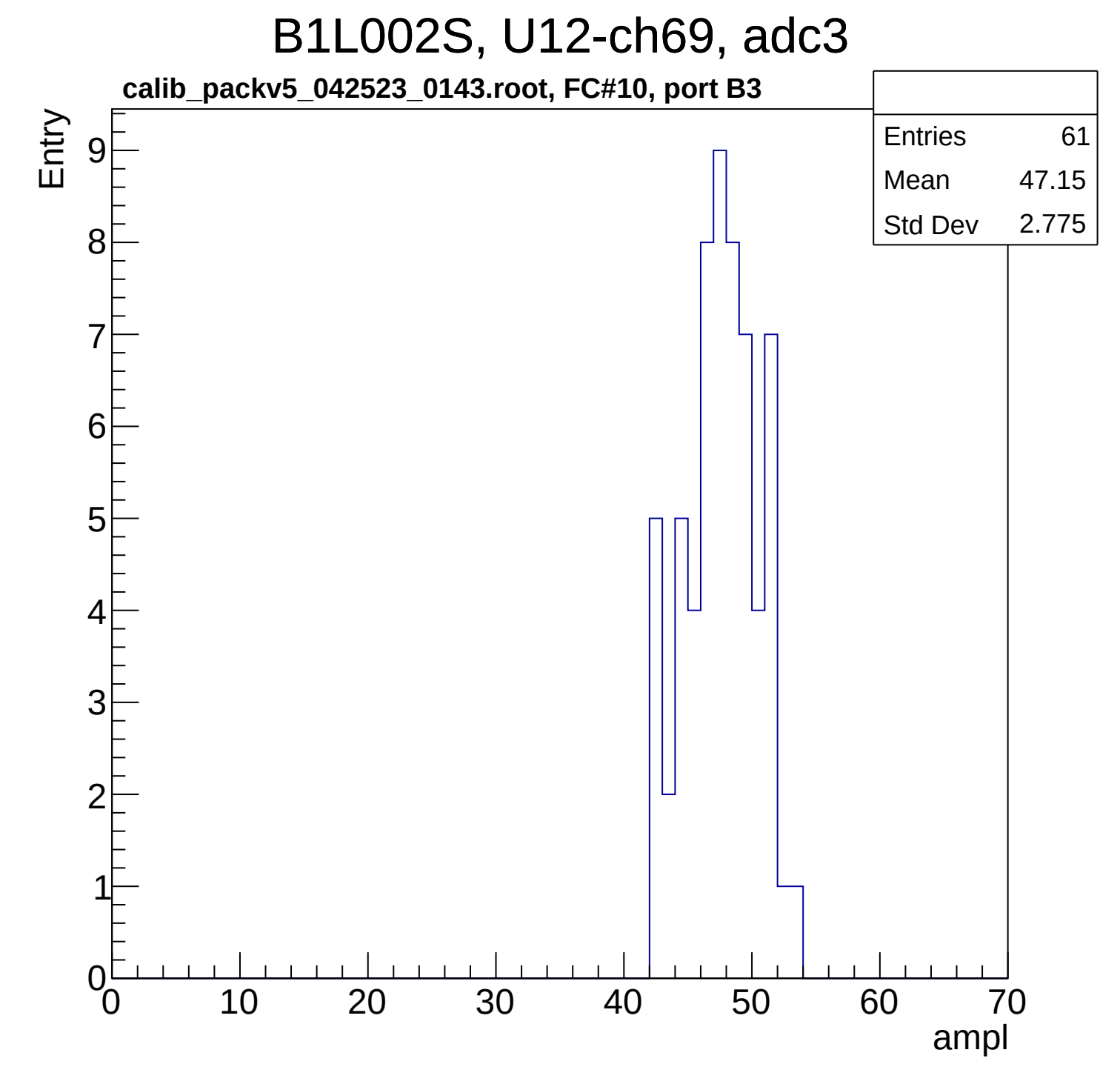
B1L002S, U12-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.15
Std Dev 2.775
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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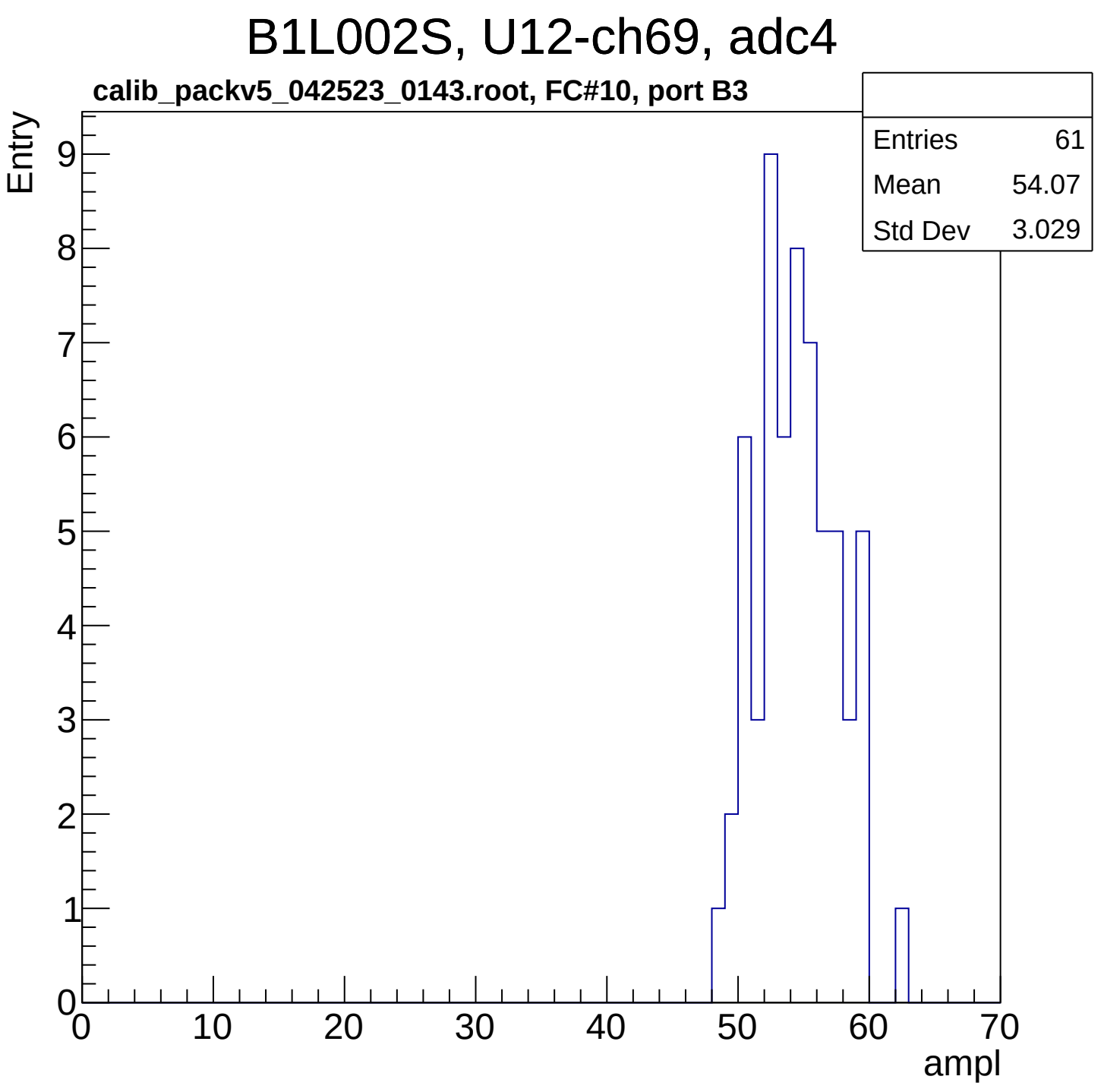
B1L002S, U12-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 54.07
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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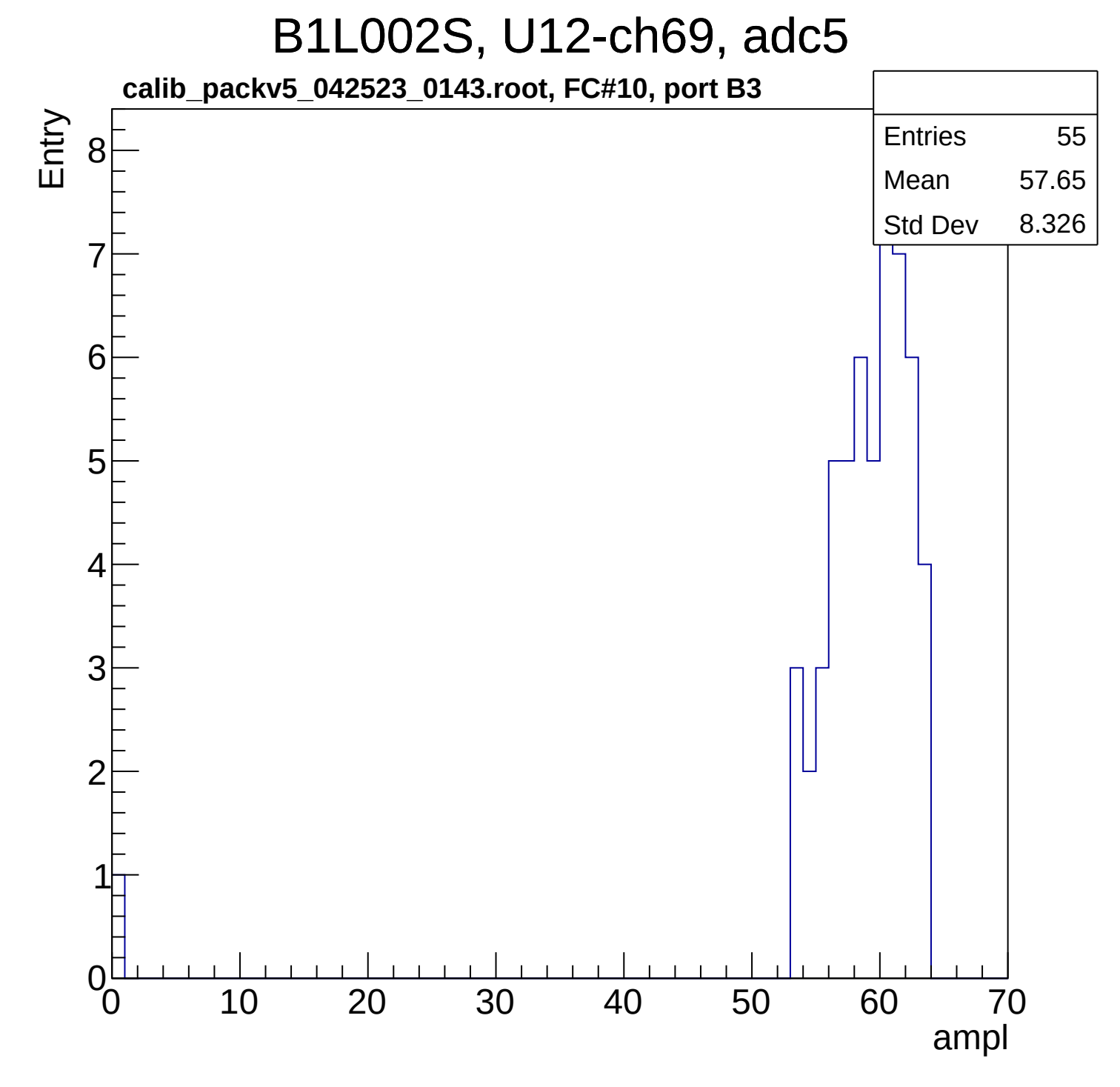
B1L002S, U12-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.65
Std Dev 8.326
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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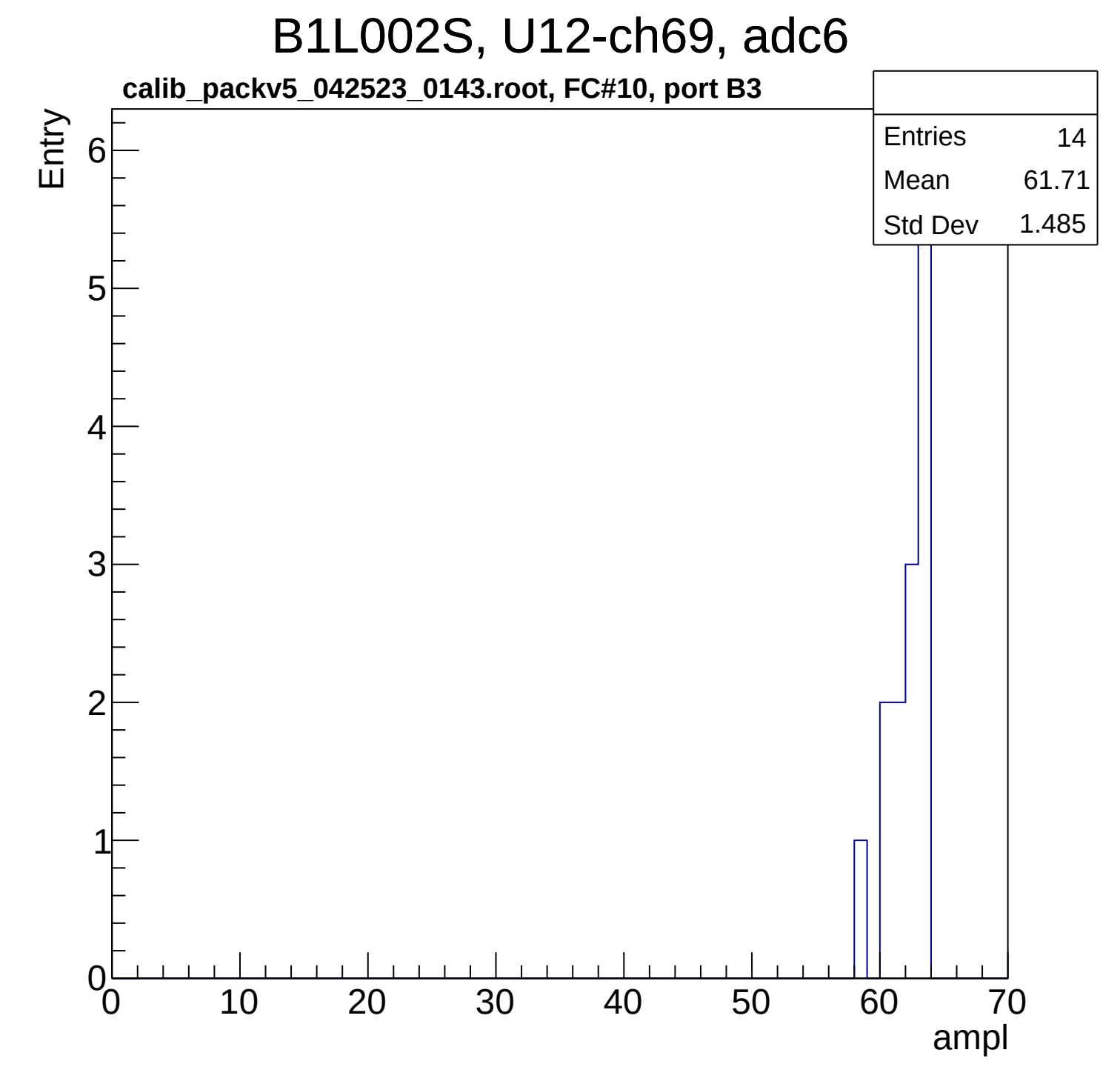
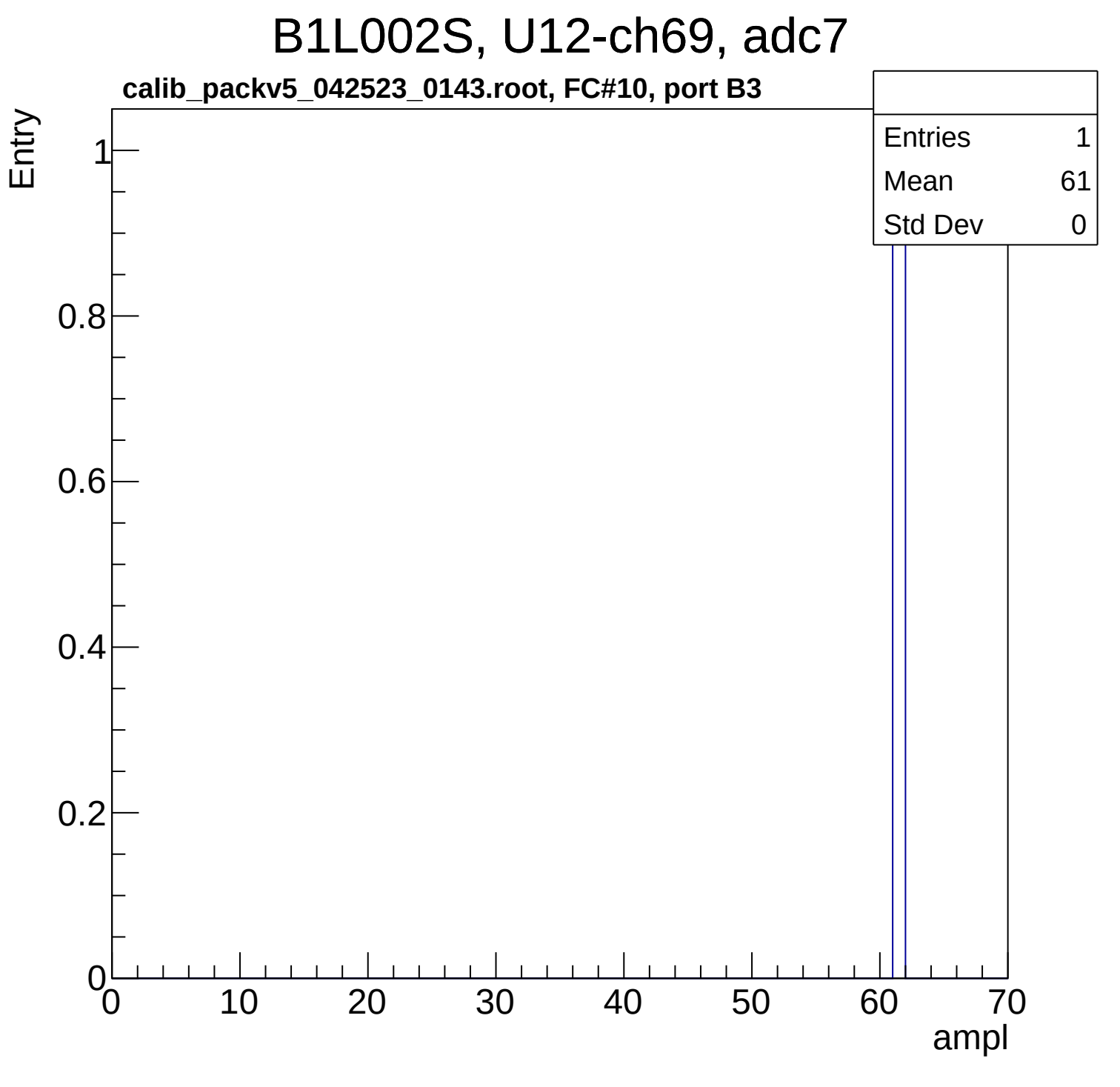
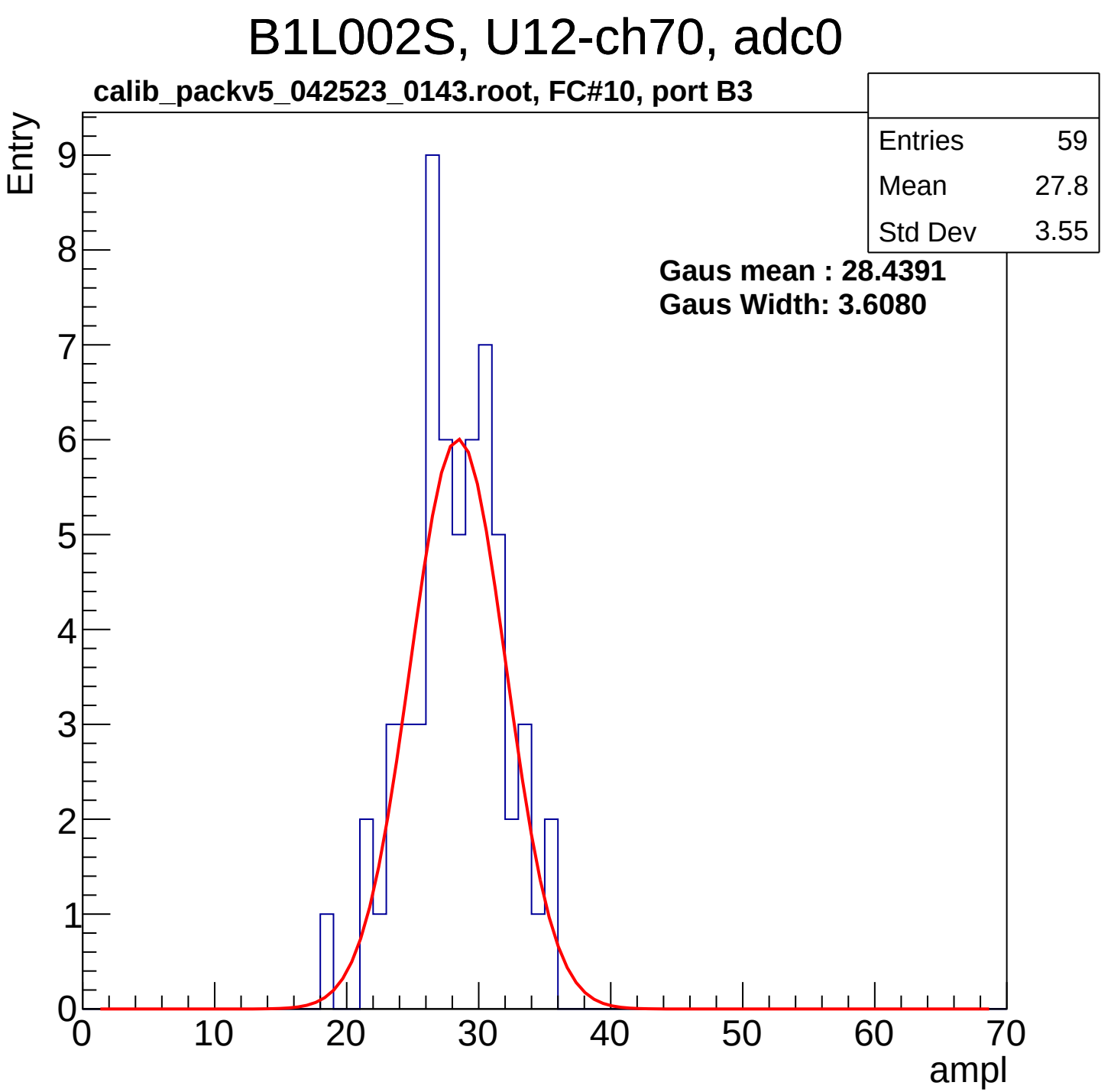

B1L002S, U12-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.71
Std Dev 1.485
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U12-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.8
Std Dev 3.55
Gaus mean : 28.4391
Gaus Width: 3.6080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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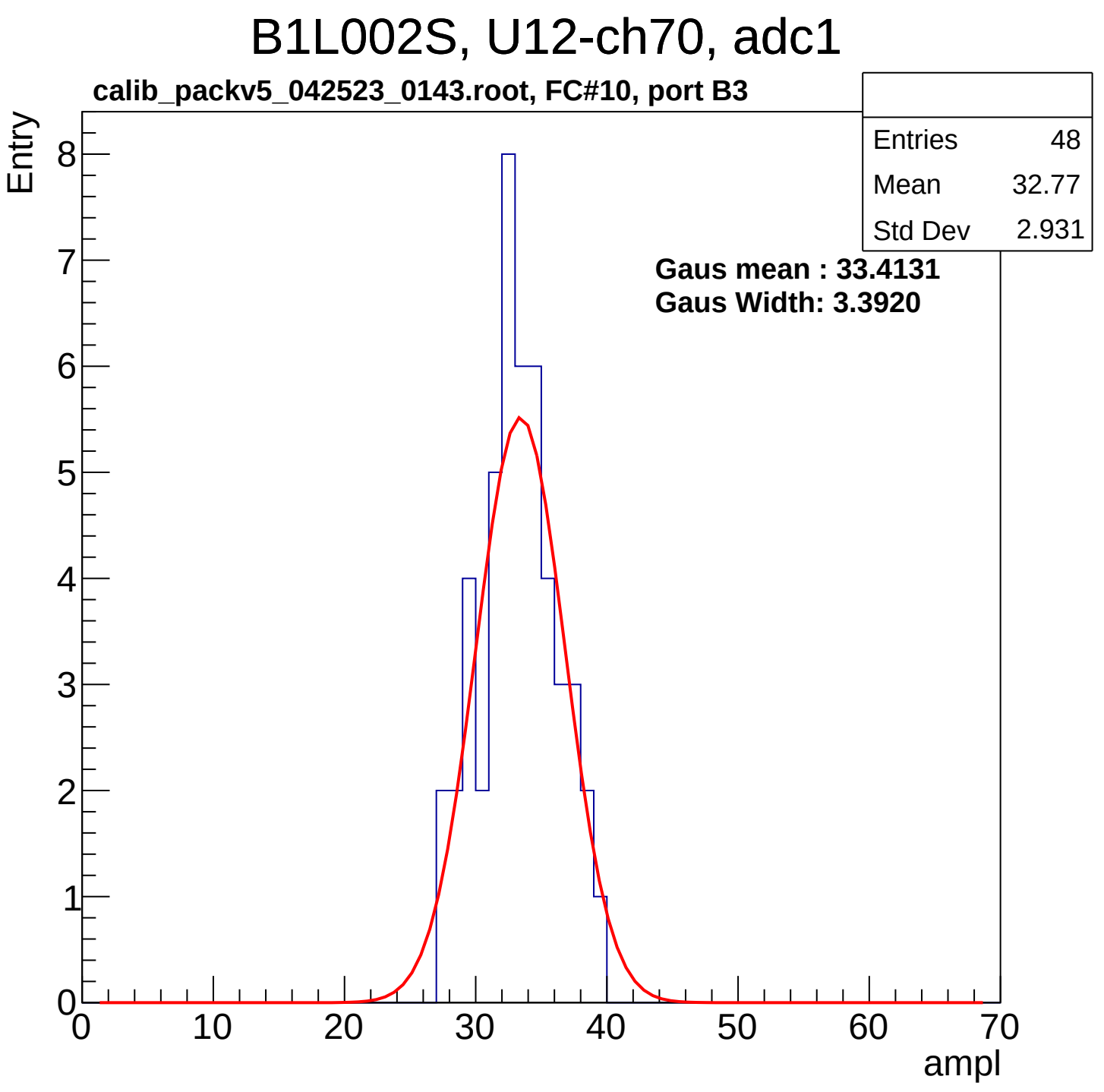
B1L002S, U12-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 32.77
Std Dev 2.931
Gaus mean : 33.4131
Gaus Width: 3.3920
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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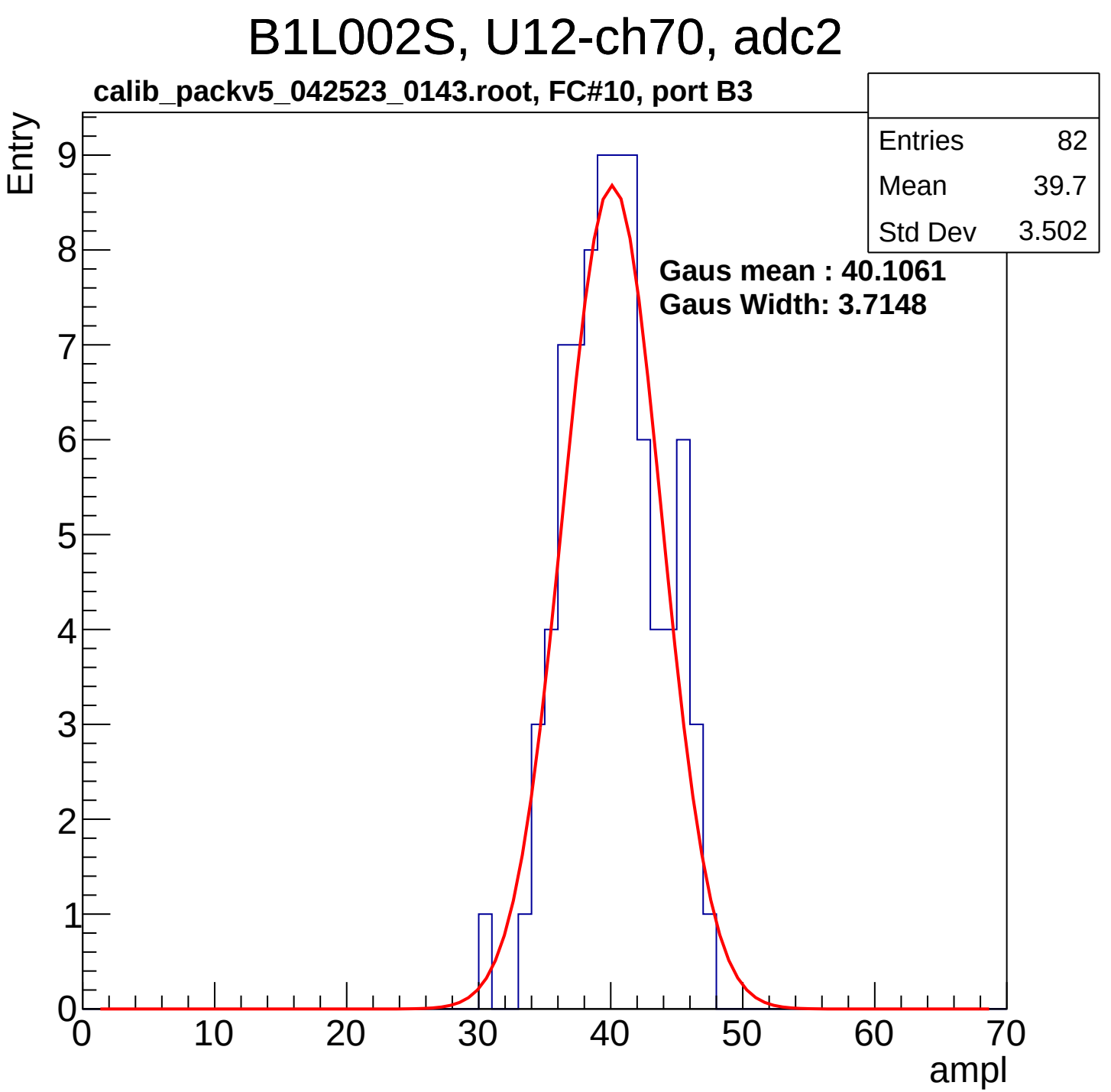
B1L002S, U12-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 39.7
Std Dev 3.502
Gaus mean : 40.1061
Gaus Width: 3.7148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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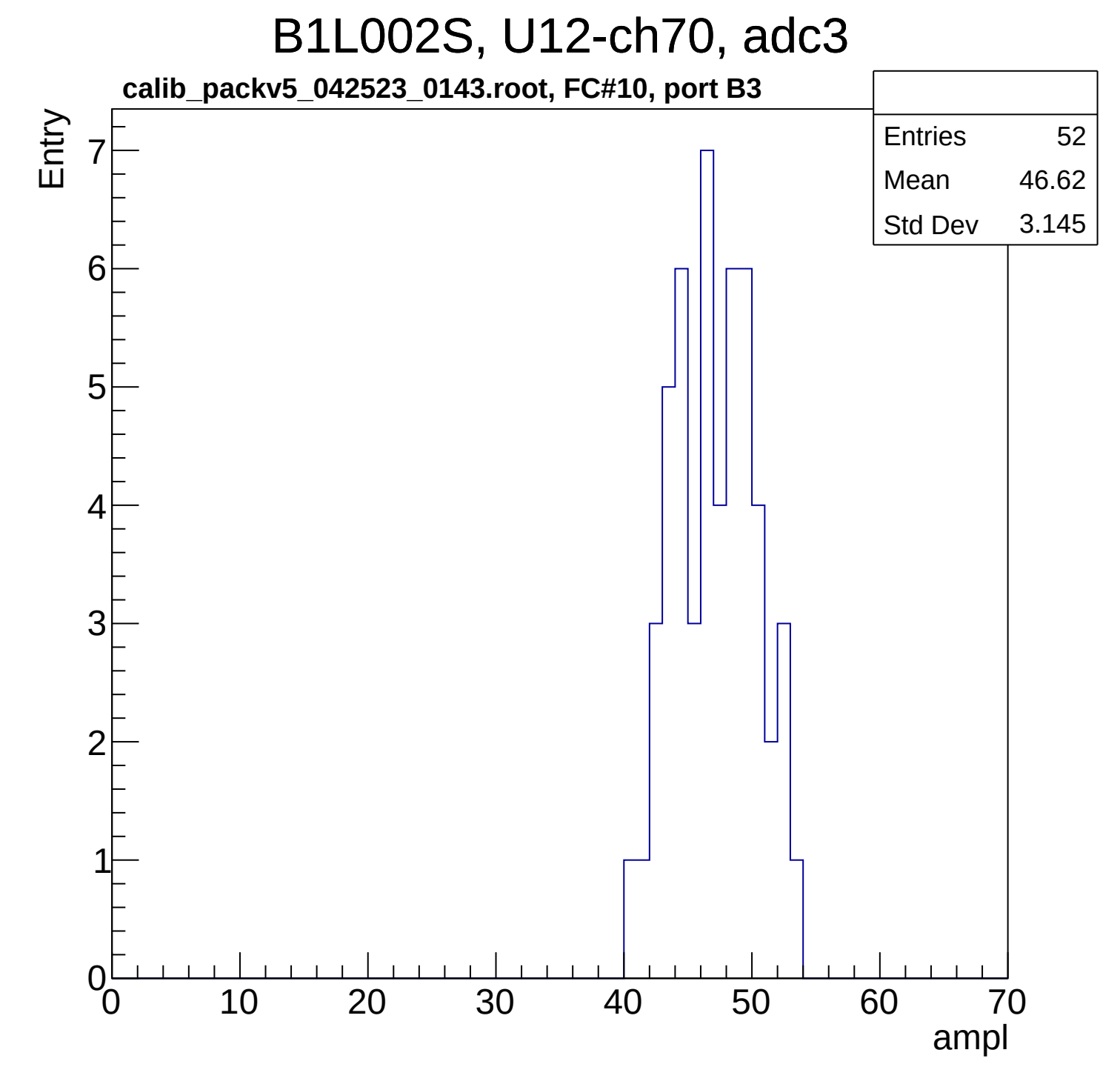
B1L002S, U12-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 46.62
Std Dev 3.145
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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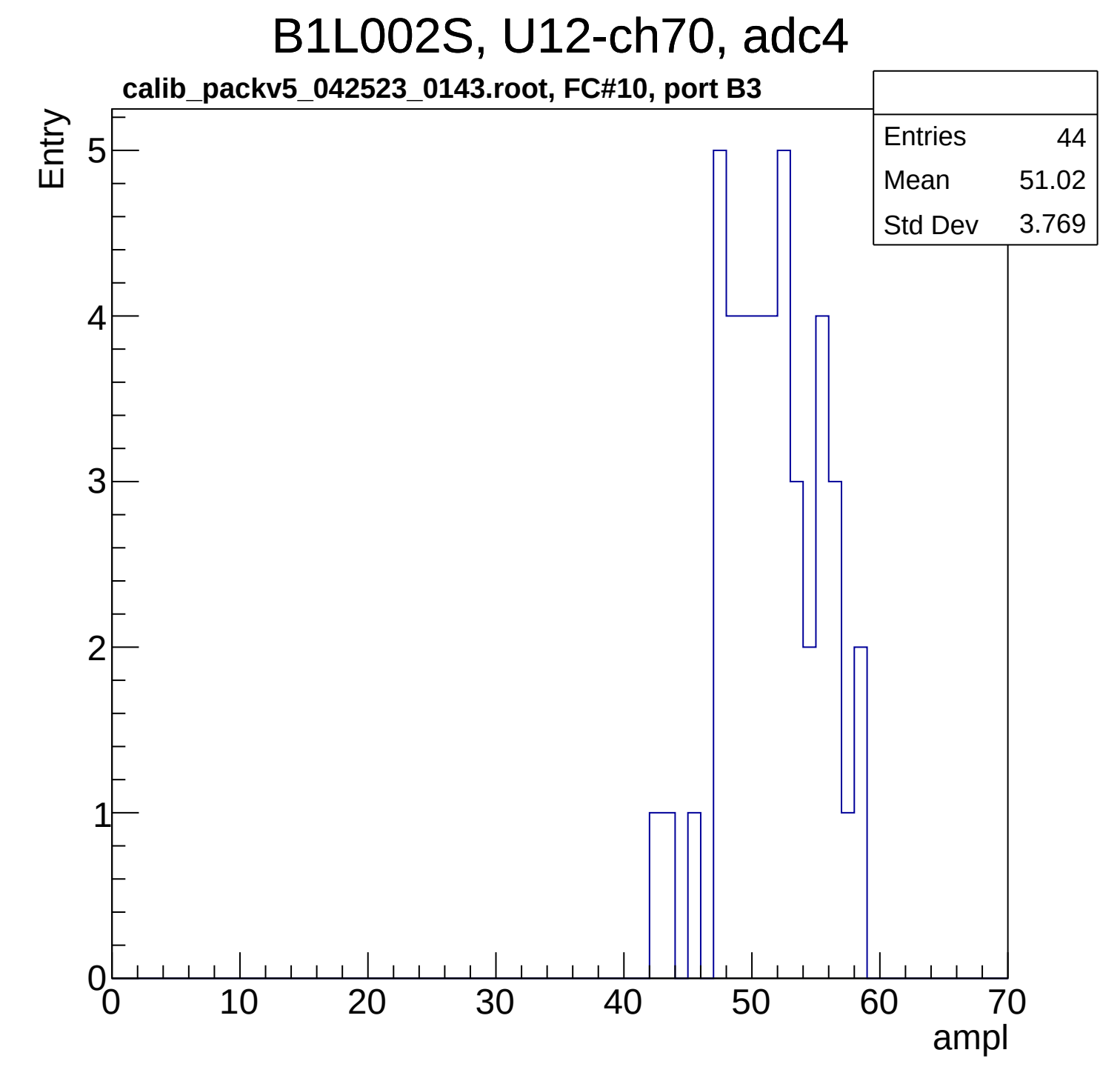
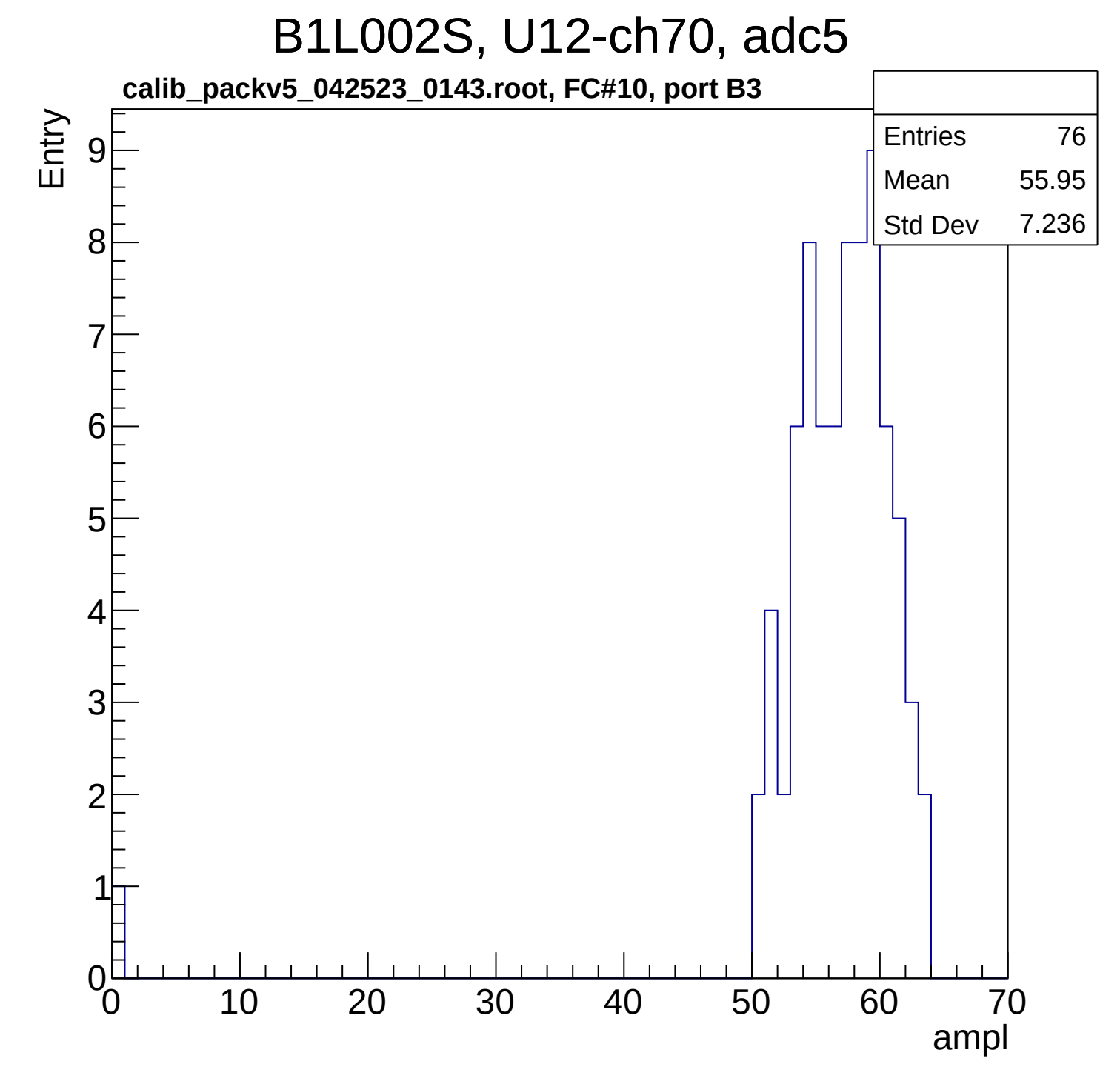
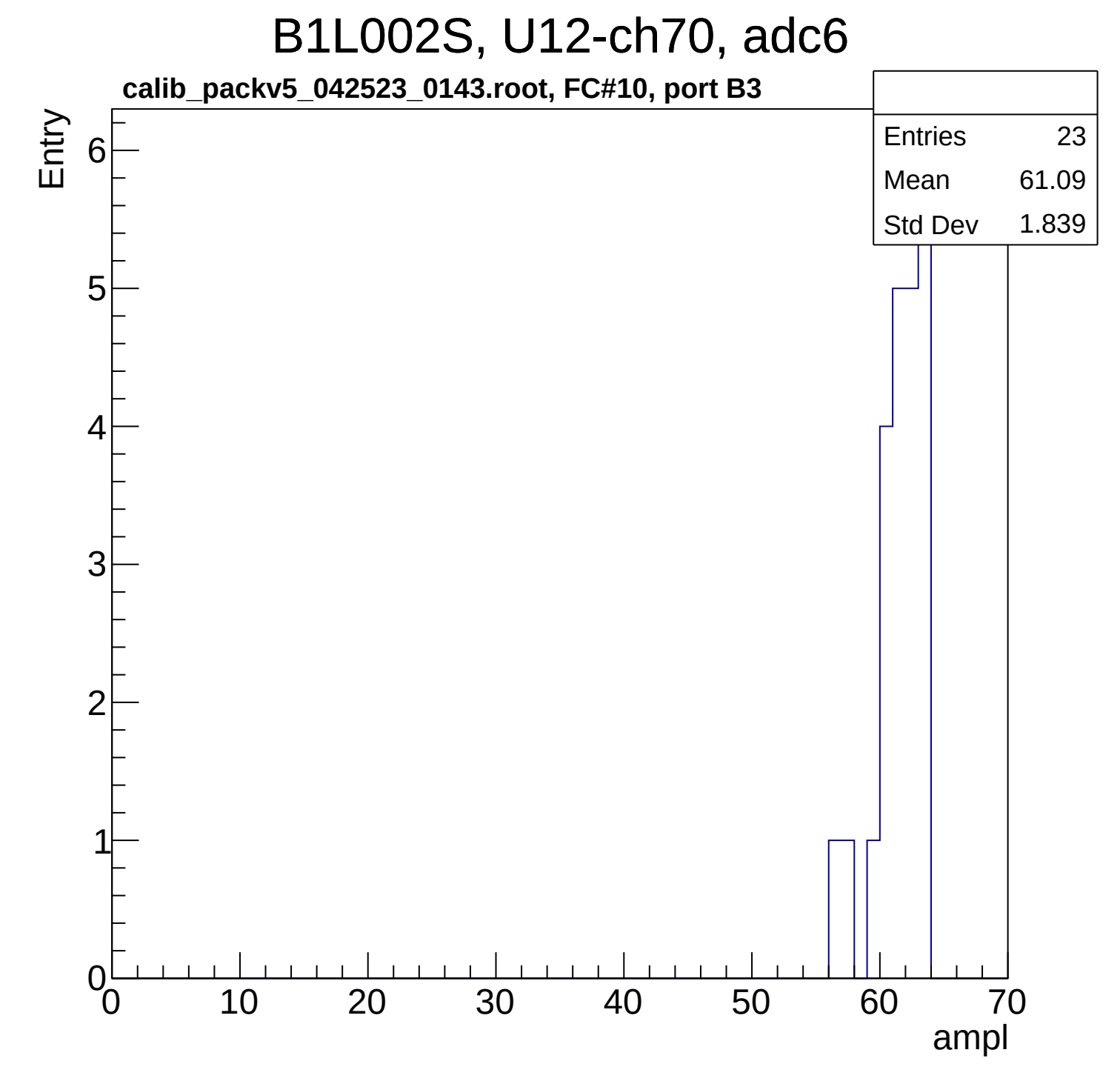
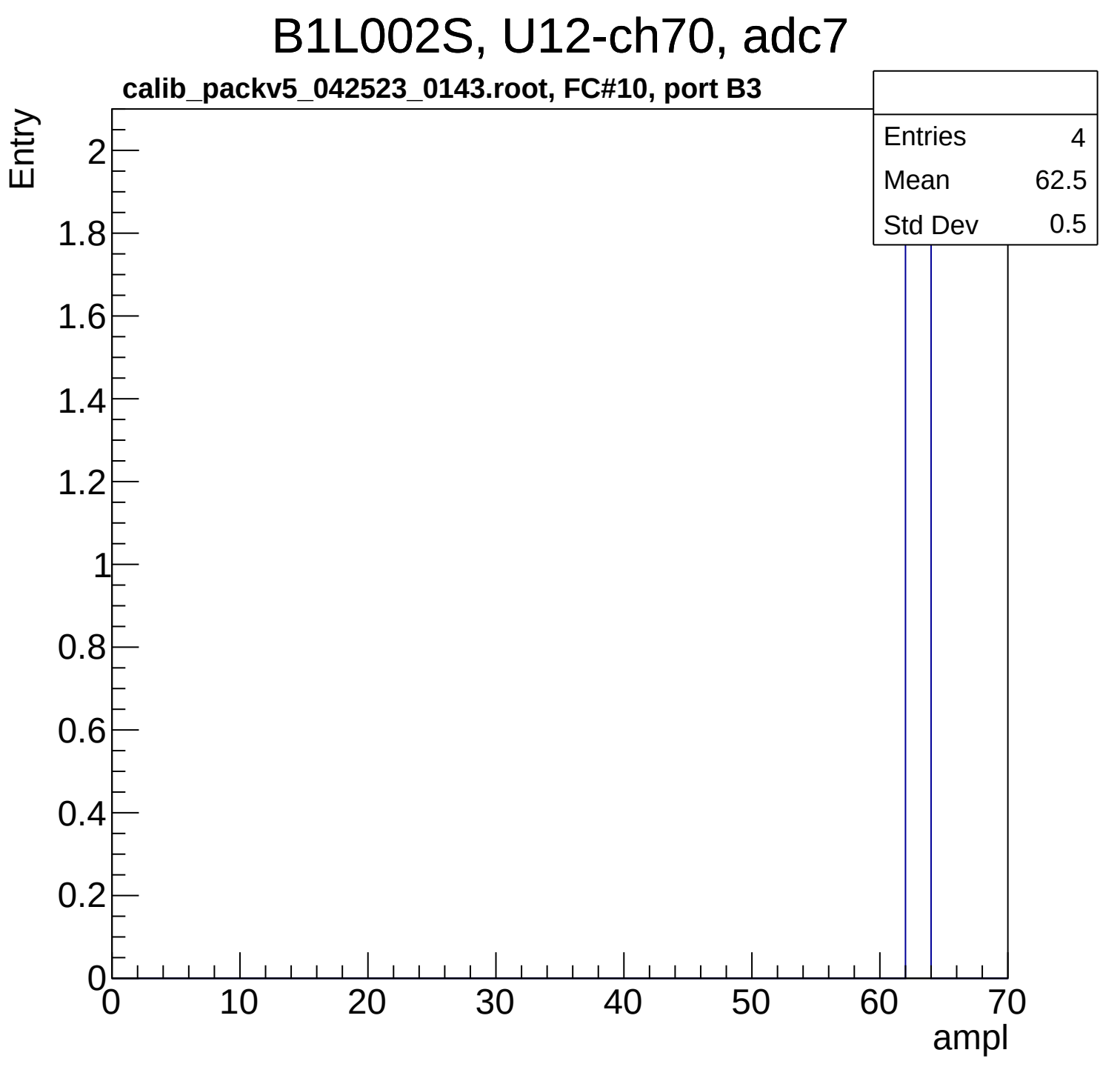
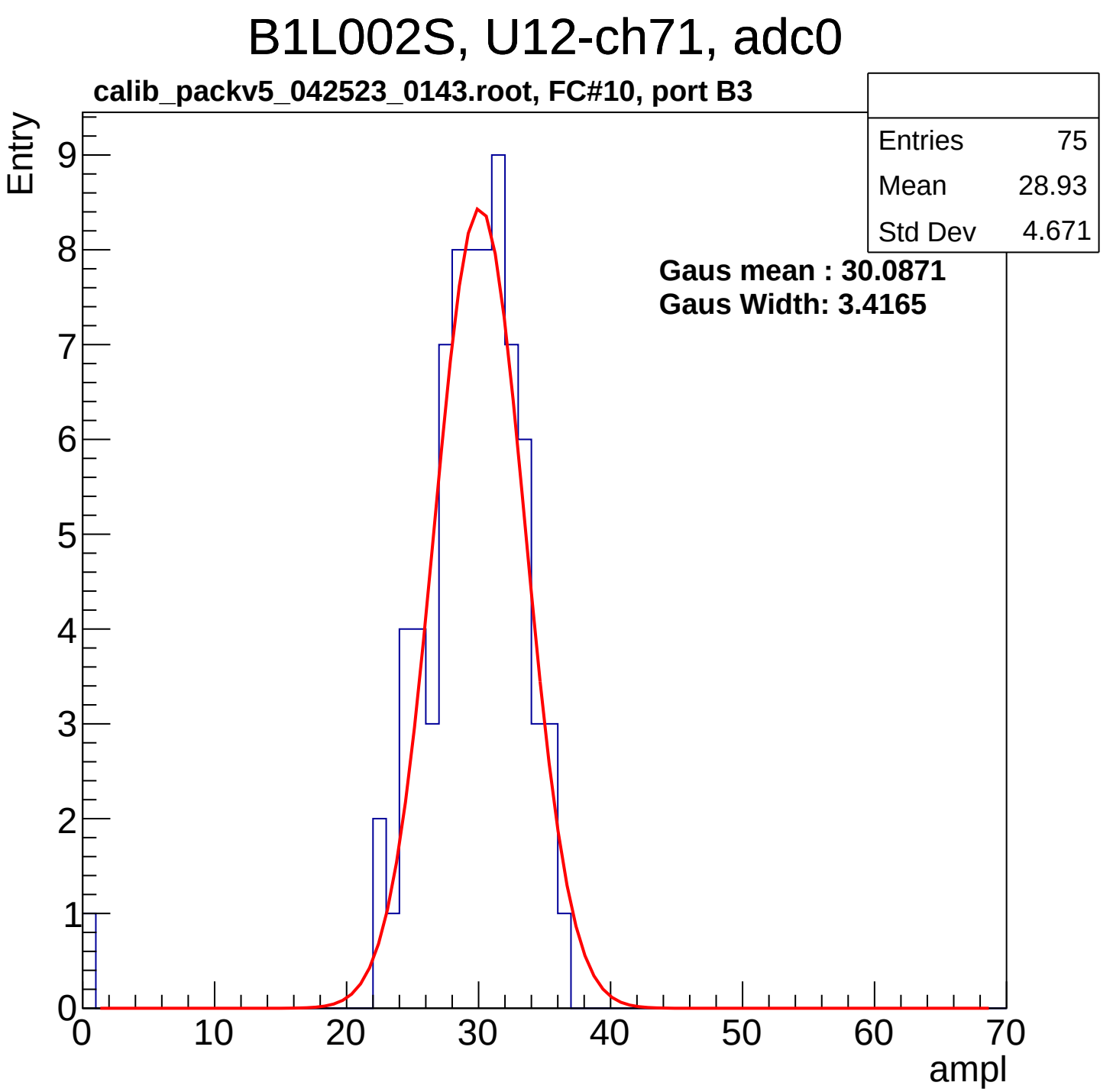
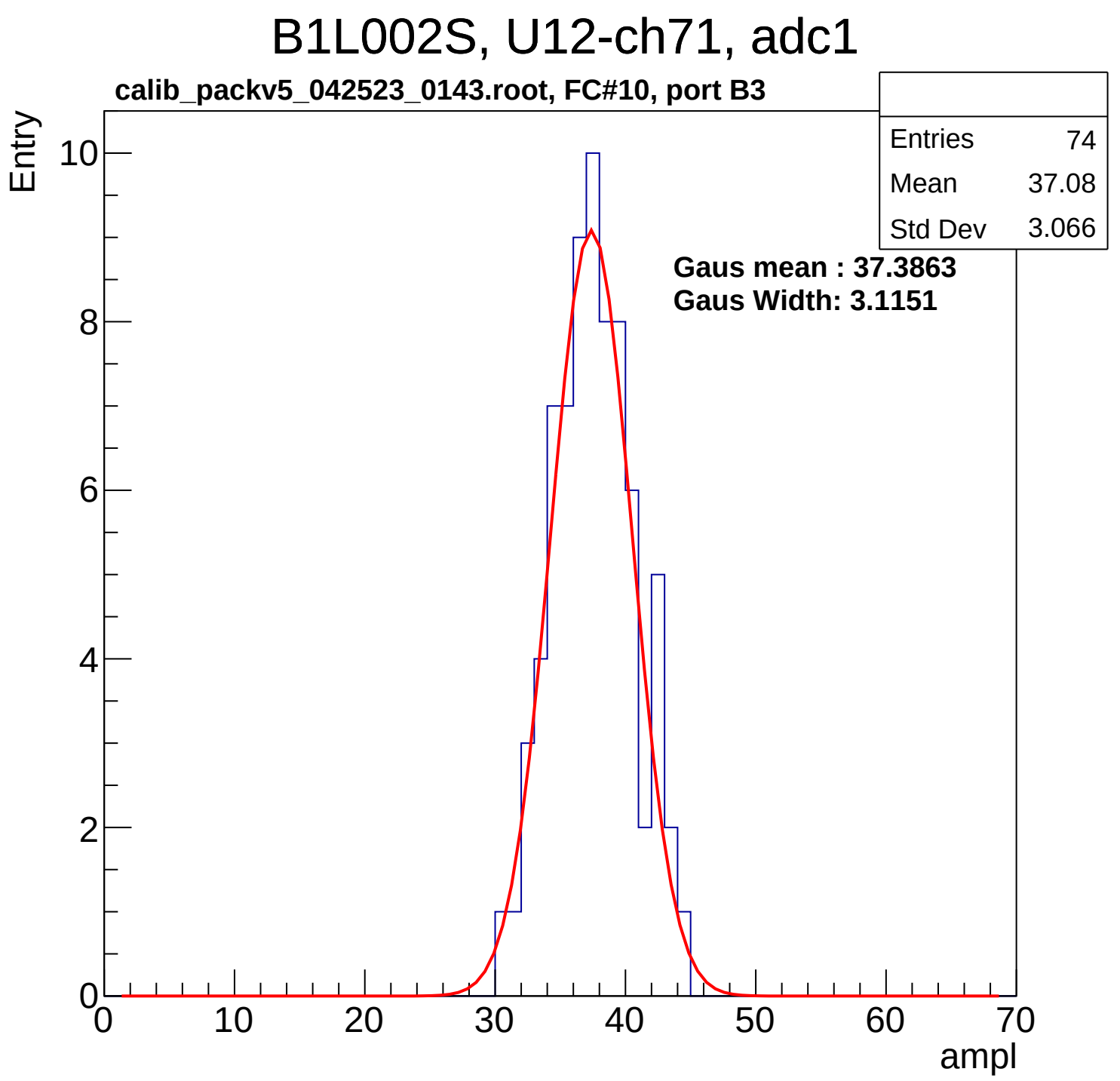

B1L002S, U12-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 51.02
Std Dev 3.769
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

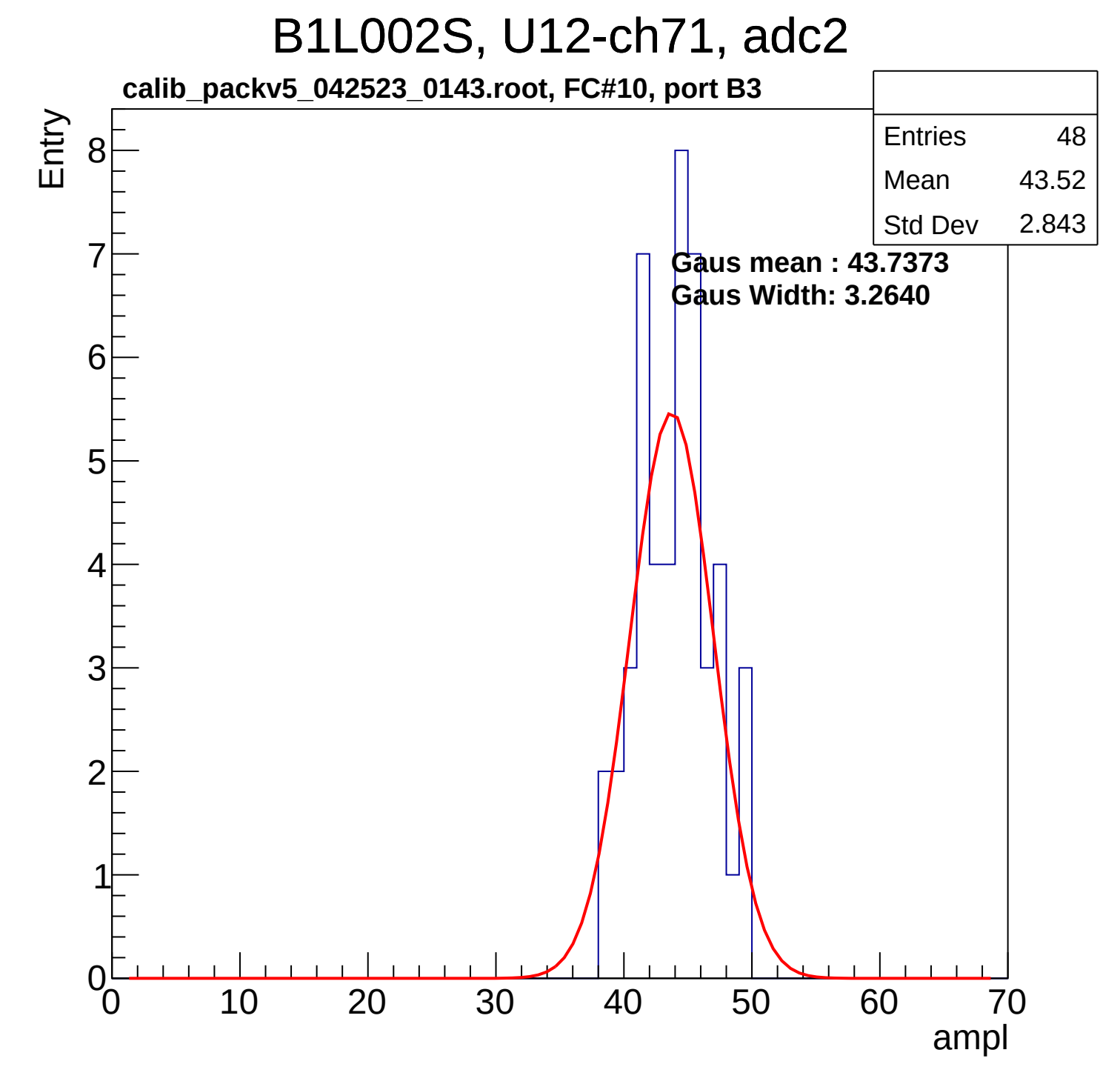

B1L002S, U12-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 55.95
Std Dev 7.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

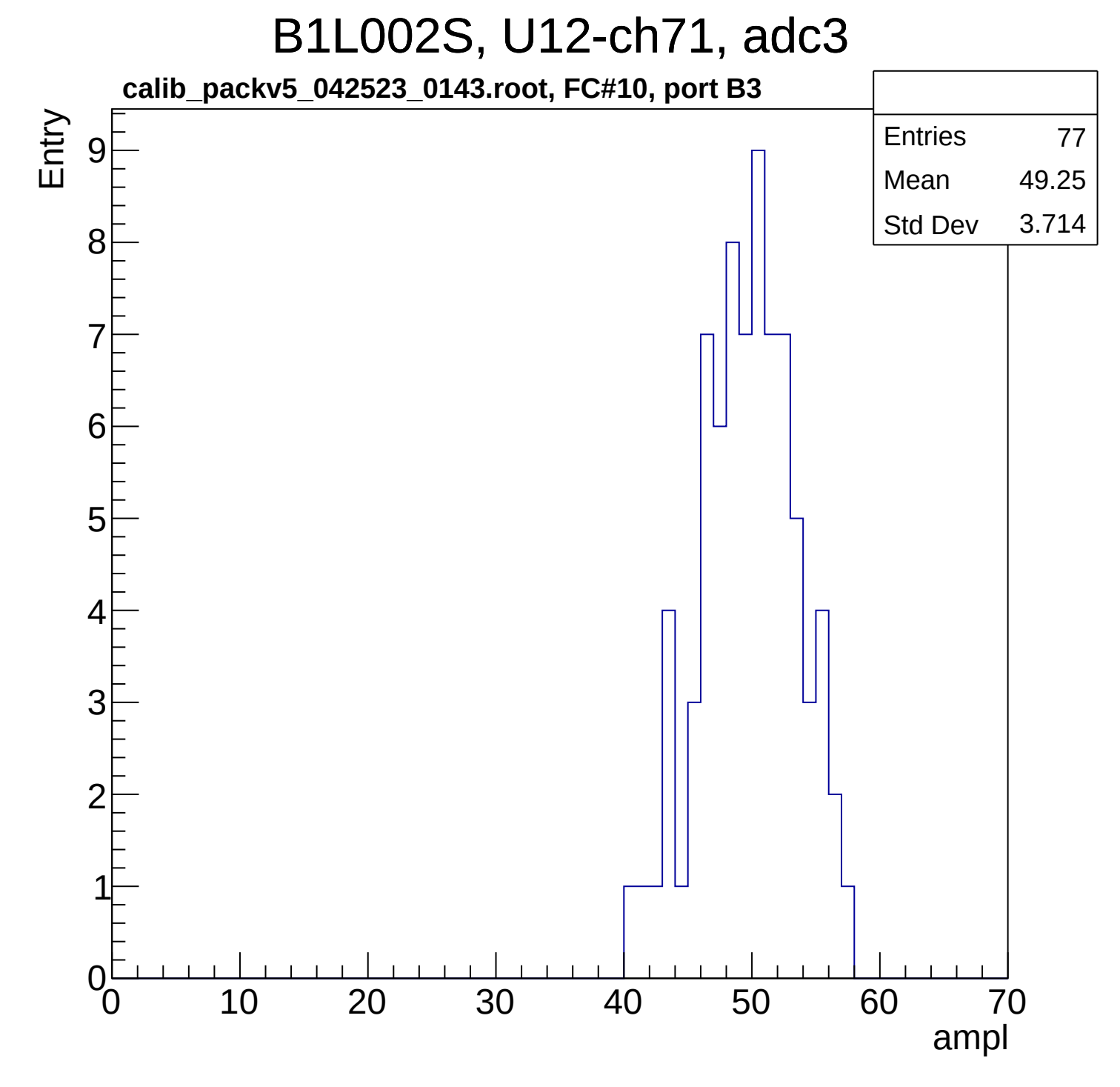

B1L002S, U12-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.09
Std Dev 1.839
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

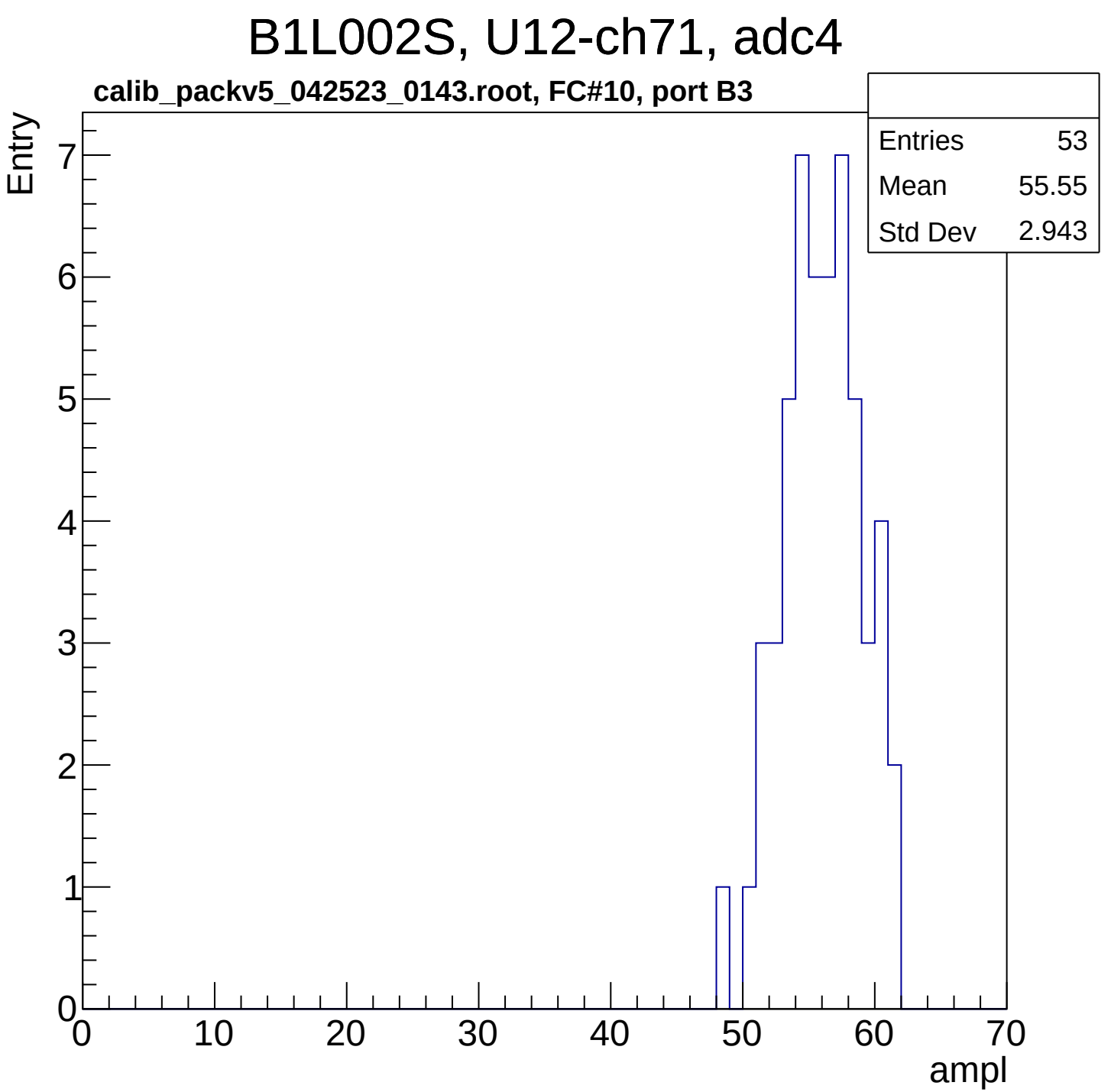

B1L002S, U12-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

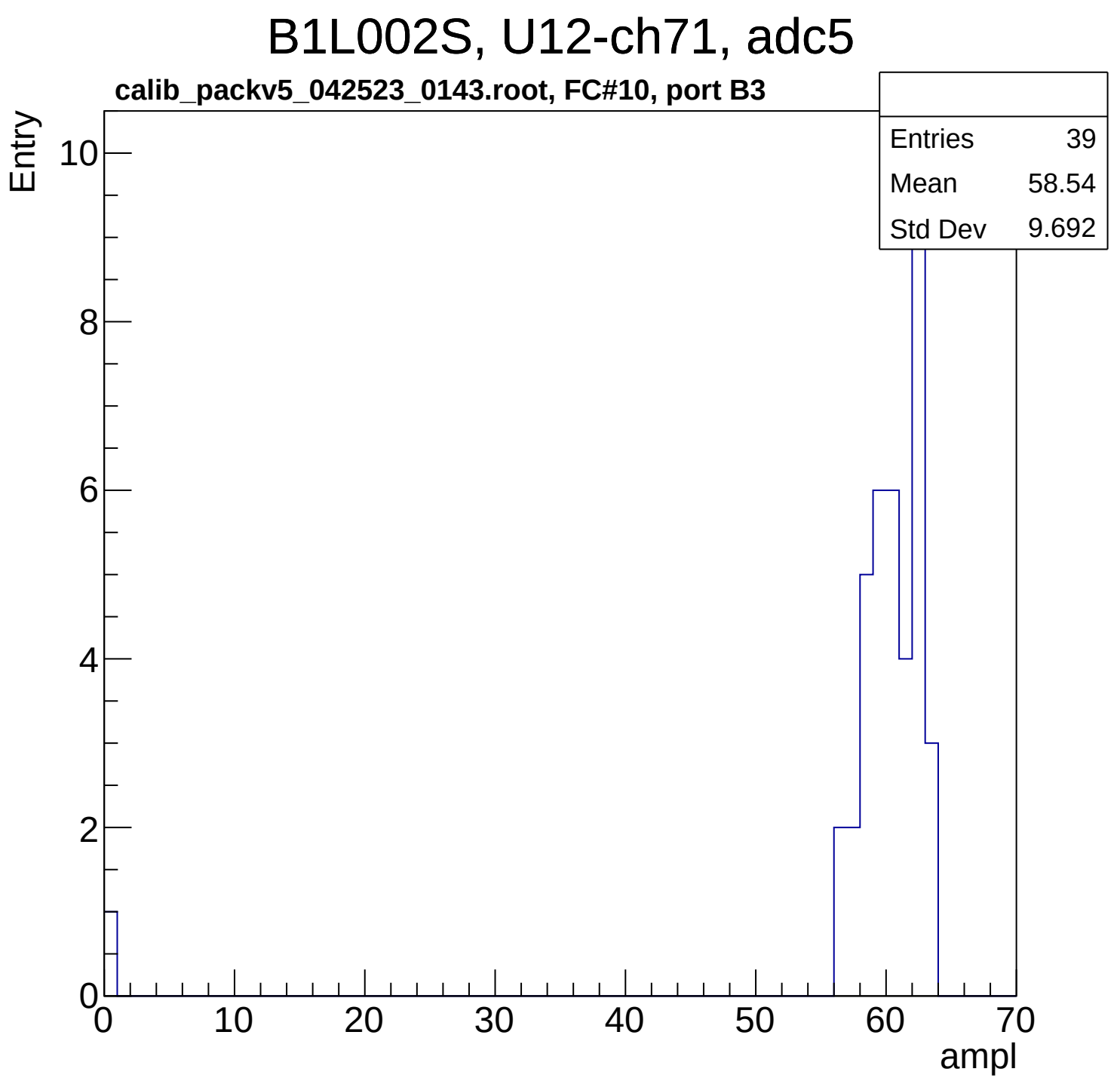

B1L002S, U12-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.93
Std Dev 4.671
Gaus mean : 30.0871
Gaus Width: 3.4165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

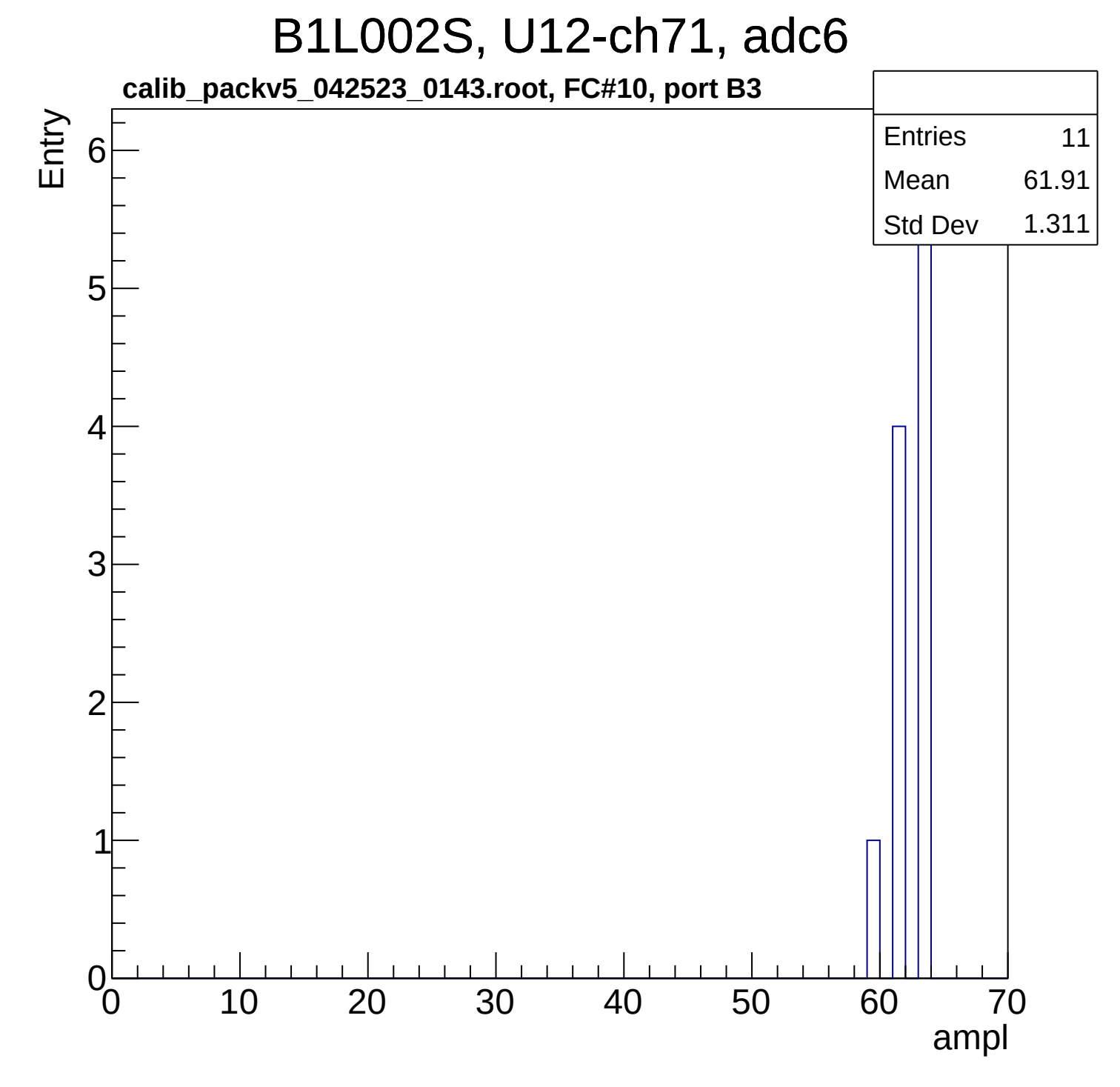

B1L002S, U12-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 37.08
Std Dev 3.066
Gaus mean : 37.3863
Gaus Width: 3.1151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

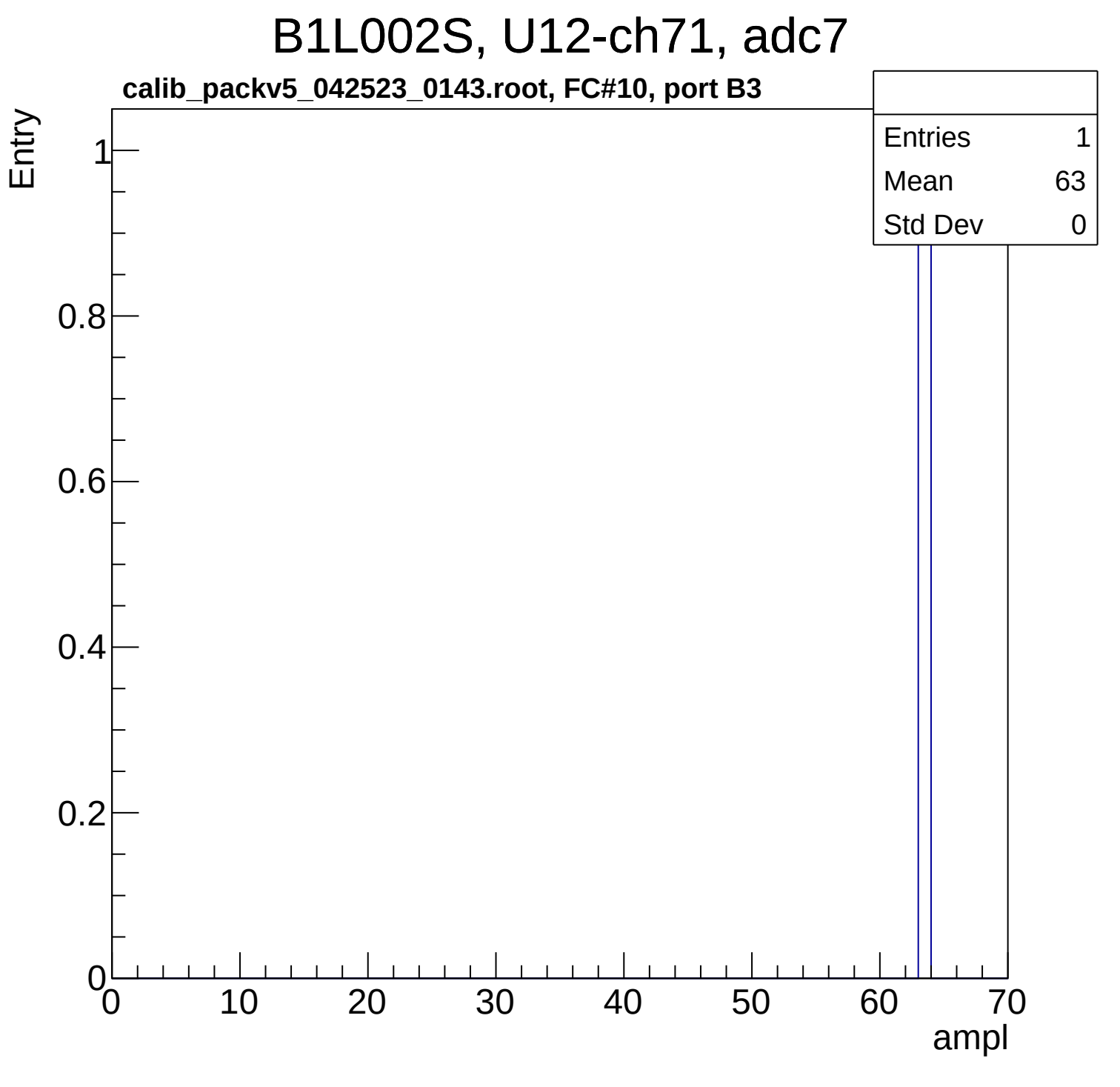

B1L002S, U12-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 43.52
Std Dev 2.843
Gaus mean : 43.7373
Gaus Width: 3.2640
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

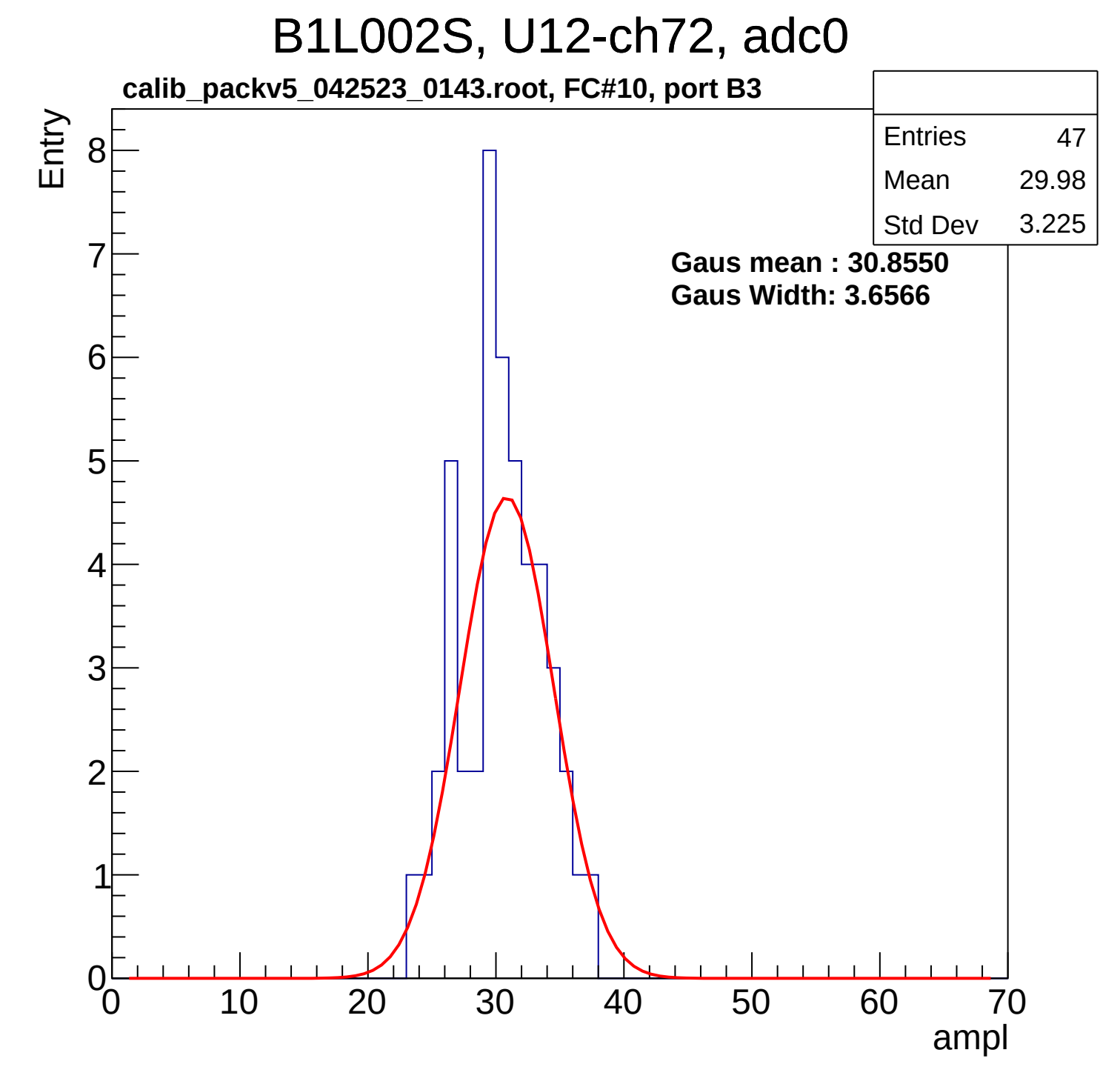

B1L002S, U12-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 49.25
Std Dev 3.714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

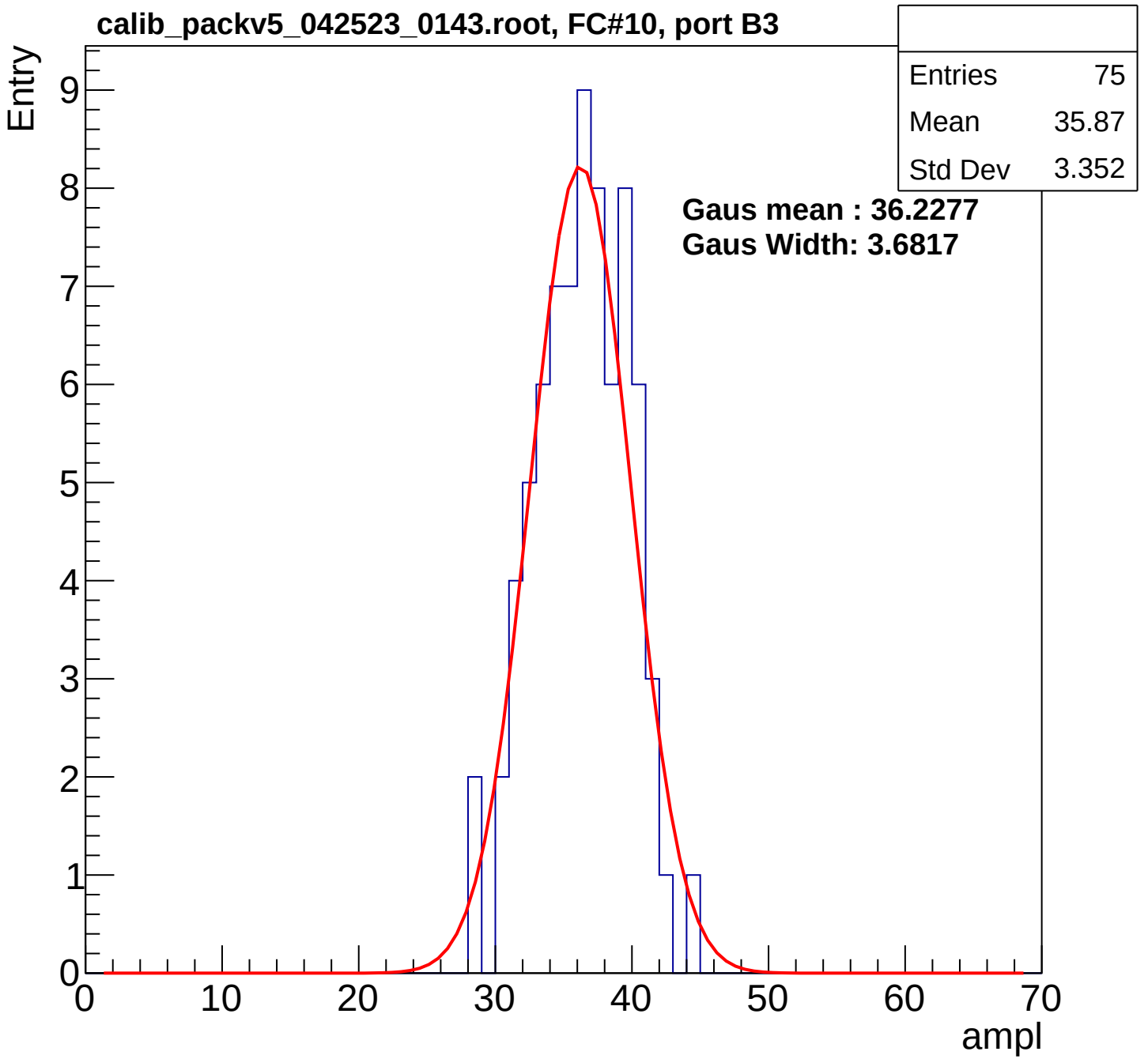

B1L002S, U12-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 55.55
Std Dev 2.943
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

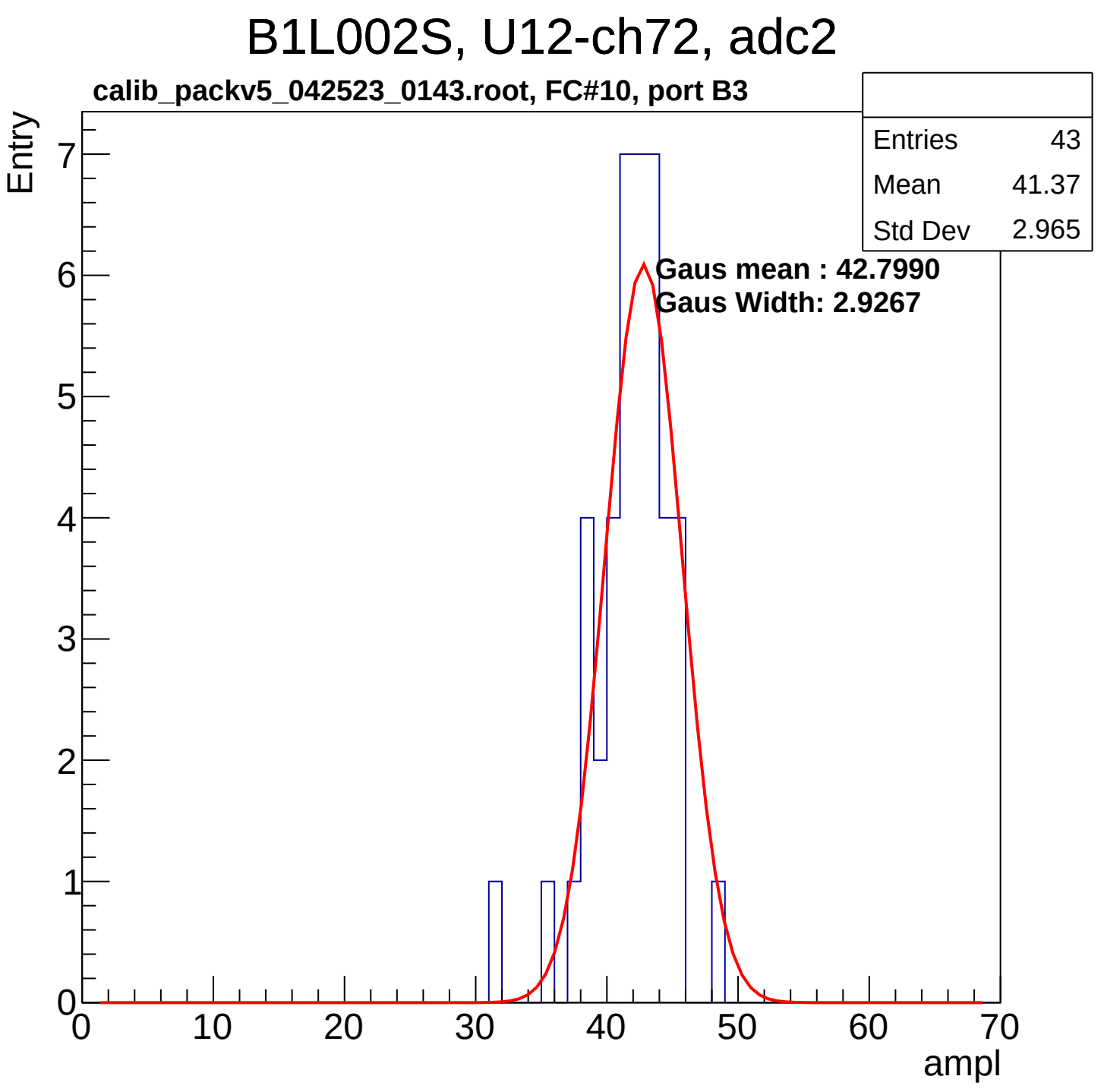

B1L002S, U12-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 58.54
Std Dev 9.692
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

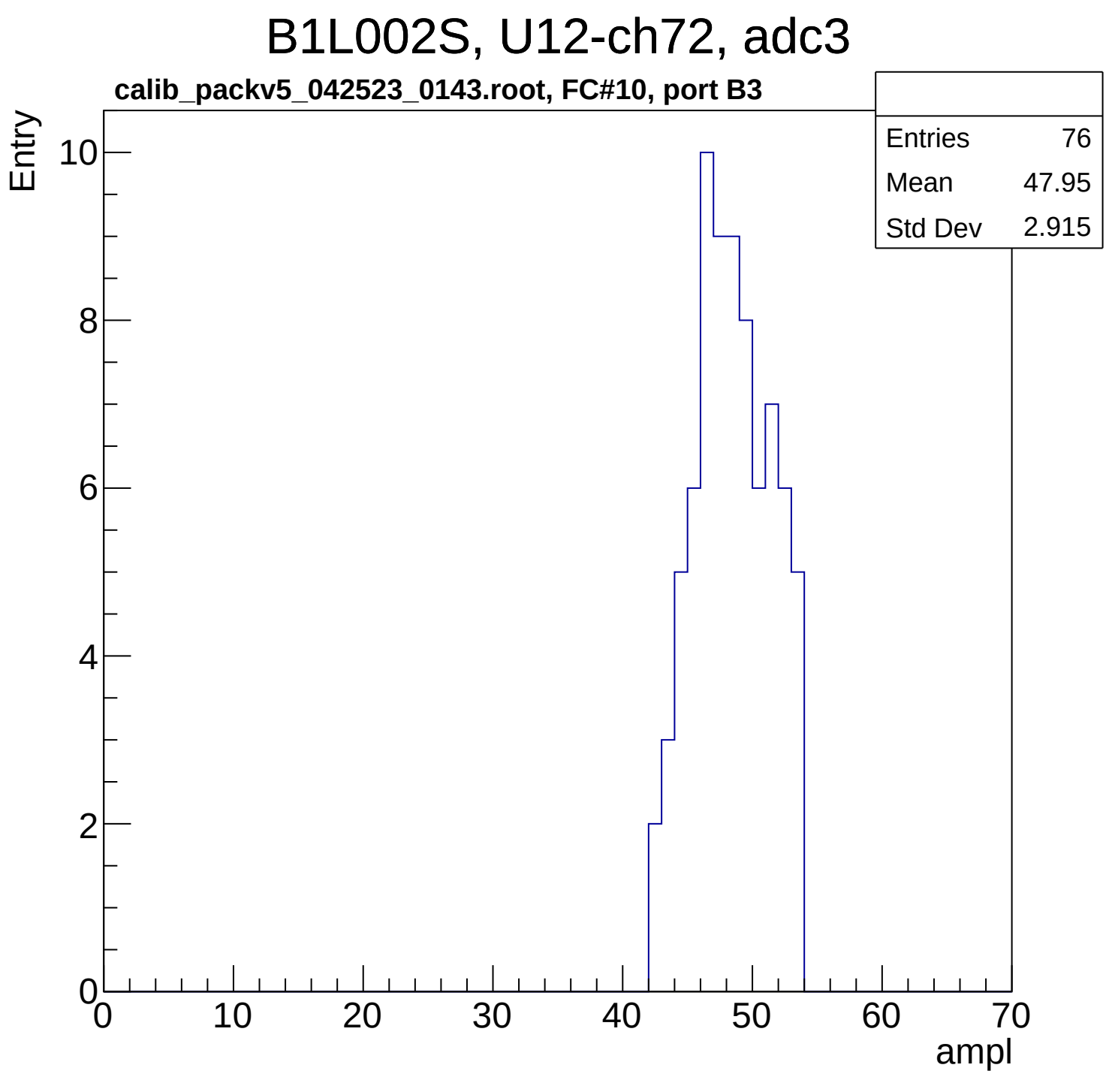

B1L002S, U12-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.91
Std Dev 1.311
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

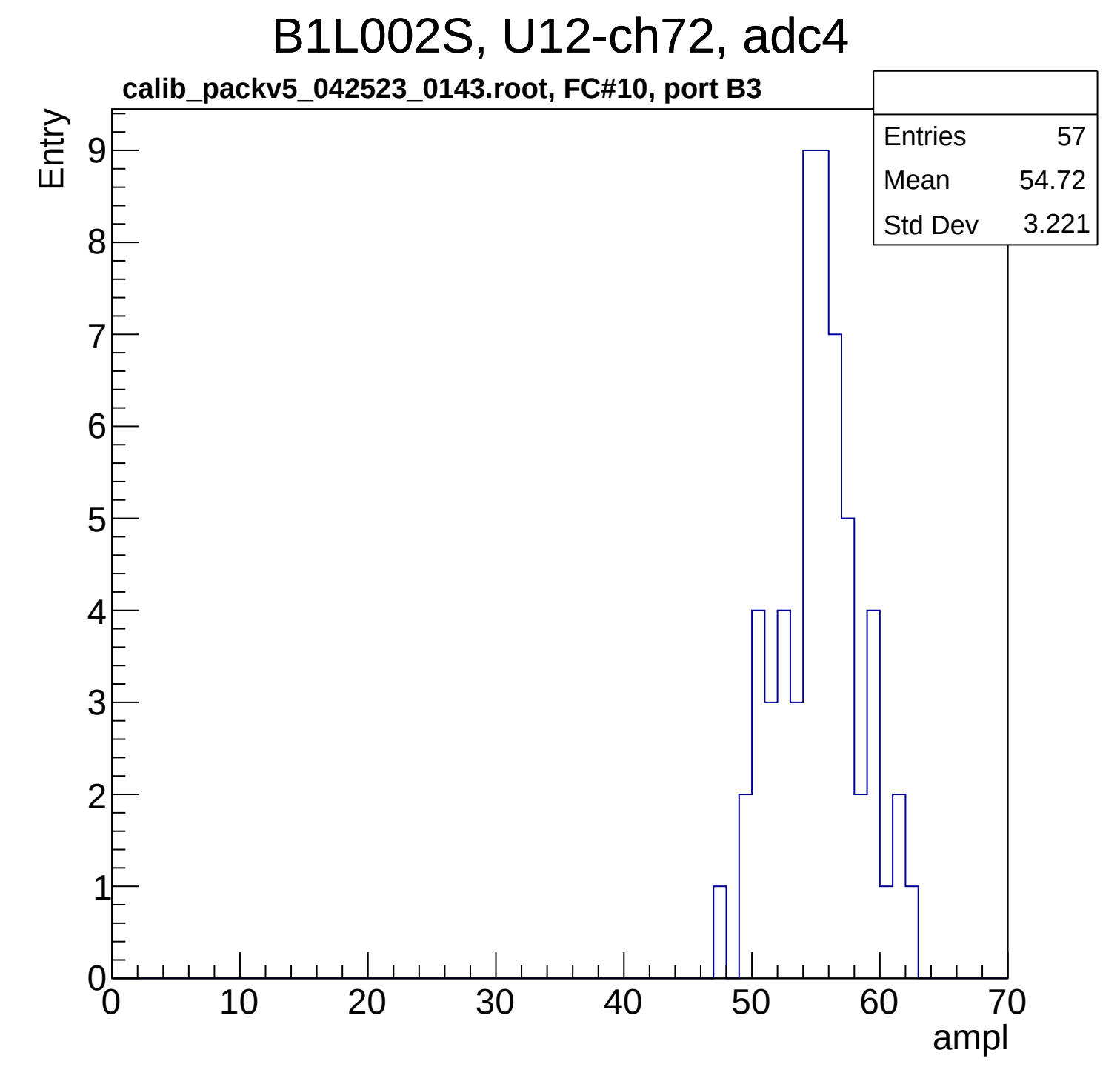

B1L002S, U12-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 29.98
Std Dev 3.225
Gaus mean : 30.8550
Gaus Width: 3.6566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.87
Std Dev 3.352
Gaus mean : 36.2277
Gaus Width: 3.6817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 41.37
Std Dev 2.965
Gaus mean : 42.7990
Gaus Width: 2.9267
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.95
Std Dev 2.915
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.72
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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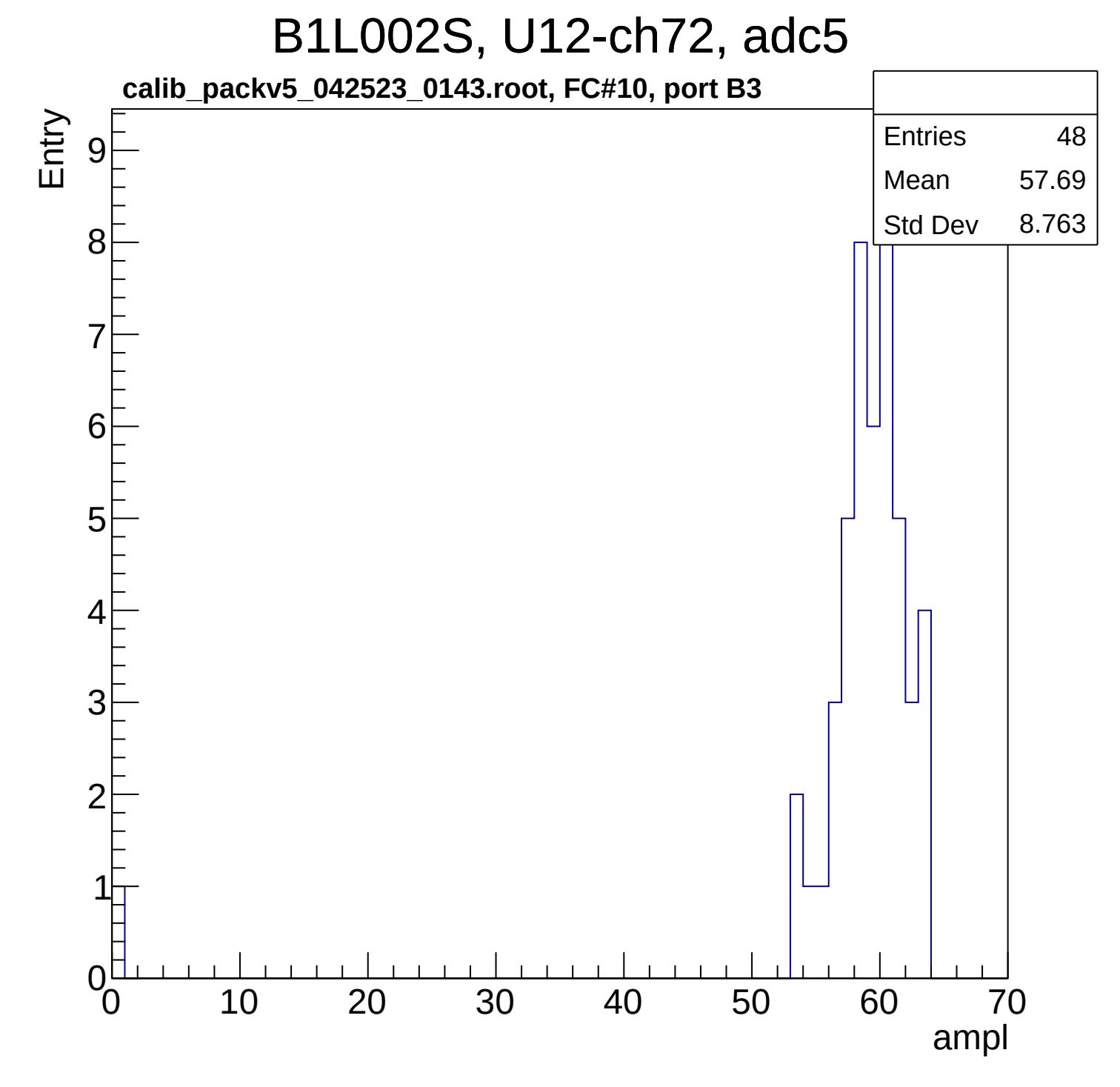
B1L002S, U12-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.69
Std Dev 8.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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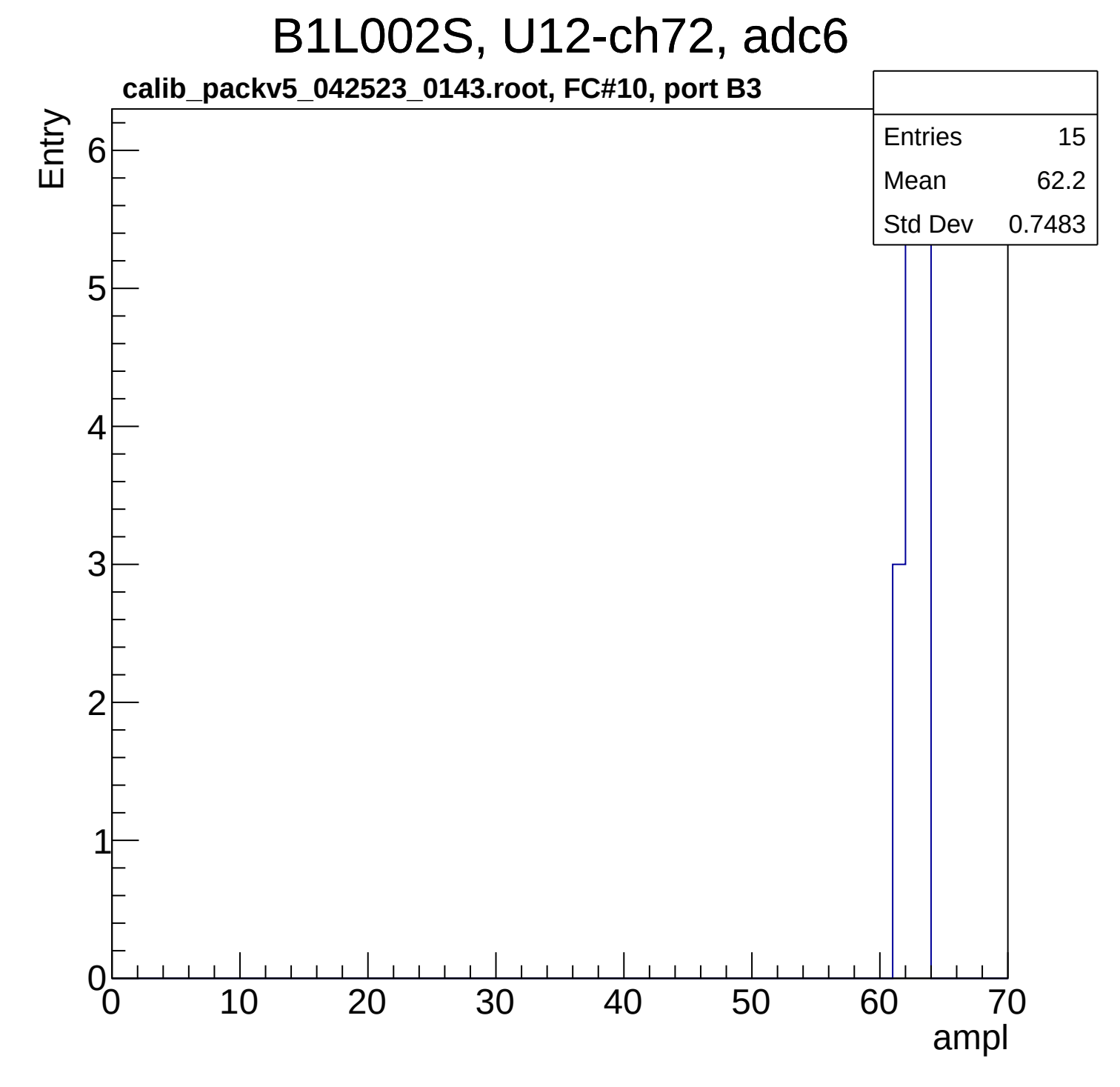
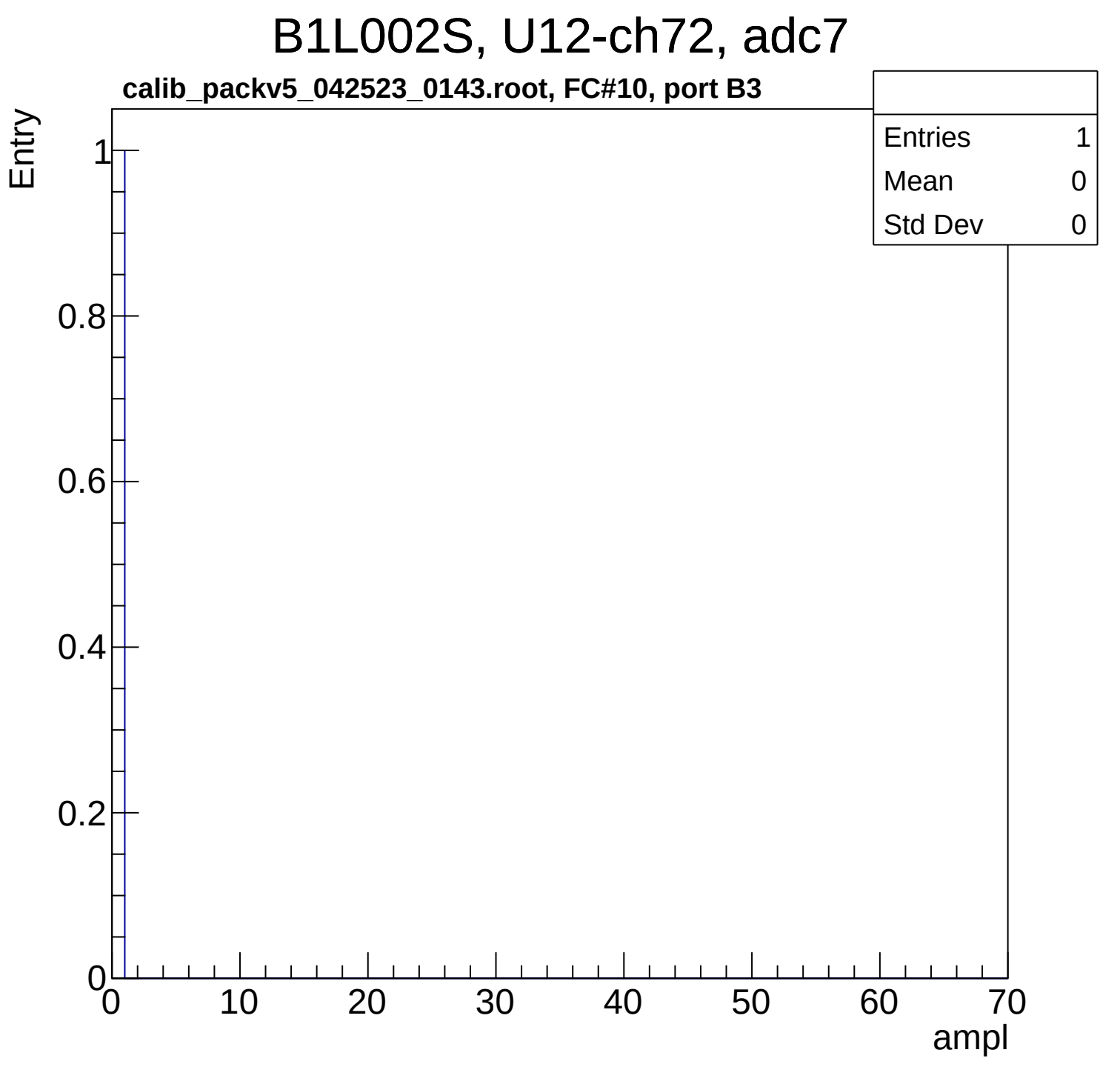
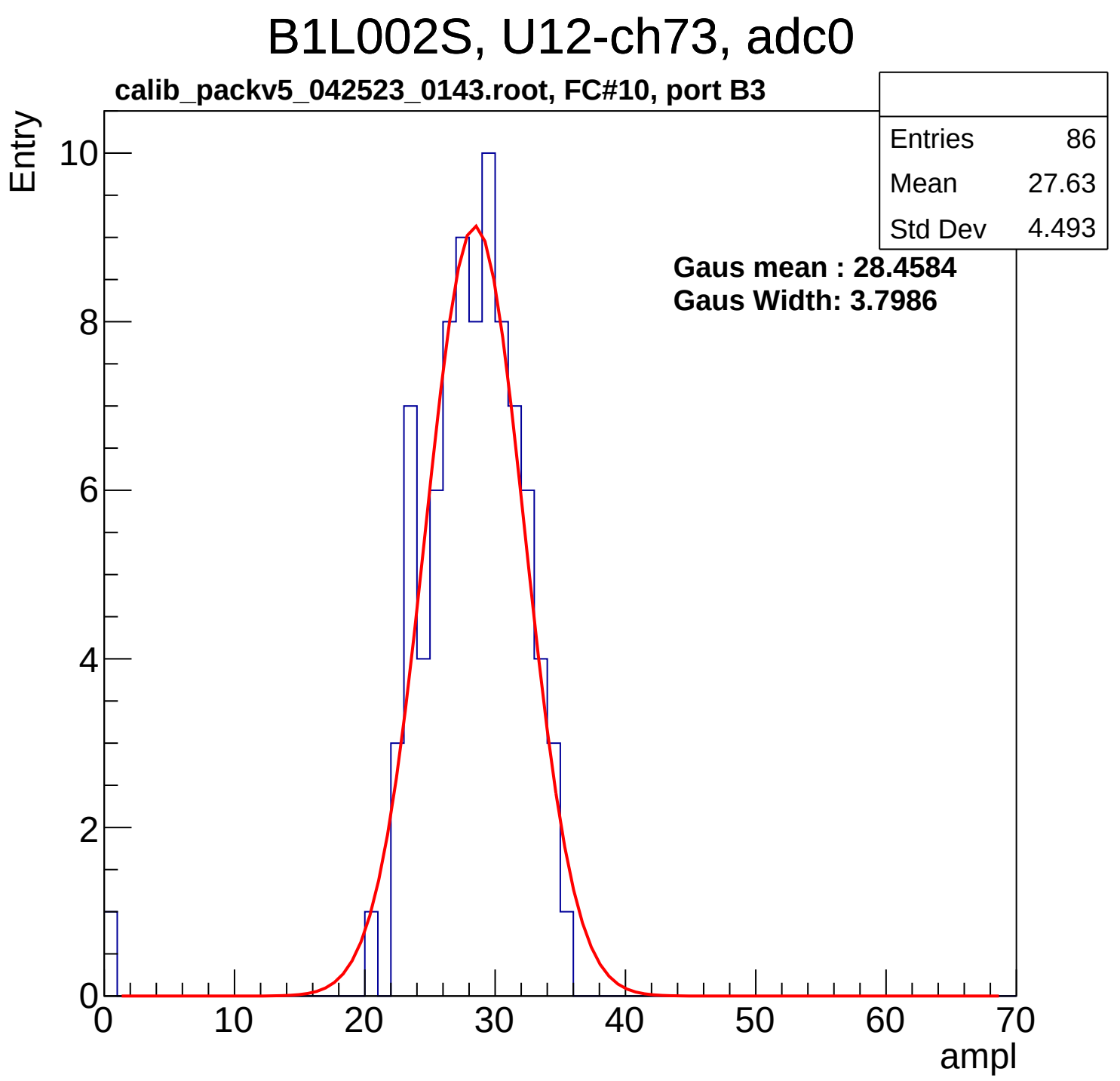

B1L002S, U12-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 62.2
Std Dev 0.7483
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 27.63
Std Dev 4.493
Gaus mean : 28.4584
Gaus Width: 3.7986
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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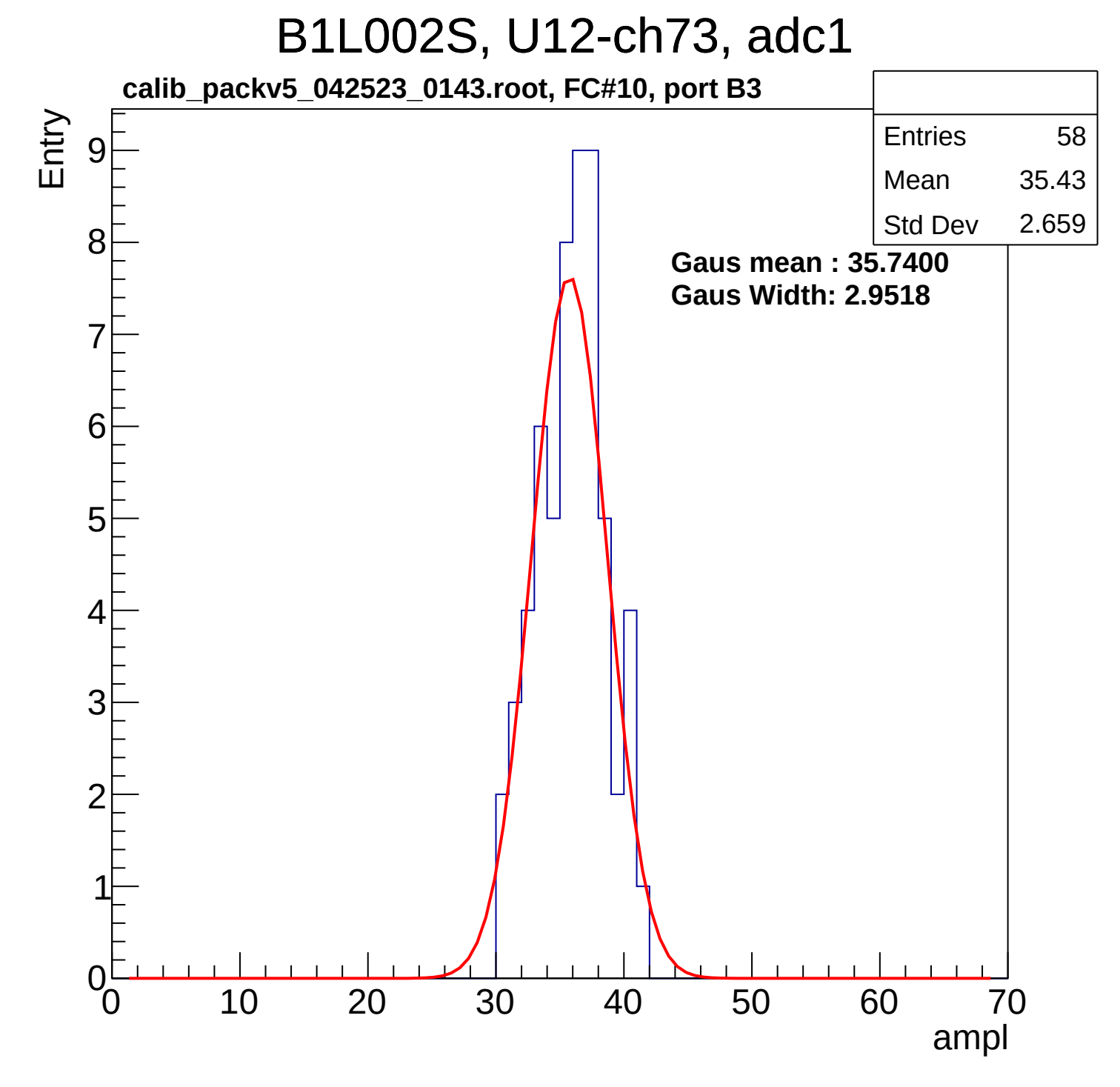
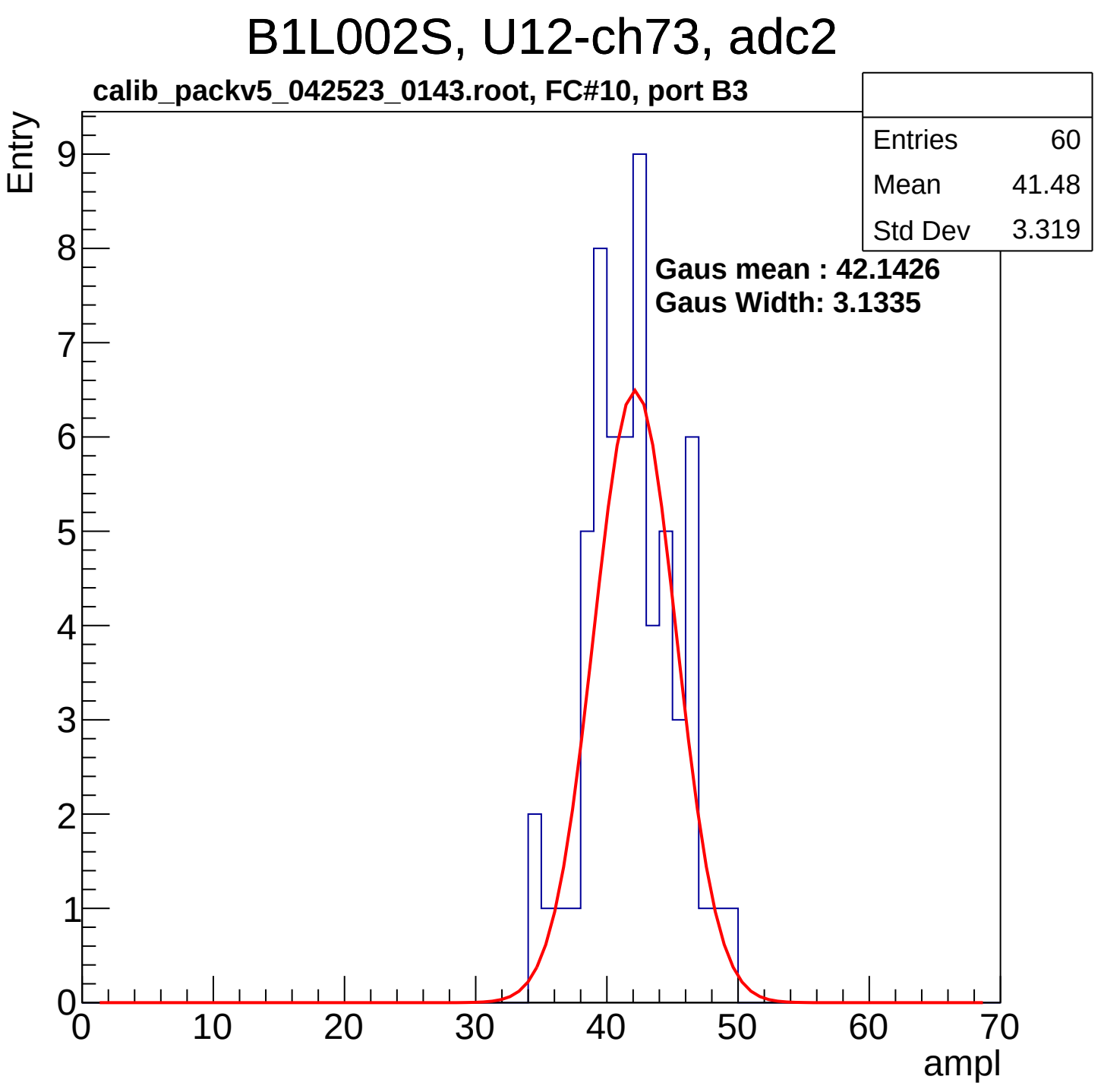
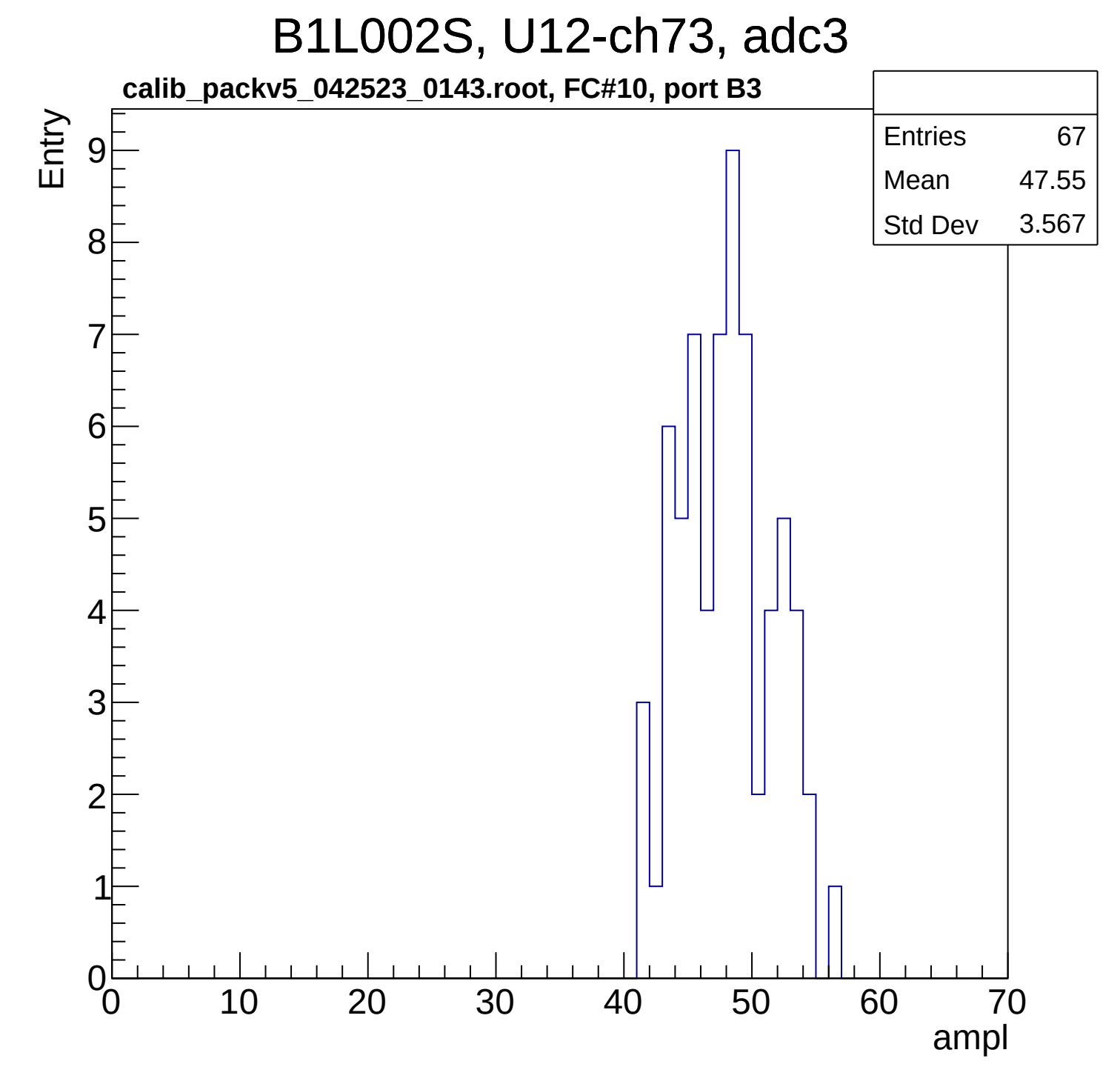

B1L002S, U12-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.43
Std Dev 2.659
Gaus mean : 35.7400
Gaus Width: 2.9518
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.48
Std Dev 3.319
Gaus mean : 42.1426
Gaus Width: 3.1335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.55
Std Dev 3.567
Entry
ampl
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2
3
4
5
6
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0
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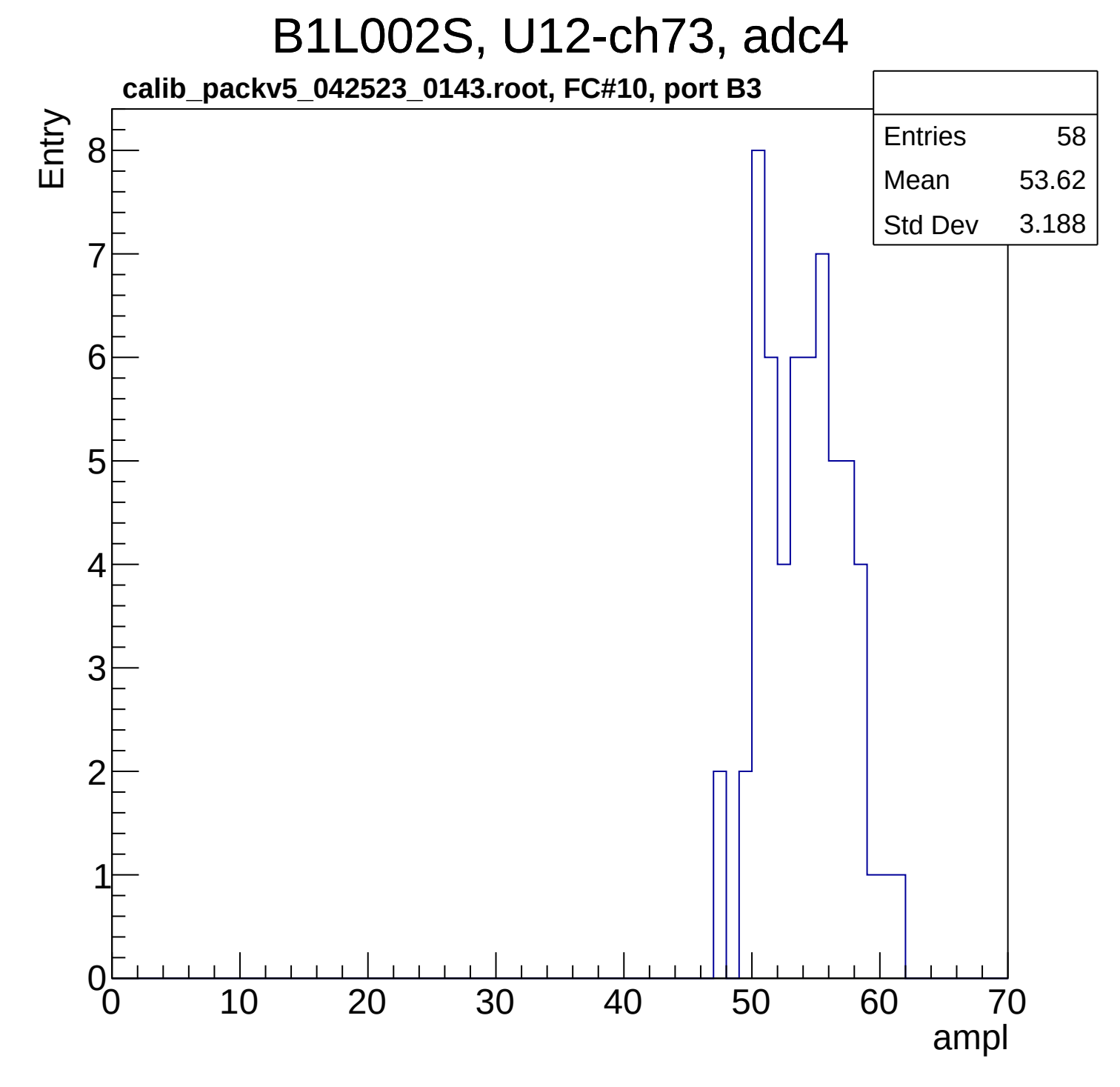
B1L002S, U12-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.62
Std Dev 3.188
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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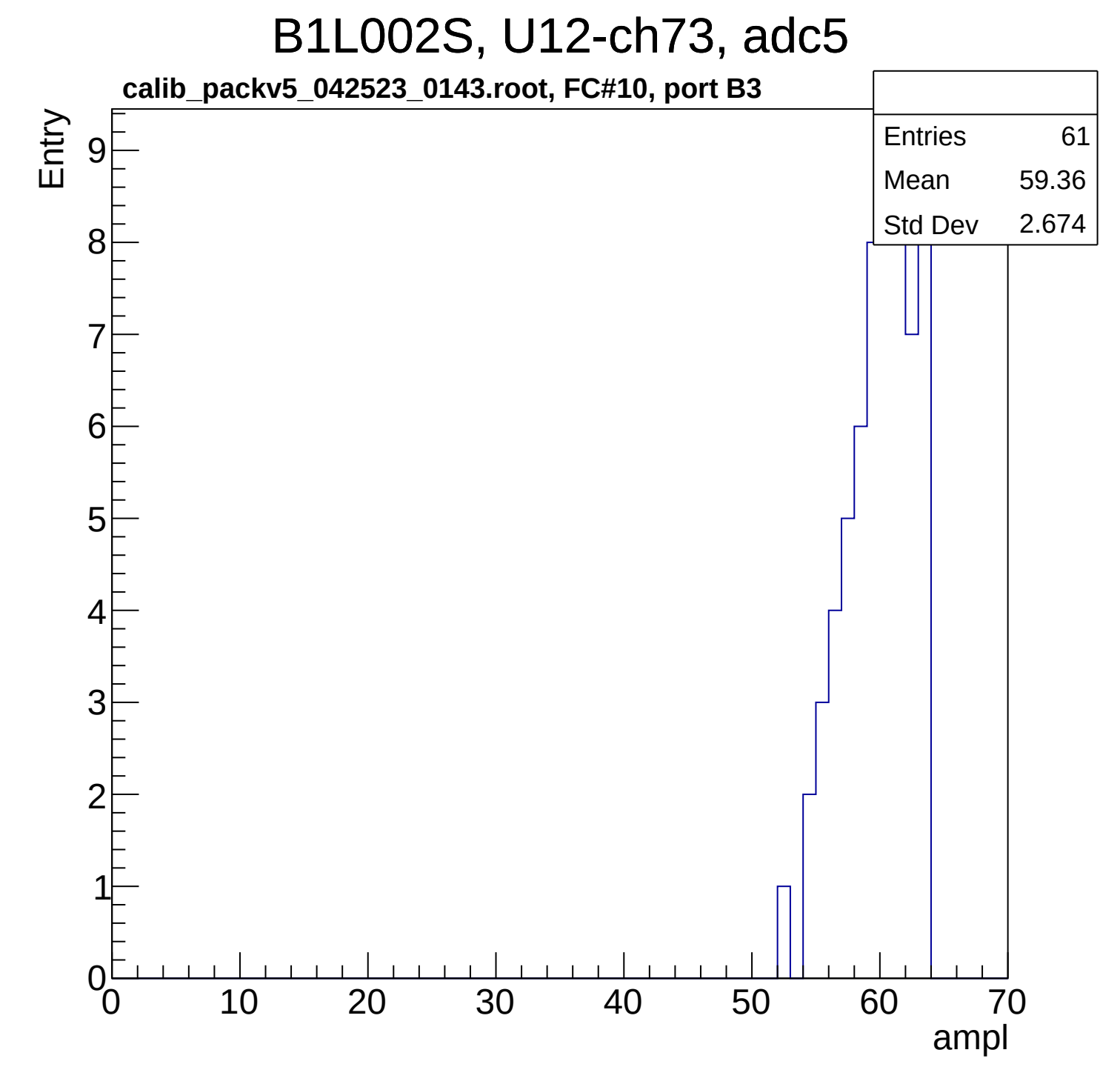
B1L002S, U12-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 59.36
Std Dev 2.674
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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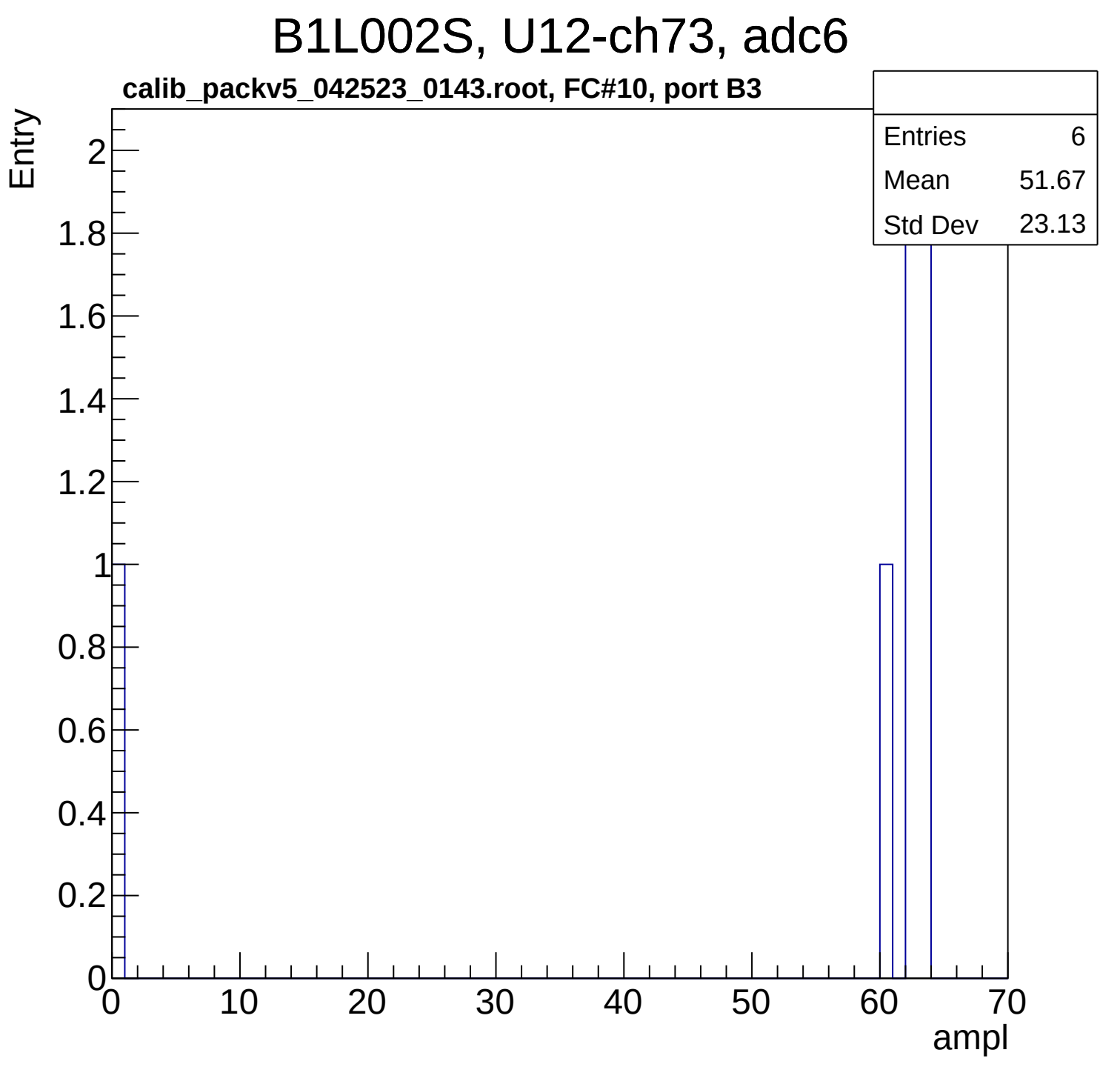
B1L002S, U12-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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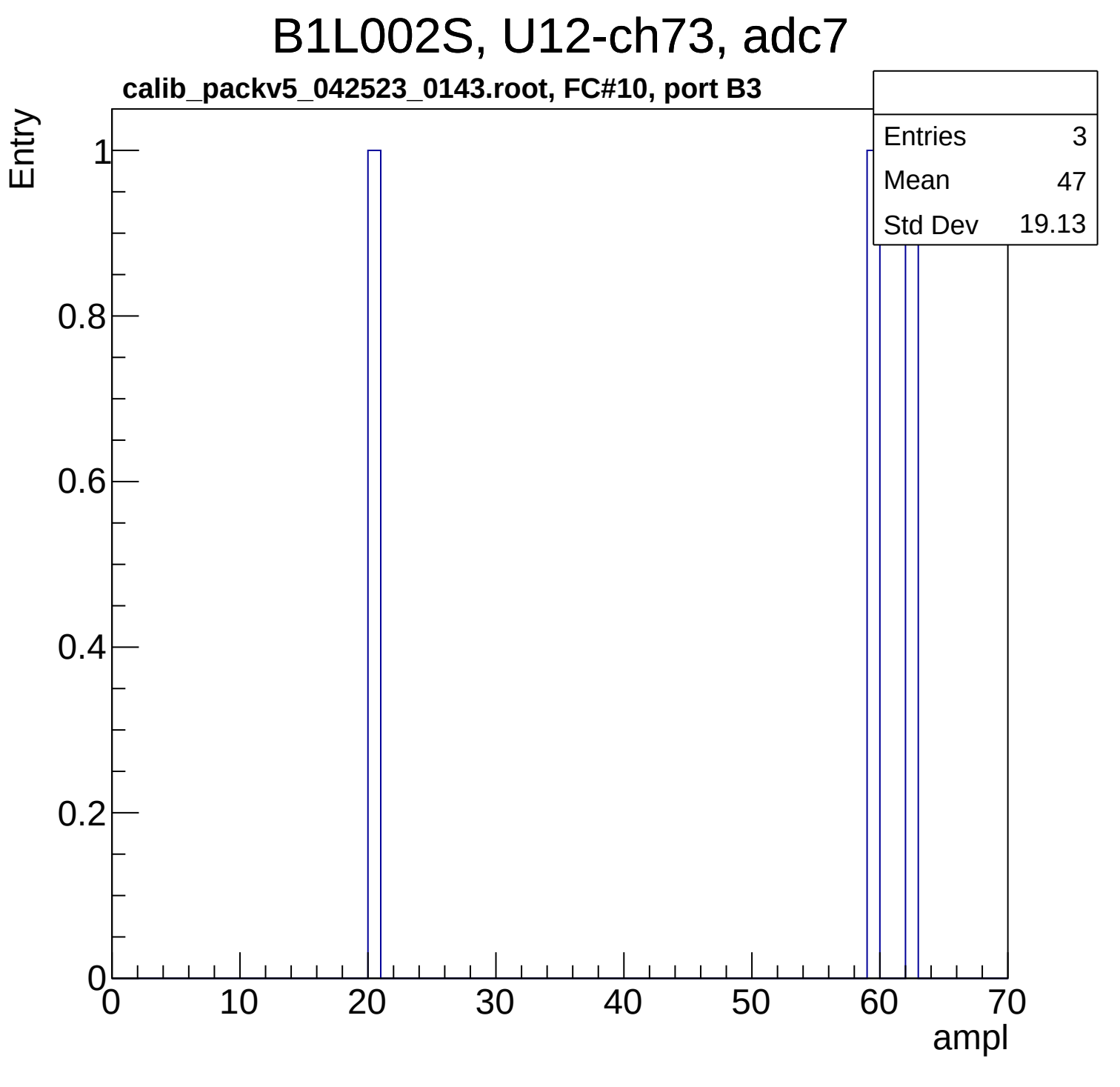
B1L002S, U12-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 47
Std Dev 19.13
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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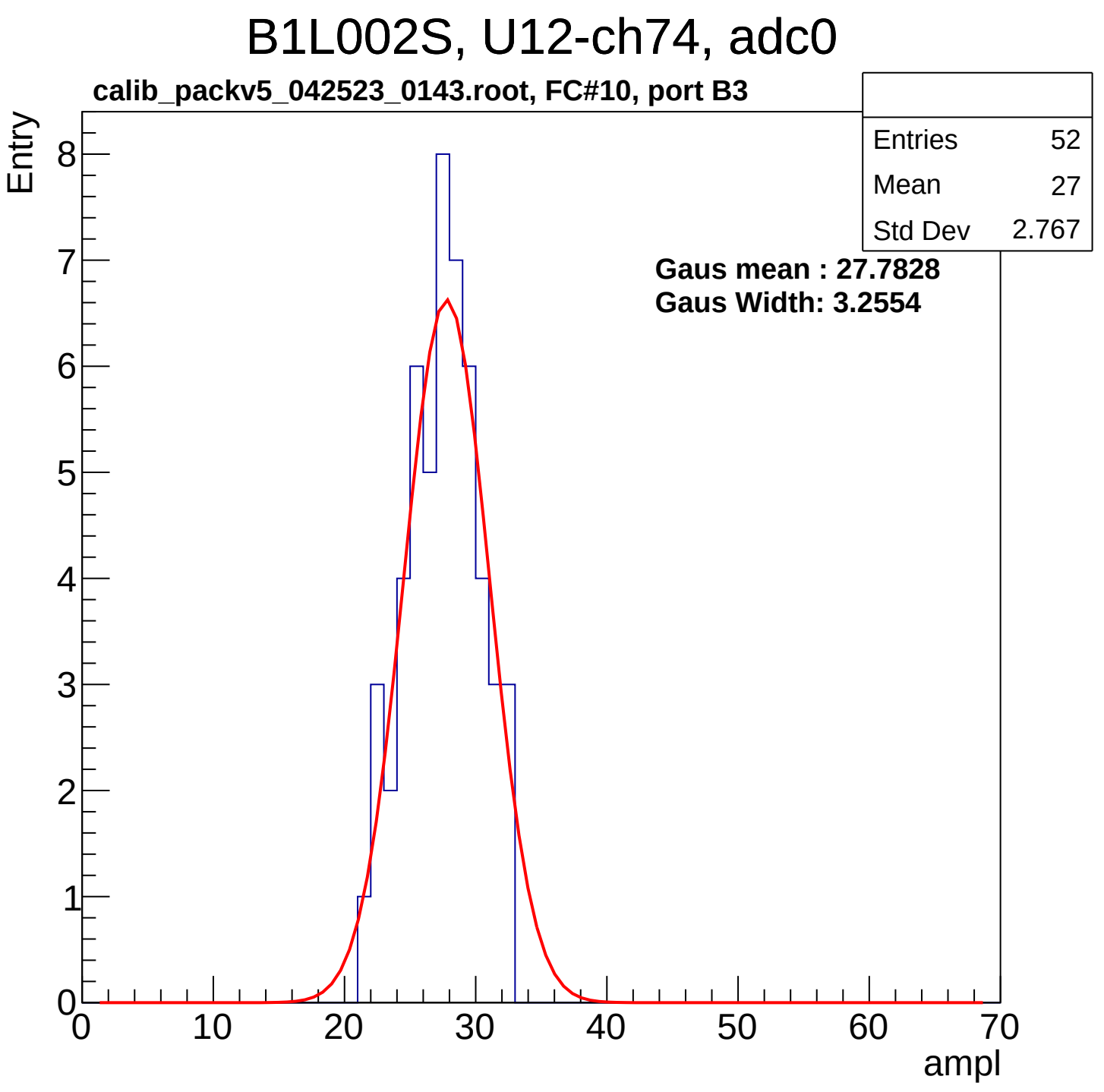
B1L002S, U12-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27
Std Dev 2.767
Gaus mean : 27.7828
Gaus Width: 3.2554
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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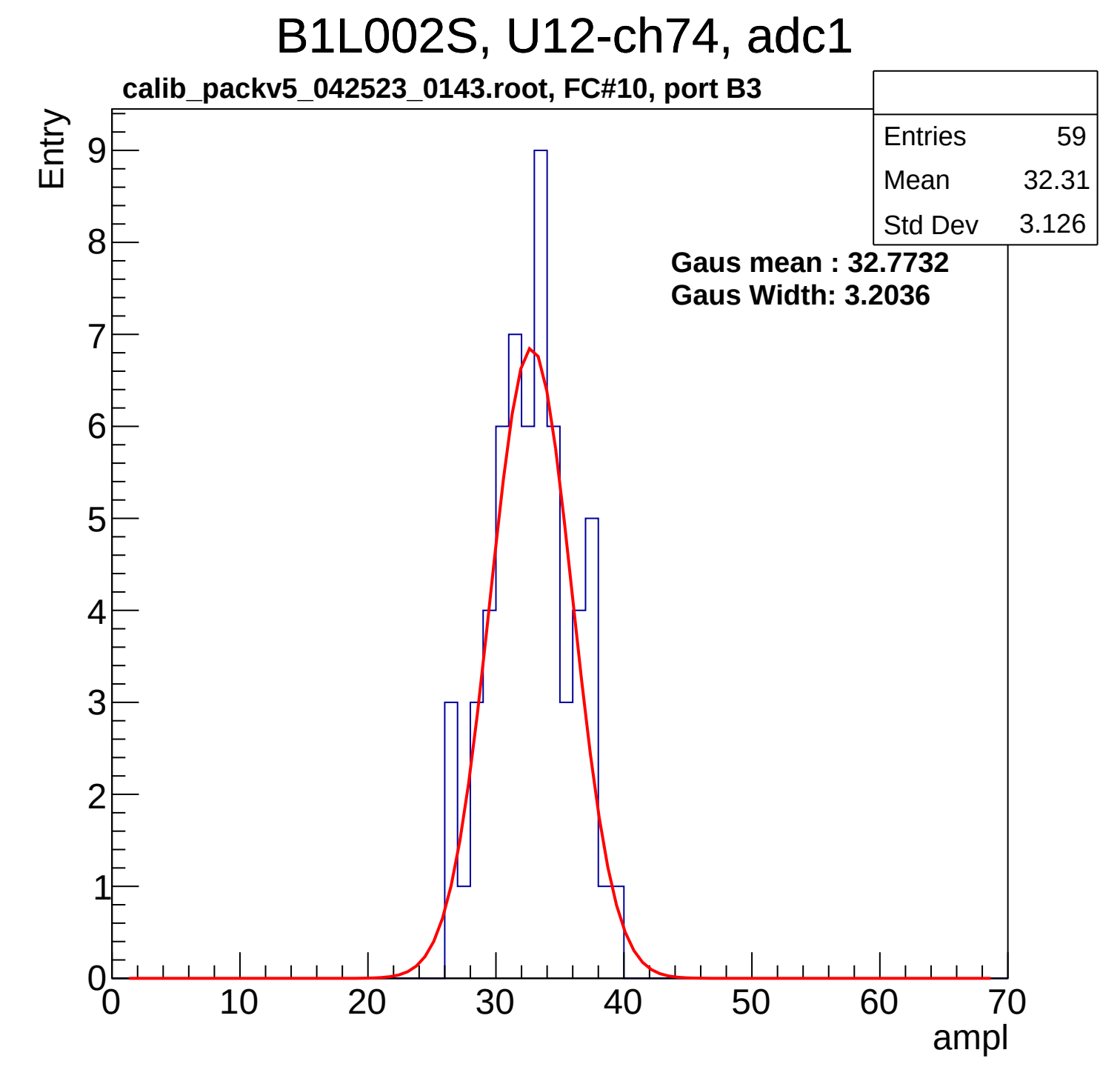
B1L002S, U12-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 32.31
Std Dev 3.126
Gaus mean : 32.7732
Gaus Width: 3.2036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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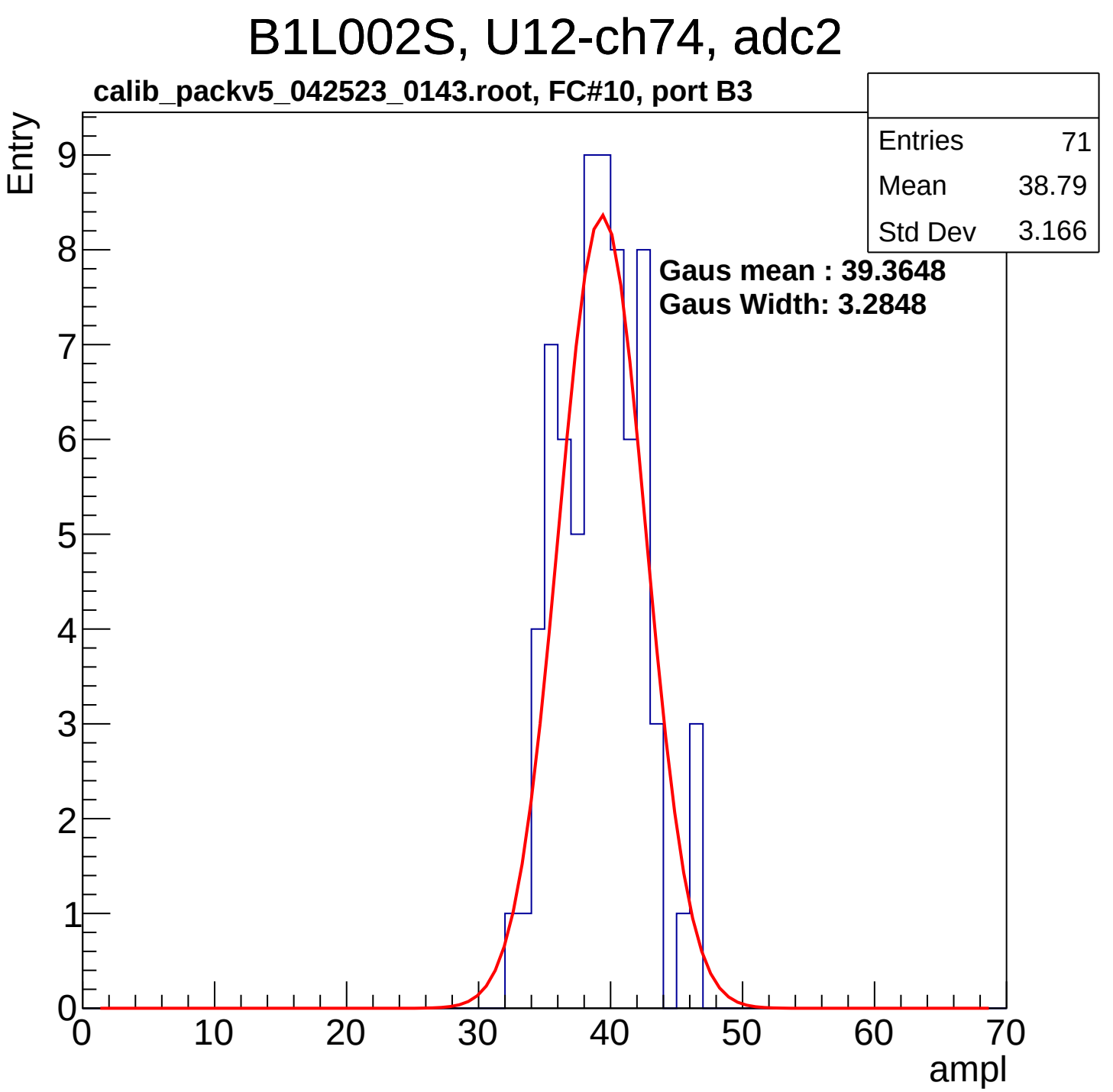
B1L002S, U12-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 38.79
Std Dev 3.166
Gaus mean : 39.3648
Gaus Width: 3.2848
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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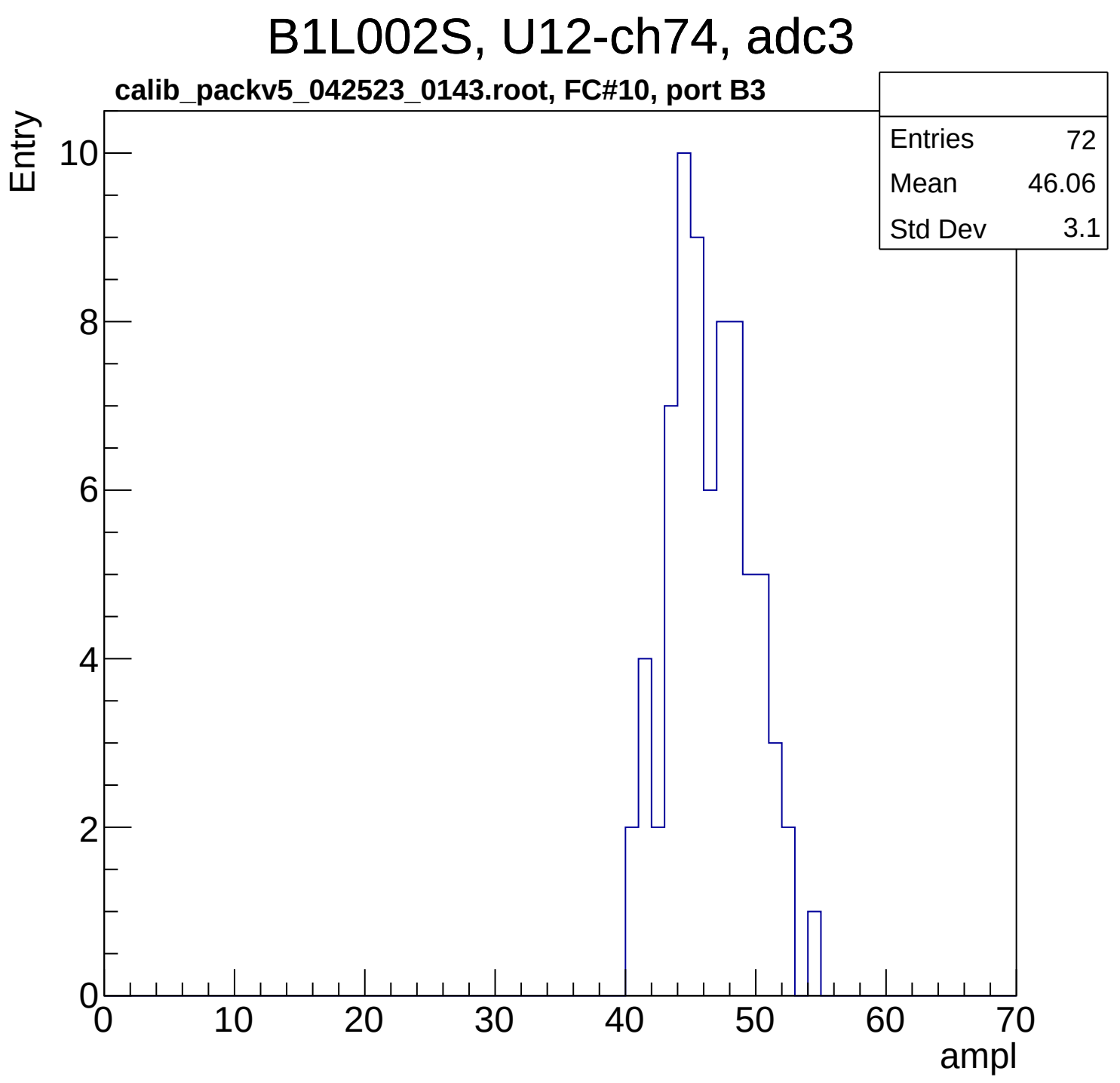
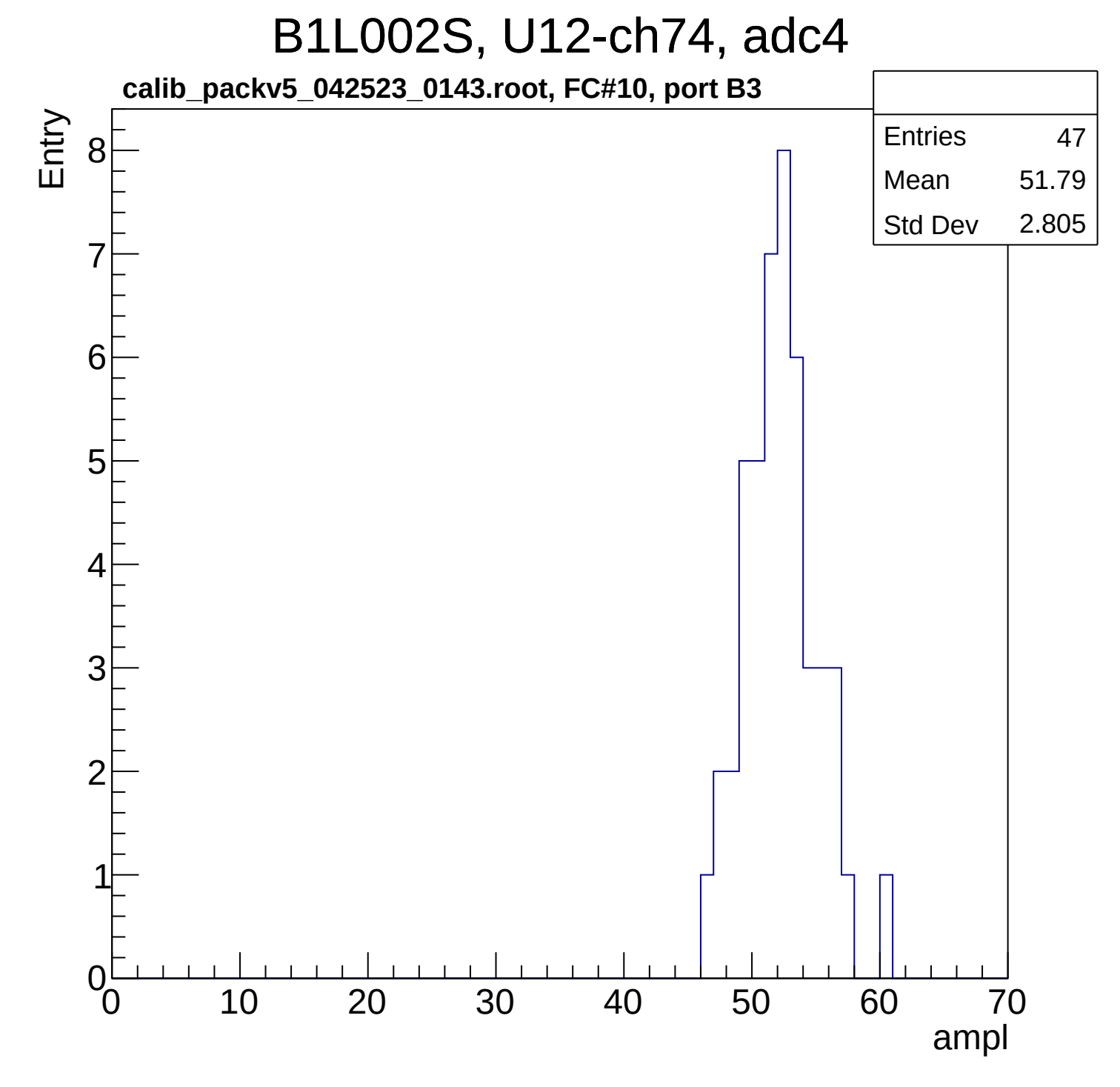
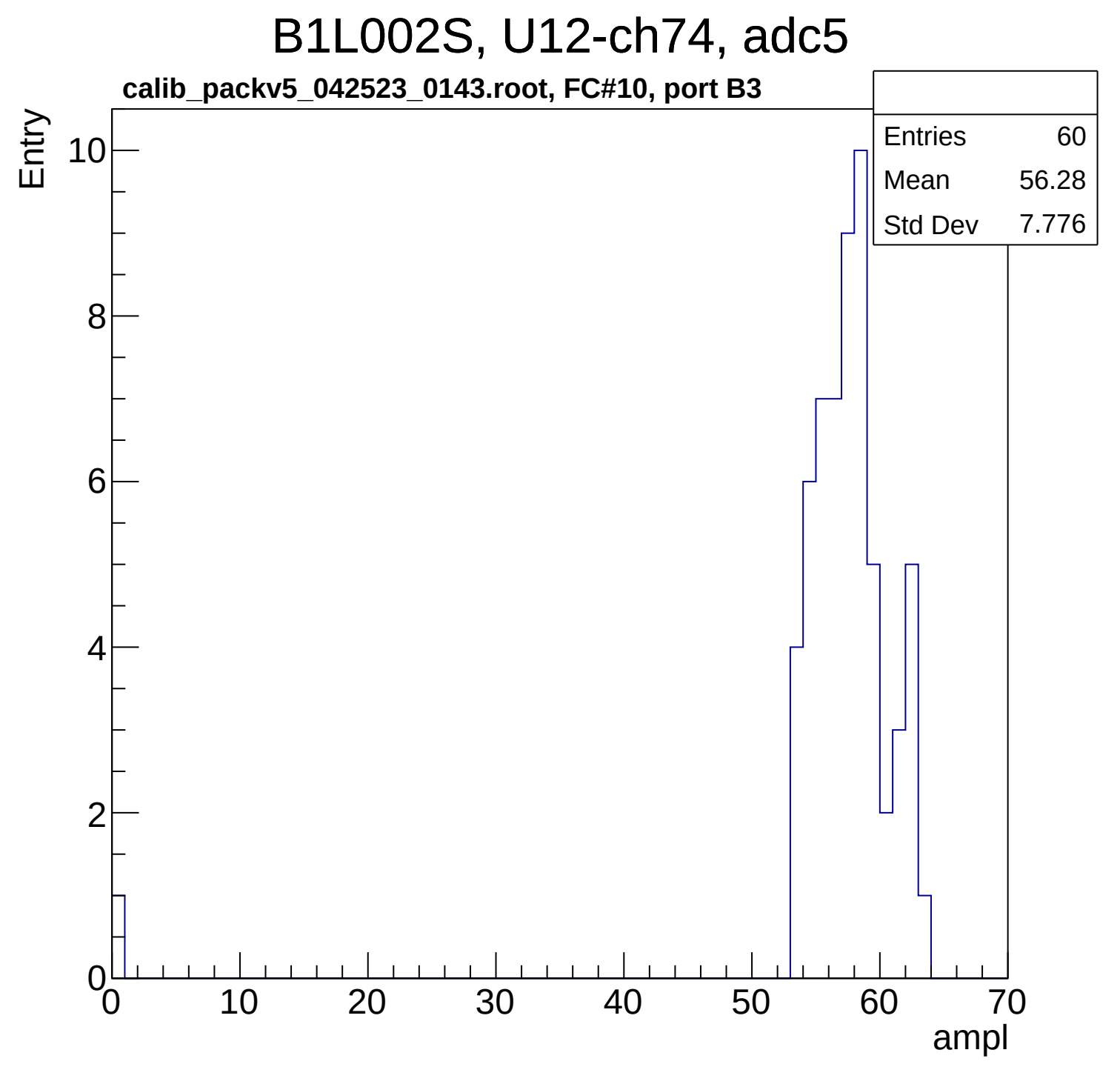
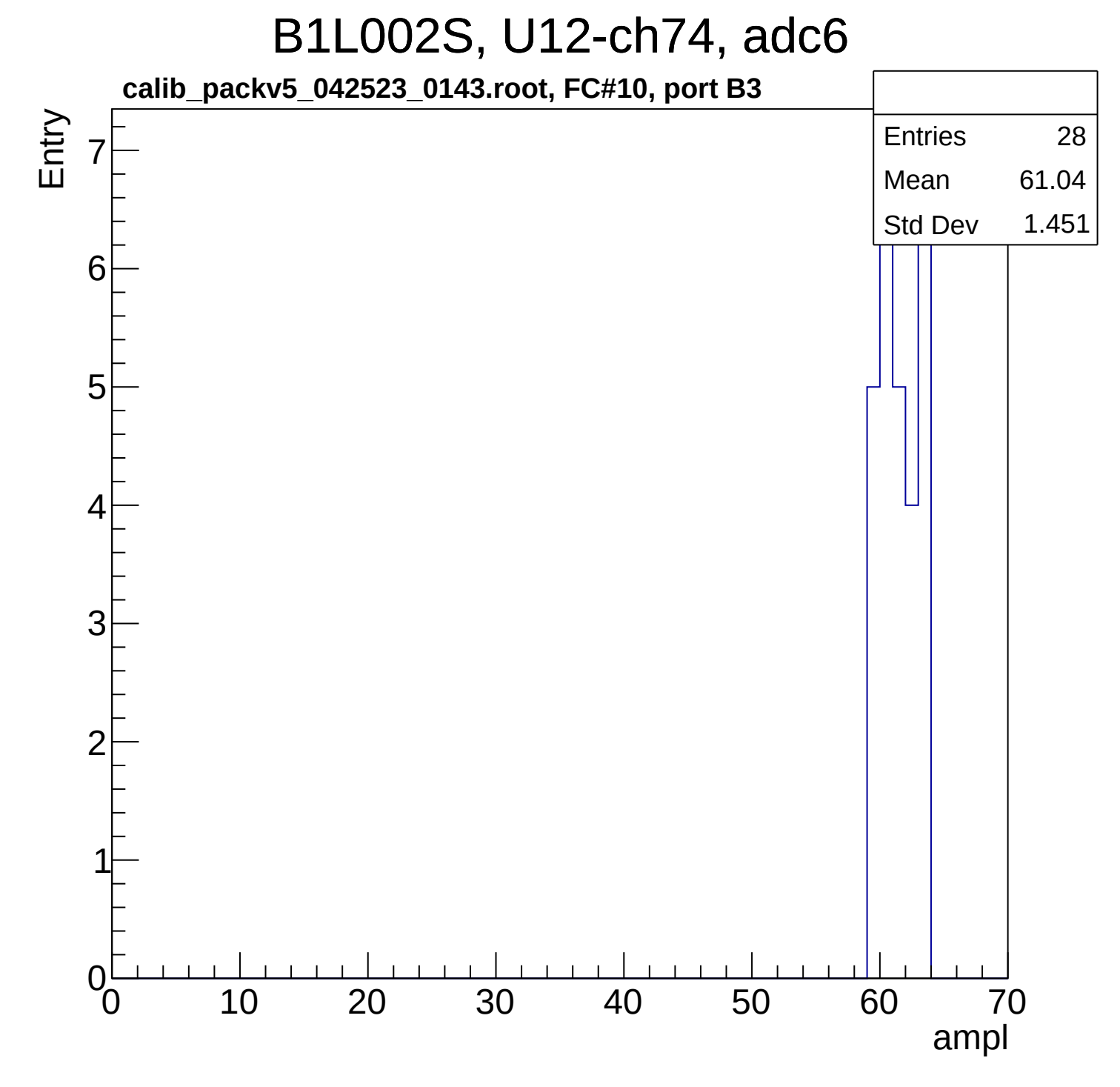
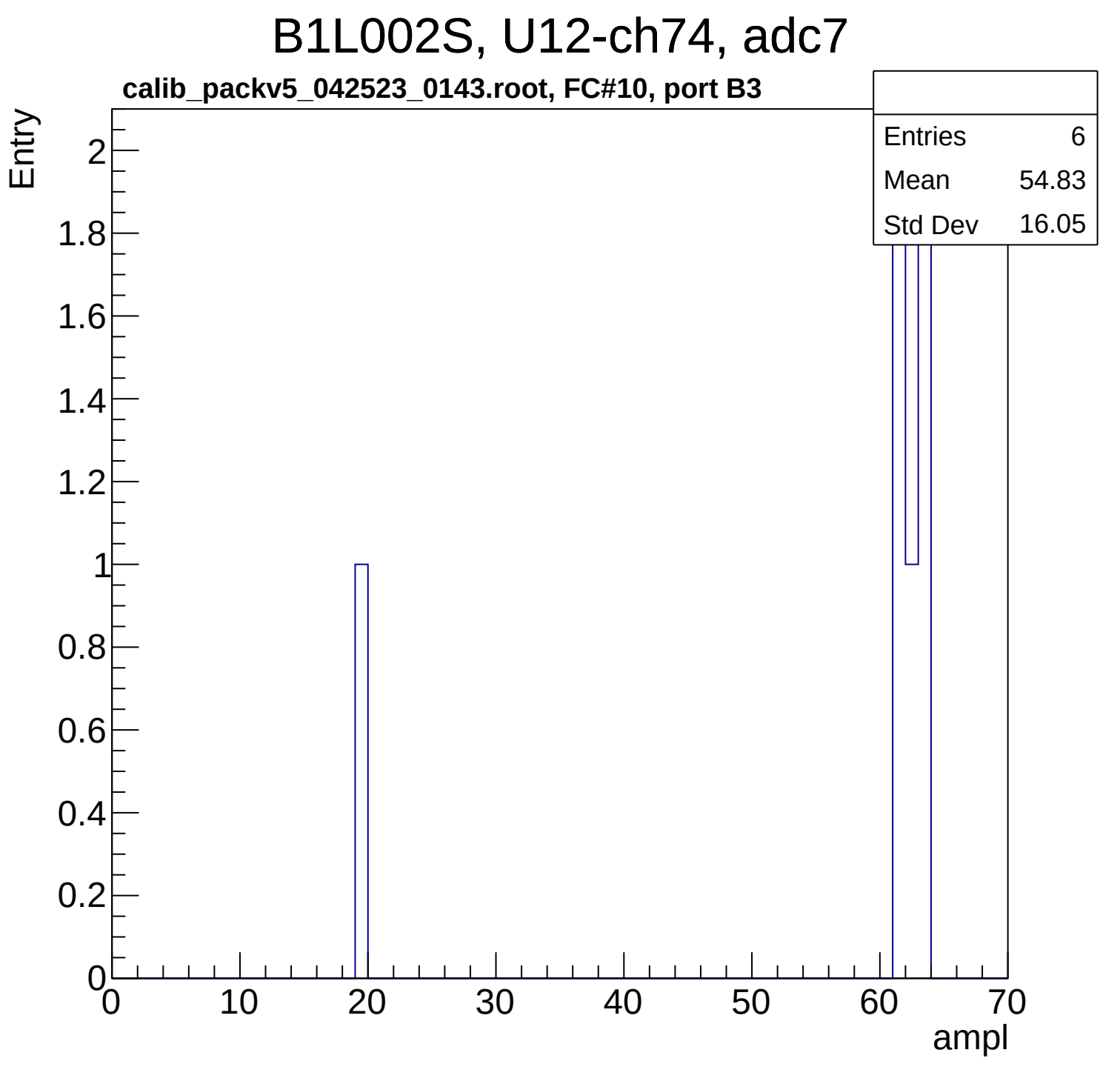
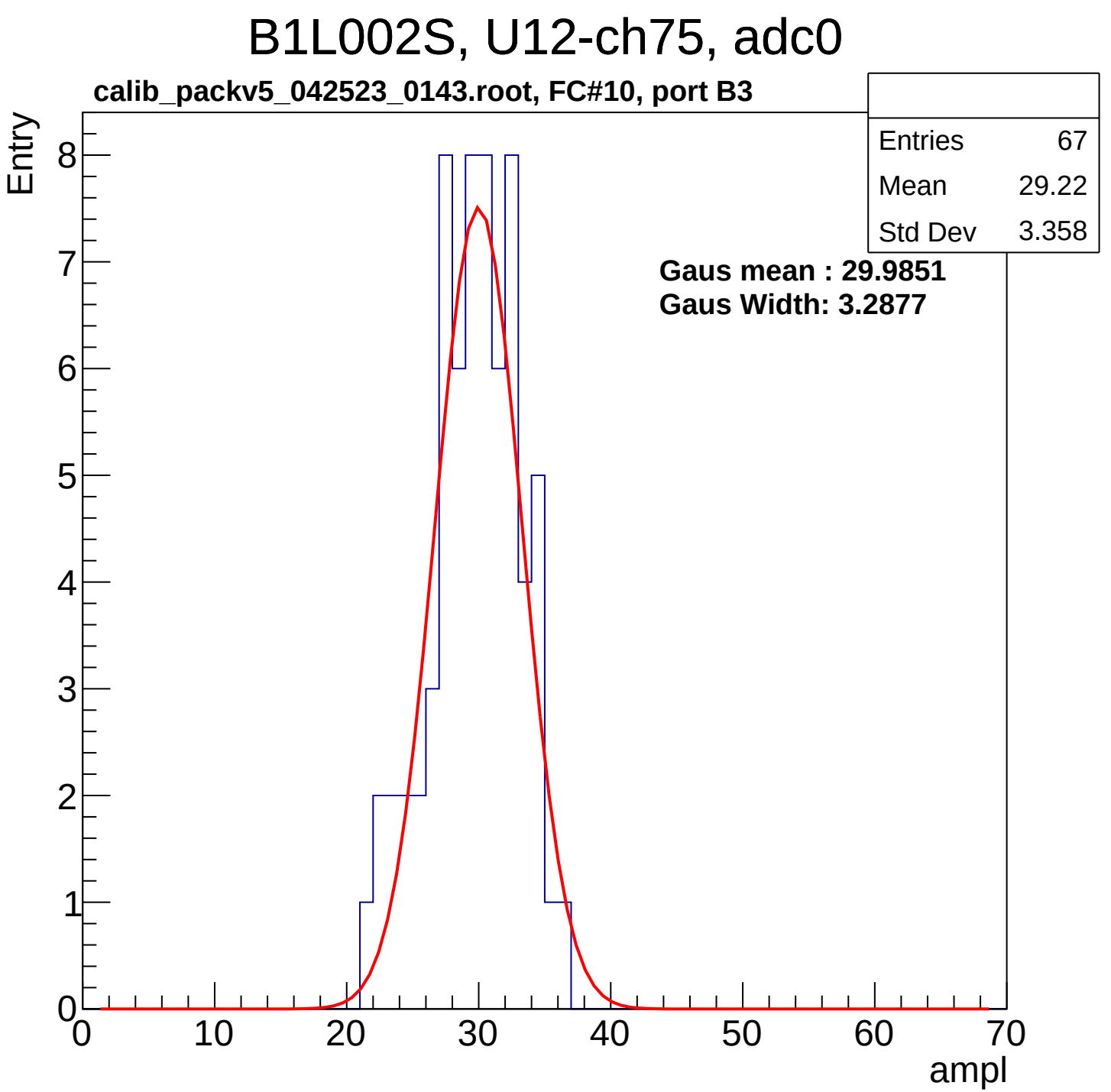

B1L002S, U12-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.06
Std Dev 3.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 51.79
Std Dev 2.805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 56.28
Std Dev 7.776
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61.04
Std Dev 1.451
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 54.83
Std Dev 16.05
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.22
Std Dev 3.358
Gaus mean : 29.9851
Gaus Width: 3.2877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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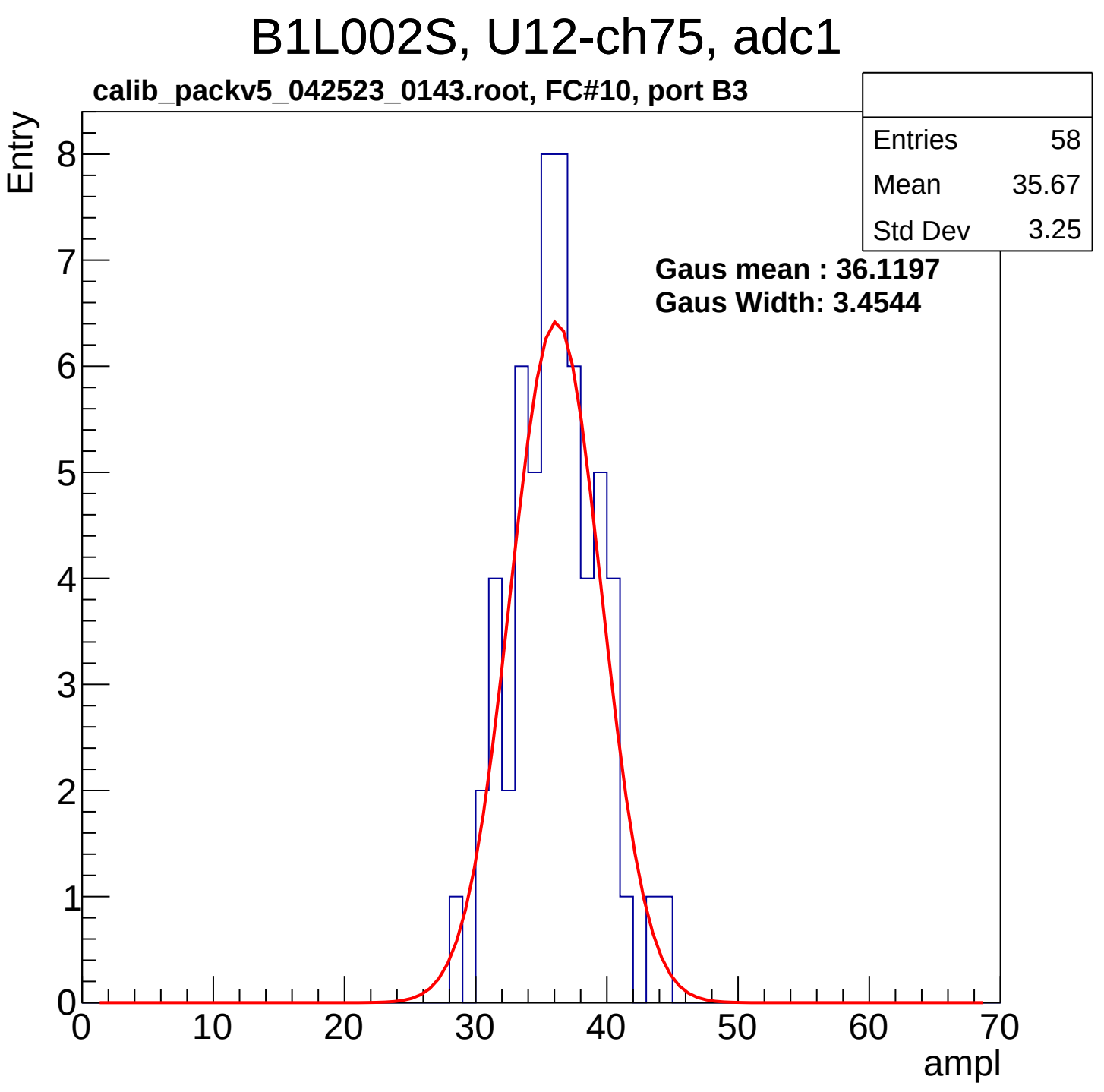
B1L002S, U12-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.67
Std Dev 3.25
Gaus mean : 36.1197
Gaus Width: 3.4544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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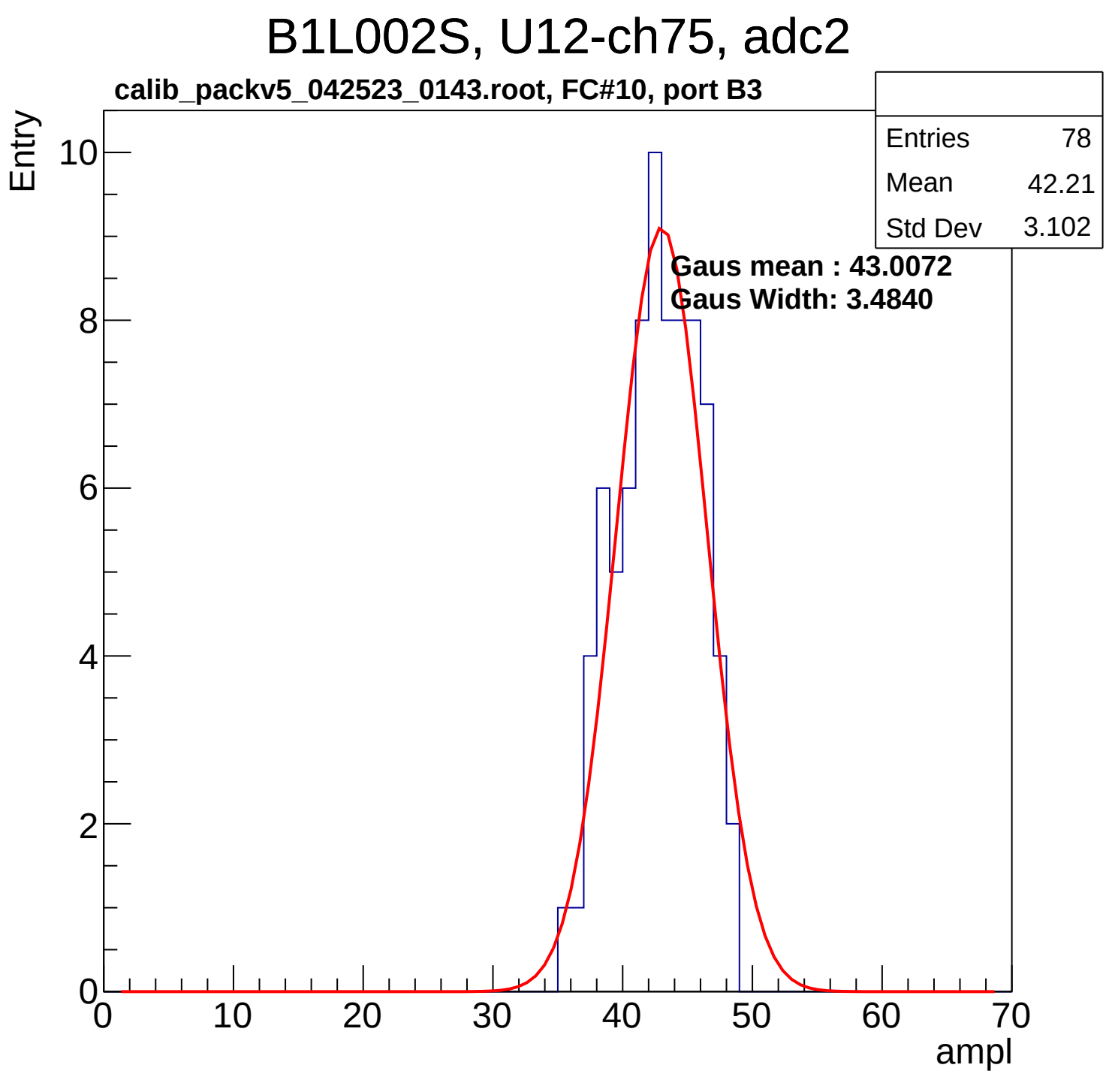
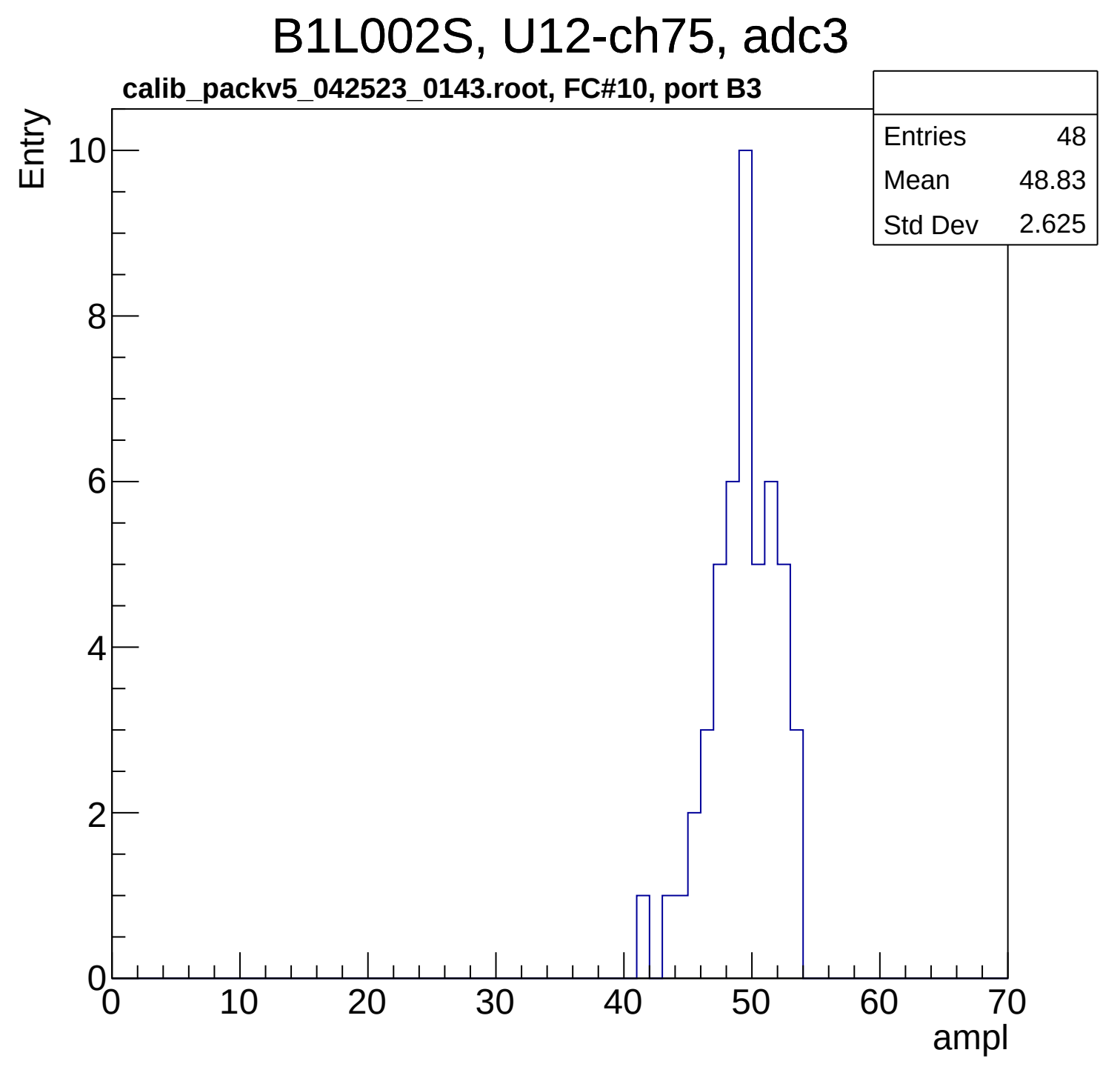
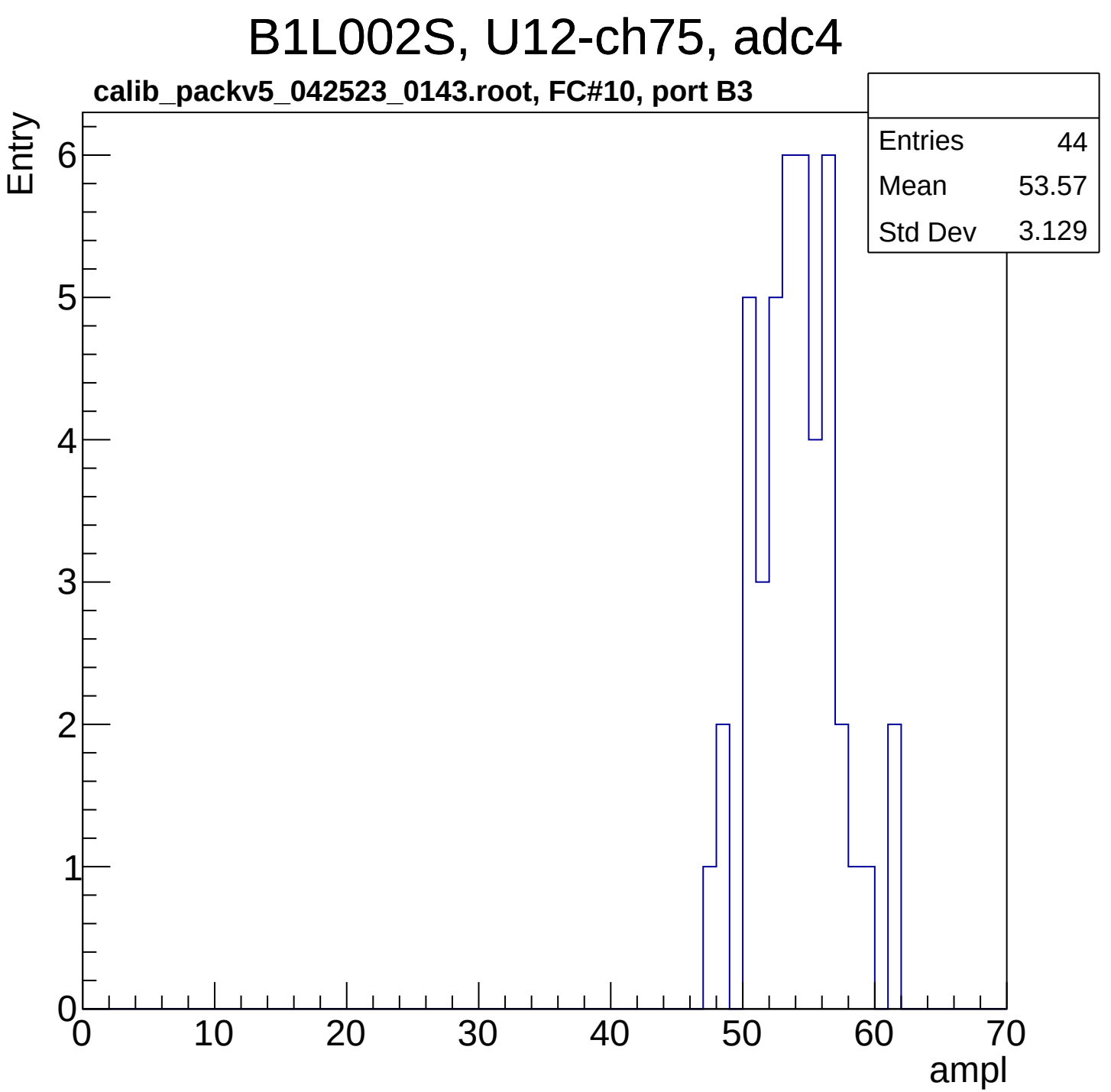
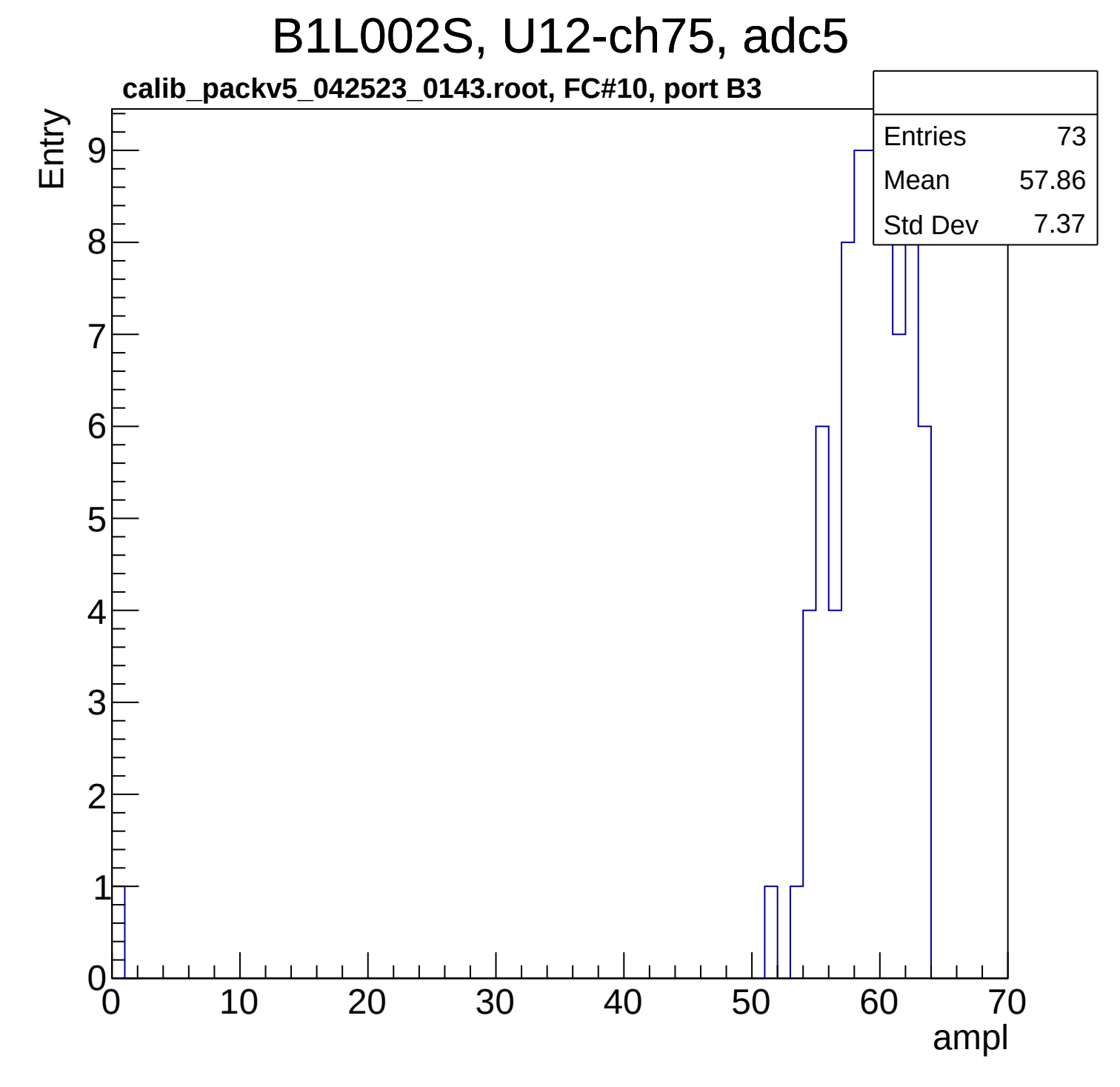

B1L002S, U12-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 42.21
Std Dev 3.102
Gaus mean : 43.0072
Gaus Width: 3.4840
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 48.83
Std Dev 2.625
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.57
Std Dev 3.129
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 57.86
Std Dev 7.37
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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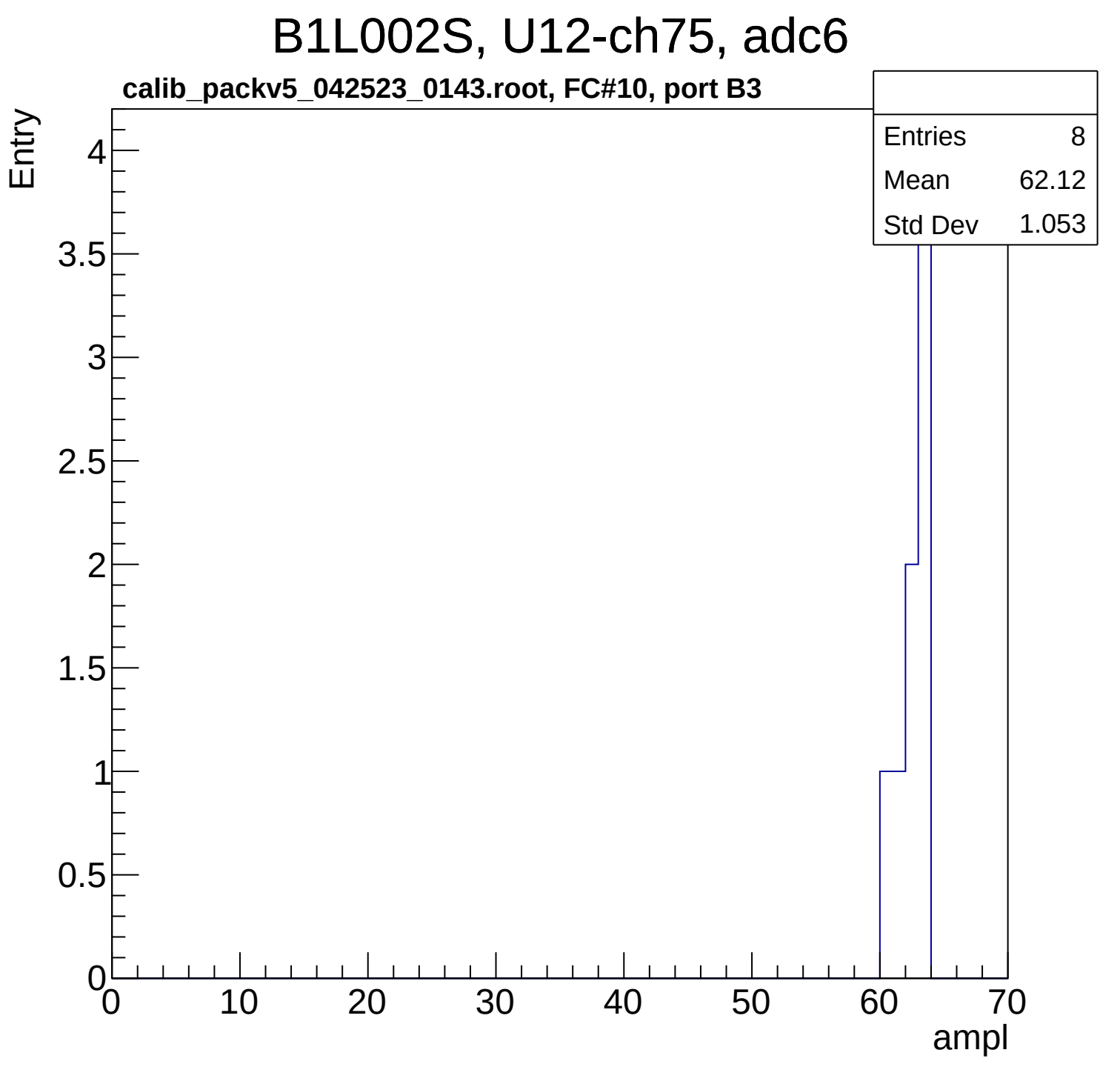
B1L002S, U12-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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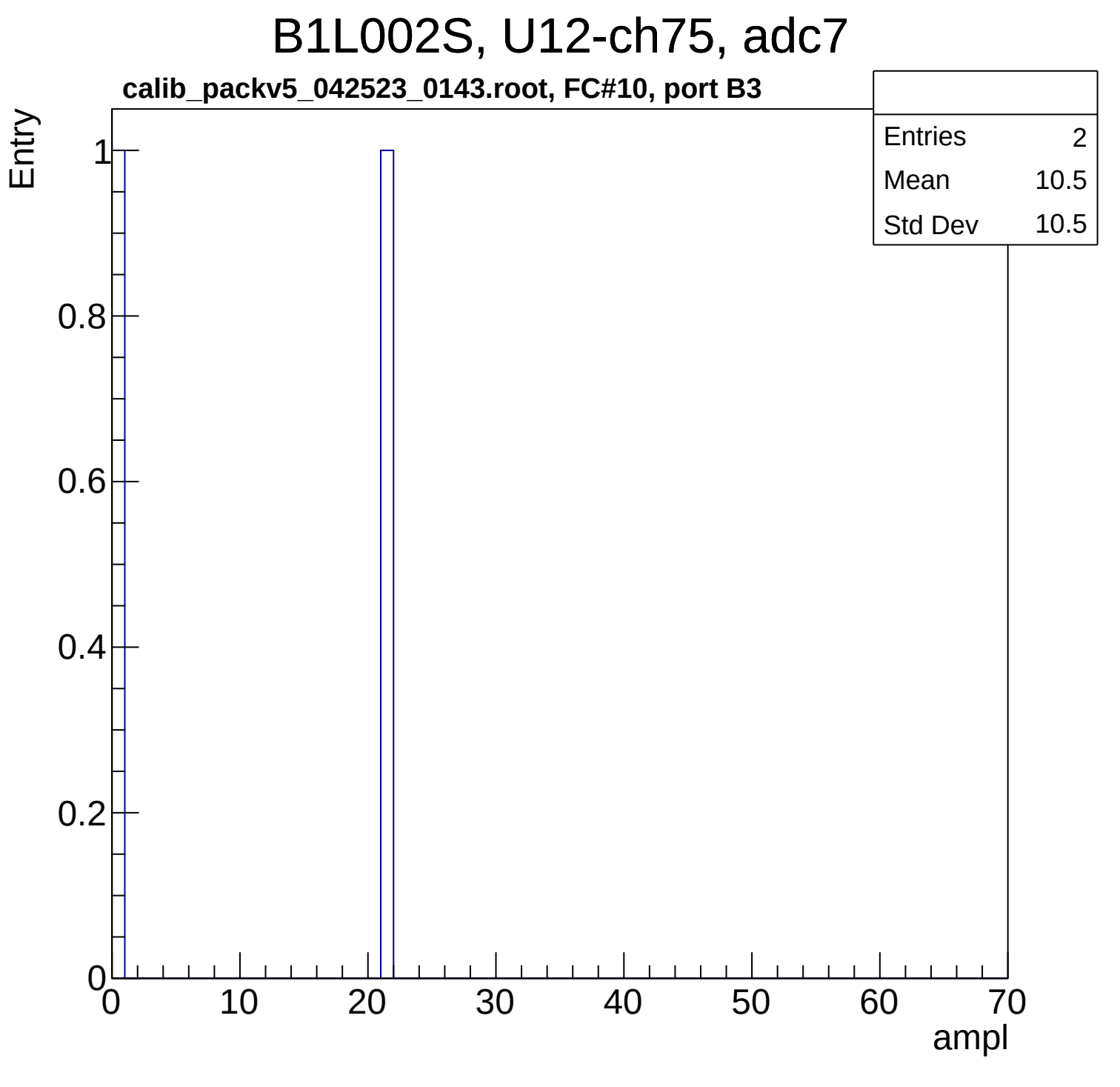
B1L002S, U12-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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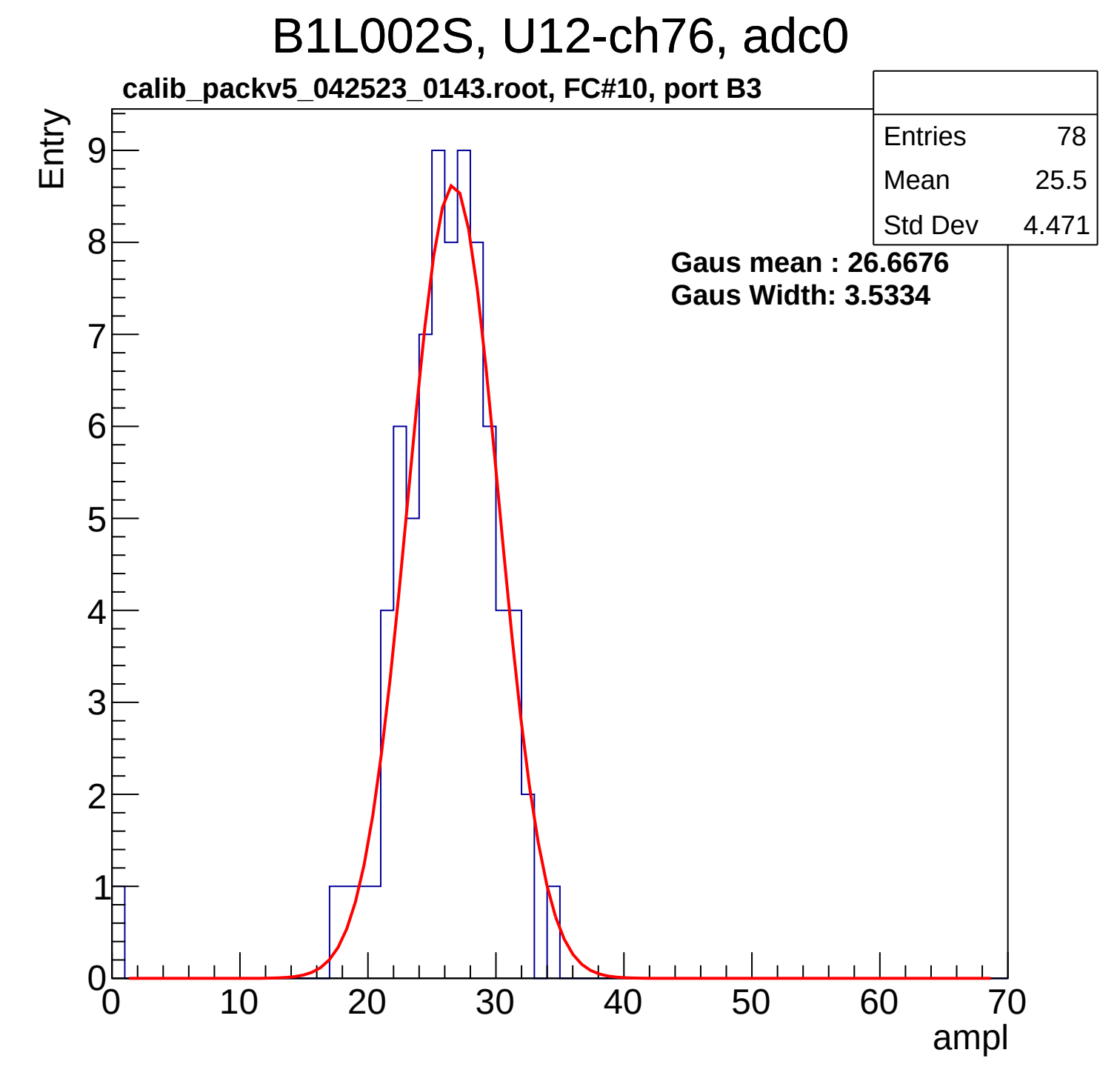
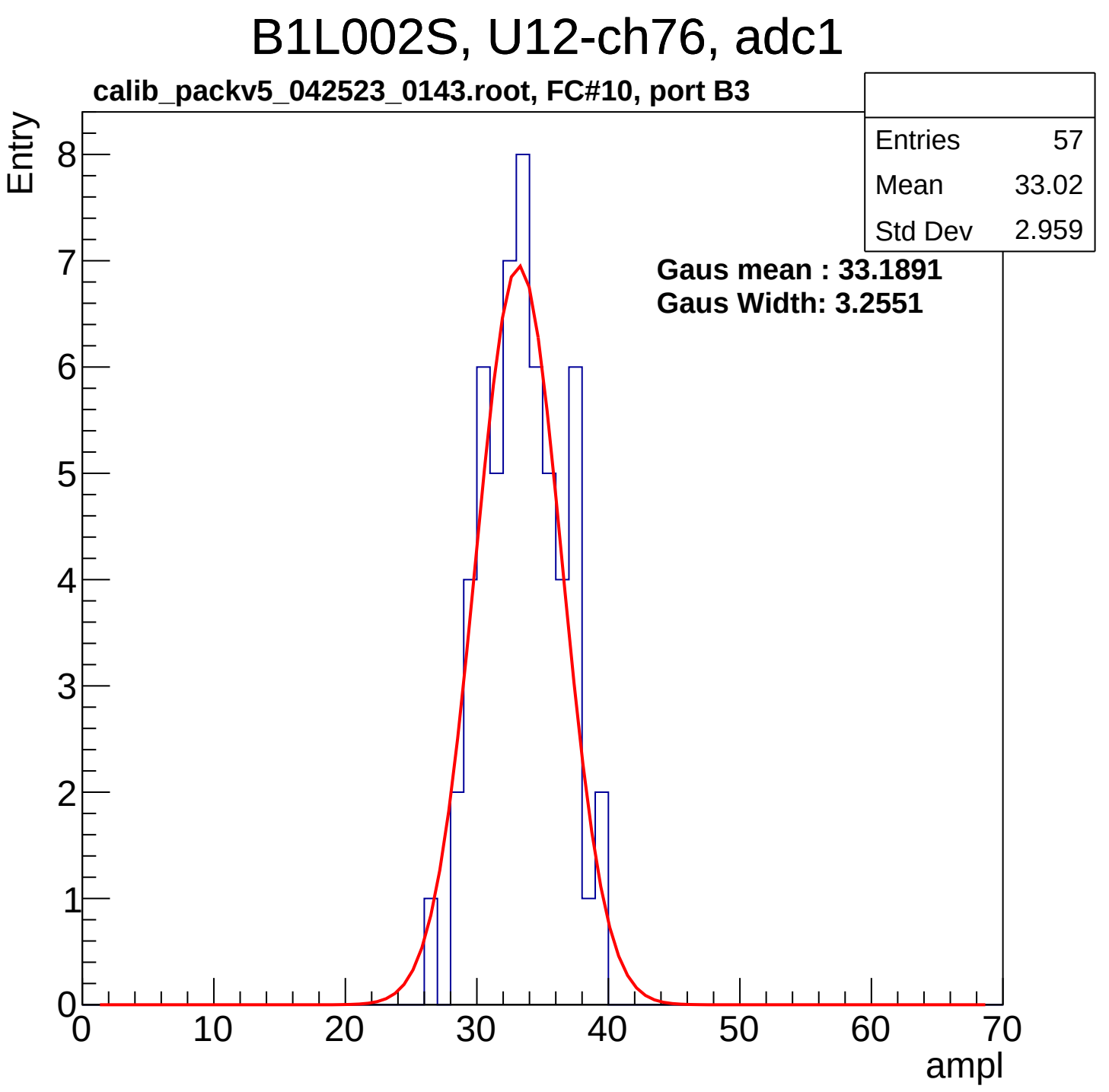
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 25.5
Std Dev 4.471
Gaus mean : 26.6676
Gaus Width: 3.5334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.02
Std Dev 2.959
Gaus mean : 33.1891
Gaus Width: 3.2551
Entry
ampl
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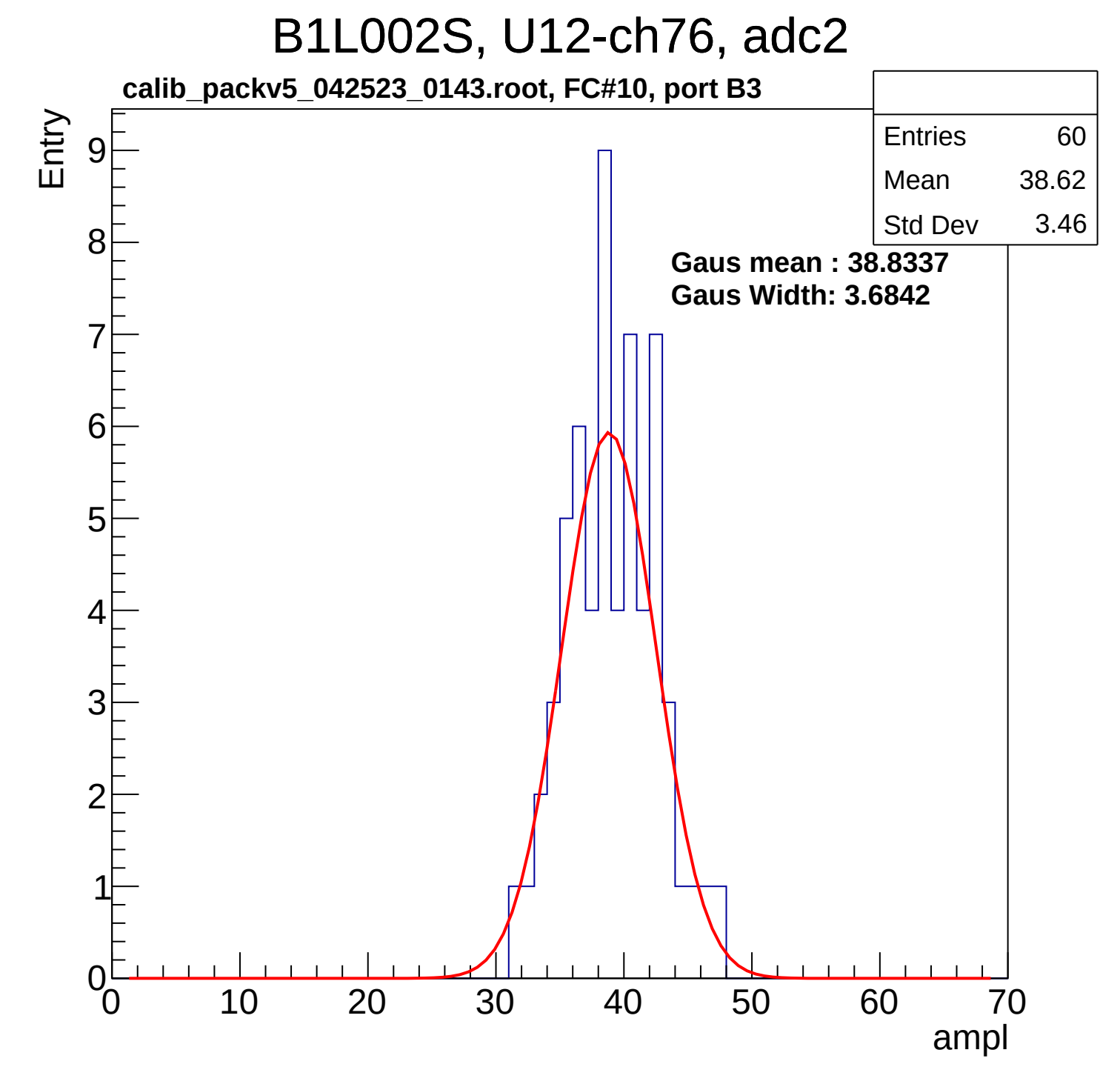
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 38.62
Std Dev 3.46
Gaus mean : 38.8337
Gaus Width: 3.6842
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
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60
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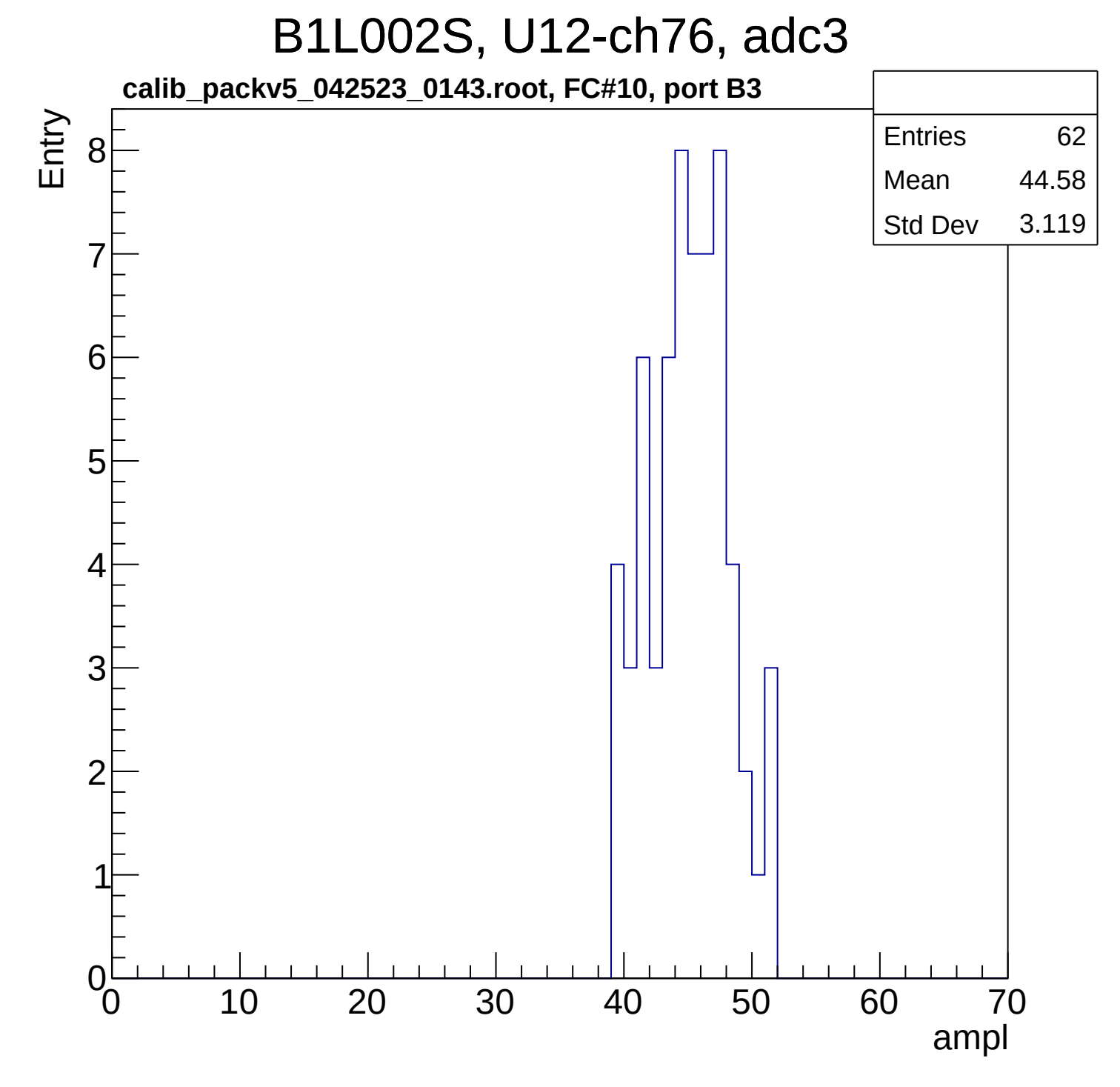
B1L002S, U12-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 44.58
Std Dev 3.119
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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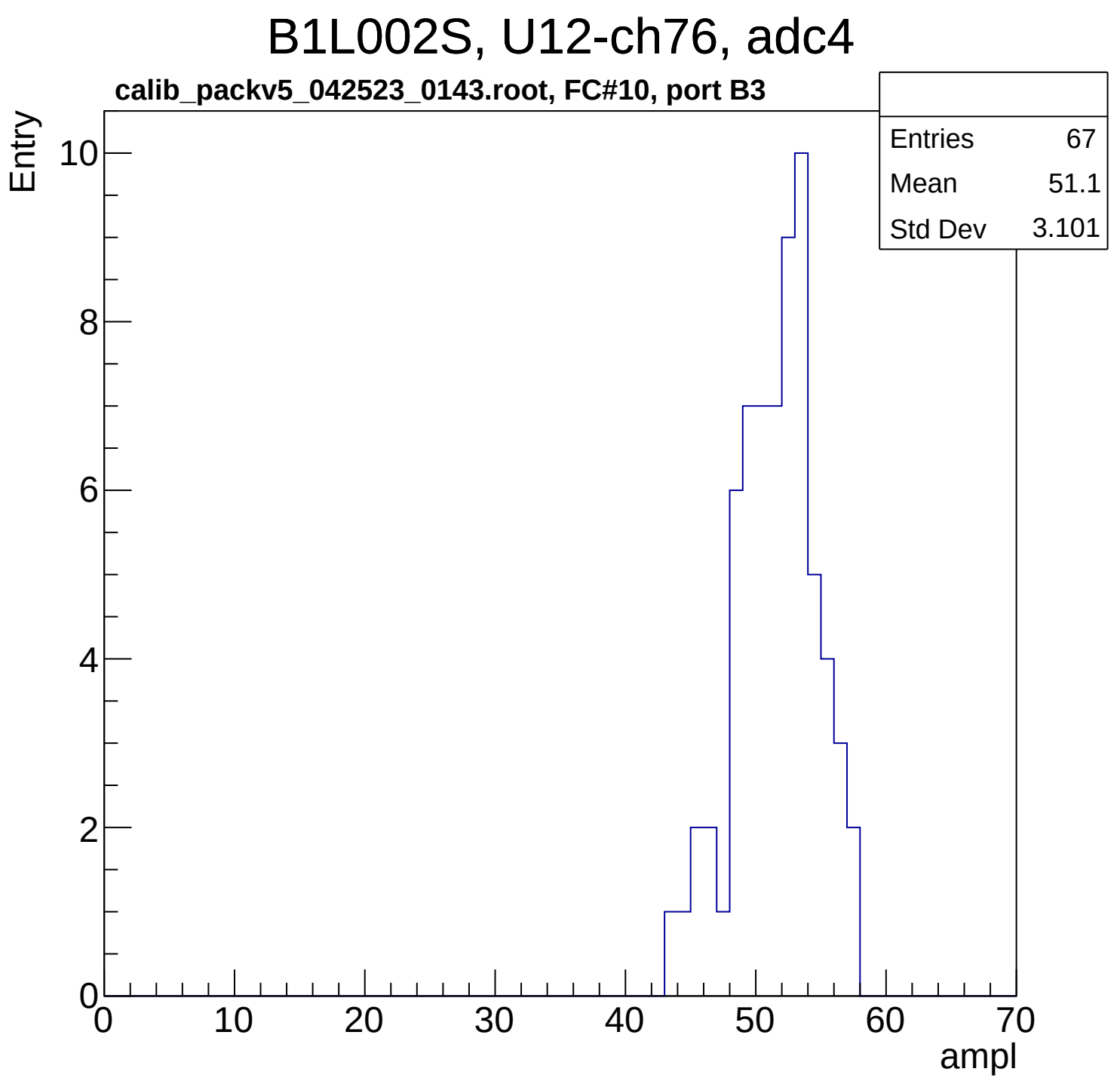
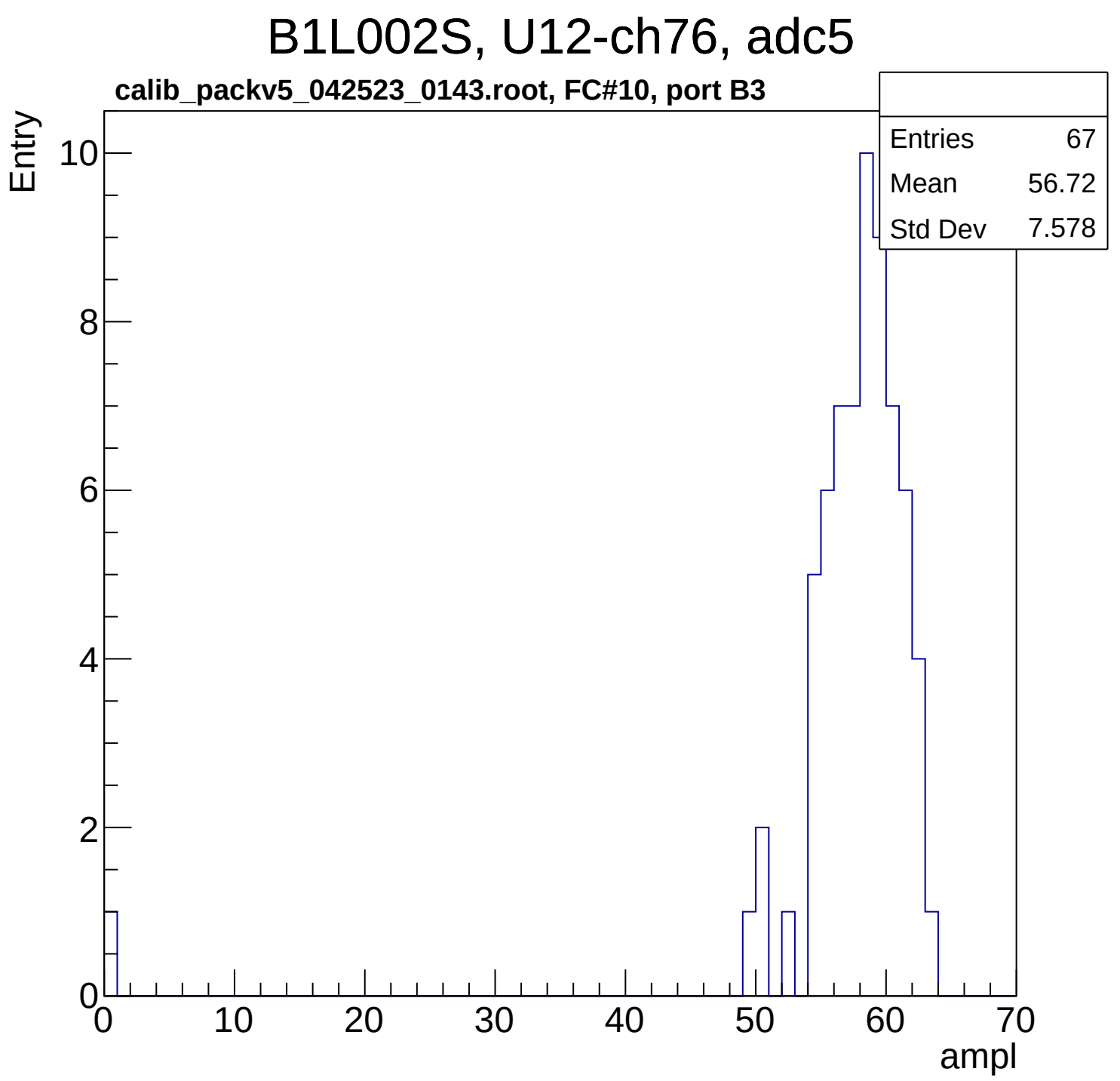
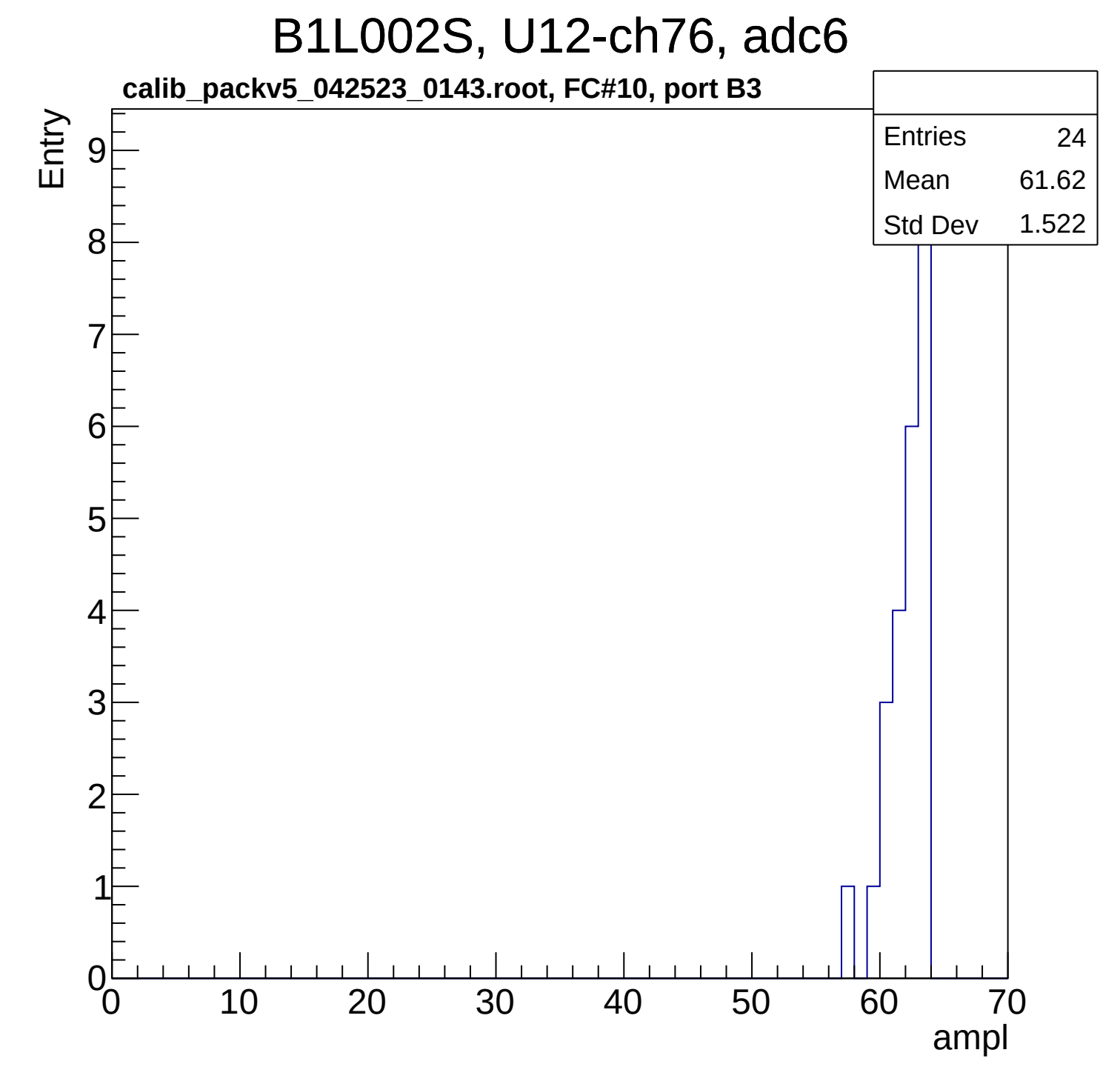

B1L002S, U12-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 51.1
Std Dev 3.101
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.72
Std Dev 7.578
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.62
Std Dev 1.522
Entry
ampl
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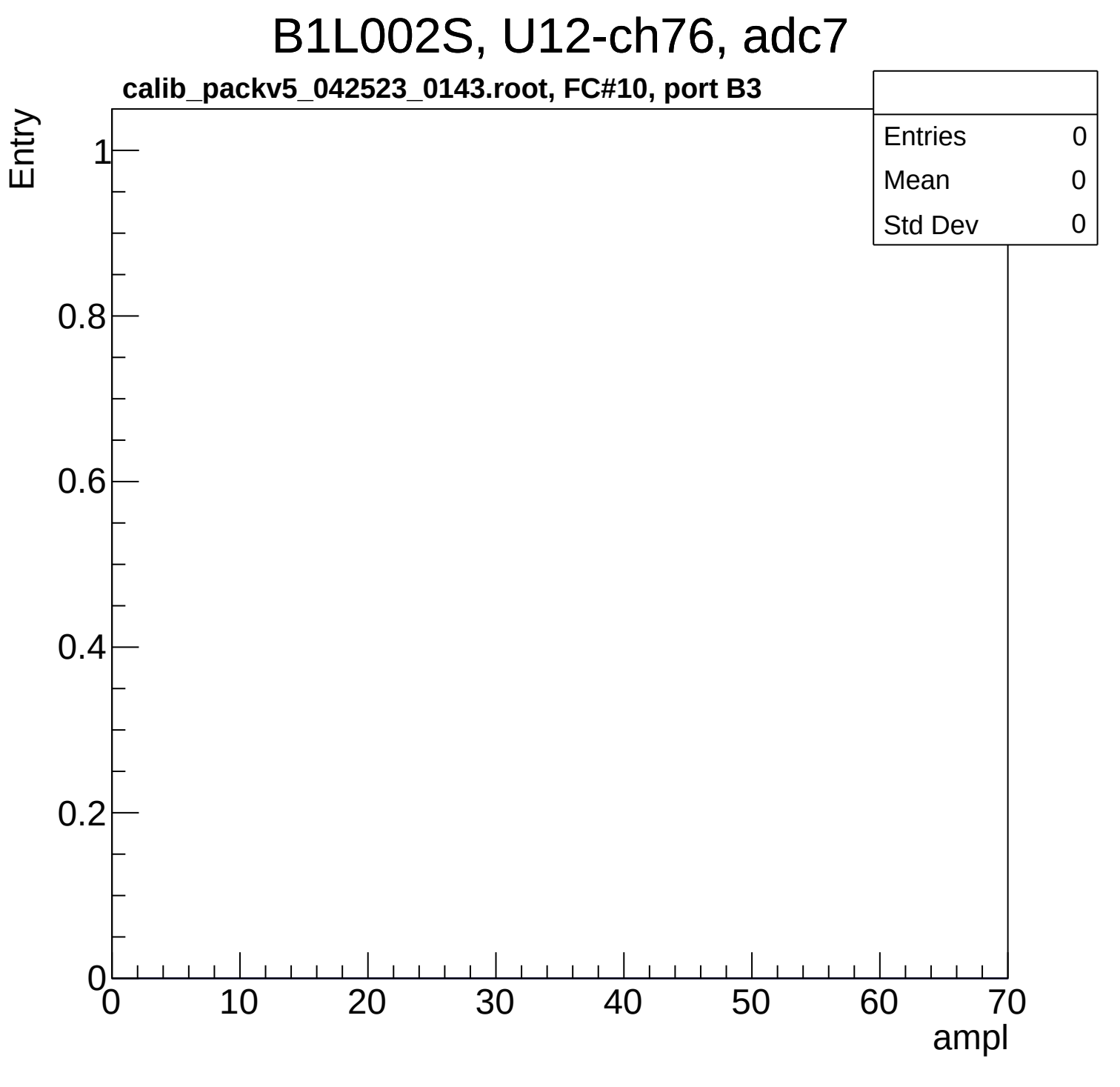
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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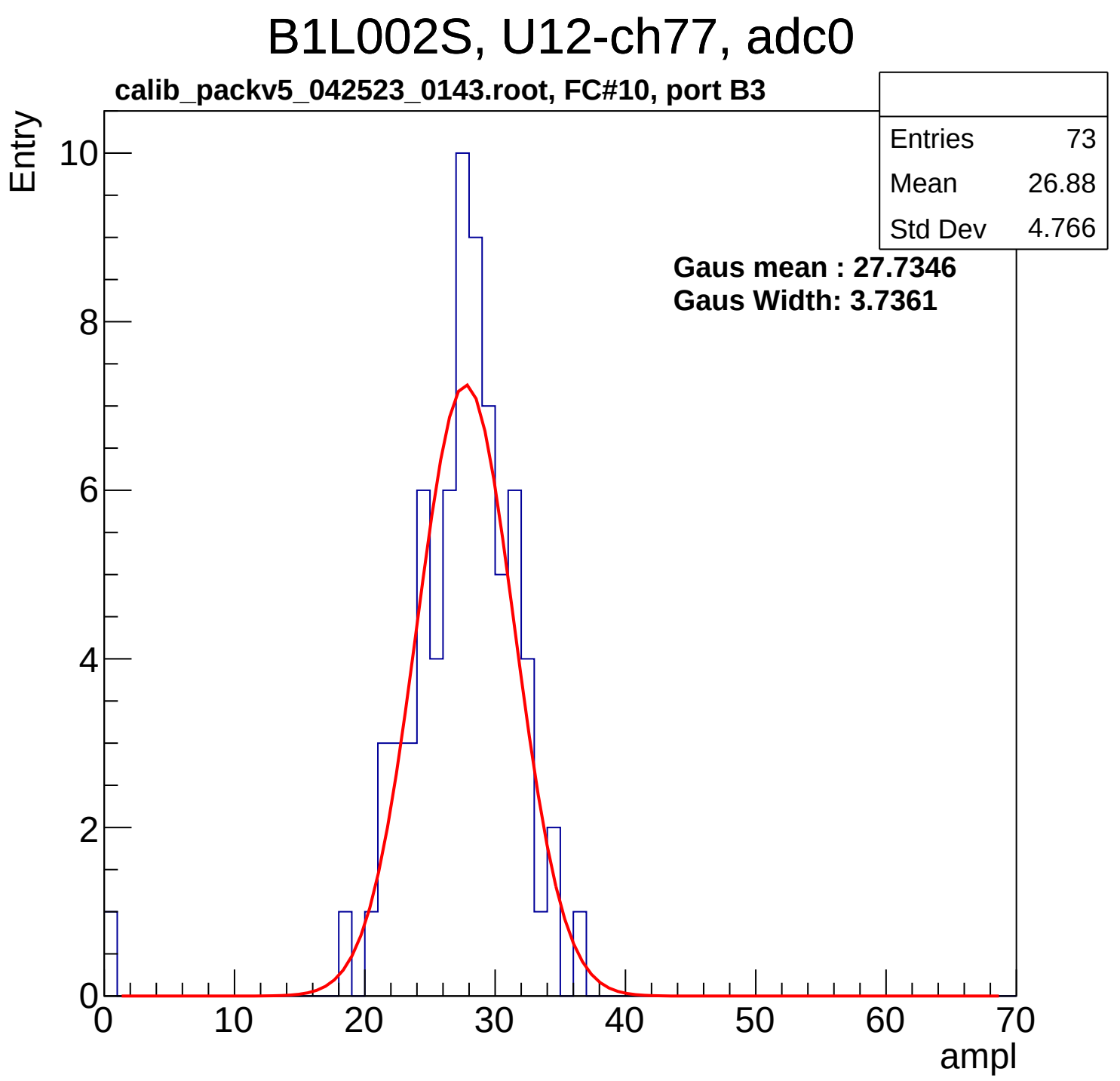
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 26.88
Std Dev 4.766
Gaus mean : 27.7346
Gaus Width: 3.7361
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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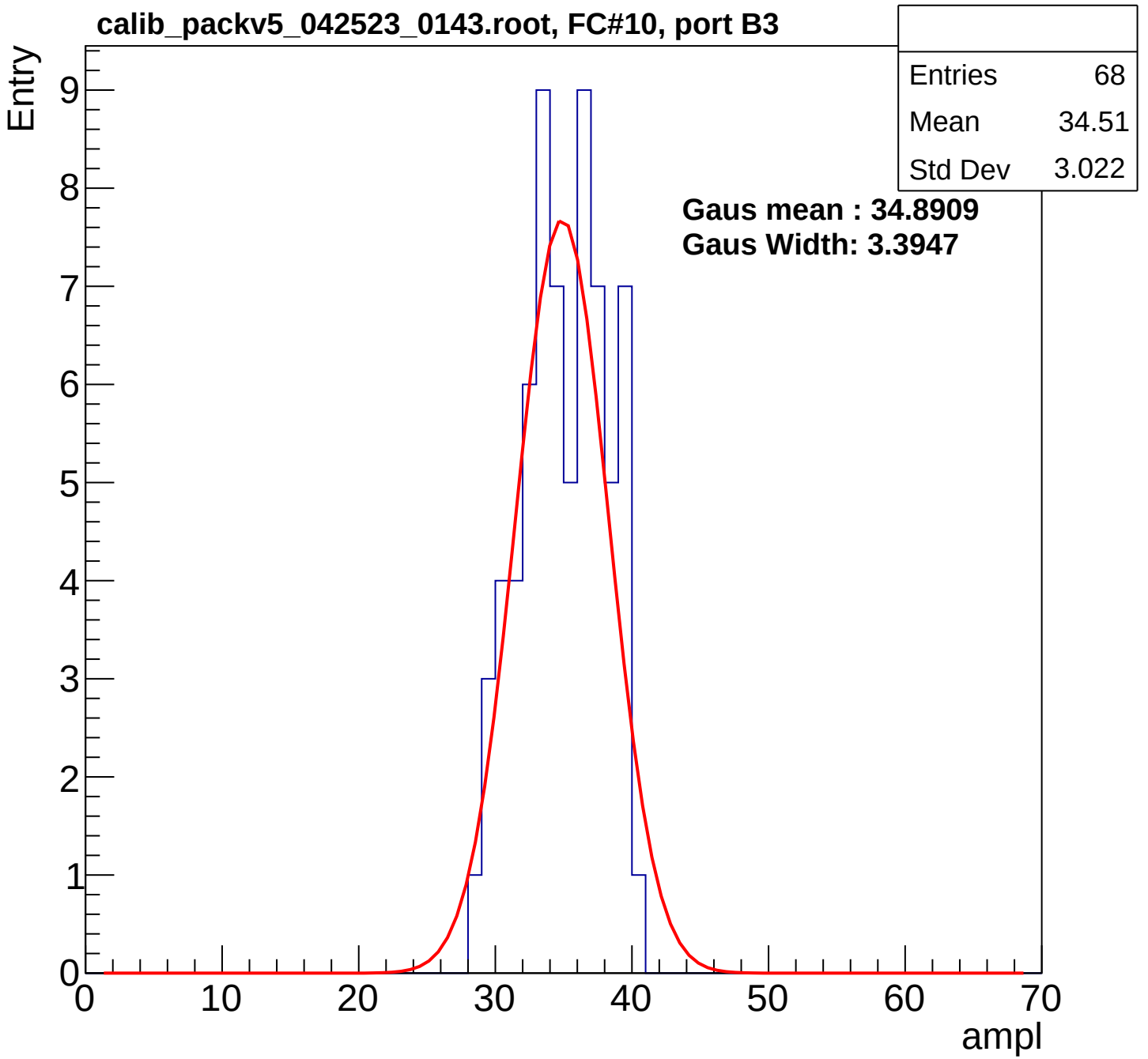
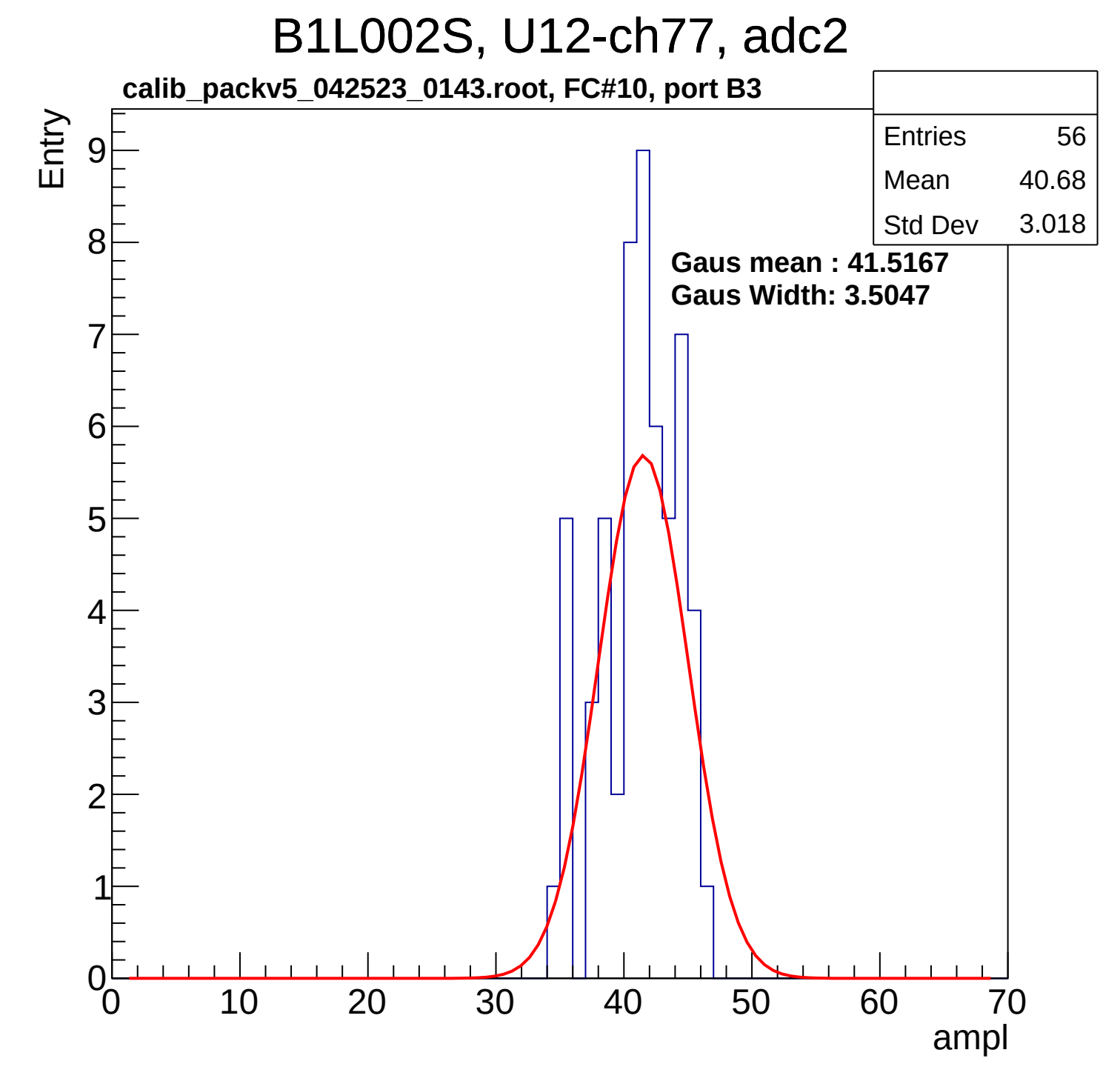

B1L002S, U12-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.51
Std Dev 3.022
Gaus mean : 34.8909
Gaus Width: 3.3947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.68
Std Dev 3.018
Gaus mean : 41.5167
Gaus Width: 3.5047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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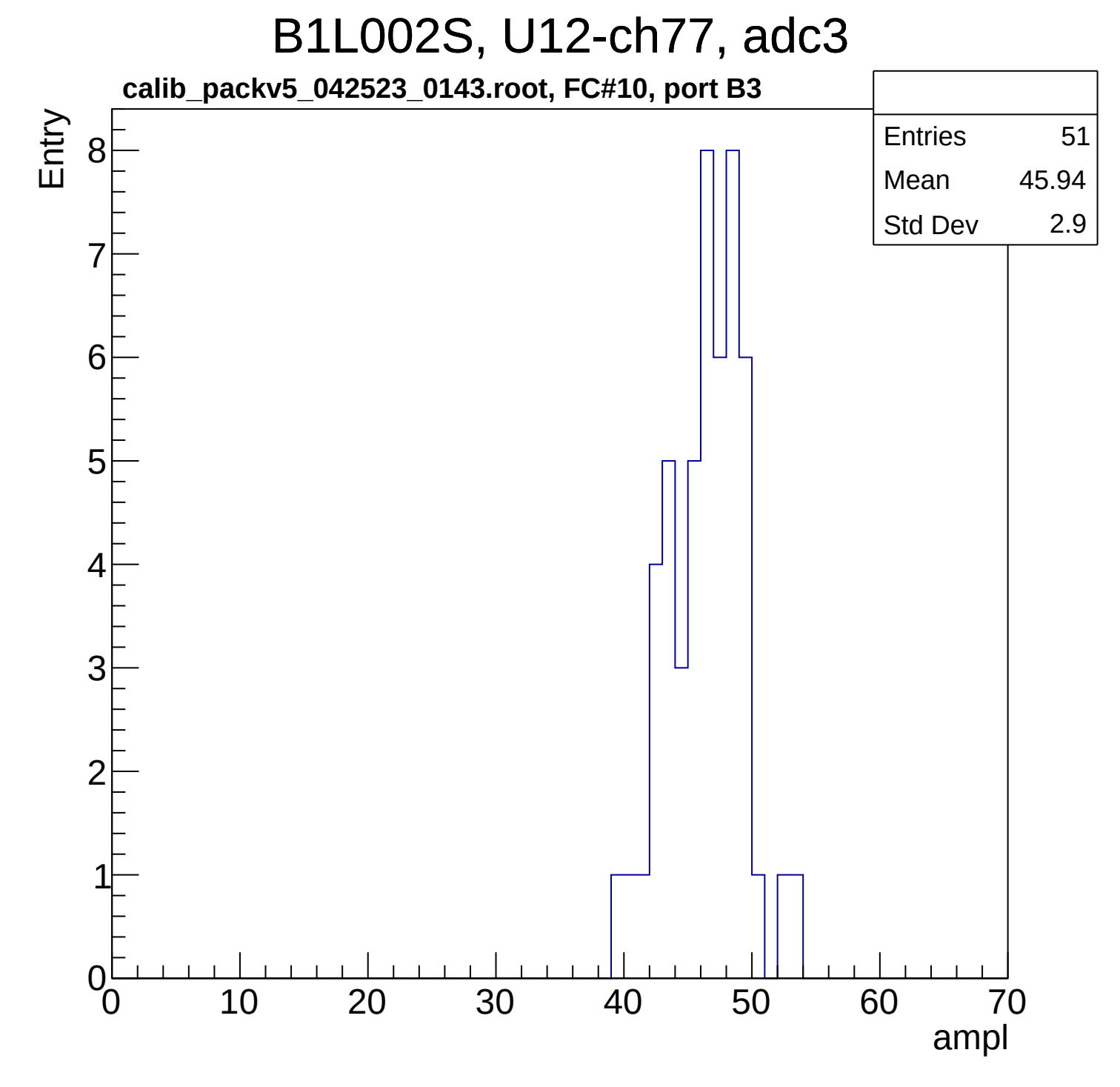
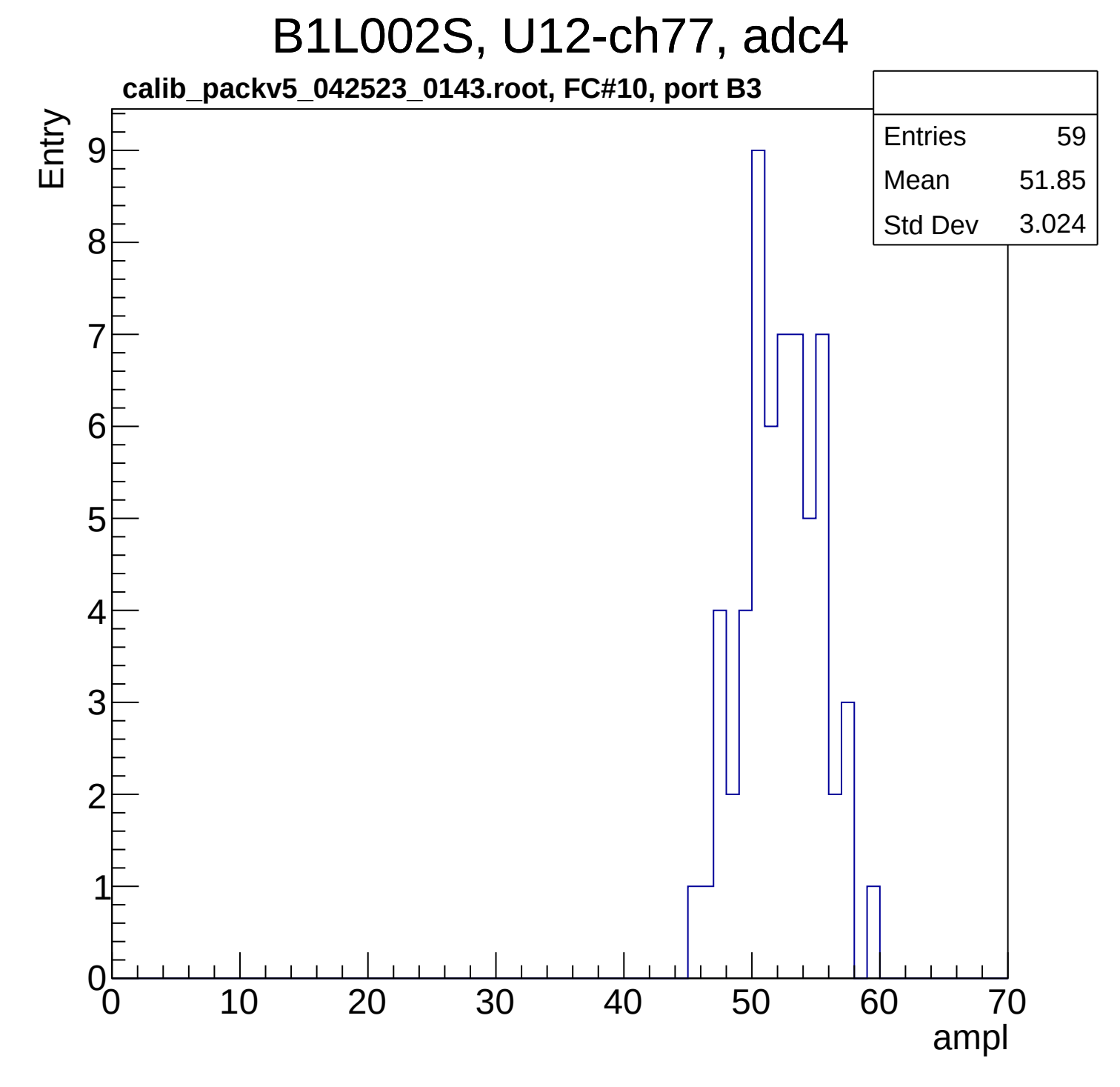

B1L002S, U12-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 45.94
Std Dev 2.9
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 51.85
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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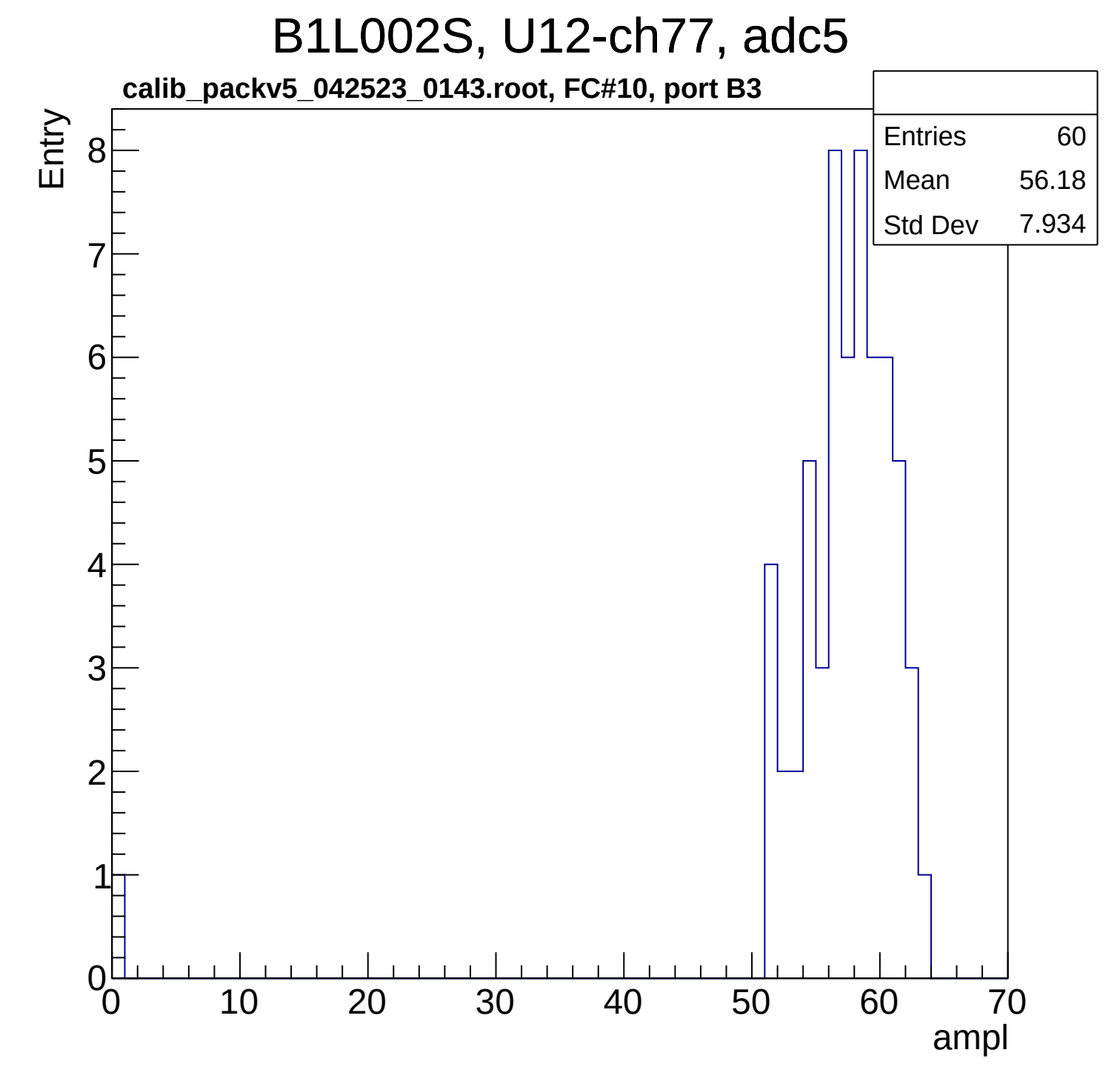
B1L002S, U12-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 56.18
Std Dev 7.934
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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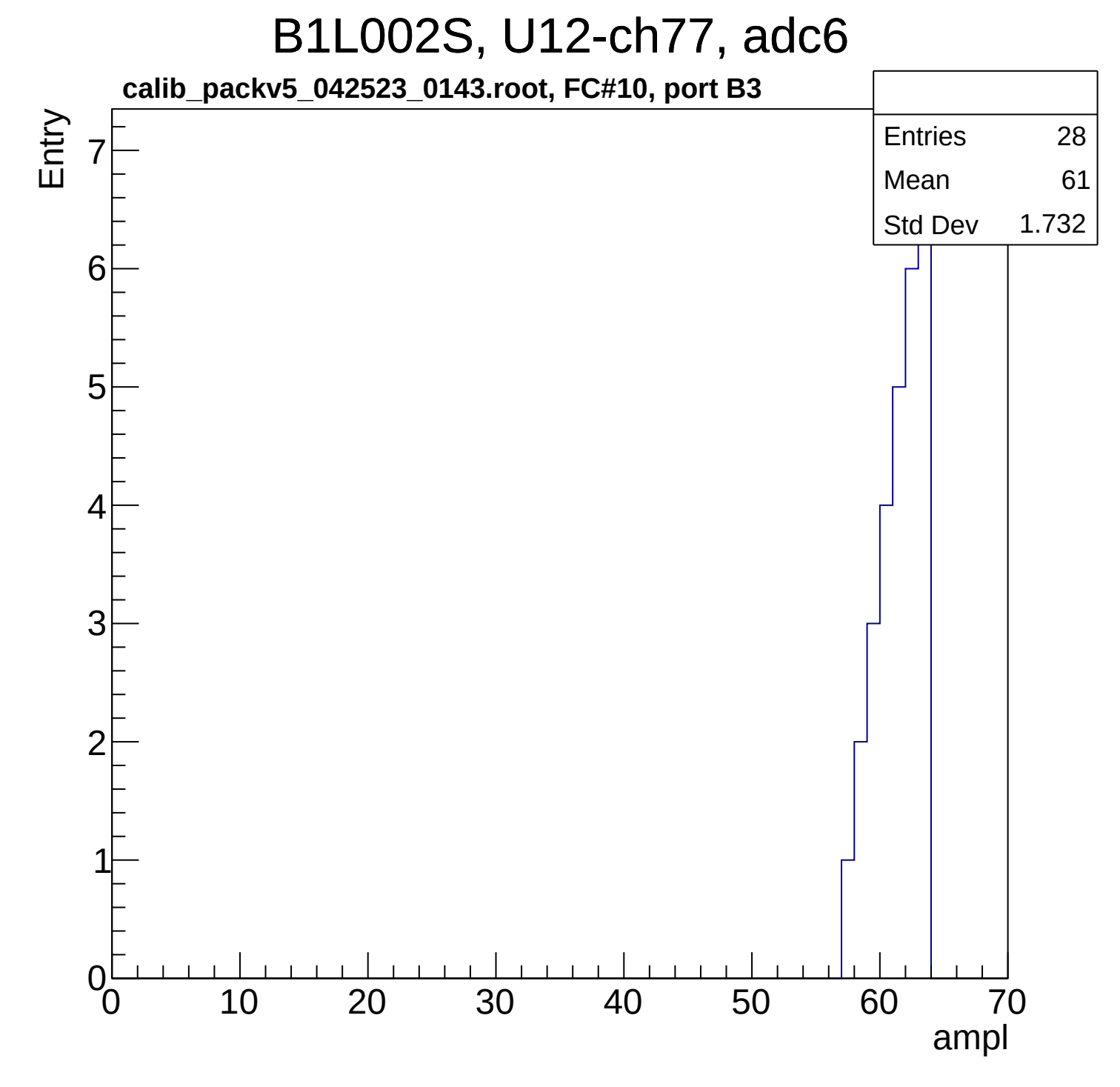
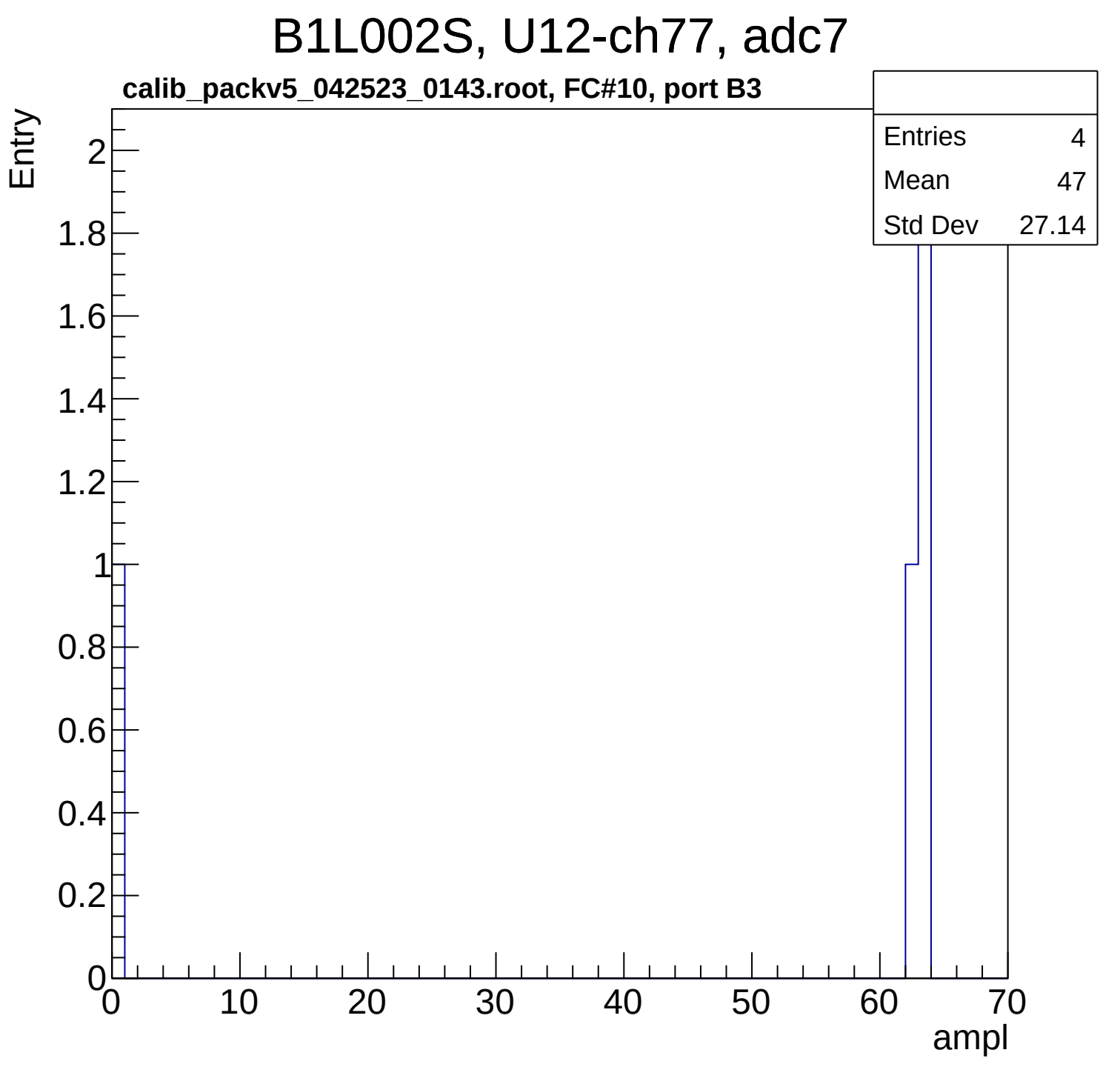

B1L002S, U12-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61
Std Dev 1.732
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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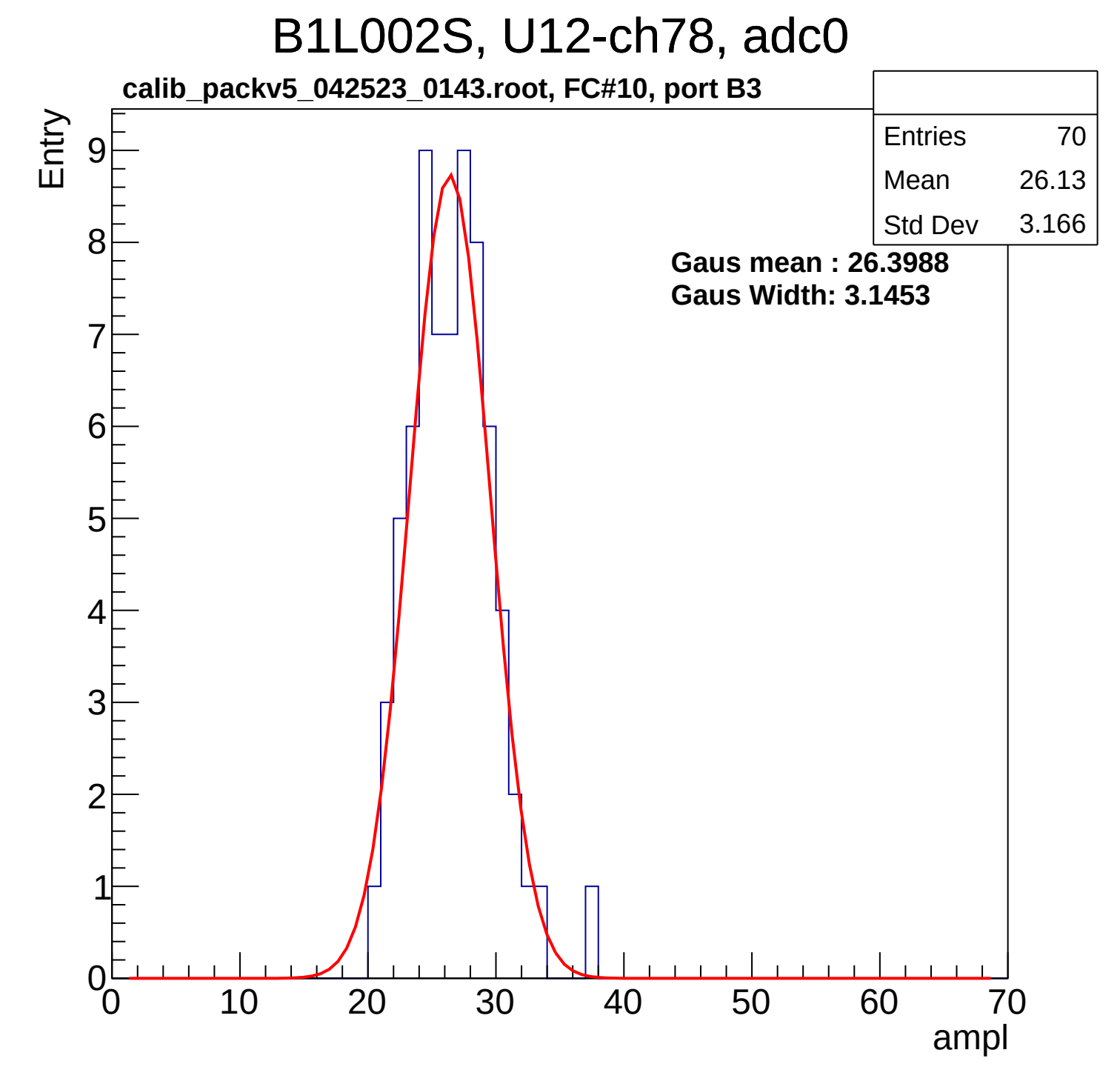
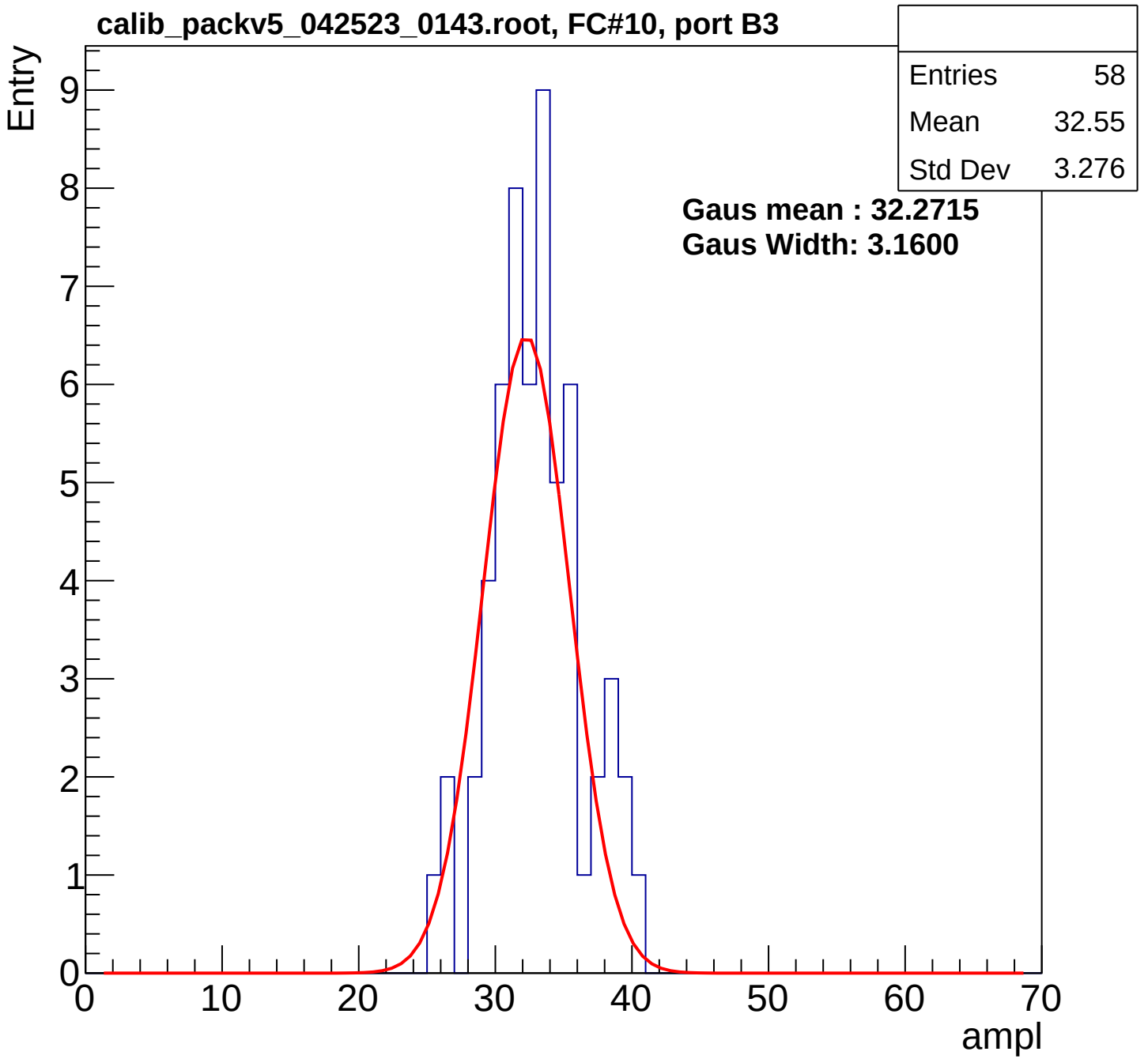
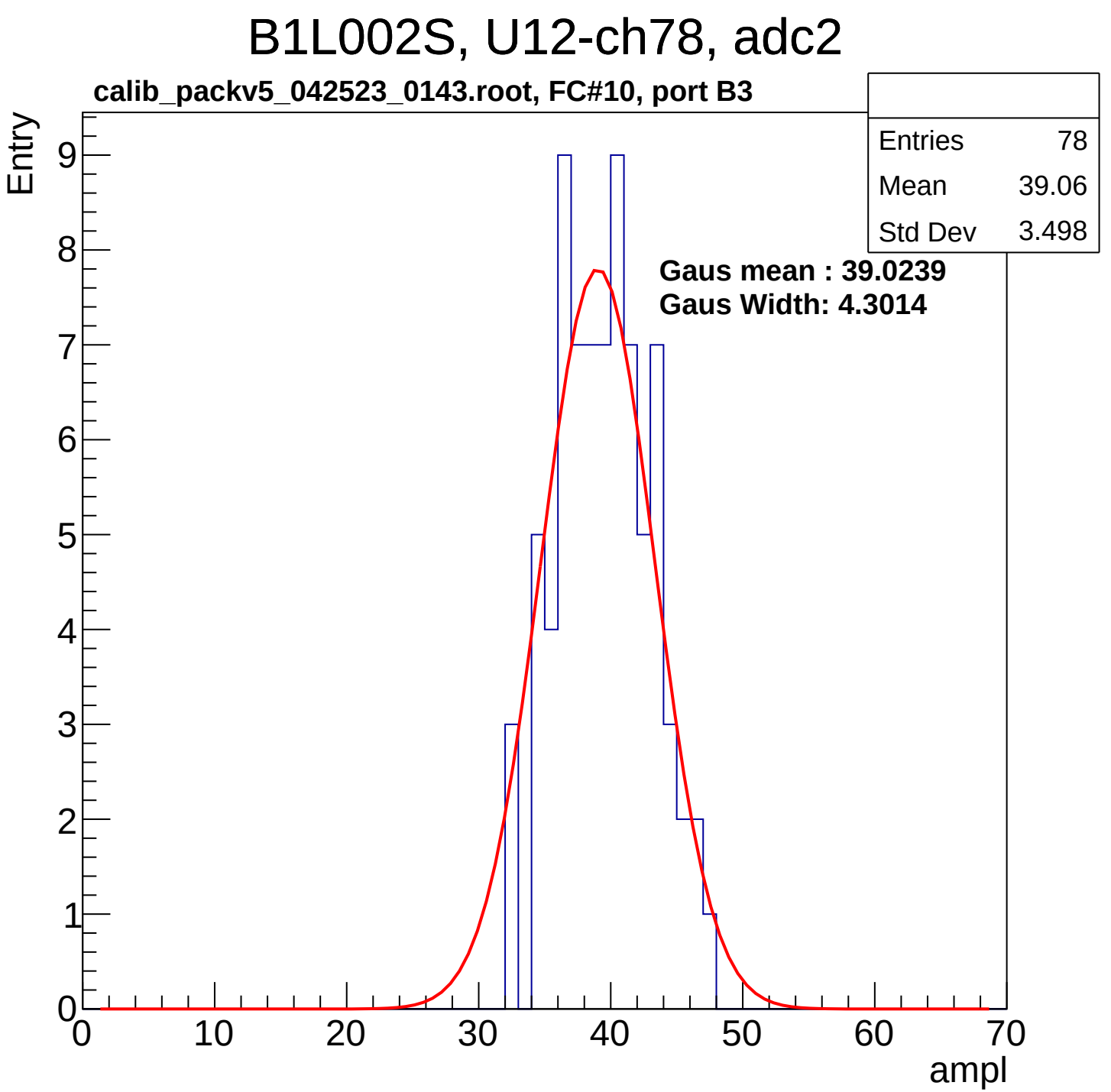
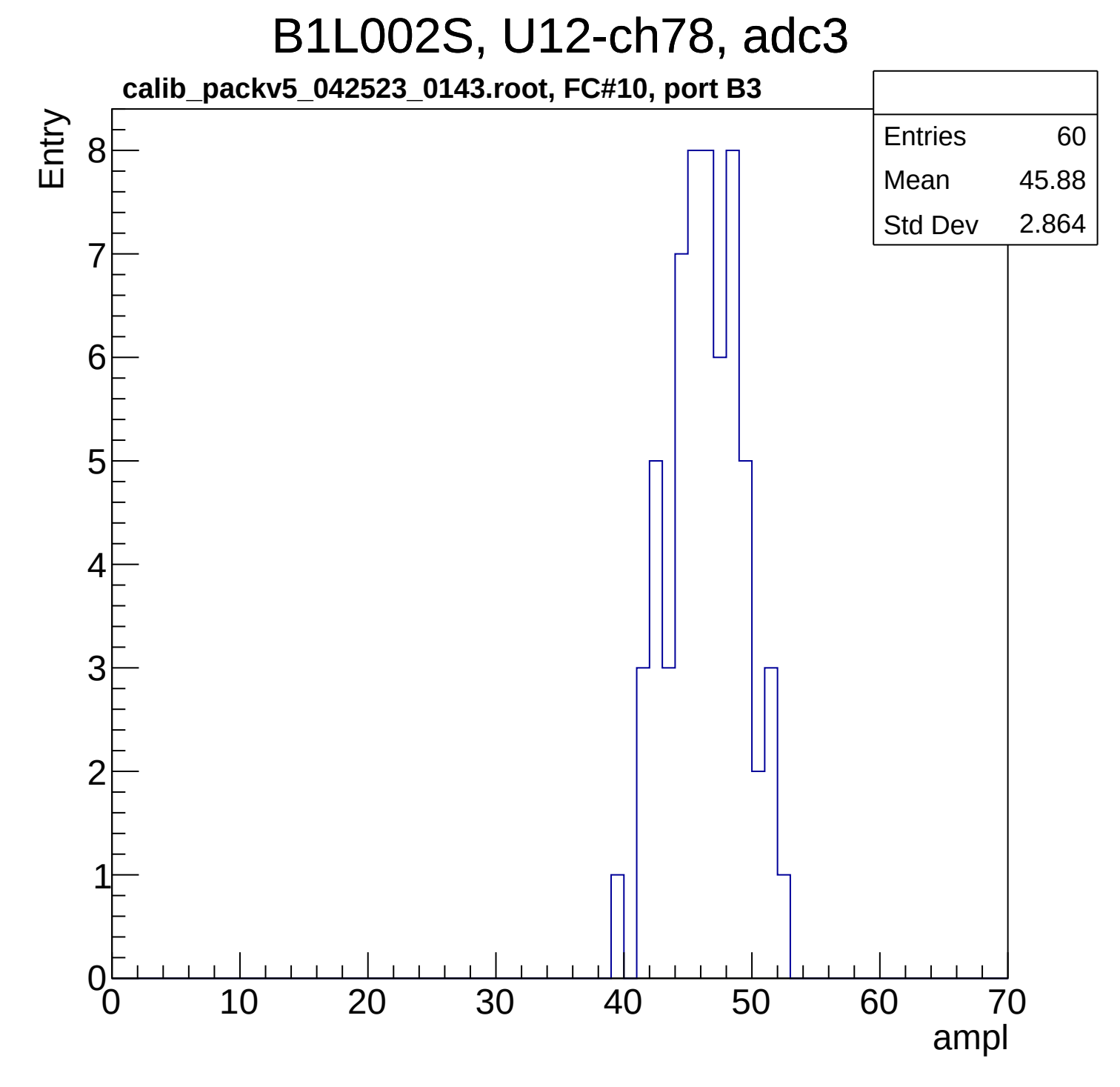
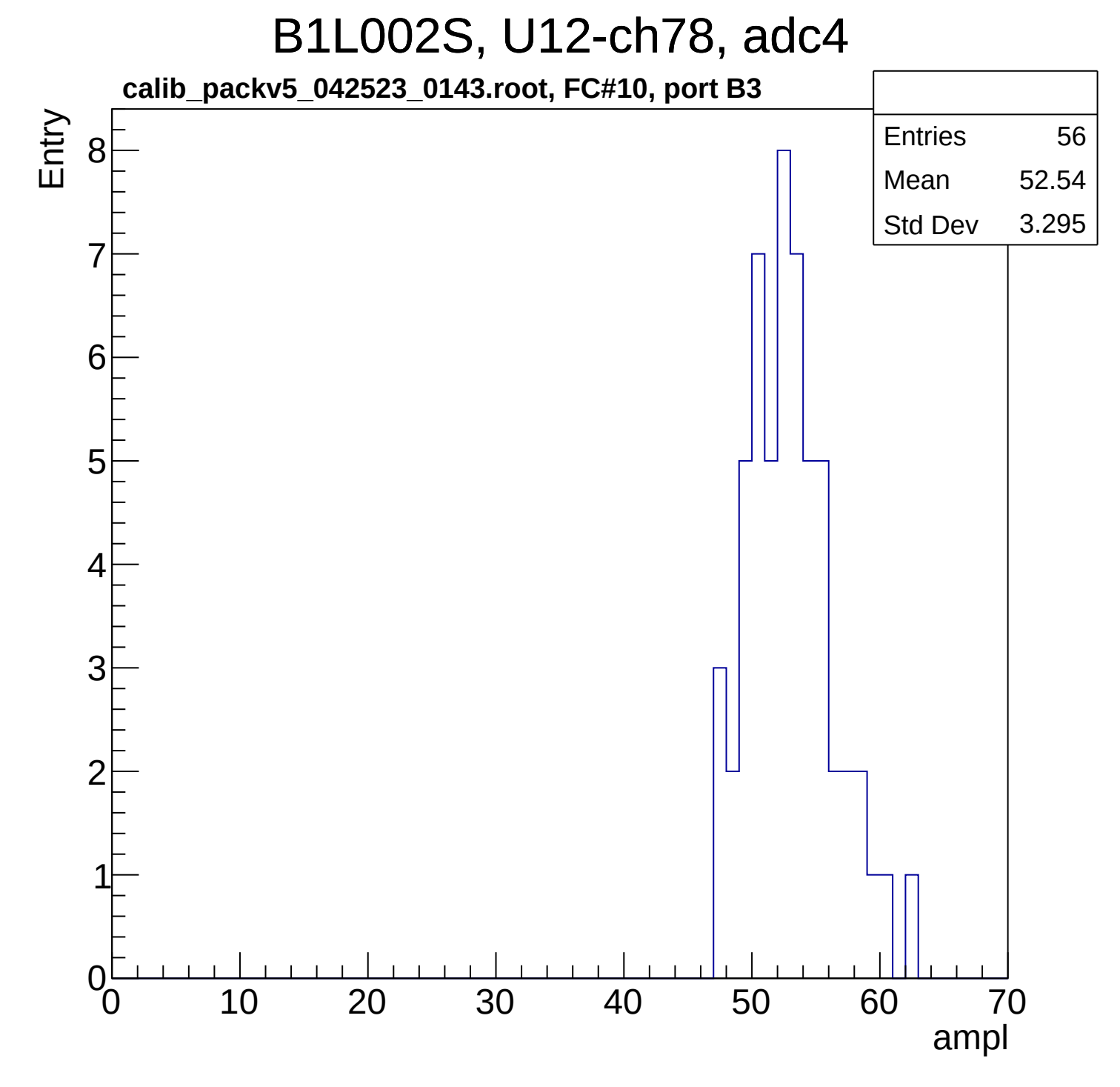
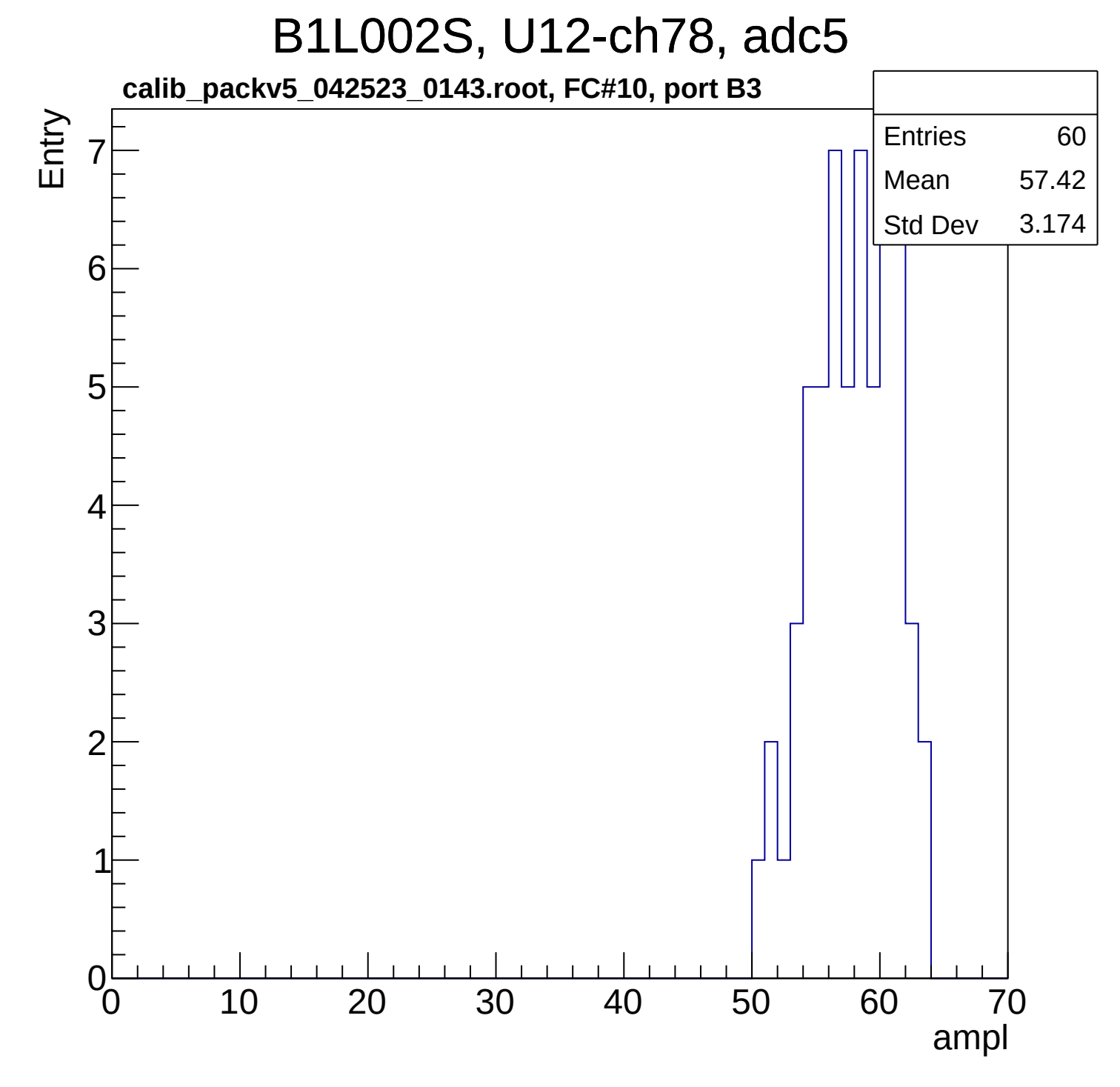

B1L002S, U12-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.13
Std Dev 3.166
Gaus mean : 26.3988
Gaus Width: 3.1453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 32.55
Std Dev 3.276
Gaus mean : 32.2715
Gaus Width: 3.1600
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 39.06
Std Dev 3.498
Gaus mean : 39.0239
Gaus Width: 4.3014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 45.88
Std Dev 2.864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.54
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.42
Std Dev 3.174
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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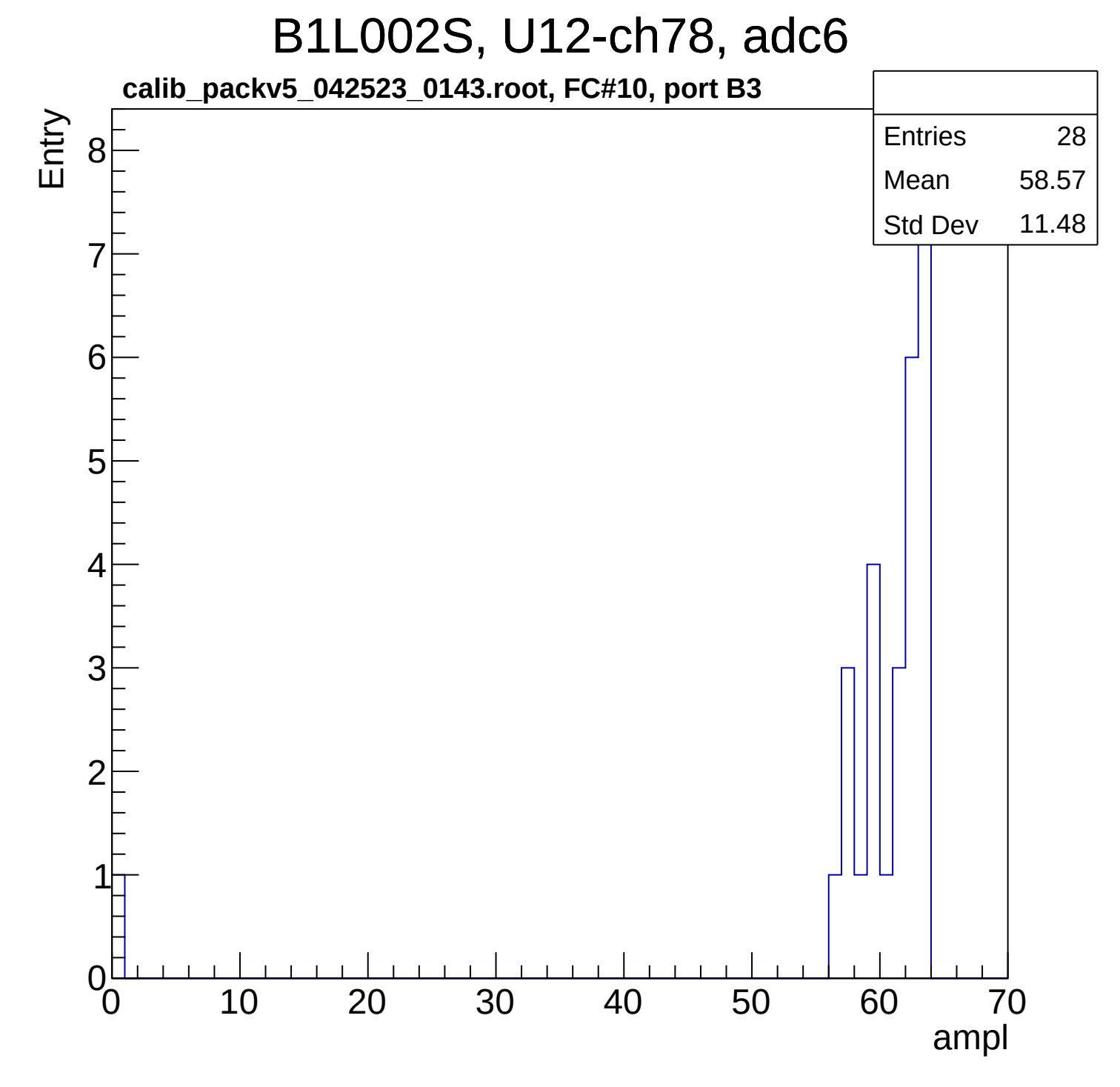
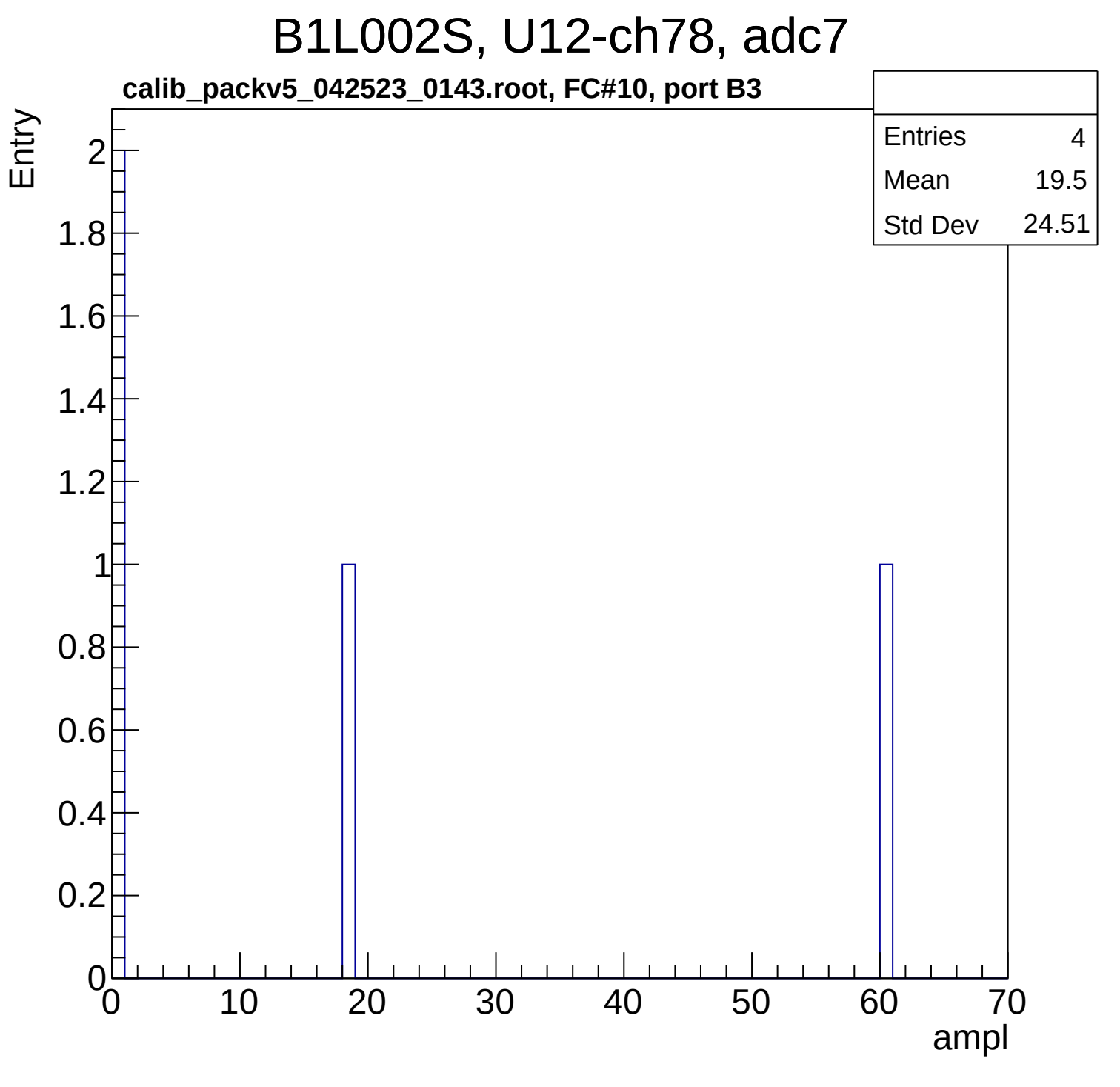
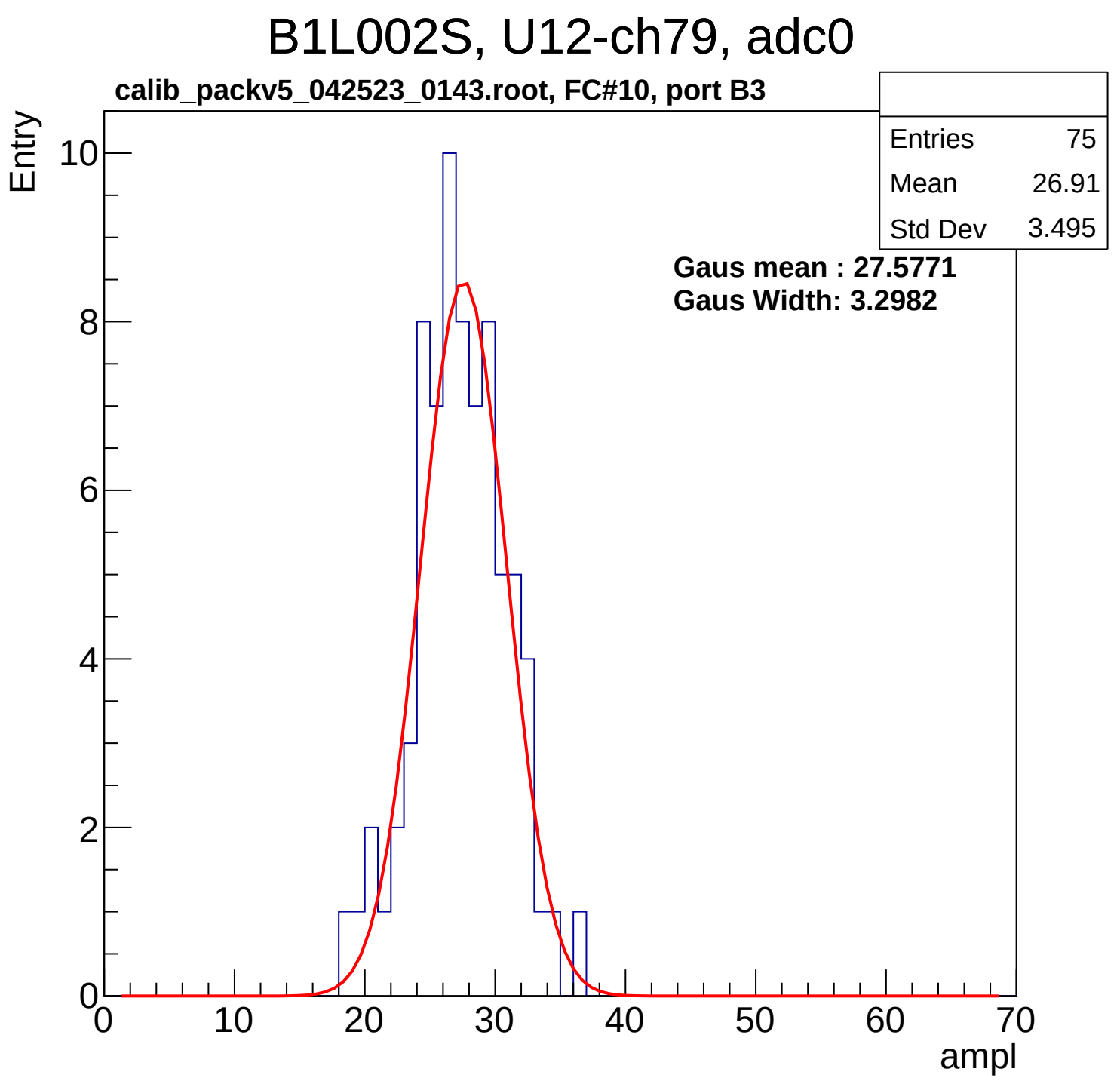
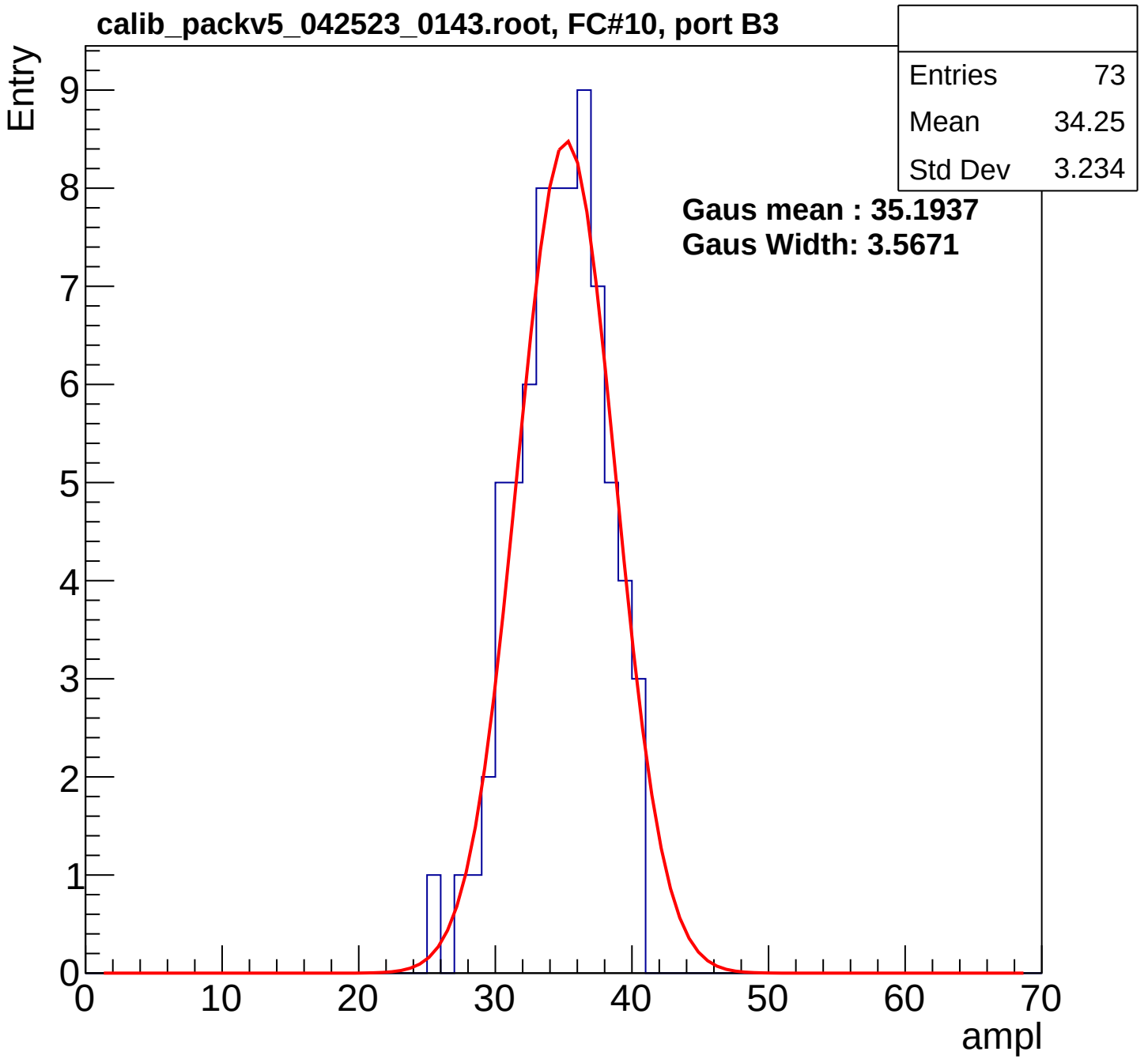
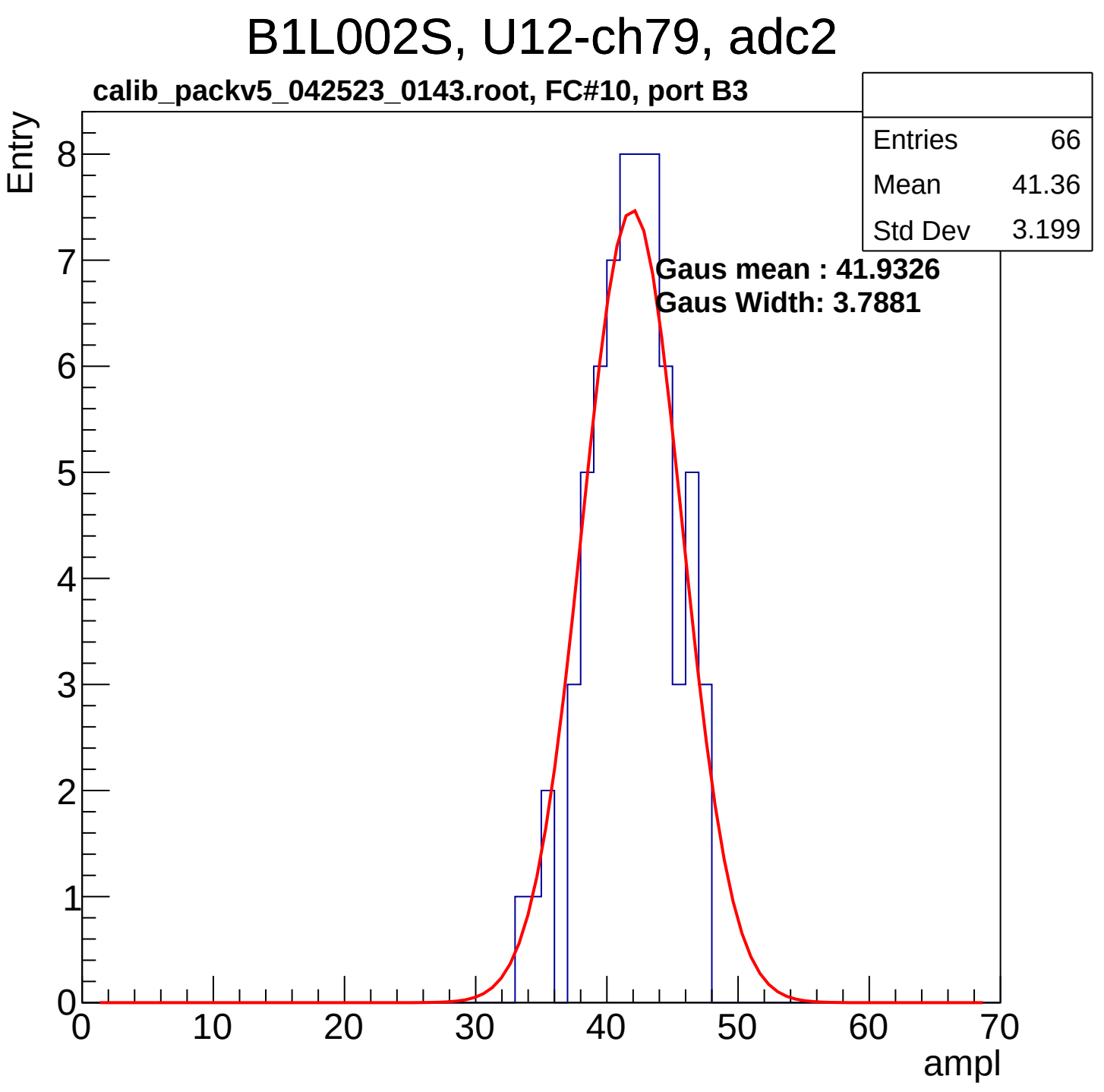
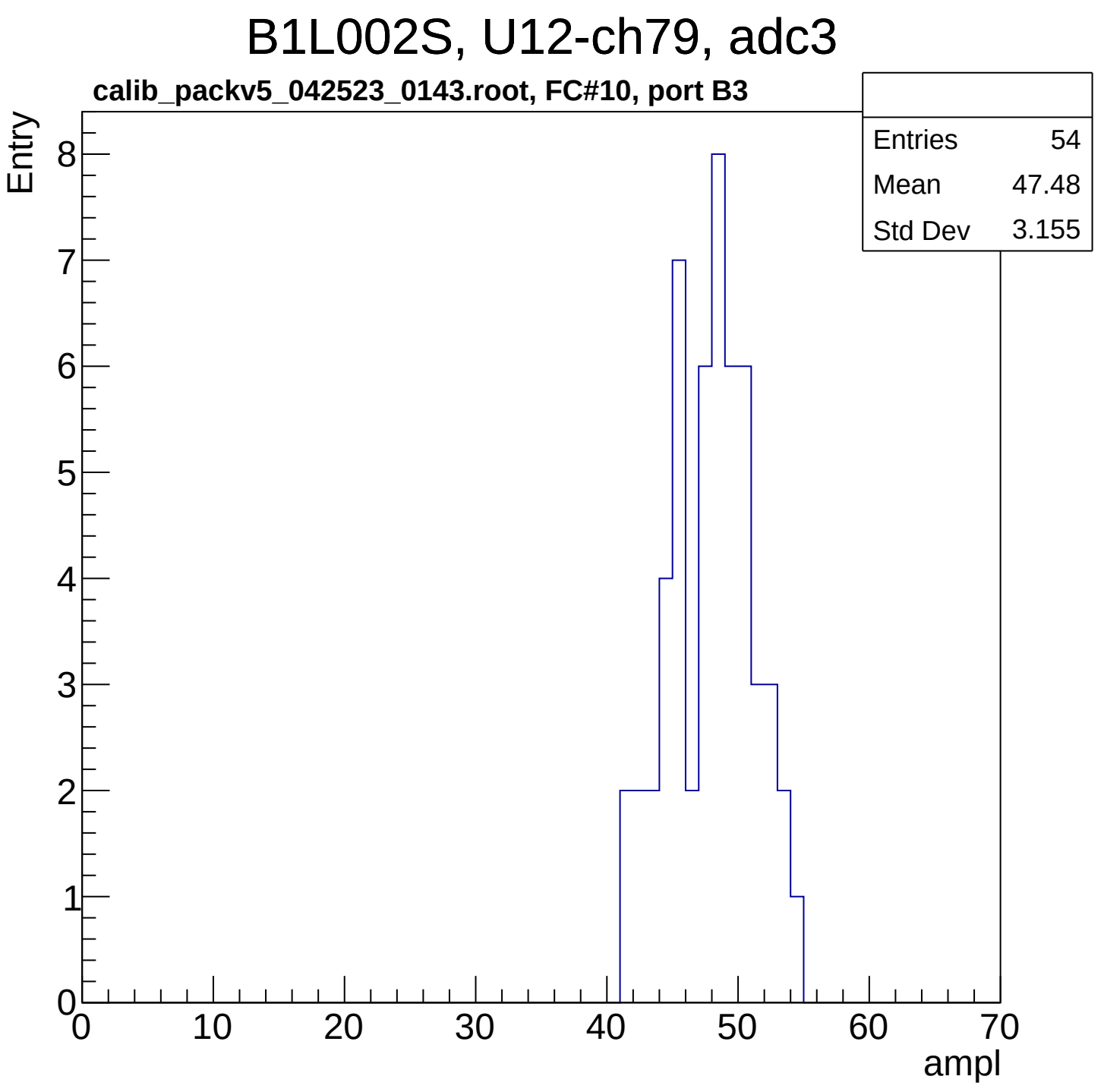

B1L002S, U12-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.57
Std Dev 11.48
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

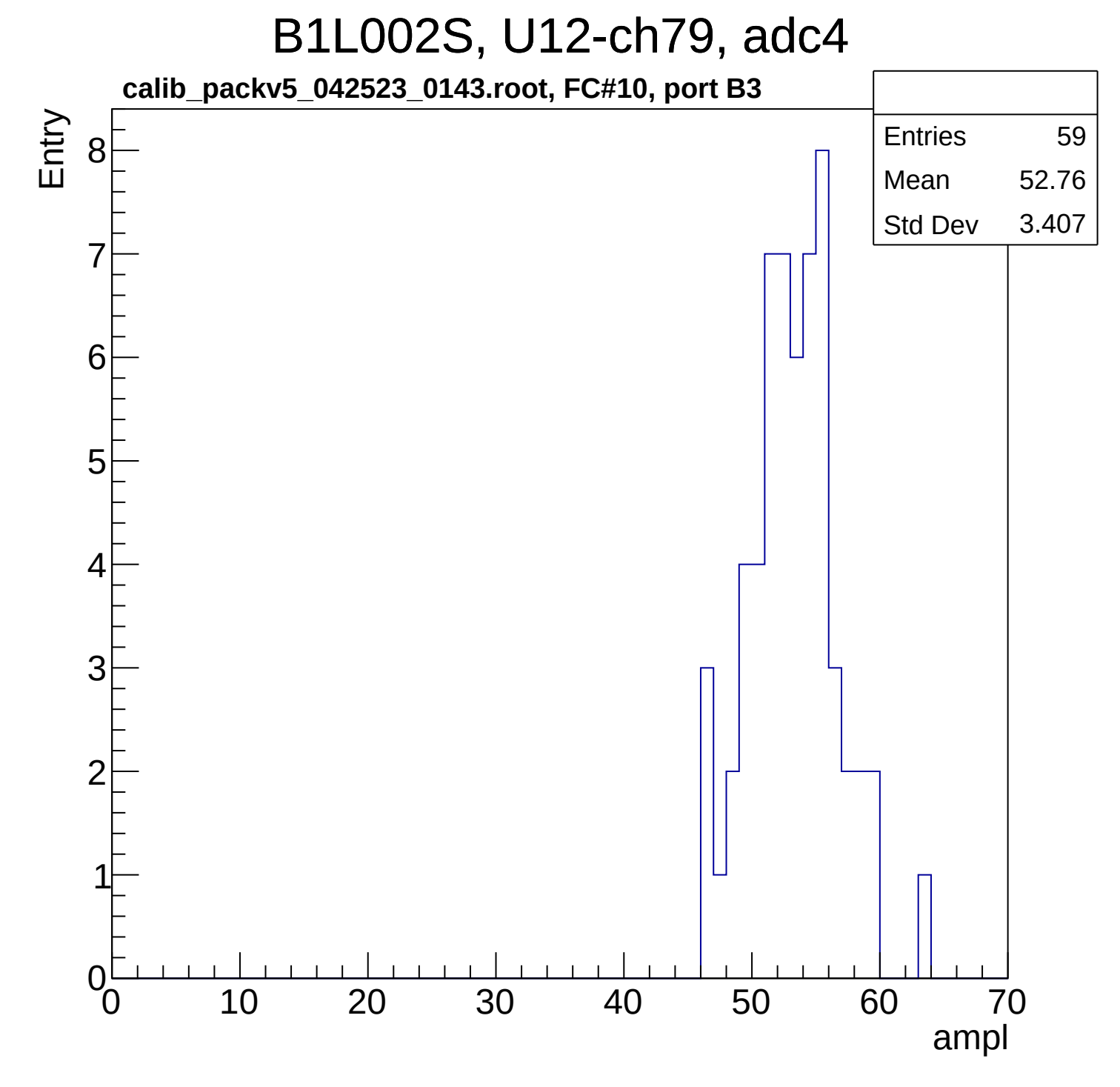

B1L002S, U12-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 19.5
Std Dev 24.51
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 26.91
Std Dev 3.495
Gaus mean : 27.5771
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.25
Std Dev 3.234
Gaus mean : 35.1937
Gaus Width: 3.5671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.36
Std Dev 3.199
Gaus mean : 41.9326
Gaus Width: 3.7881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.48
Std Dev 3.155
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.76
Std Dev 3.407
Entry
ampl
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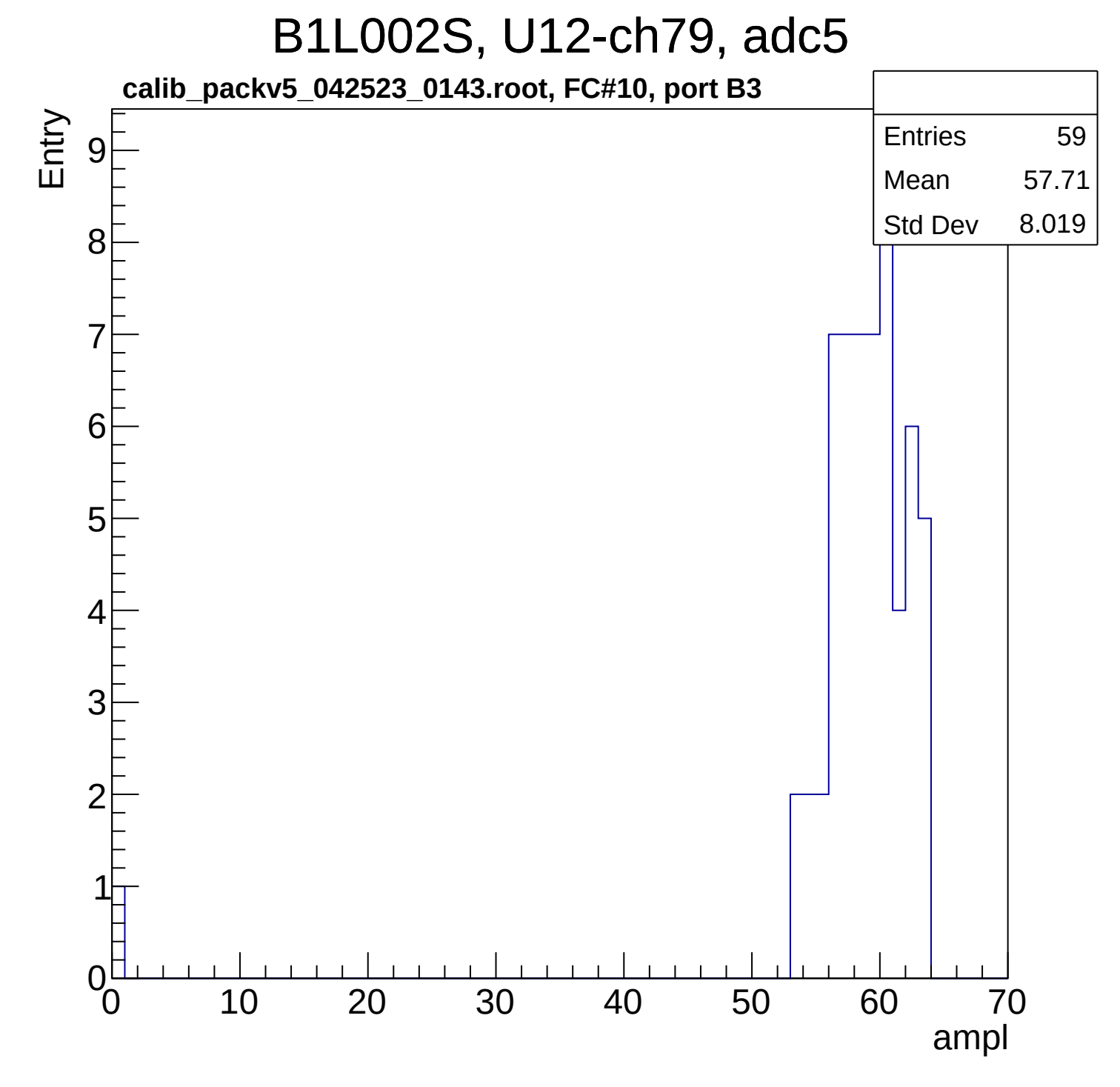
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.71
Std Dev 8.019
Entry
ampl
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2
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4
5
6
7
8
9
0
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20
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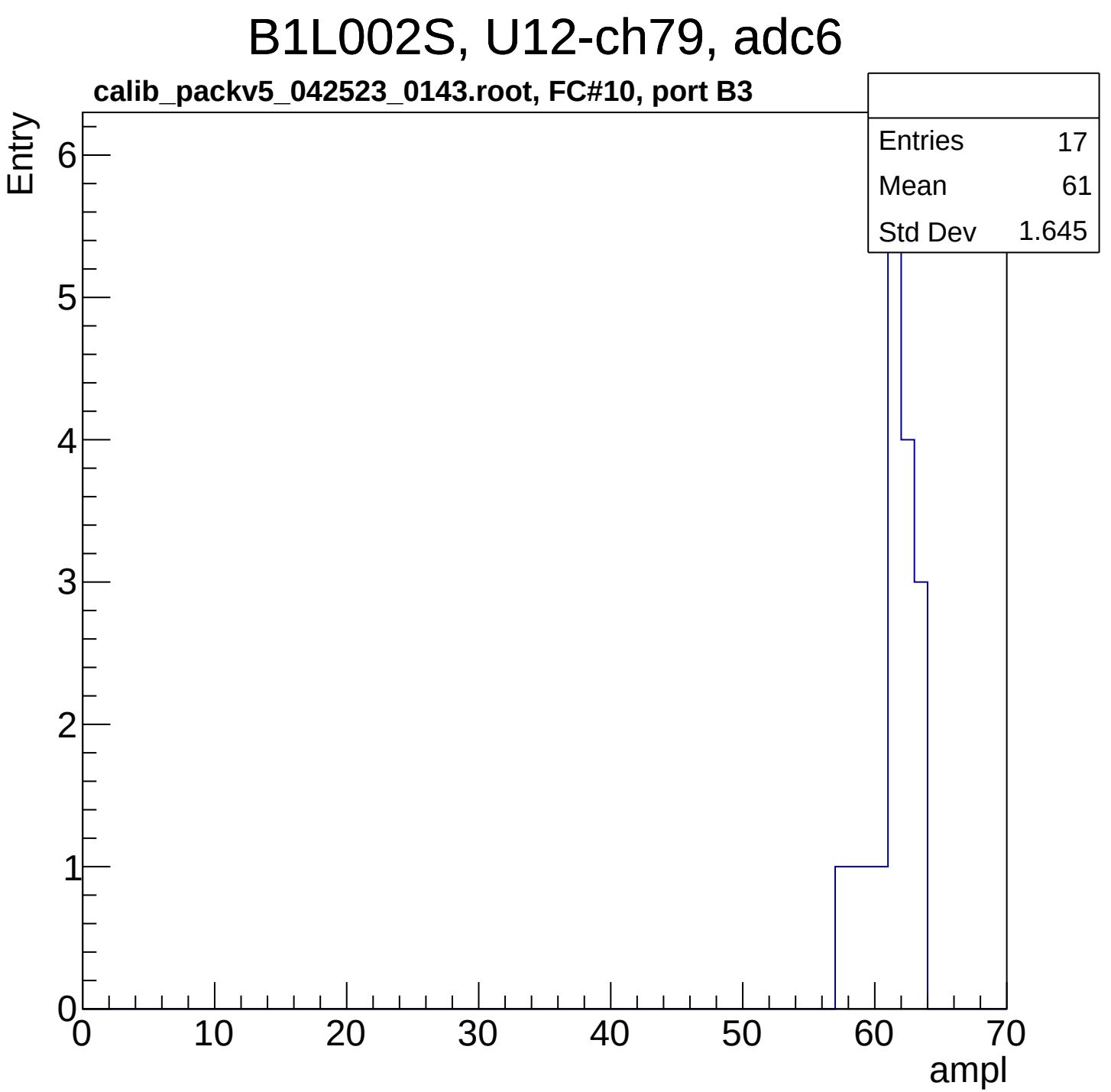
40
50
60
70


B1L002S, U12-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61
Std Dev 1.645
Entry
0
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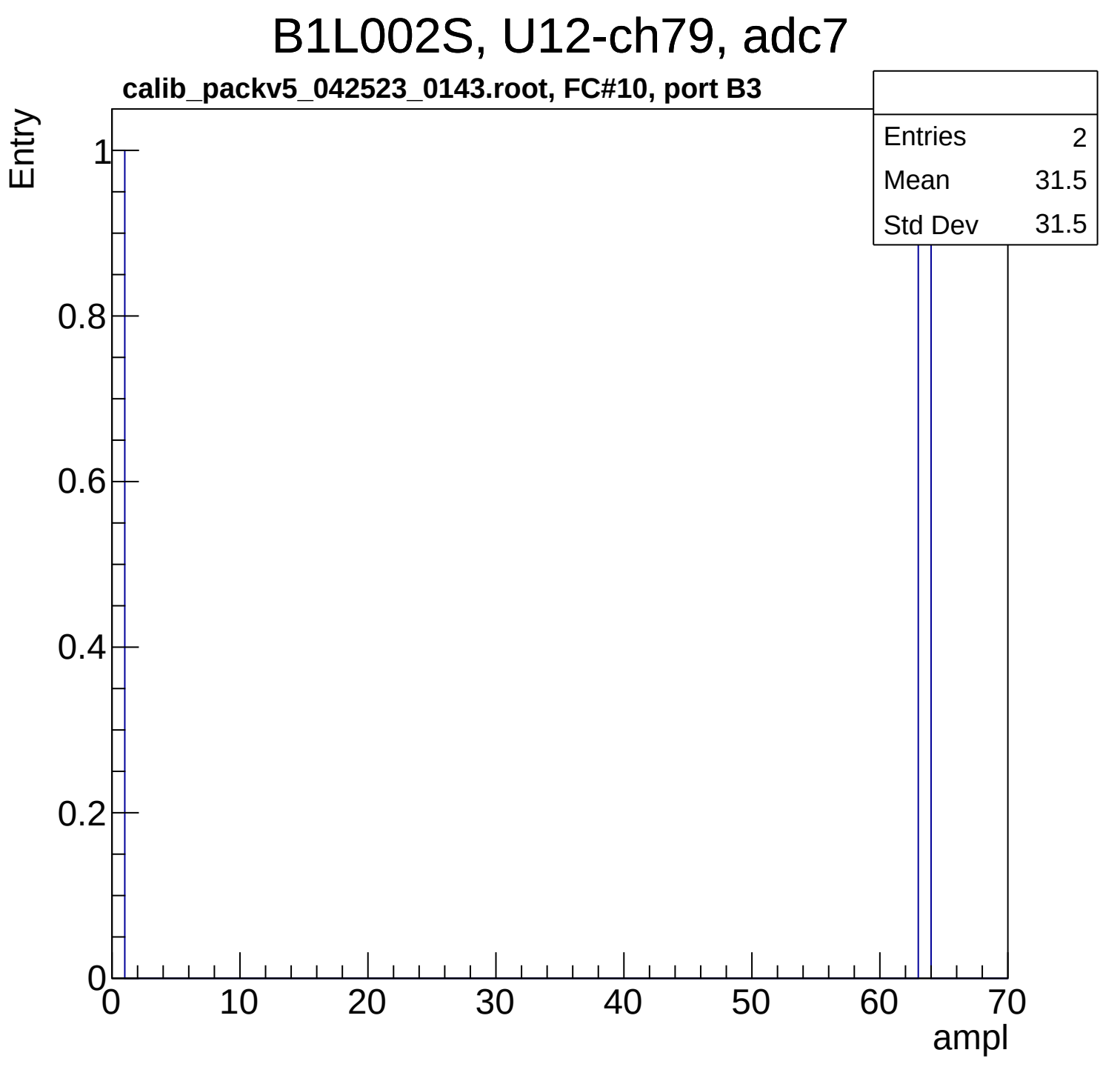
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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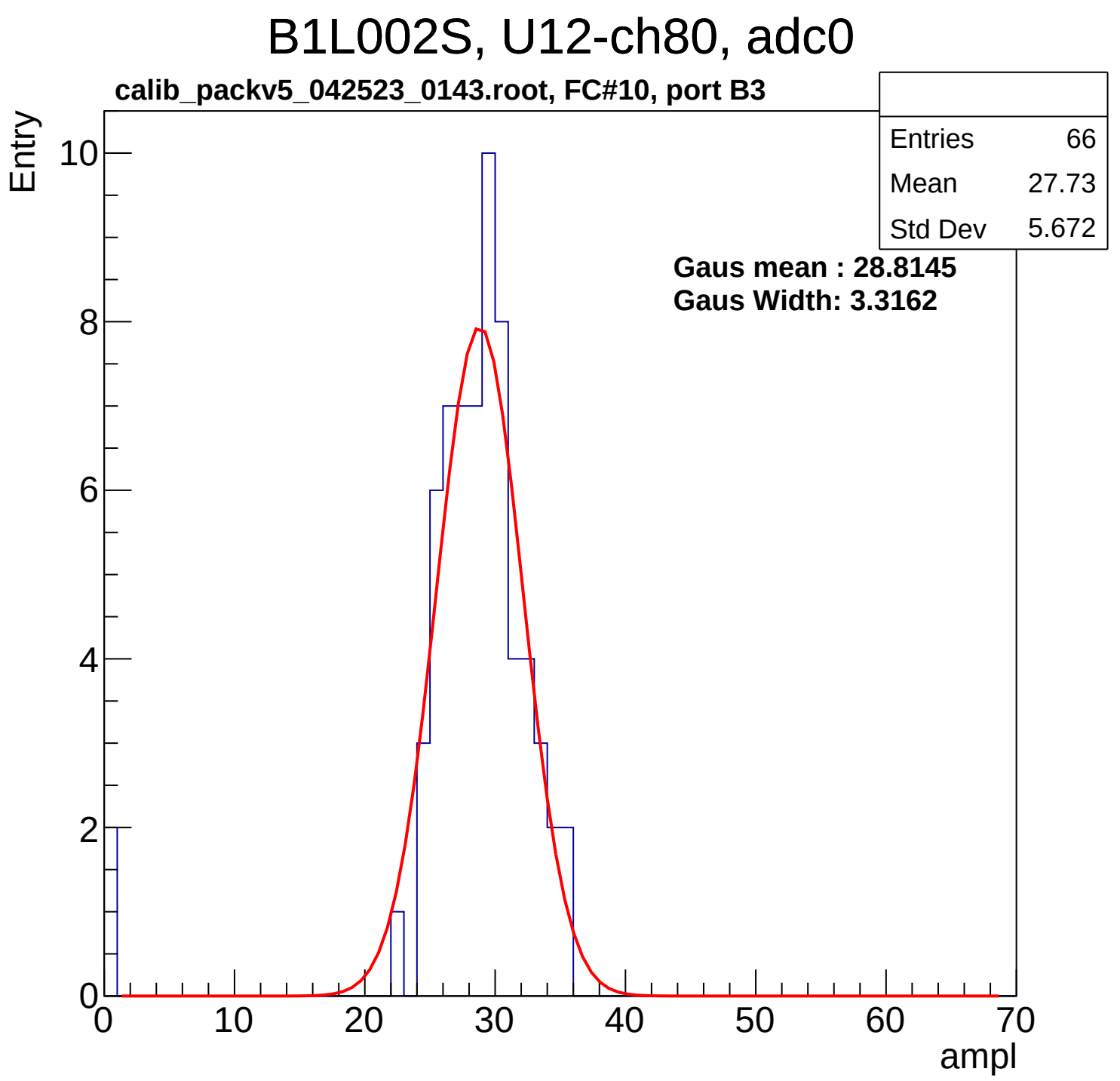
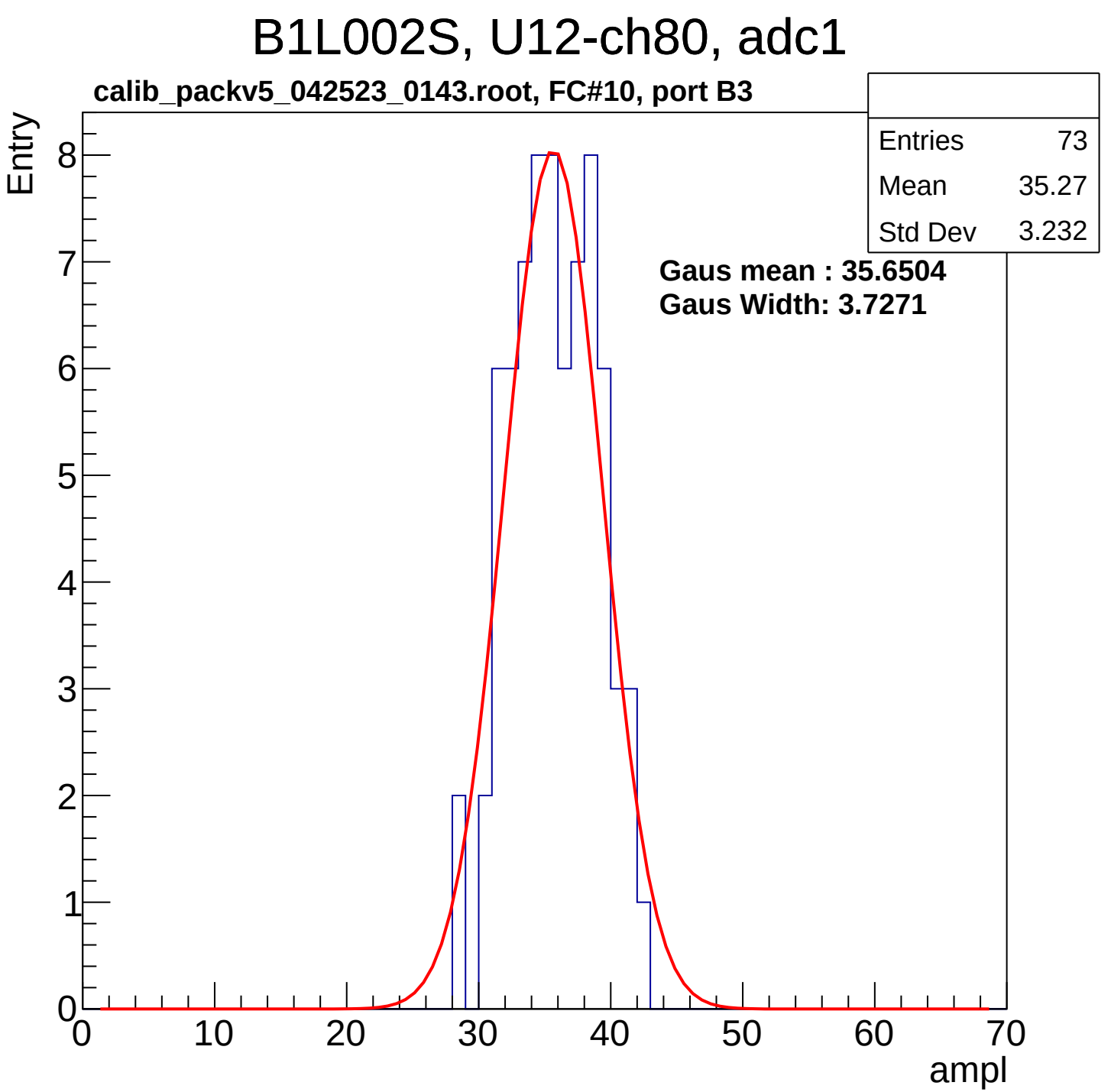
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.73
Std Dev 5.672
Gaus mean : 28.8145
Gaus Width: 3.3162
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.27
Std Dev 3.232
Gaus mean : 35.6504
Gaus Width: 3.7271
Entry
ampl
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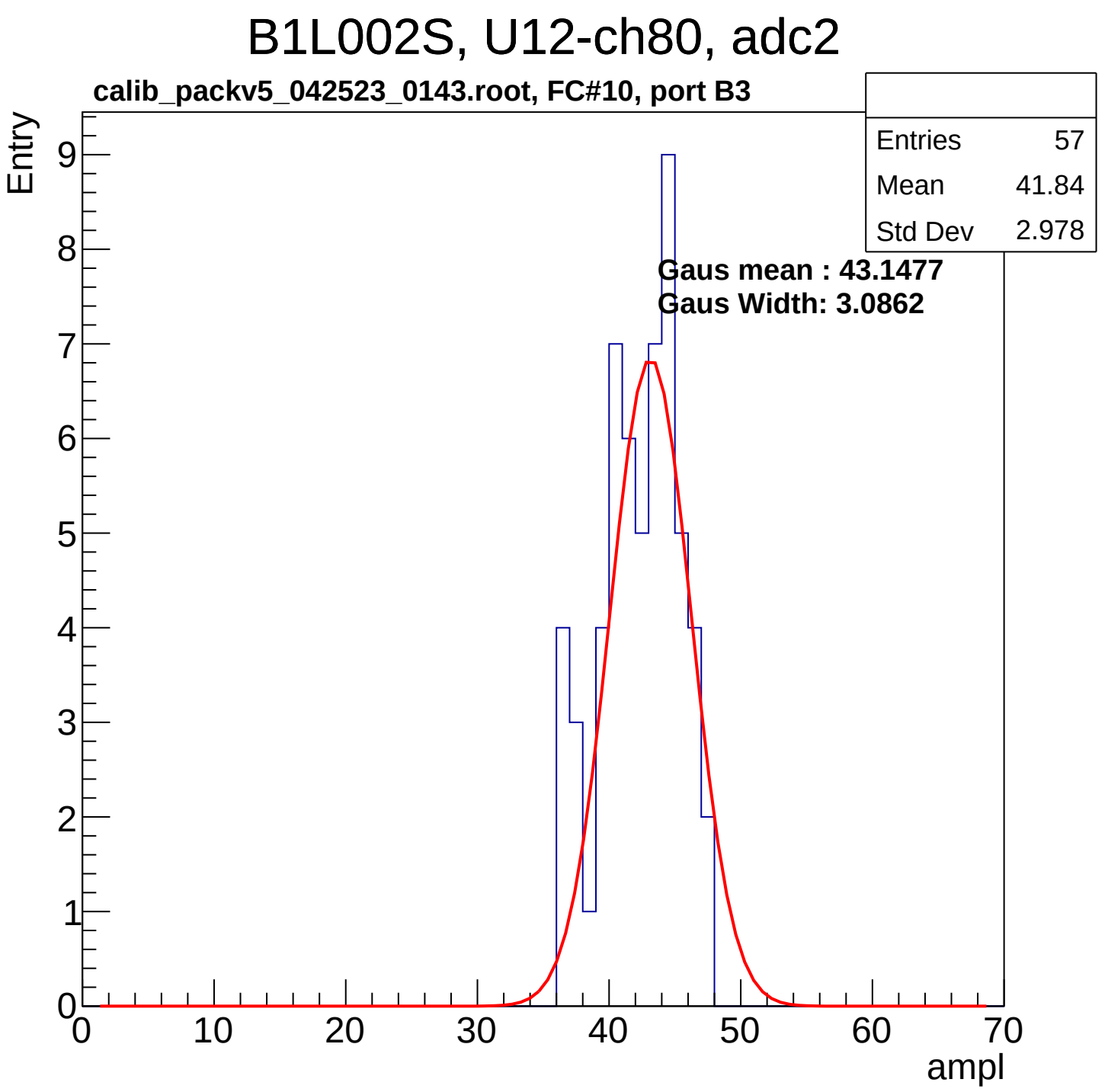
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.84
Std Dev 2.978
Gaus mean : 43.1477
Gaus Width: 3.0862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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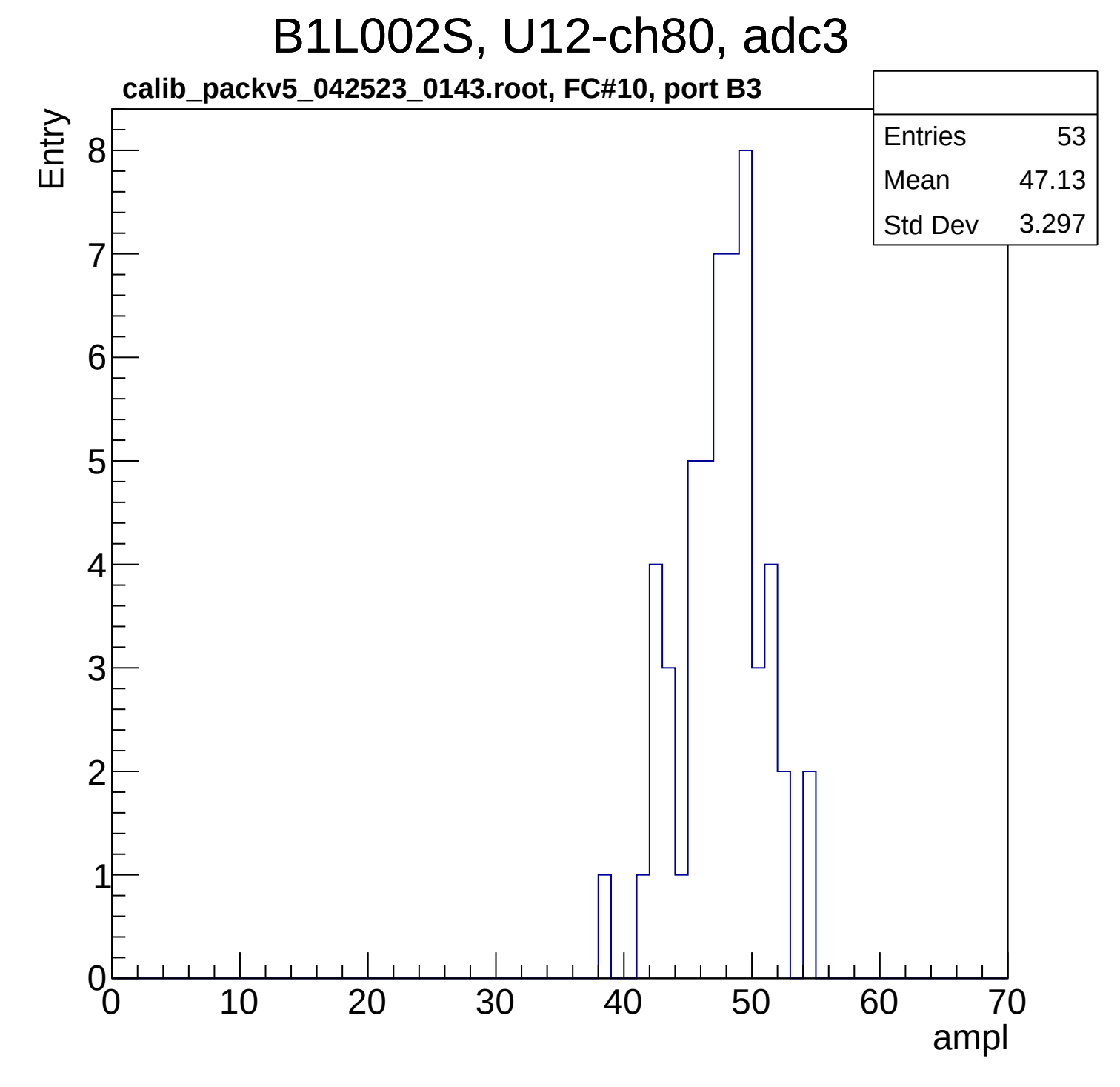
B1L002S, U12-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.13
Std Dev 3.297
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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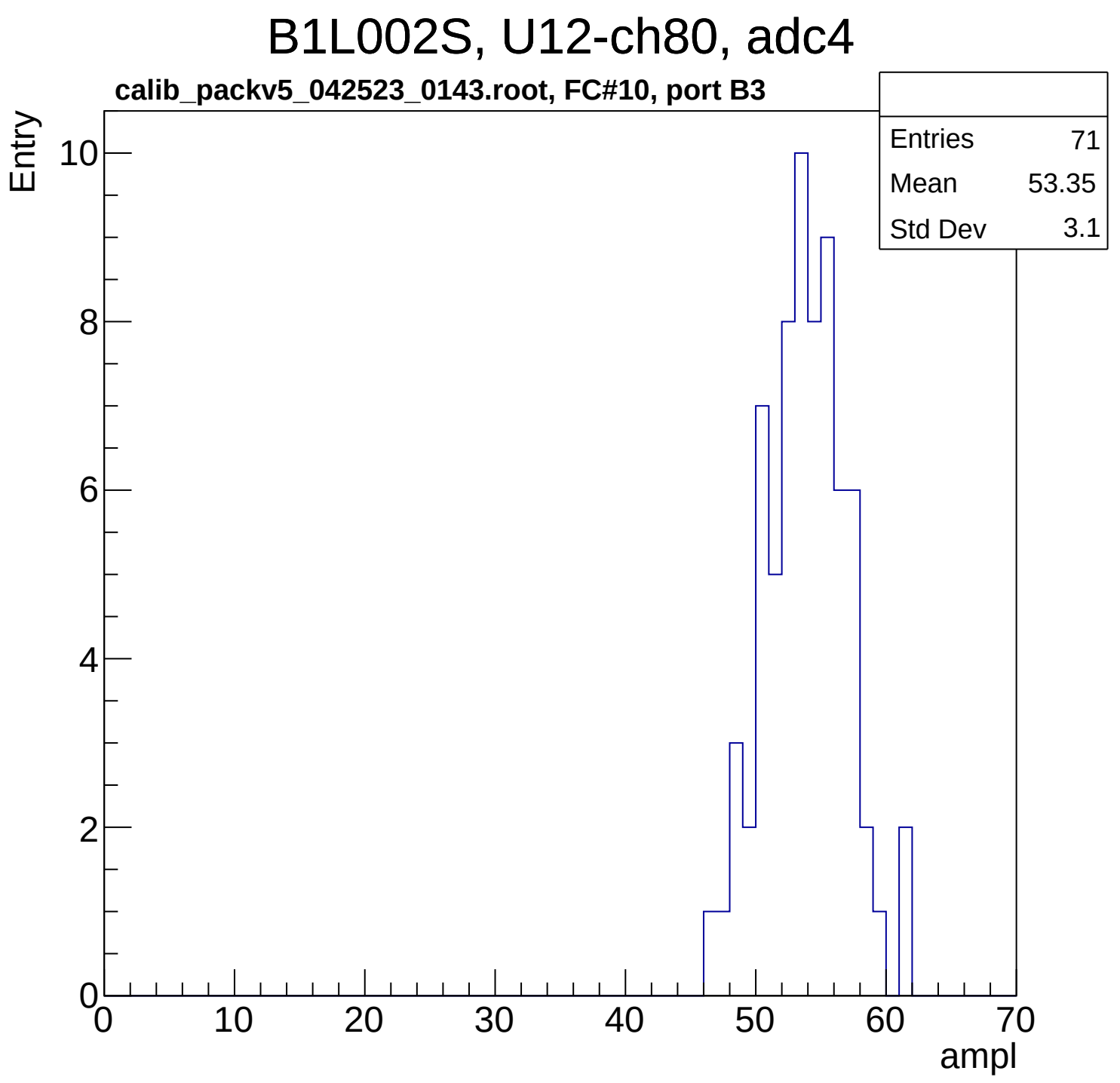
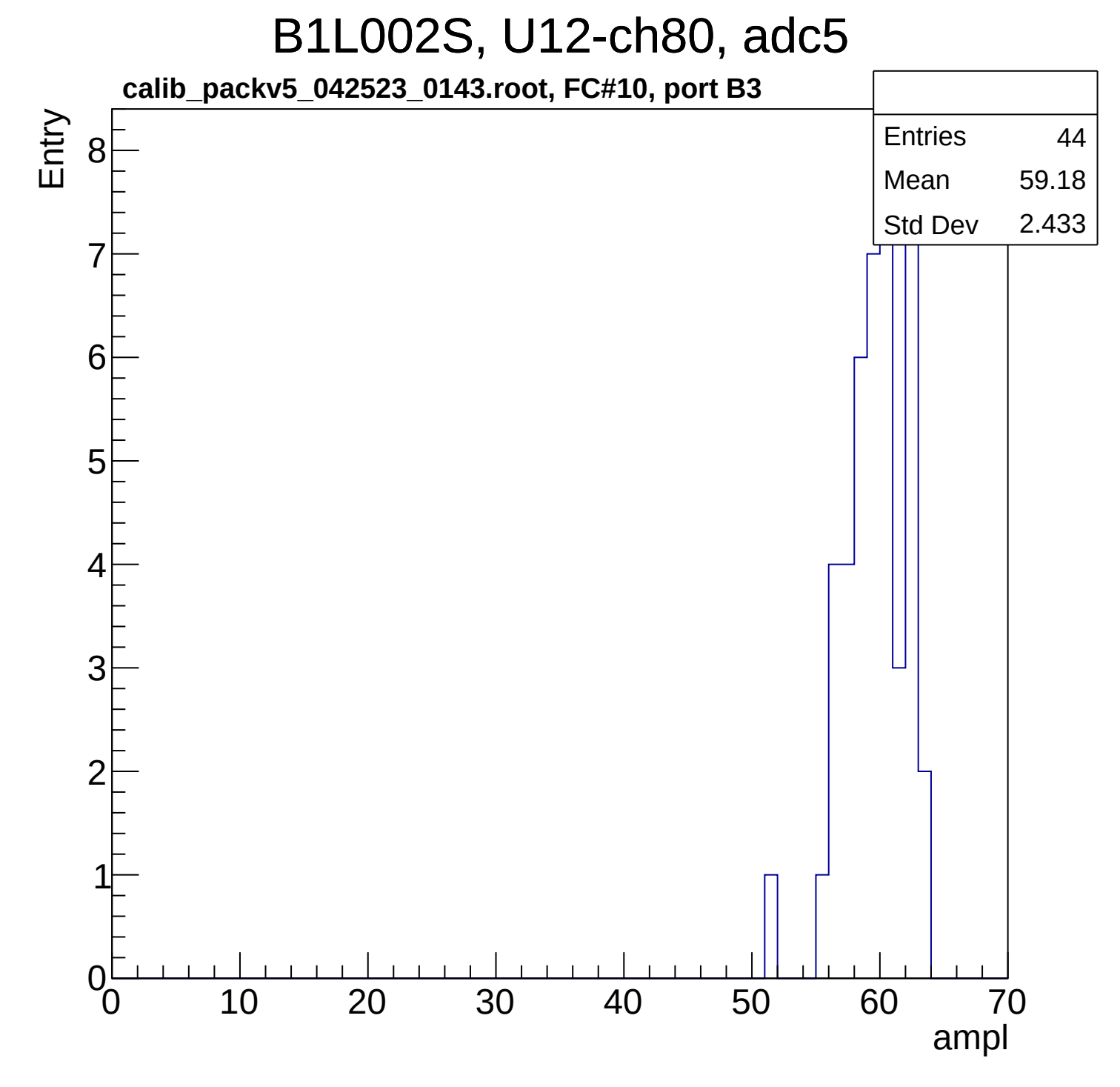
40
50
60
70


B1L002S, U12-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 53.35
Std Dev 3.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.18
Std Dev 2.433
Entry
ampl
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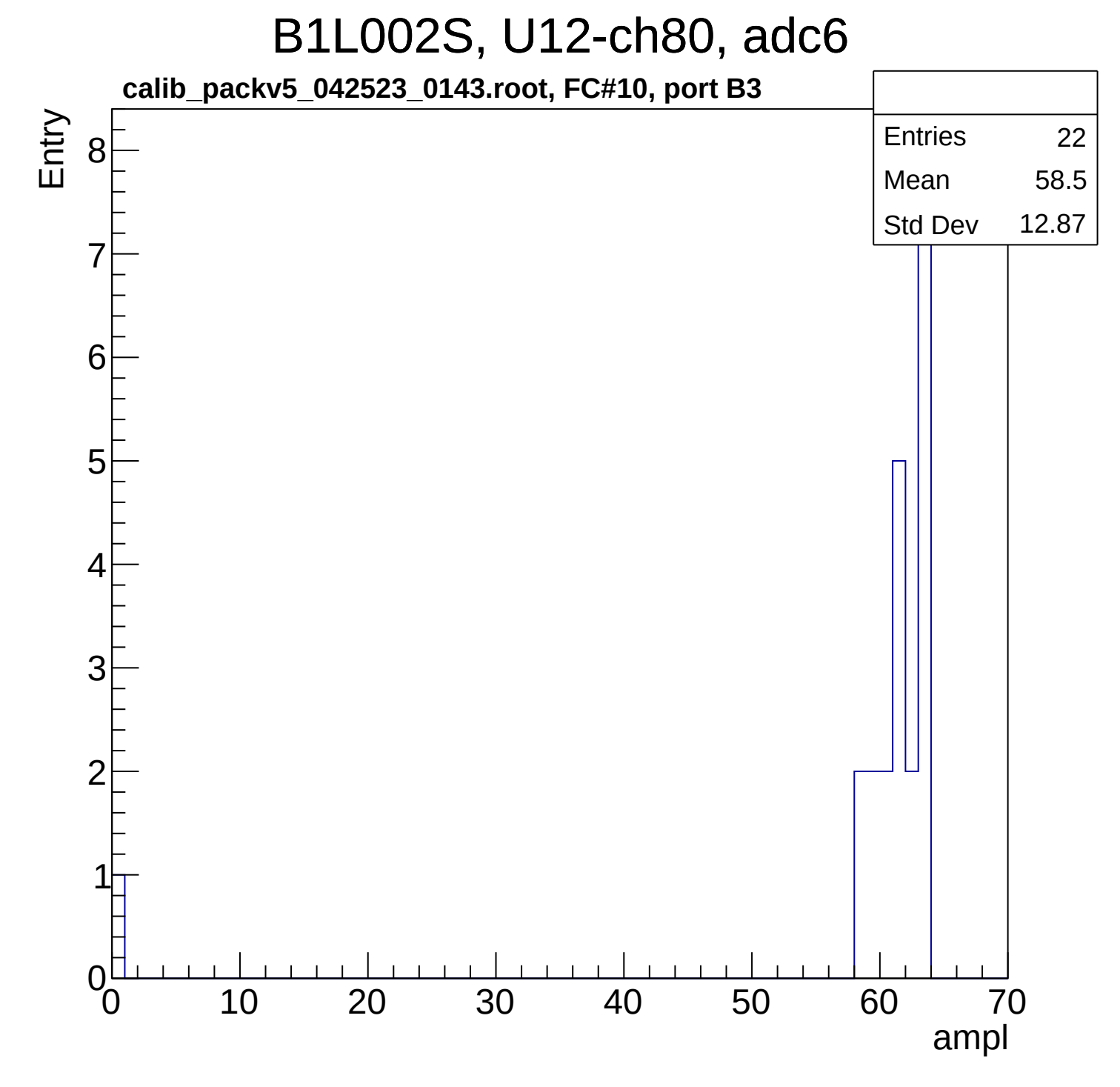
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.5
Std Dev 12.87
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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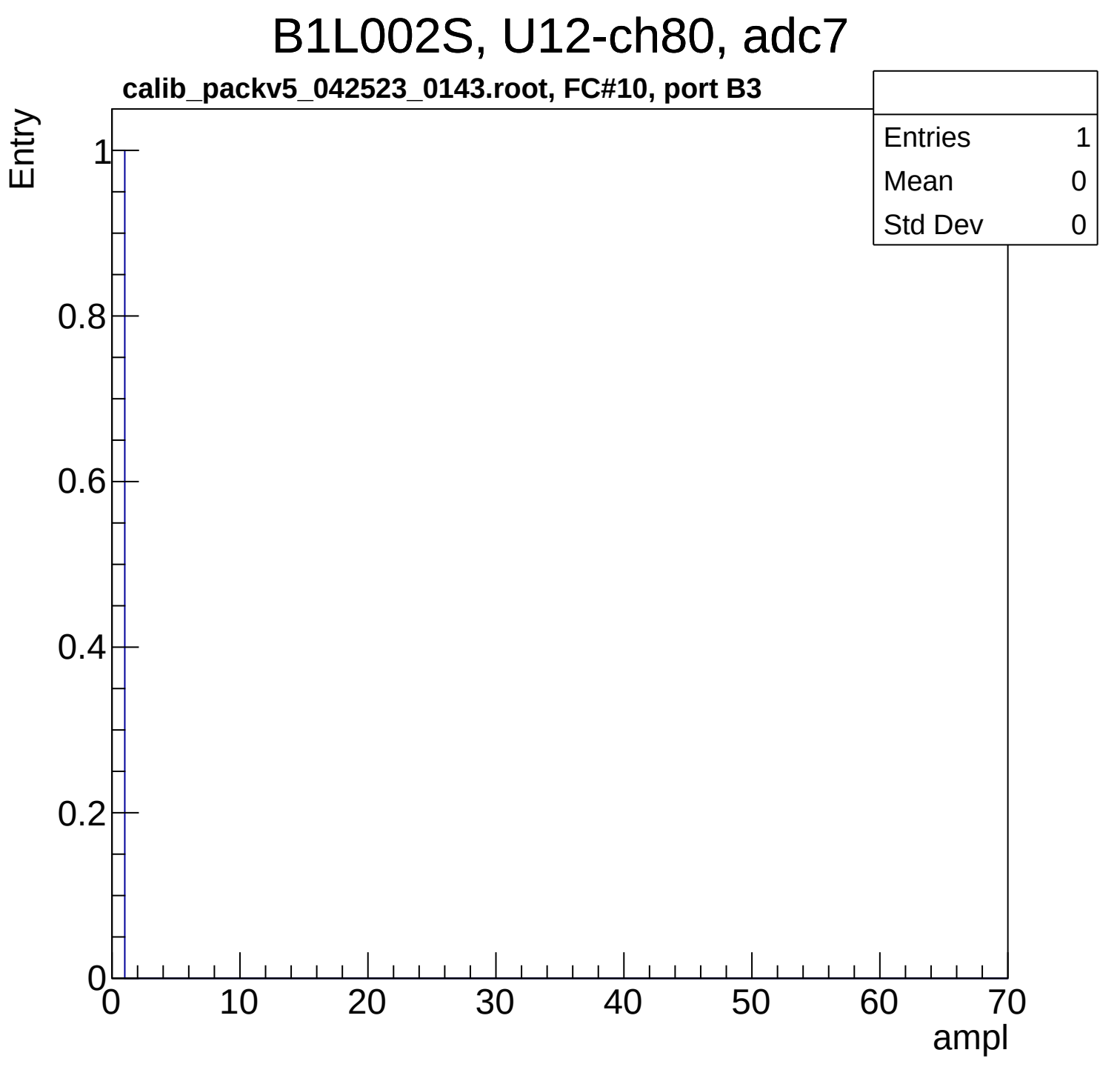
B1L002S, U12-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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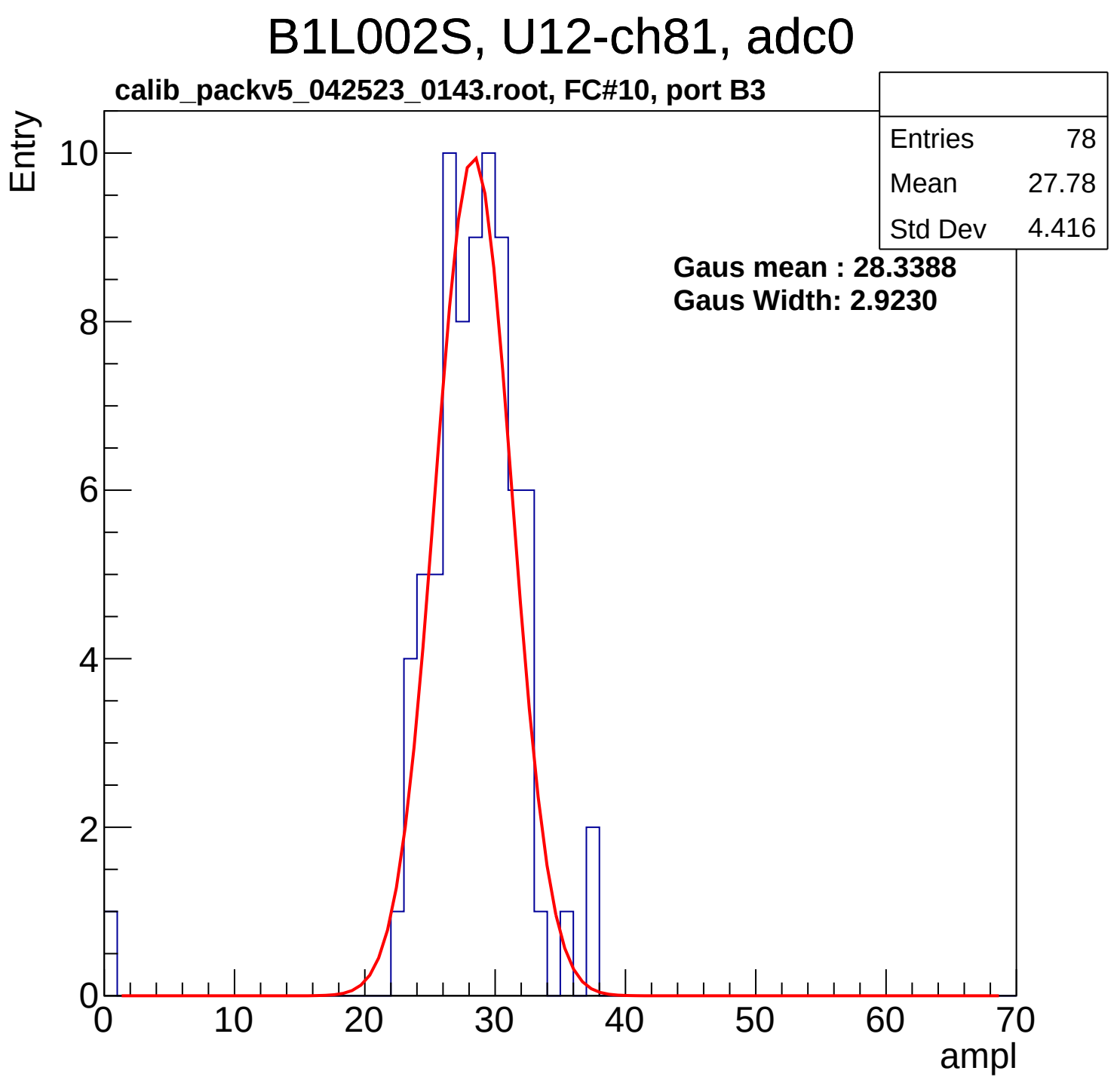
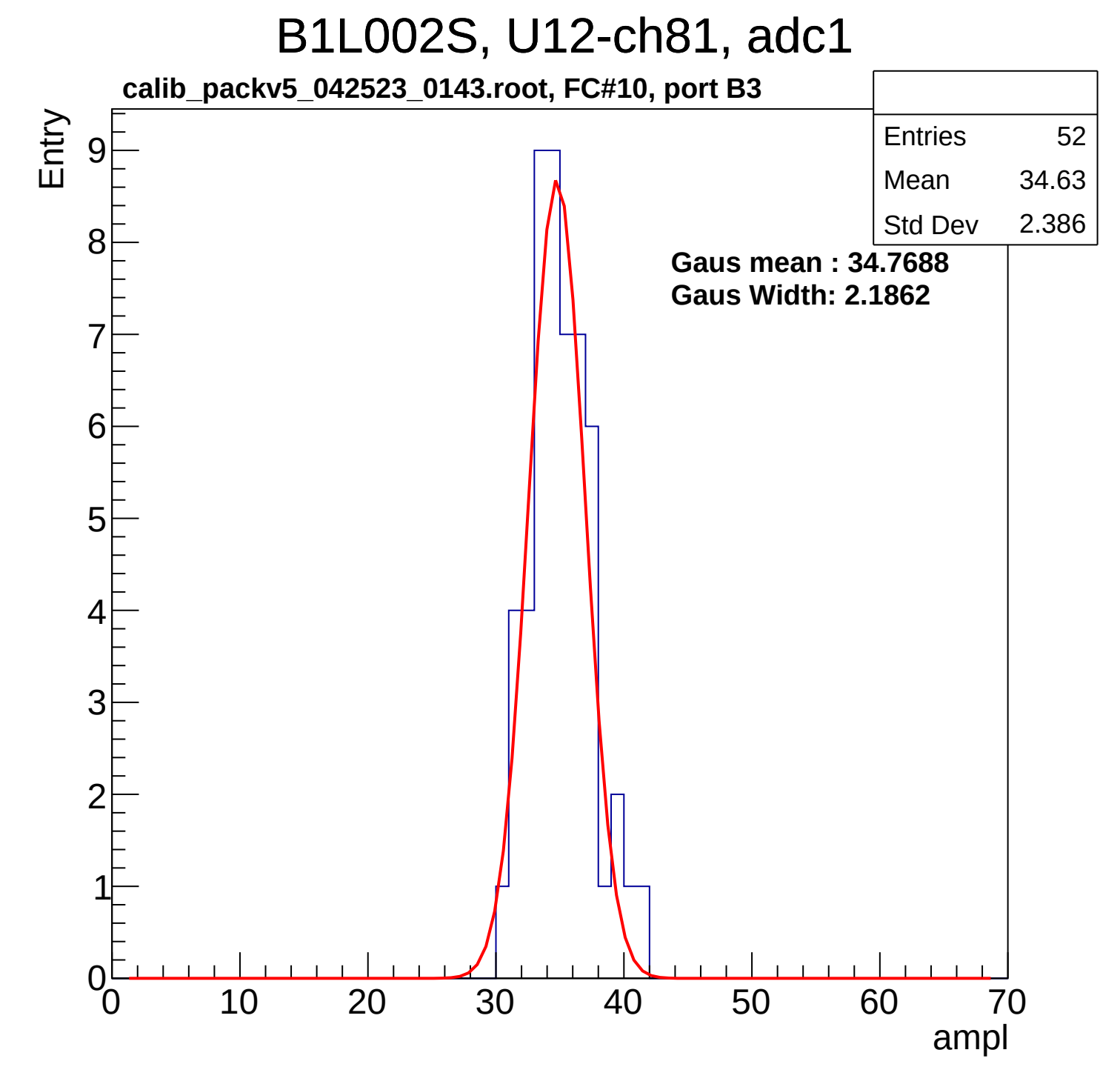
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.78
Std Dev 4.416
Gaus mean : 28.3388
Gaus Width: 2.9230
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.63
Std Dev 2.386
Gaus mean : 34.7688
Gaus Width: 2.1862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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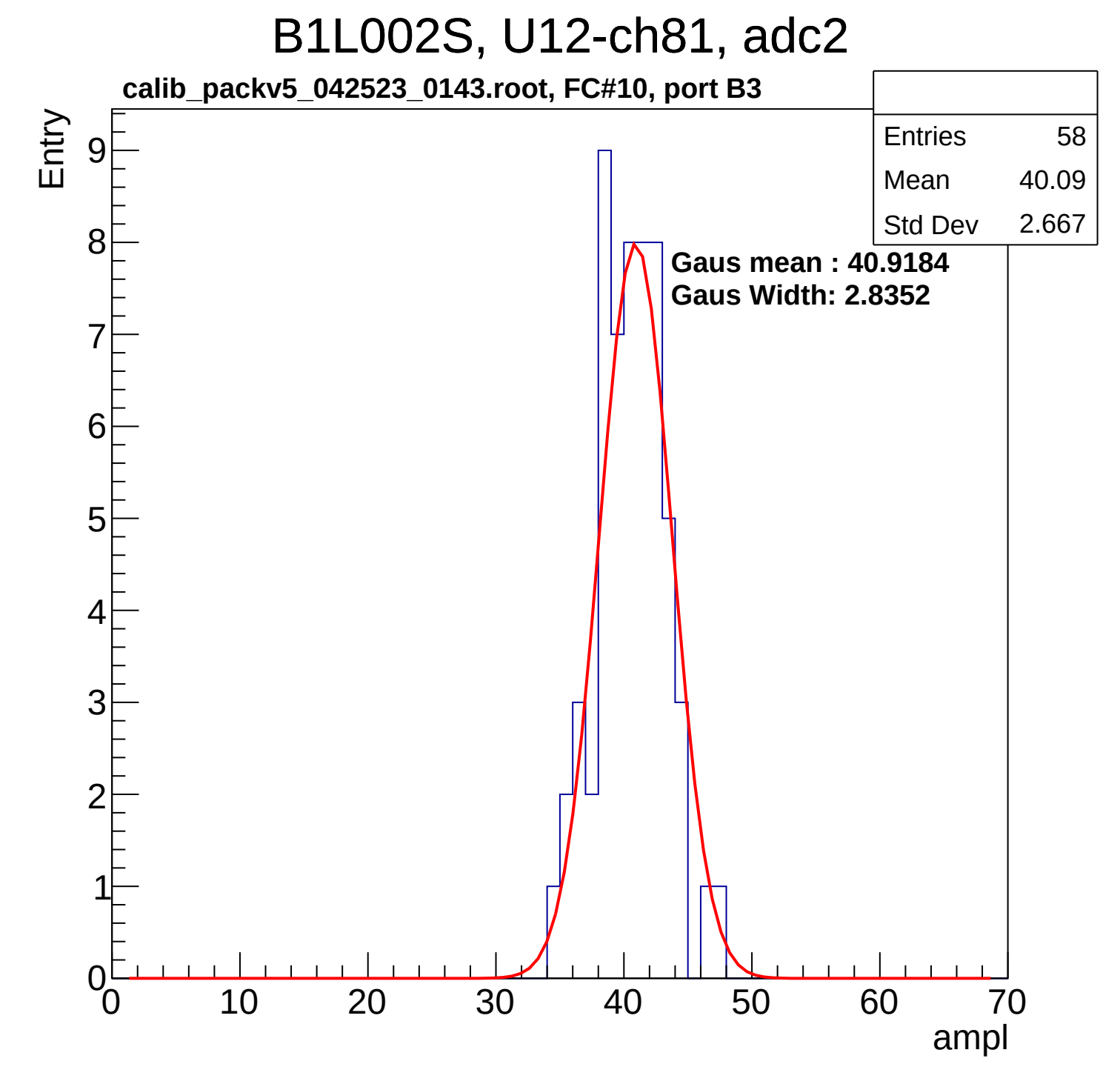
B1L002S, U12-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.09
Std Dev 2.667
Gaus mean : 40.9184
Gaus Width: 2.8352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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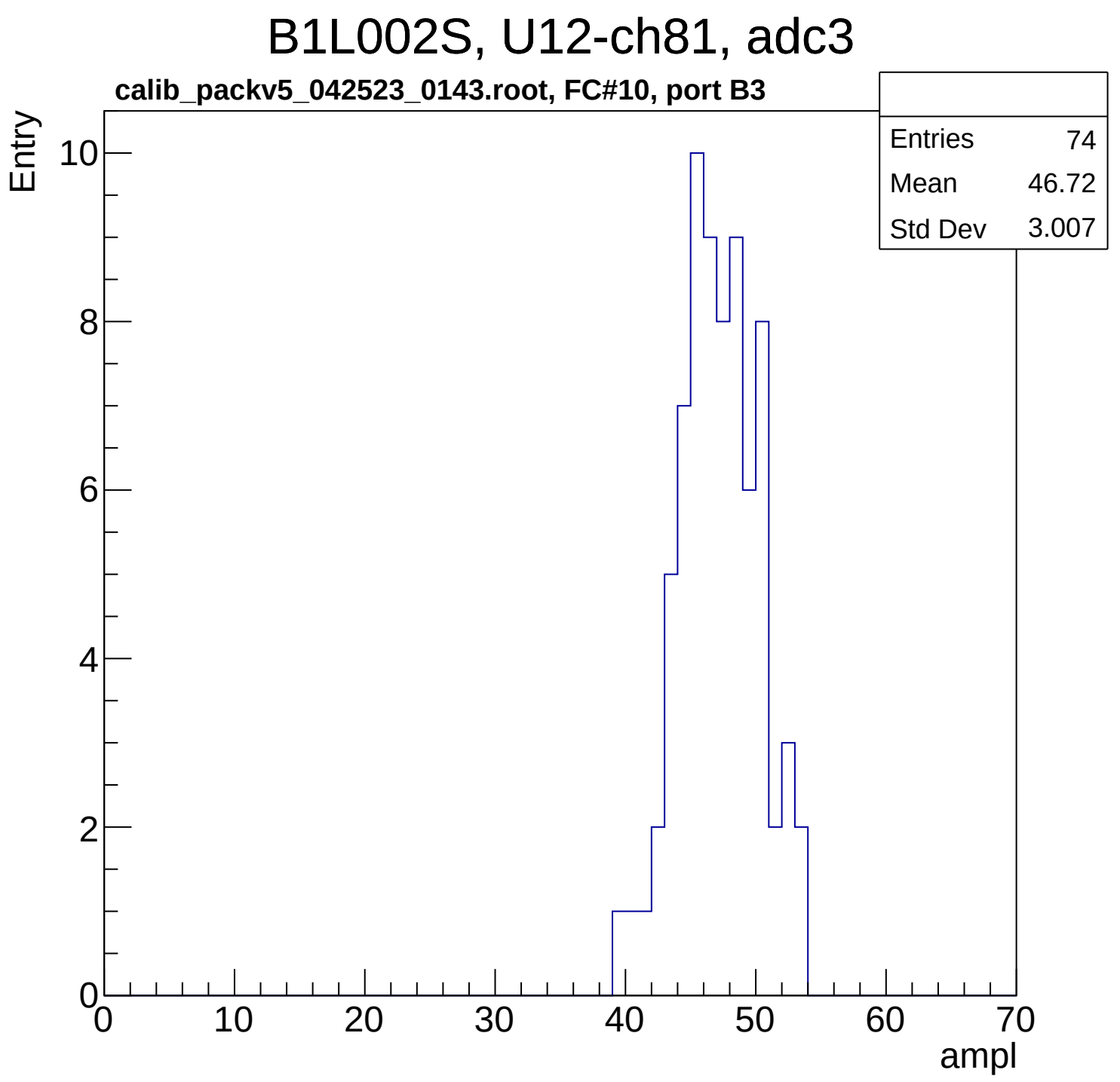
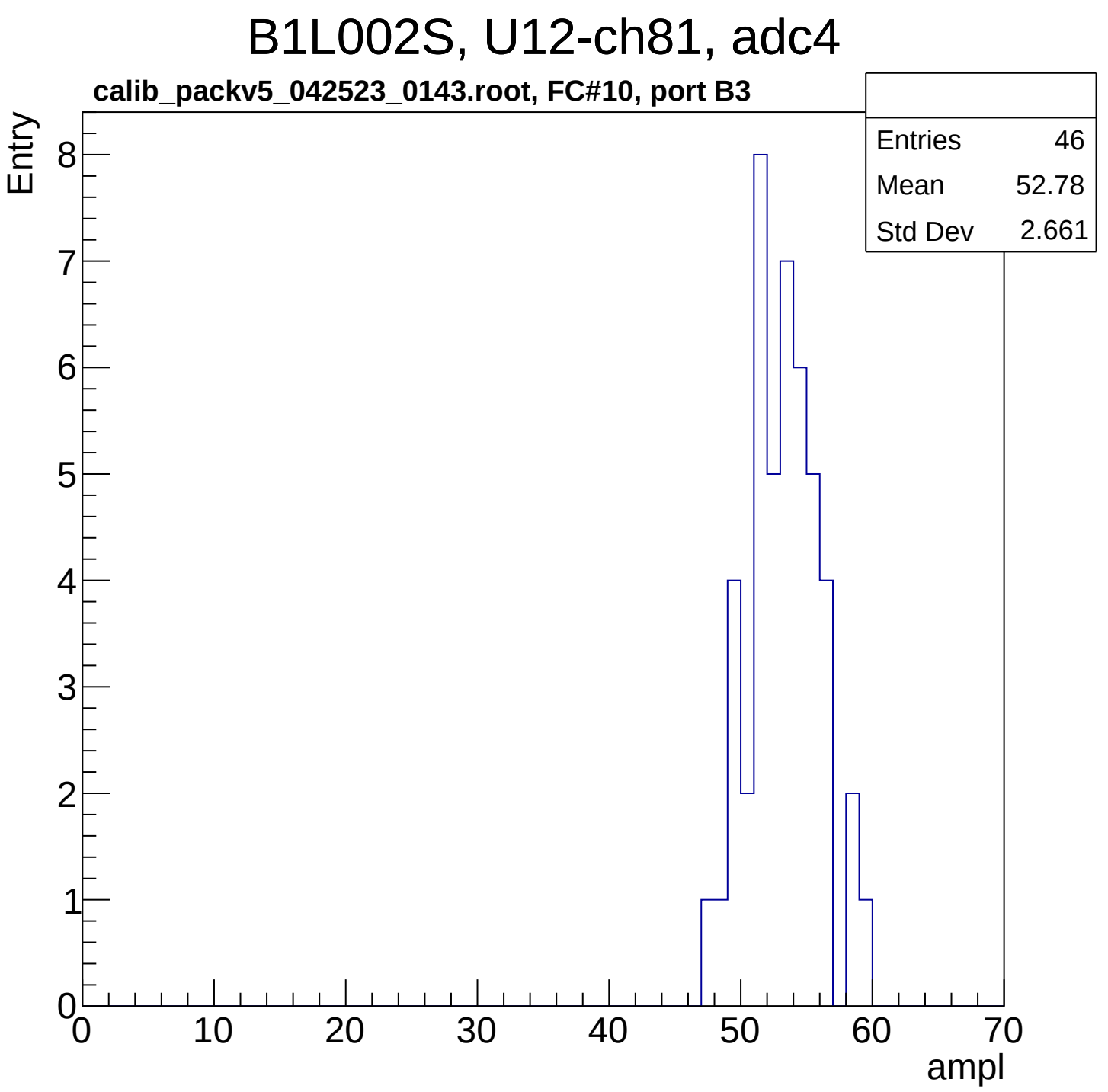

B1L002S, U12-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.72
Std Dev 3.007
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 52.78
Std Dev 2.661
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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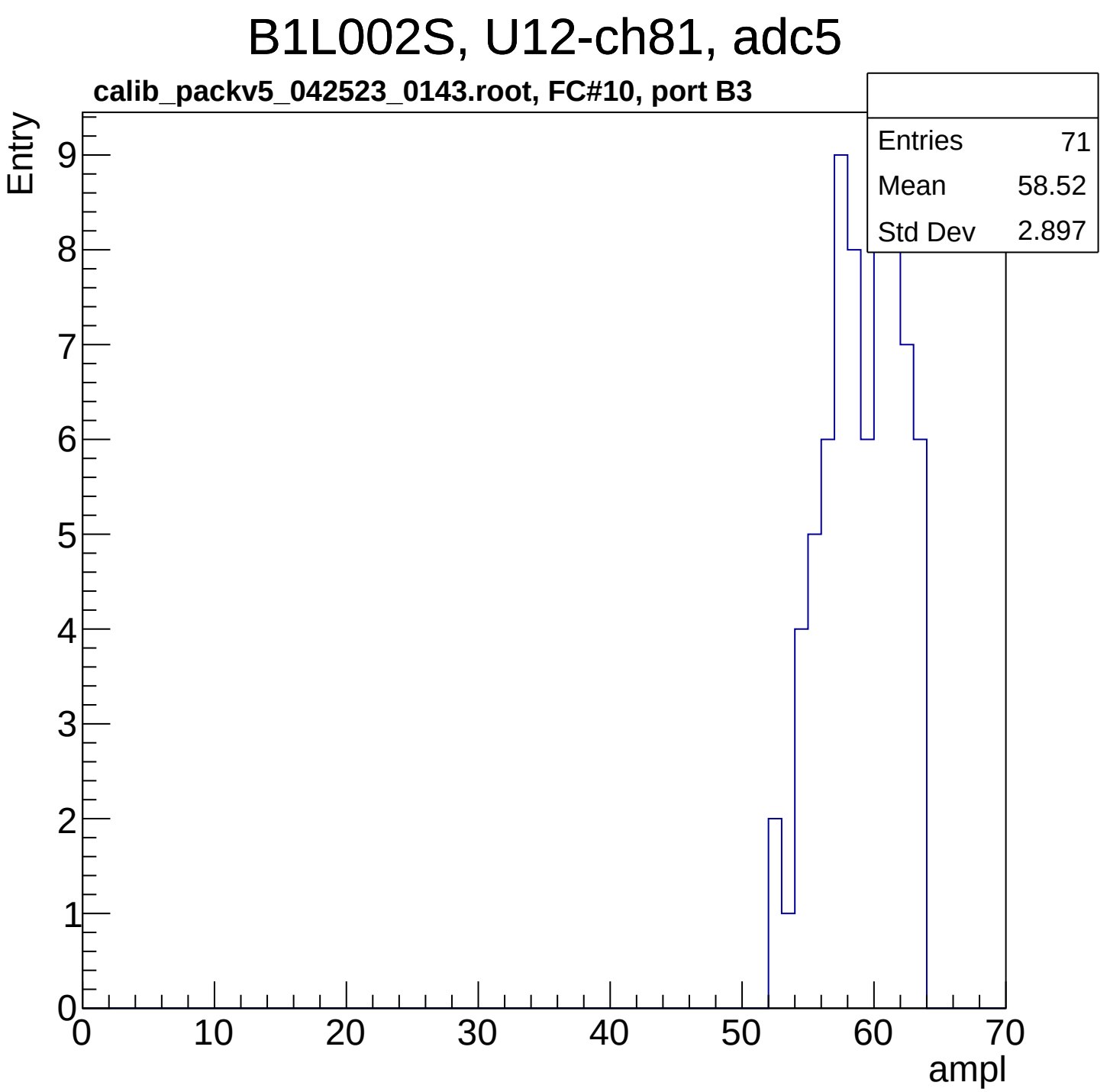
B1L002S, U12-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 58.52
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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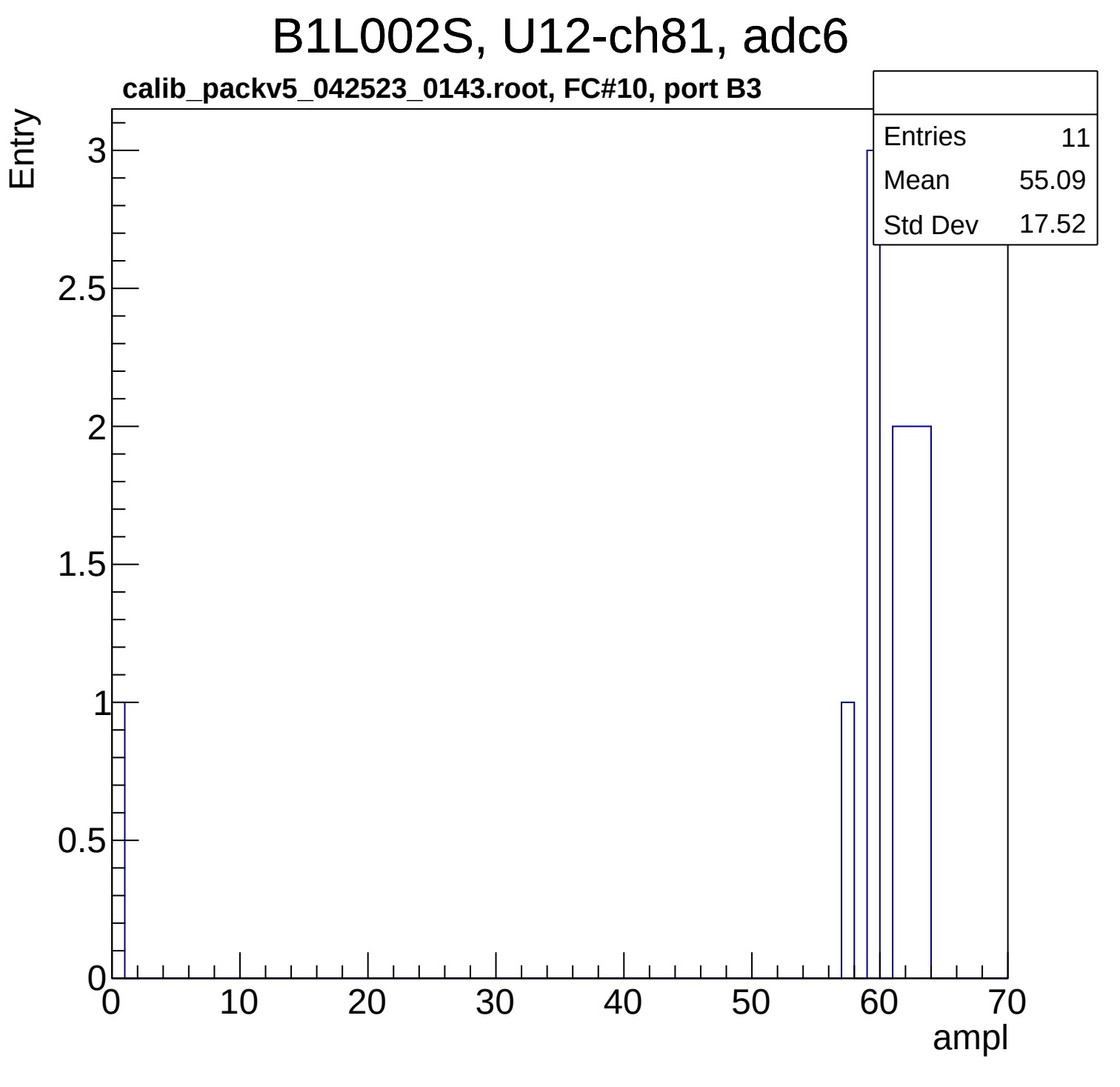
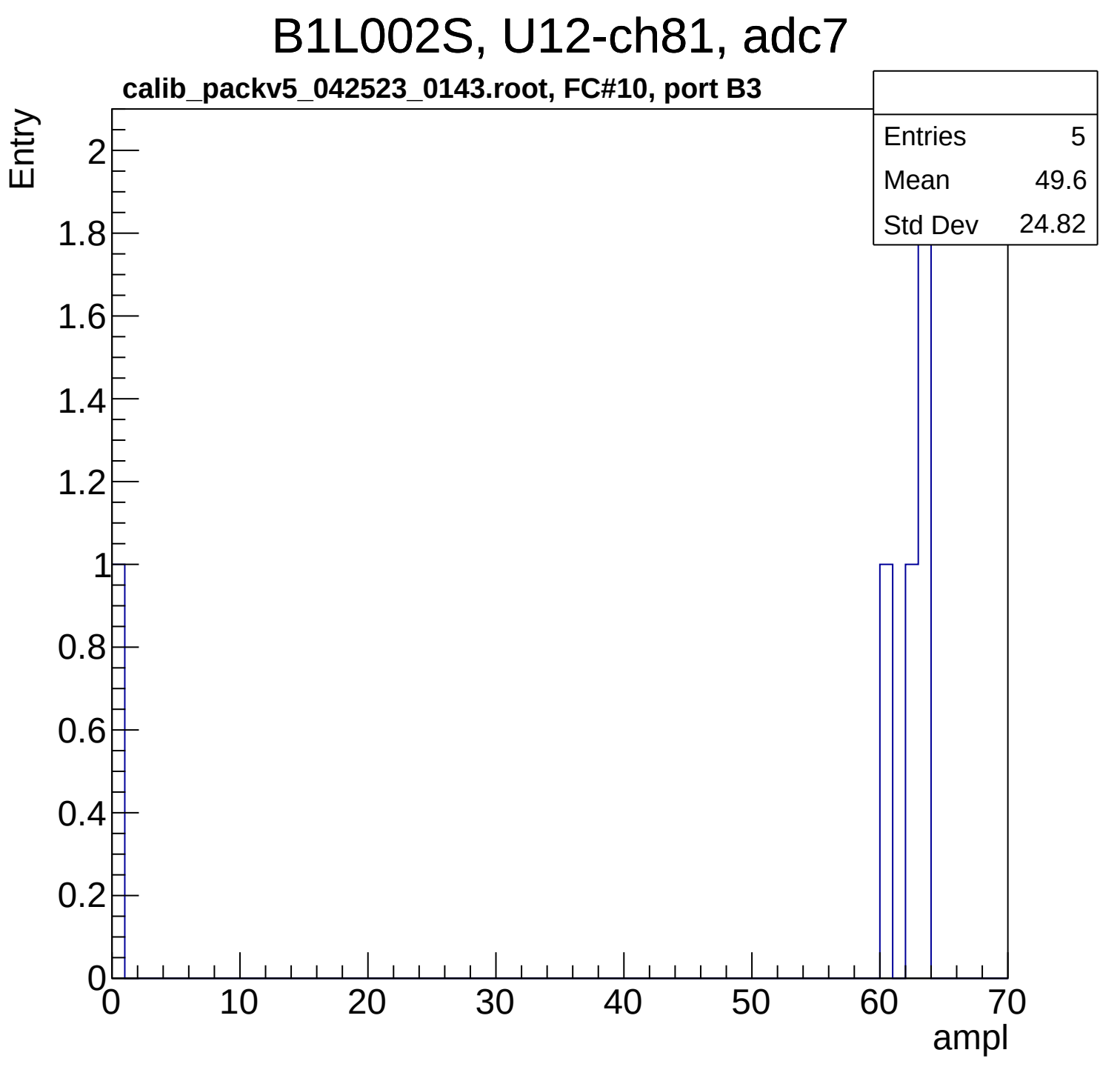

B1L002S, U12-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 55.09
Std Dev 17.52
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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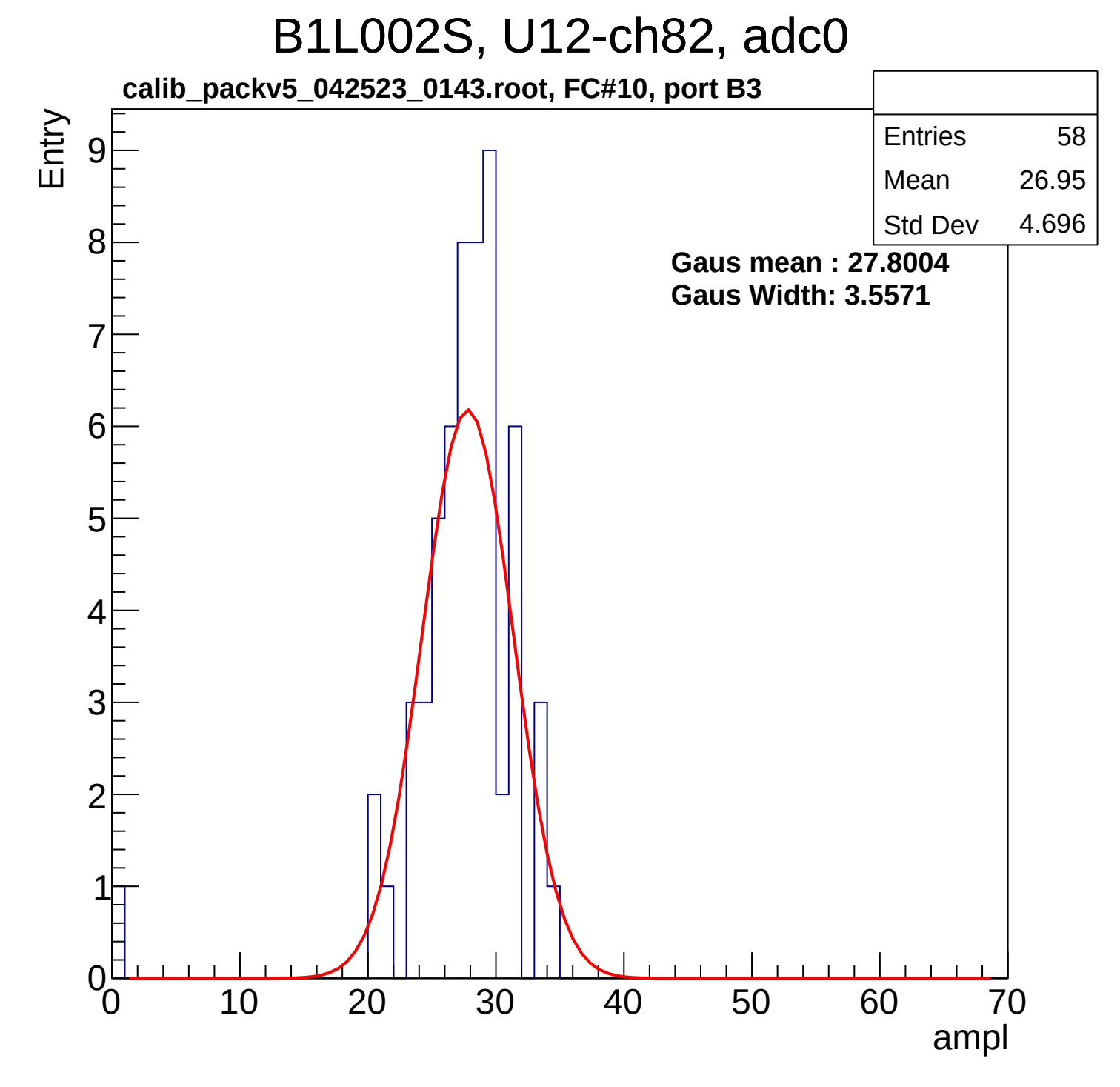
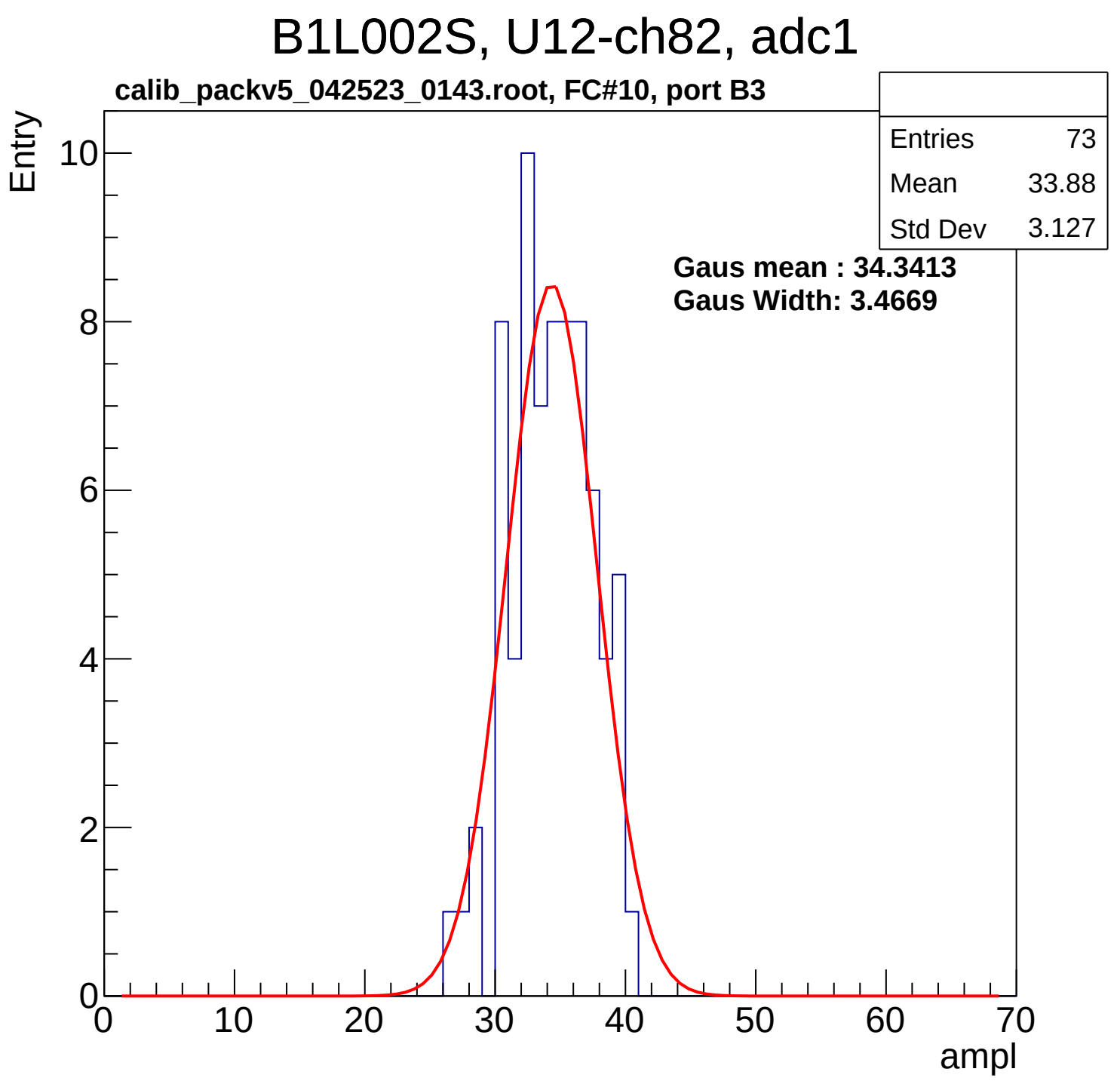
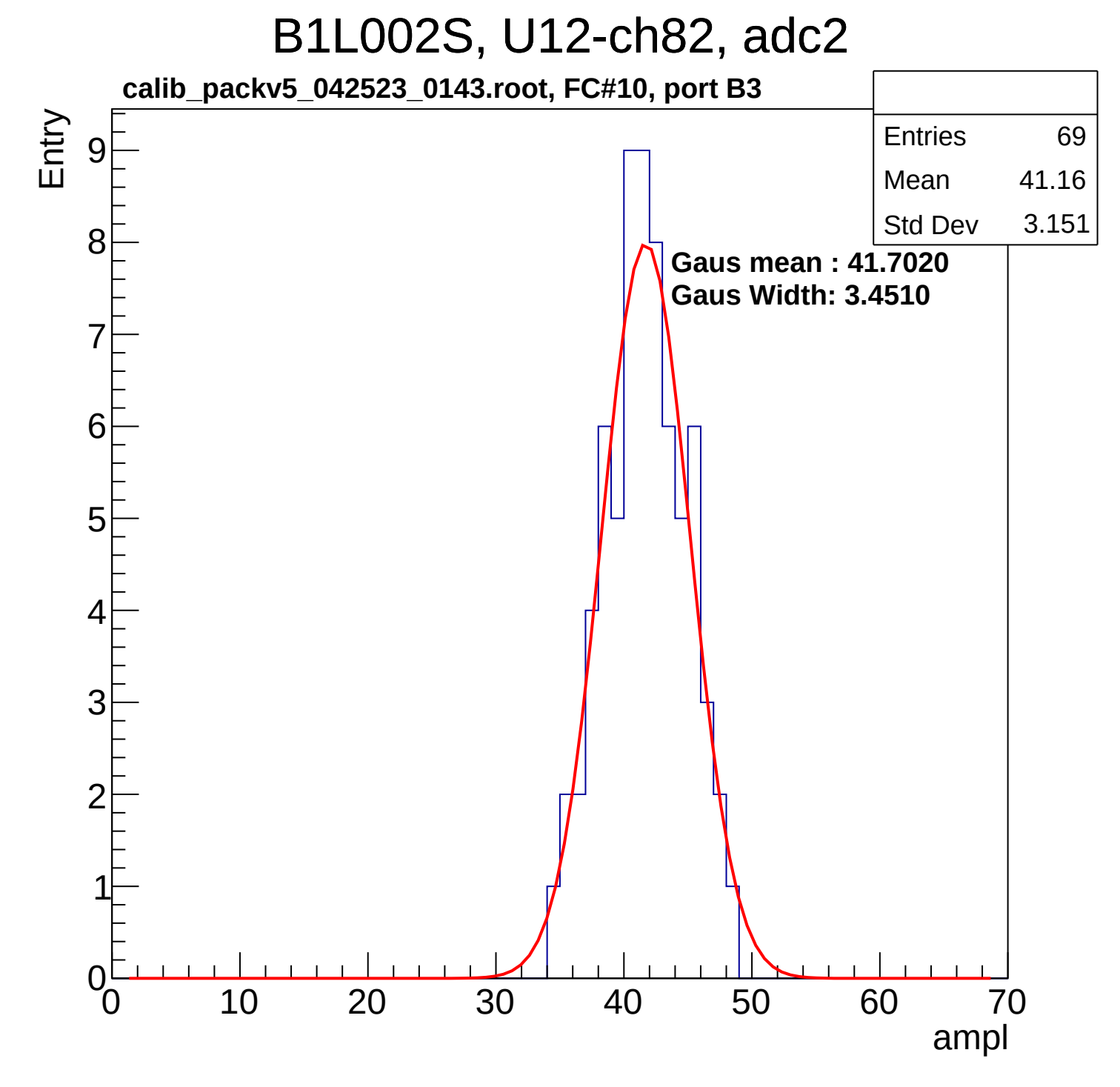
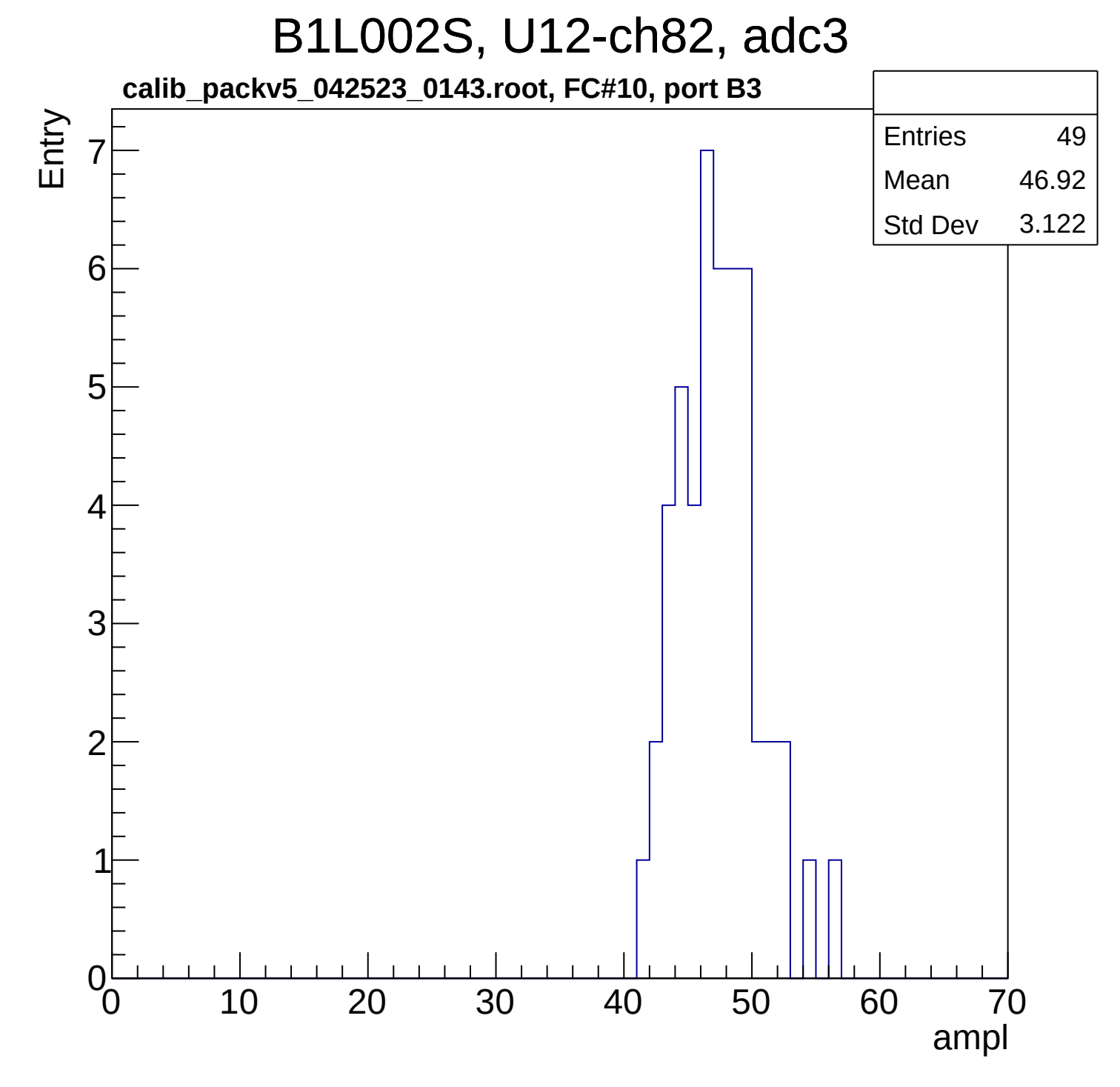

B1L002S, U12-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.95
Std Dev 4.696
Gaus mean : 27.8004
Gaus Width: 3.5571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 33.88
Std Dev 3.127
Gaus mean : 34.3413
Gaus Width: 3.4669
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.16
Std Dev 3.151
Gaus mean : 41.7020
Gaus Width: 3.4510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 46.92
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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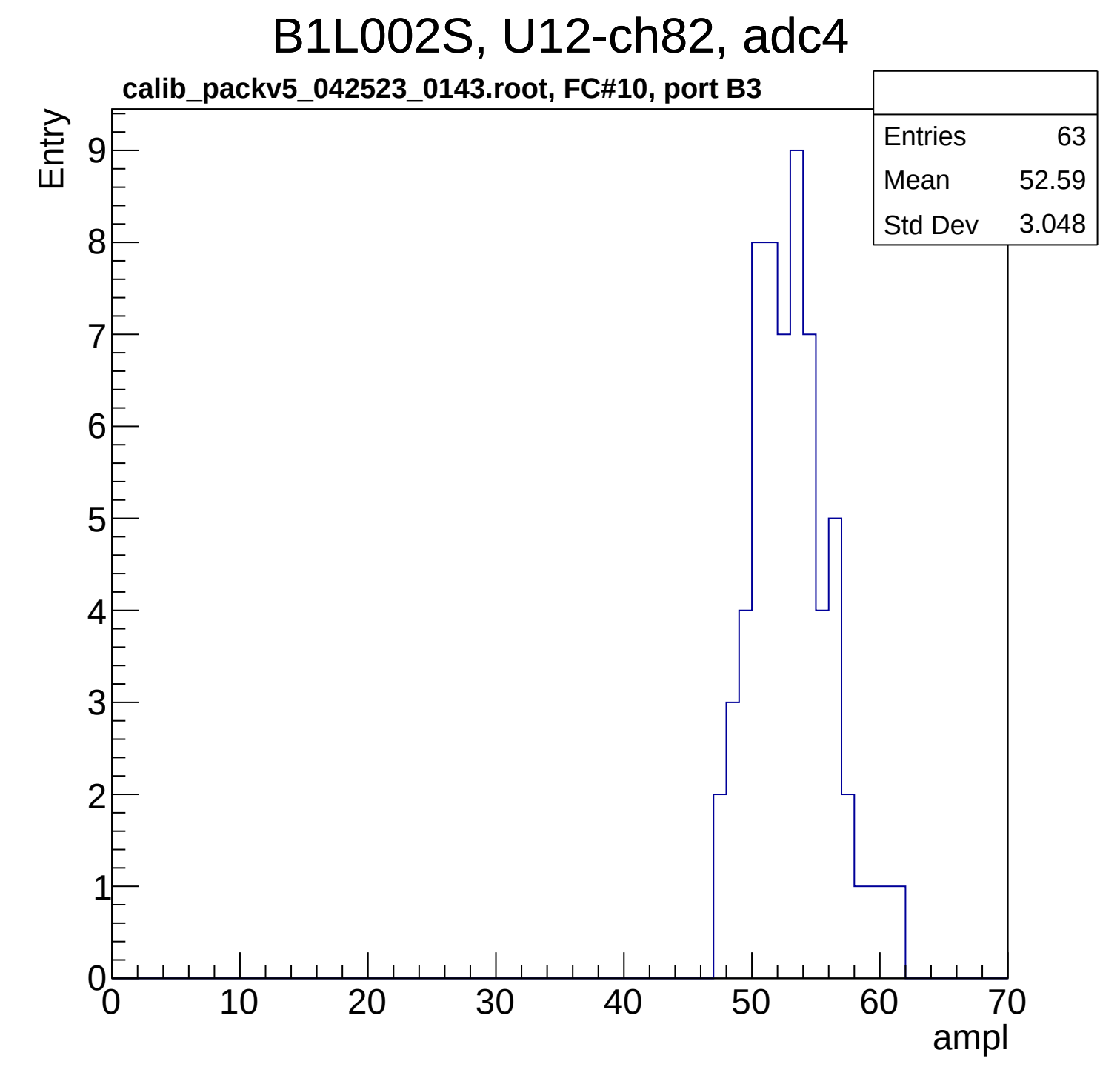
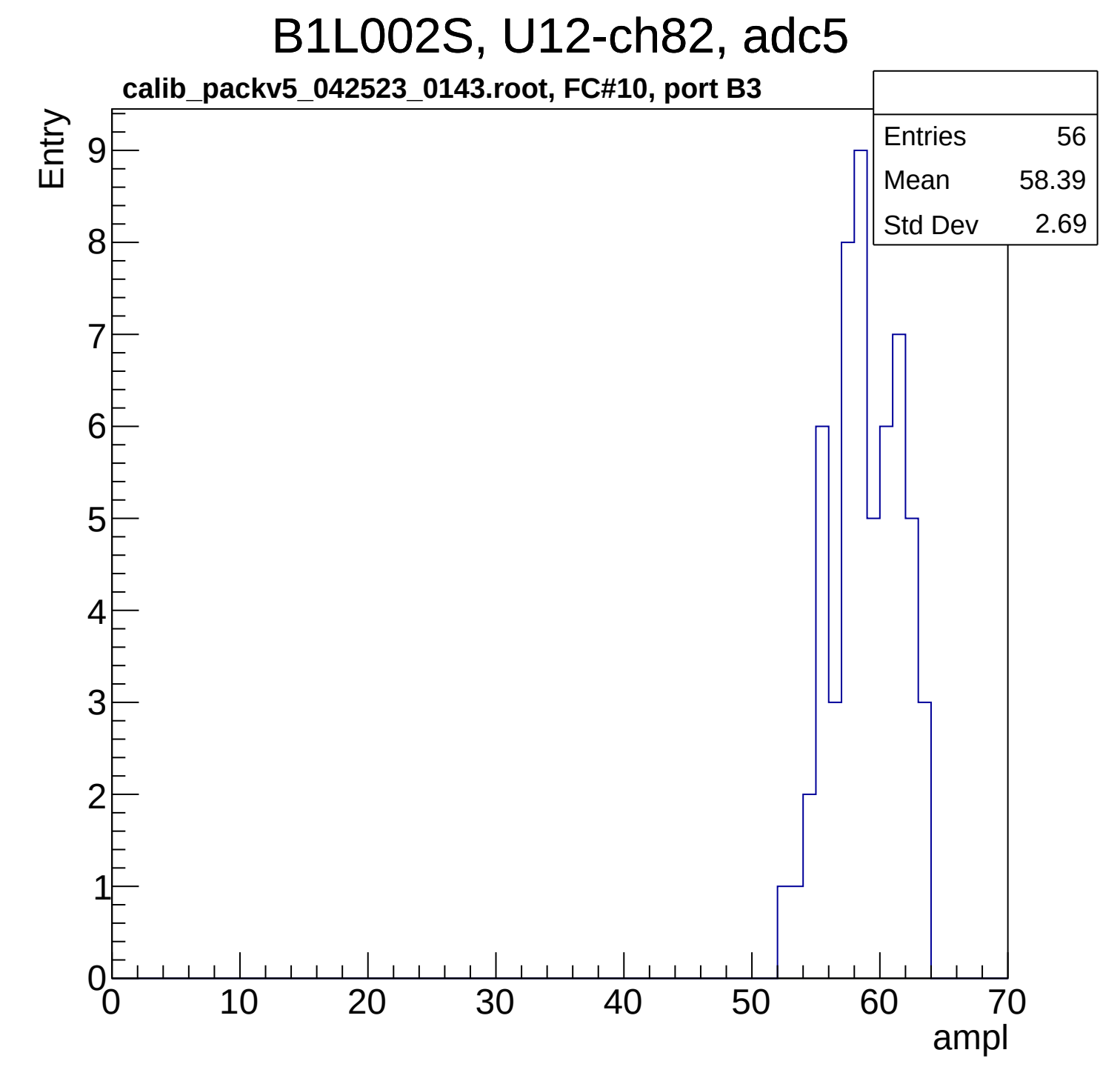
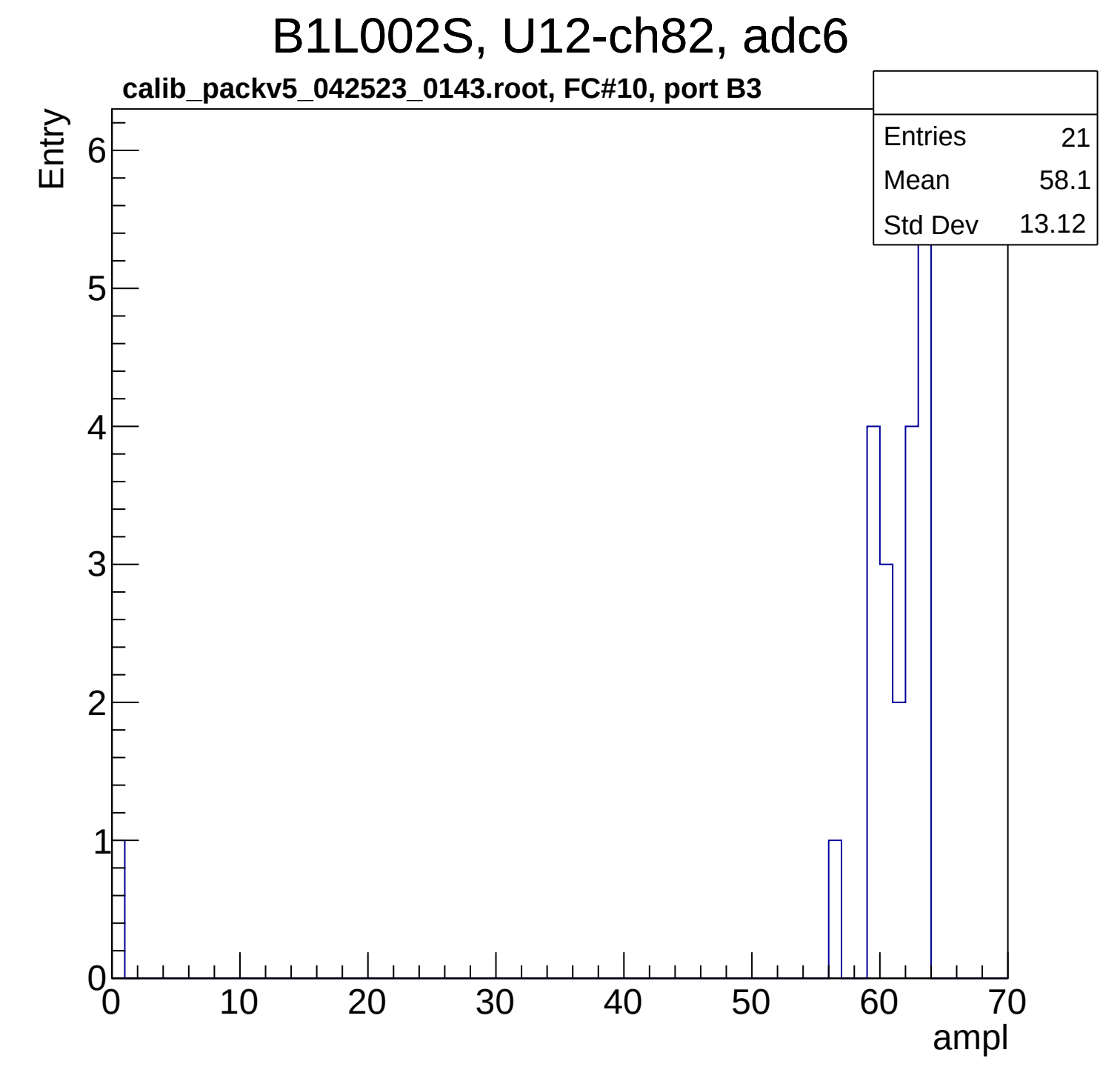
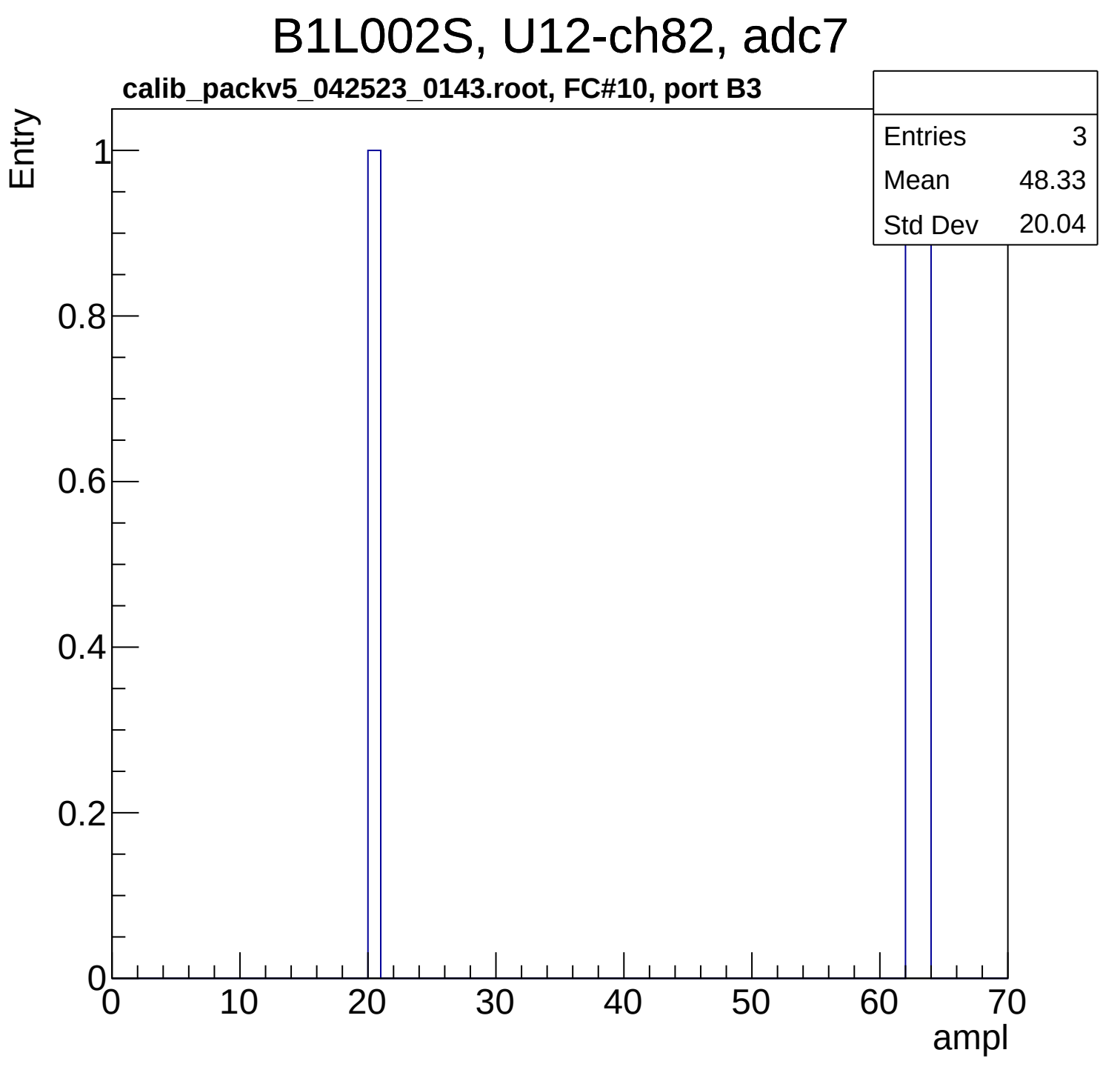
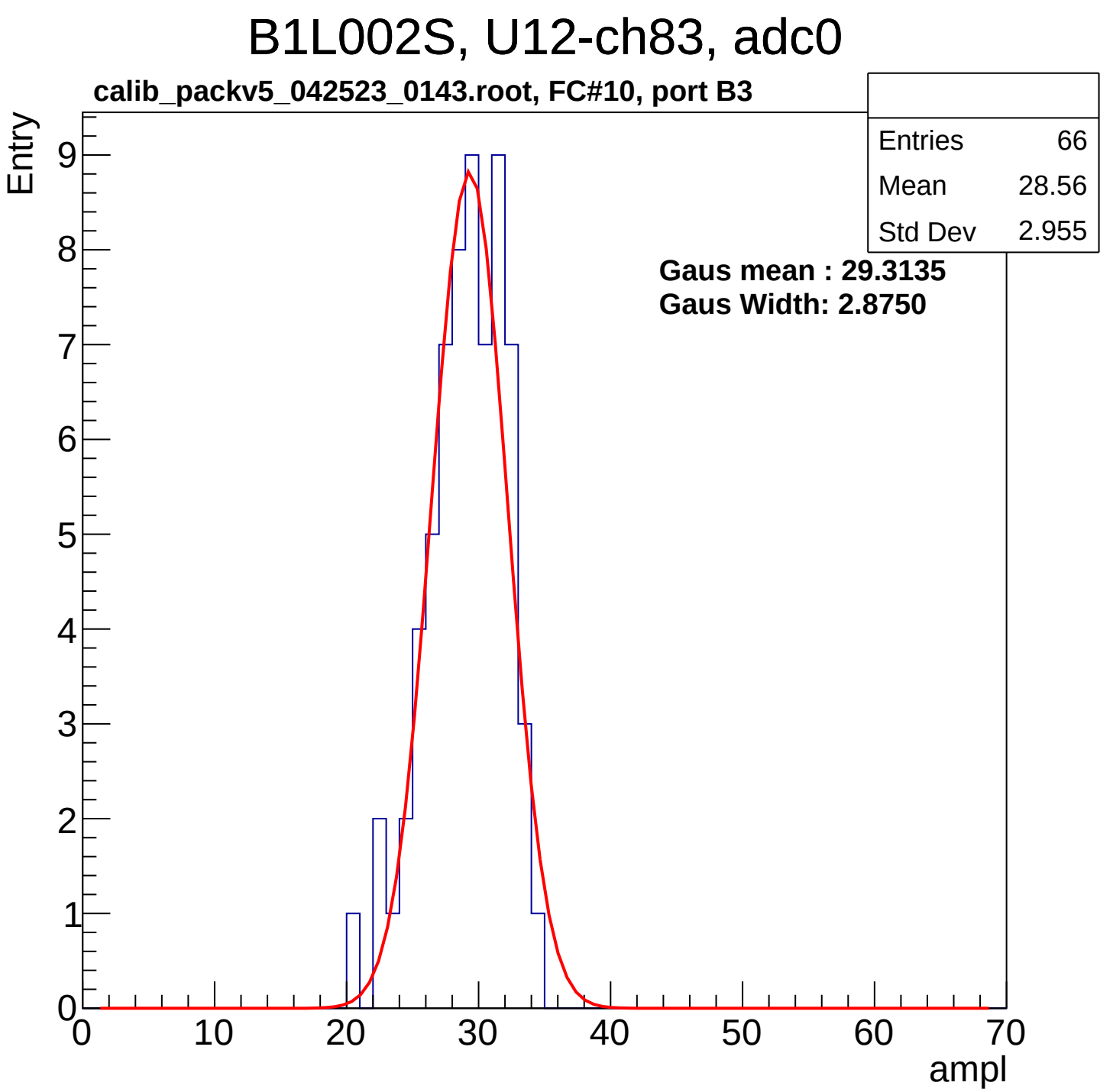
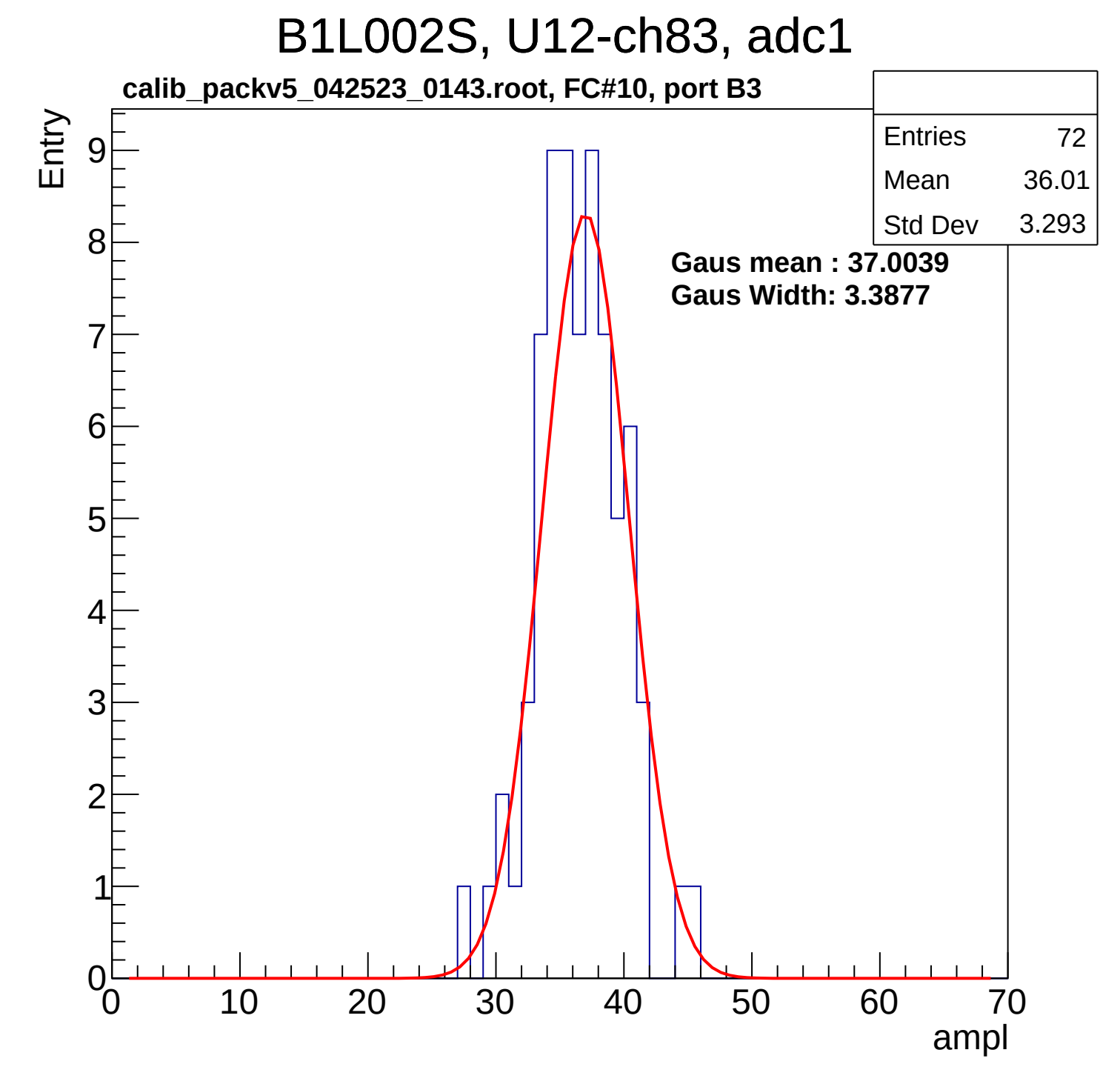

B1L002S, U12-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.59
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

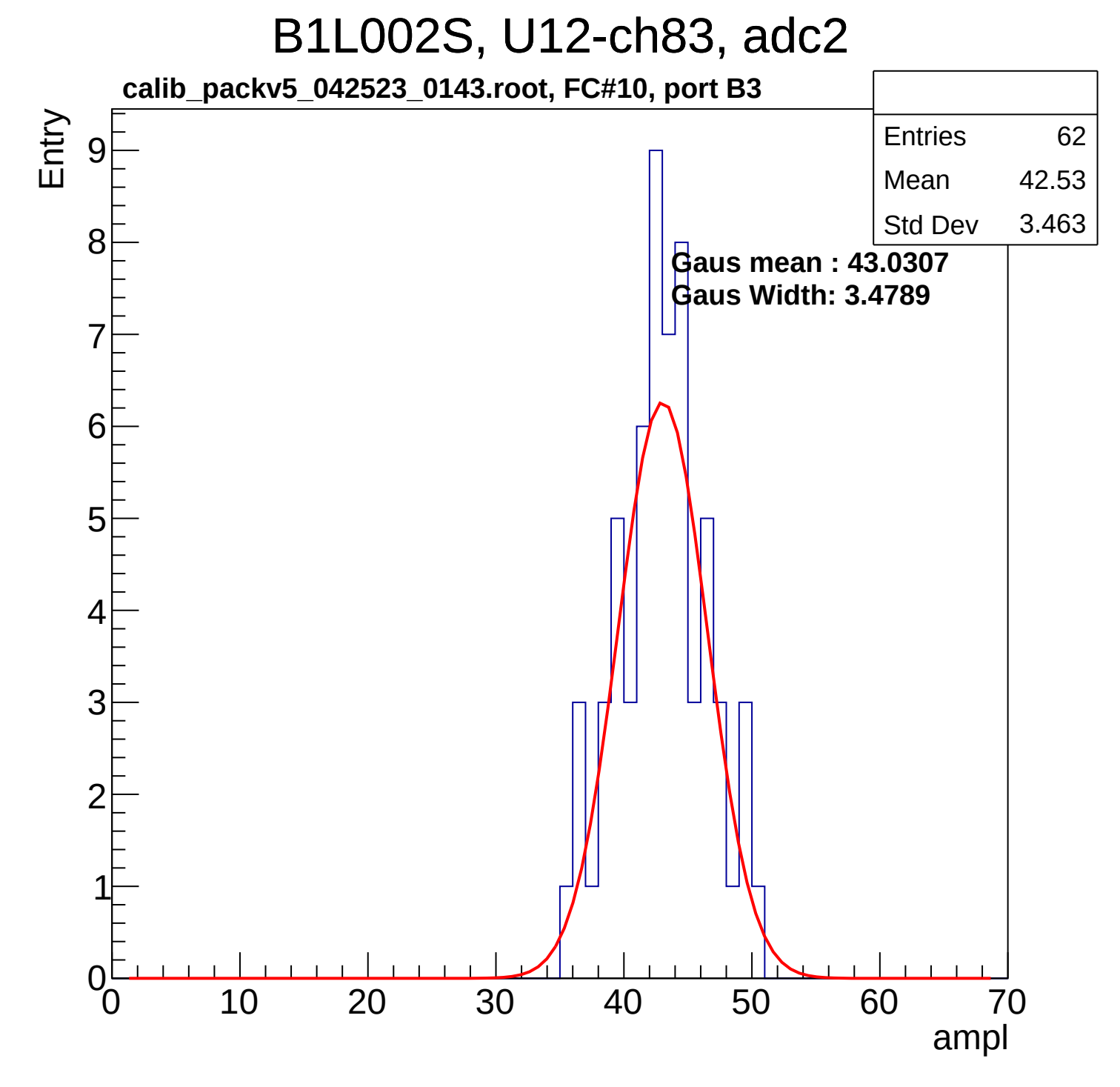

B1L002S, U12-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.39
Std Dev 2.69
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.1
Std Dev 13.12
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 20.04
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U12-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.56
Std Dev 2.955
Gaus mean : 29.3135
Gaus Width: 2.8750
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 36.01
Std Dev 3.293
Gaus mean : 37.0039
Gaus Width: 3.3877
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 42.53
Std Dev 3.463
Gaus mean : 43.0307
Gaus Width: 3.4789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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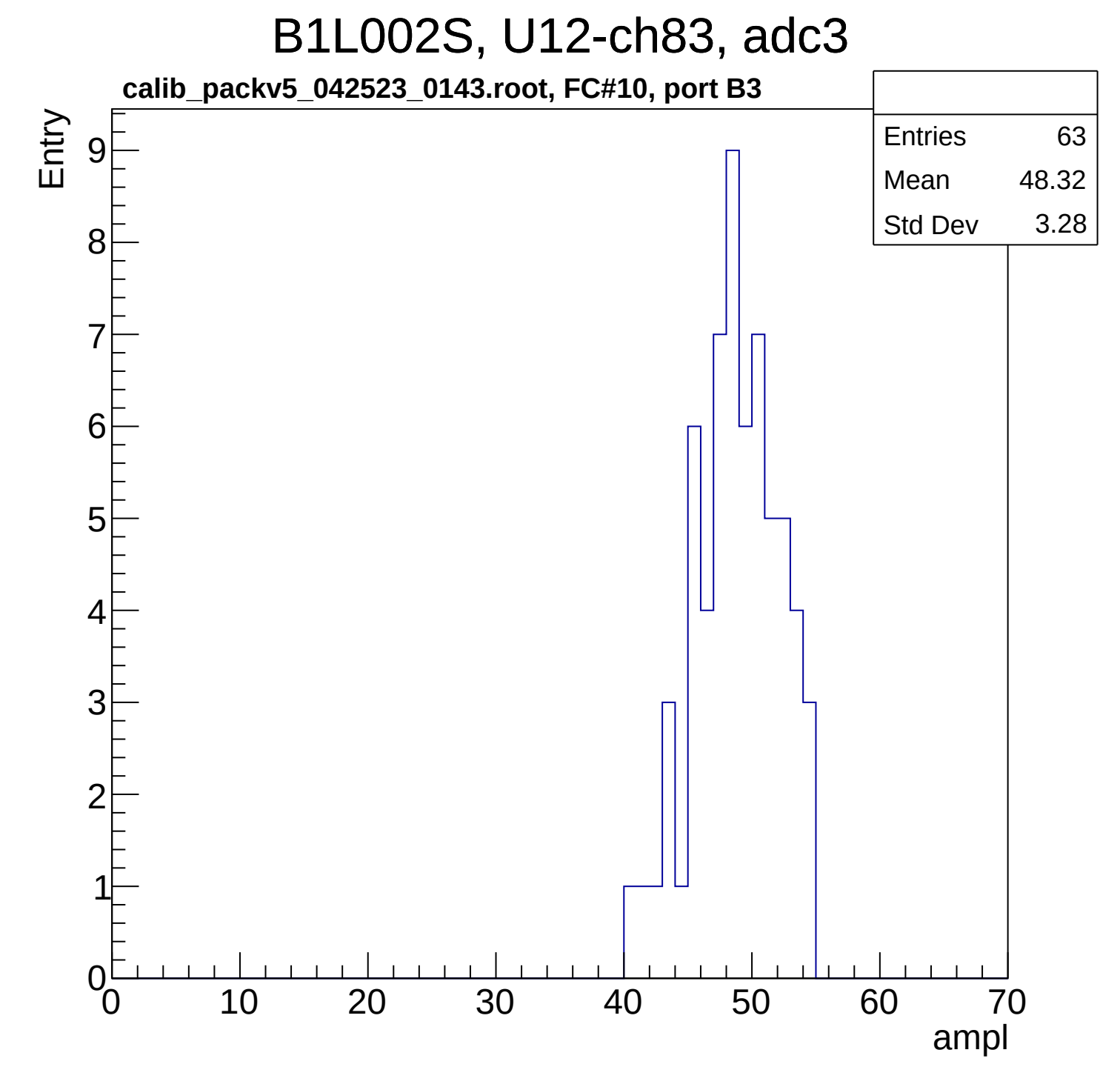
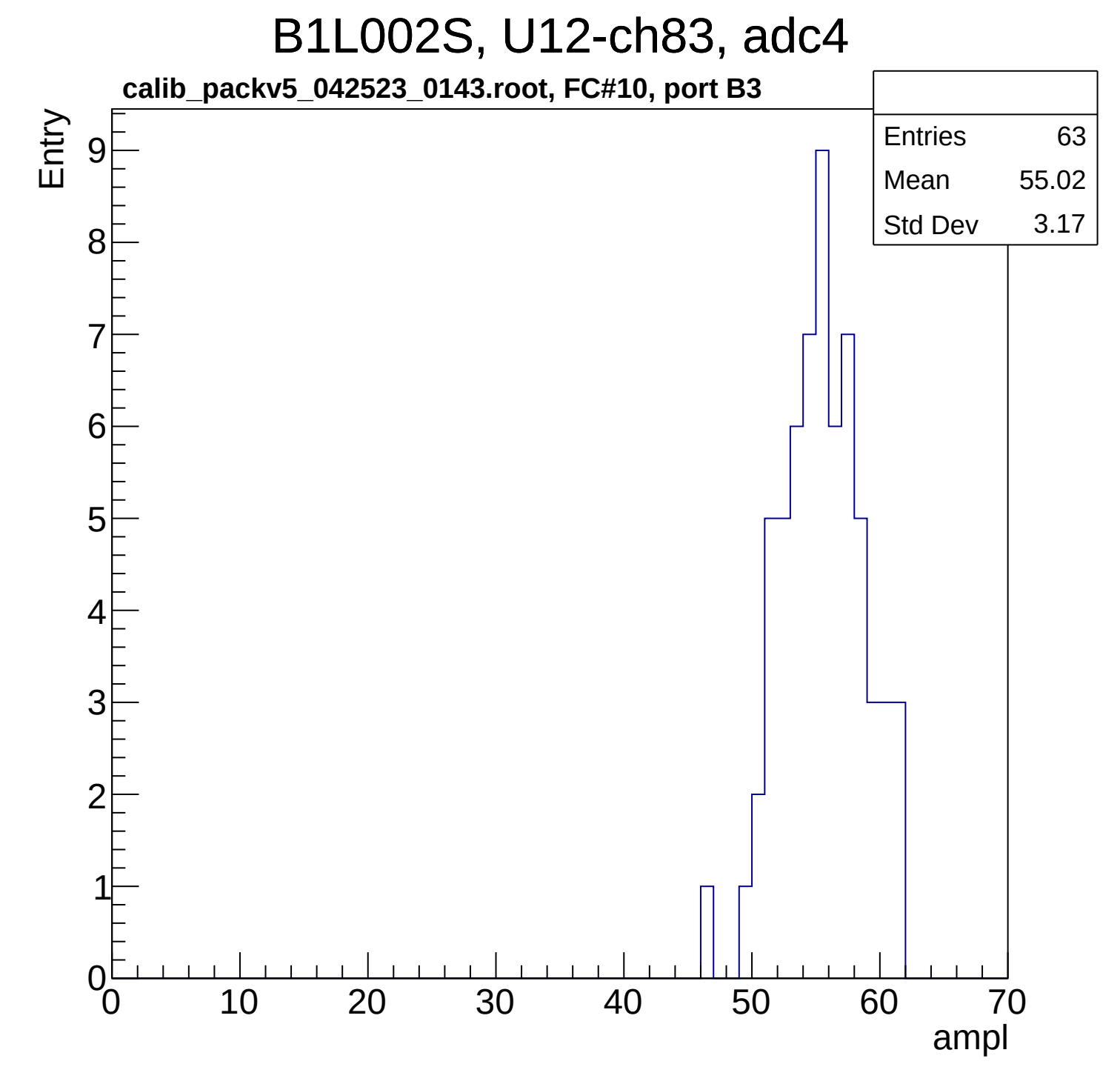

B1L002S, U12-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 48.32
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 55.02
Std Dev 3.17
Entry
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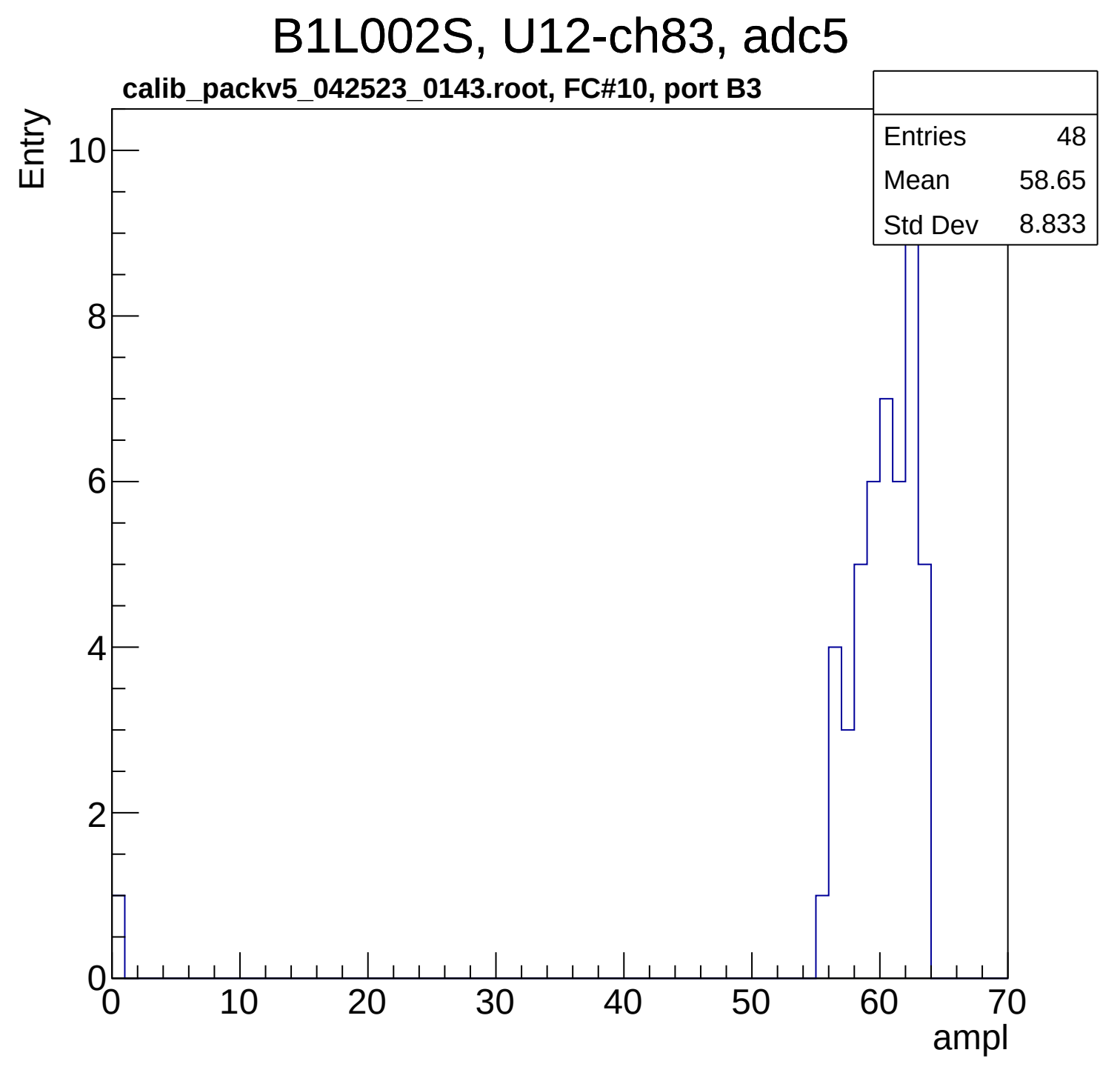
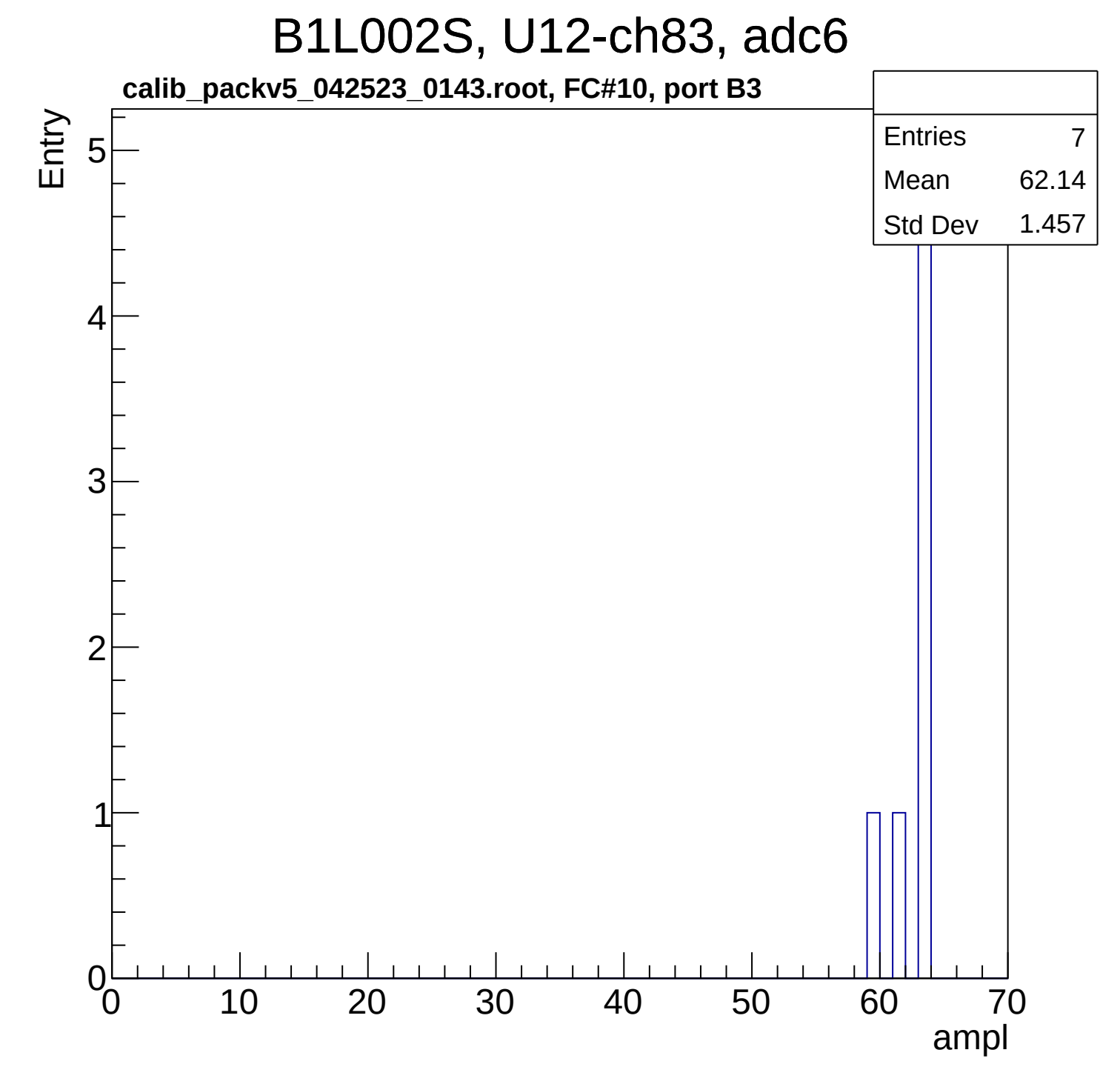
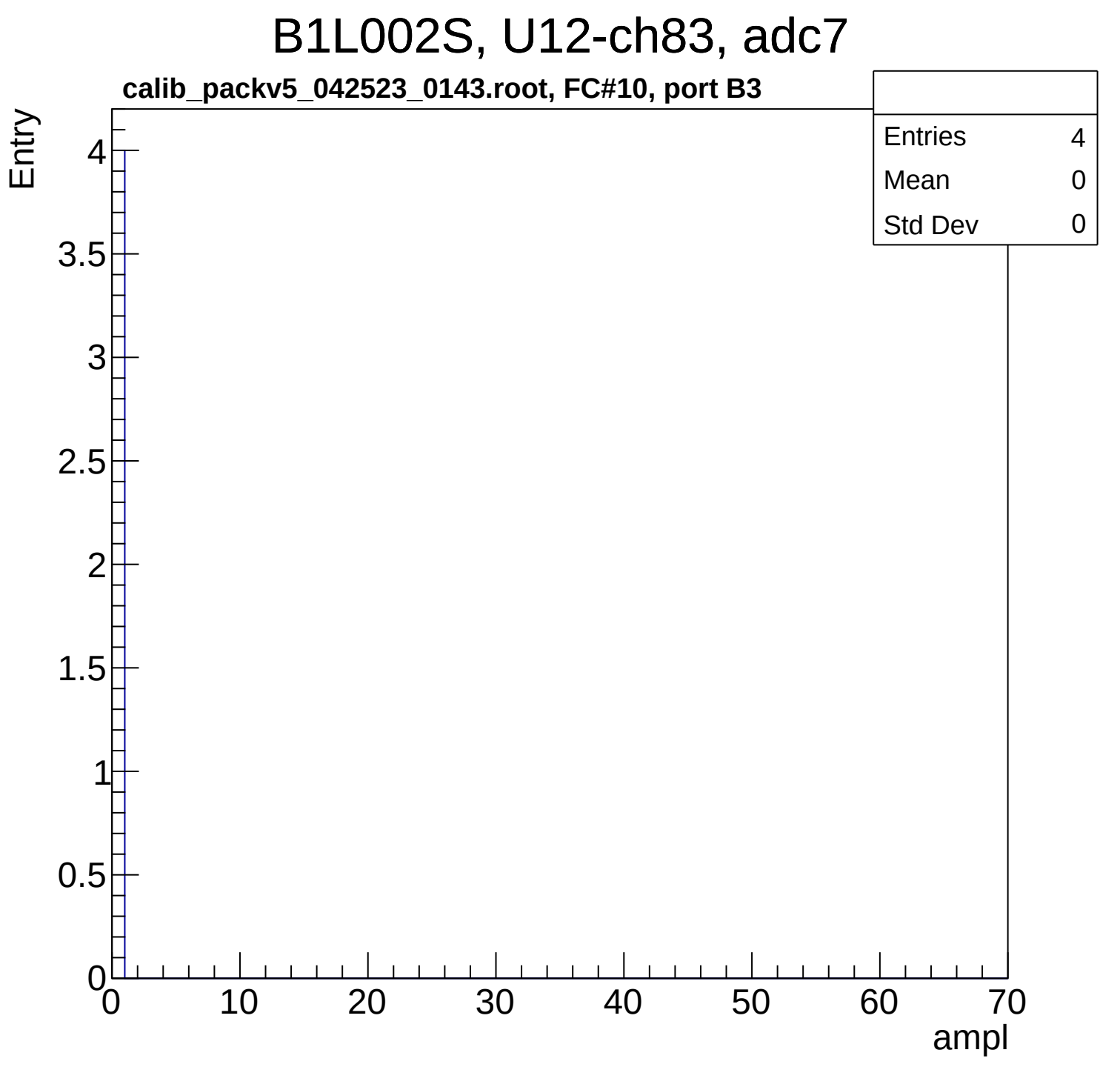
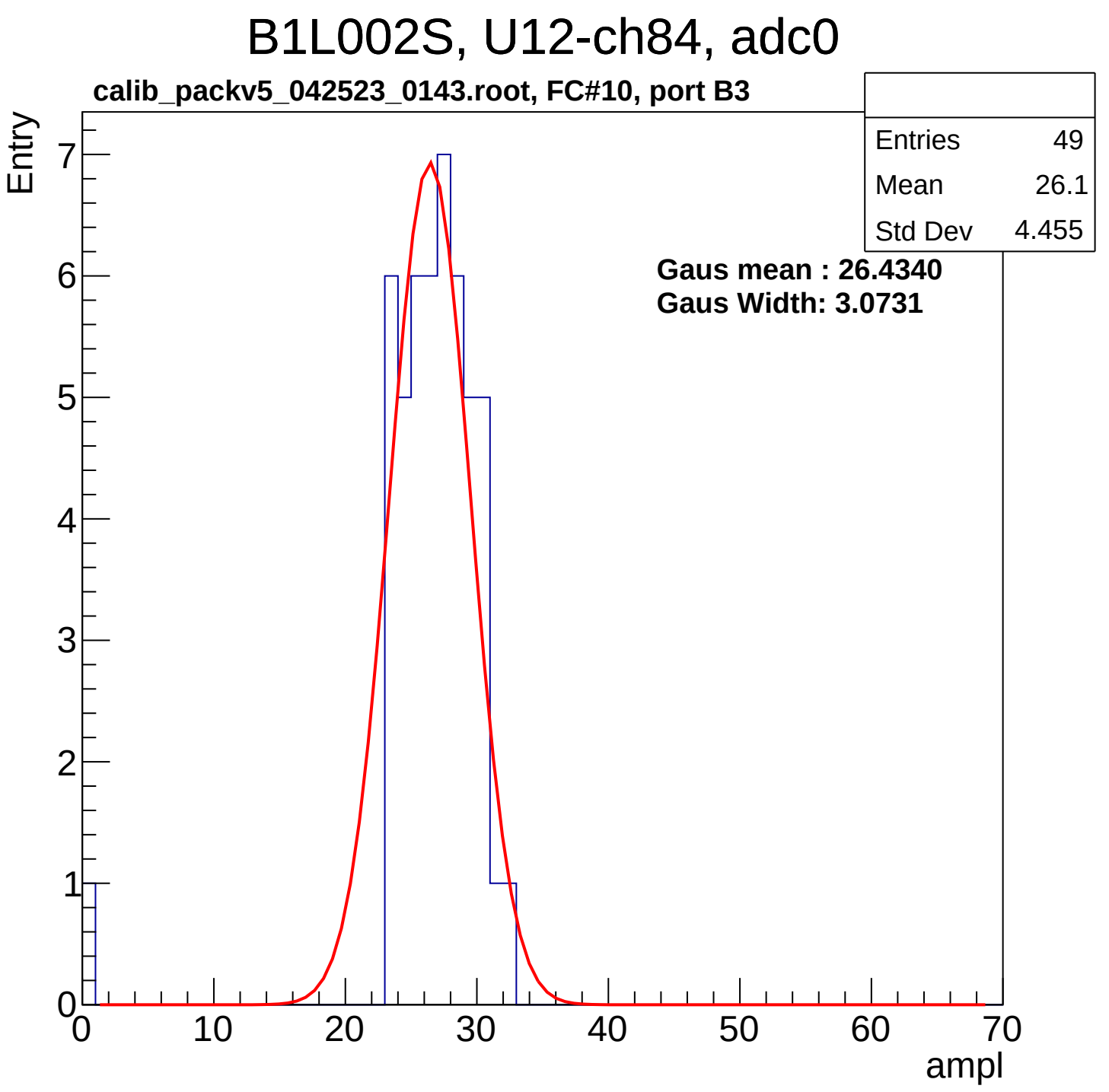
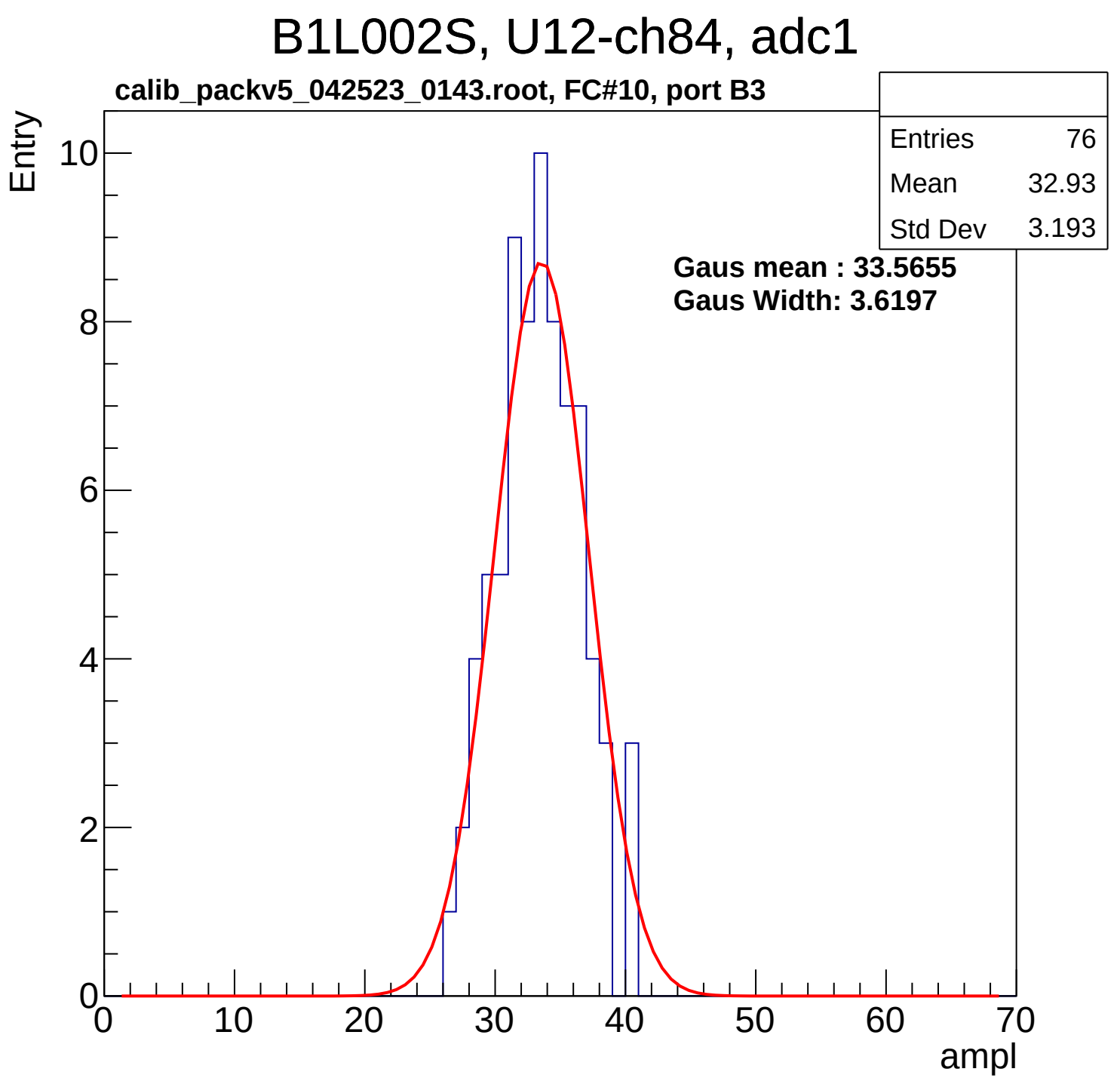
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

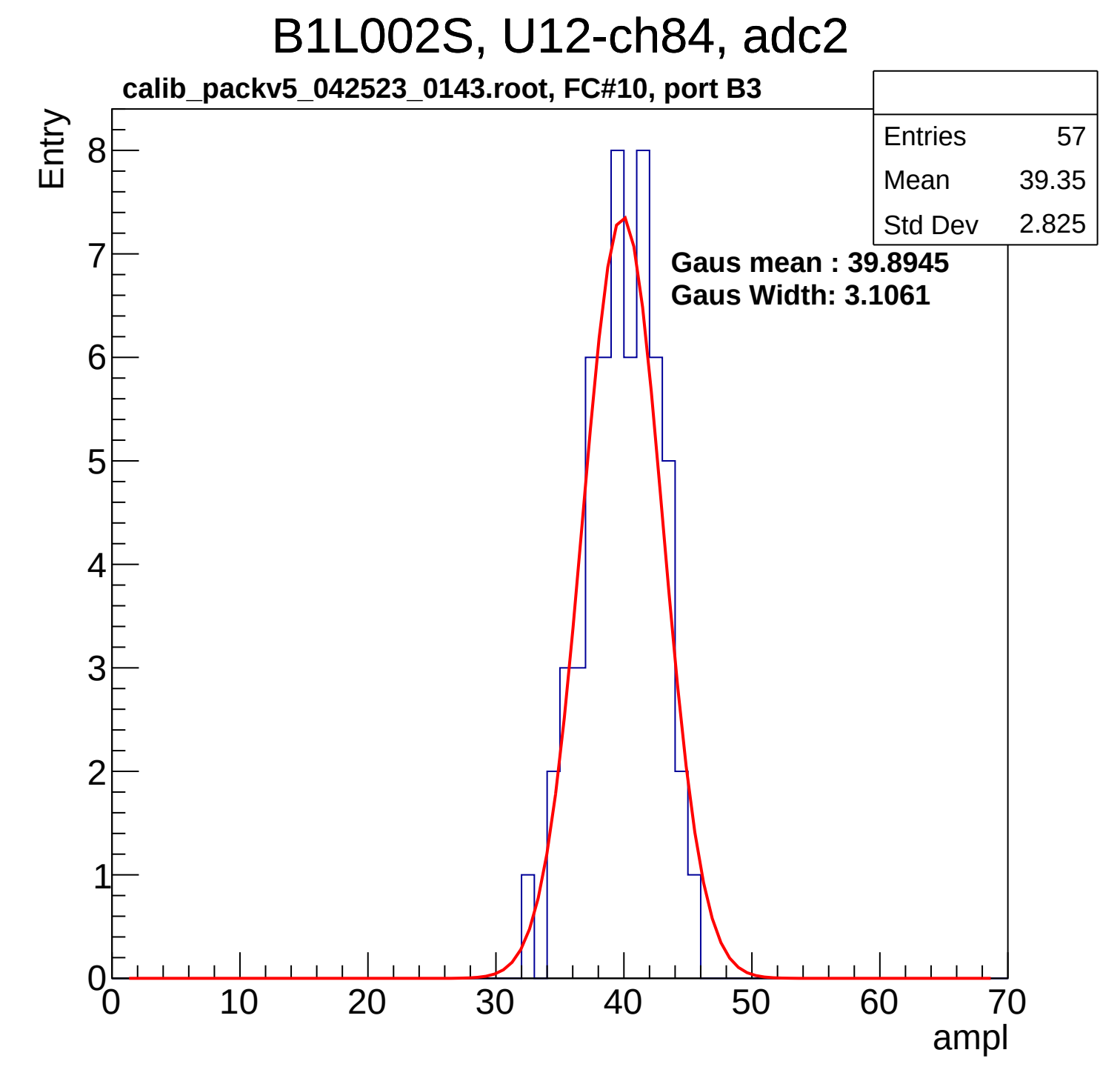

B1L002S, U12-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.65
Std Dev 8.833
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

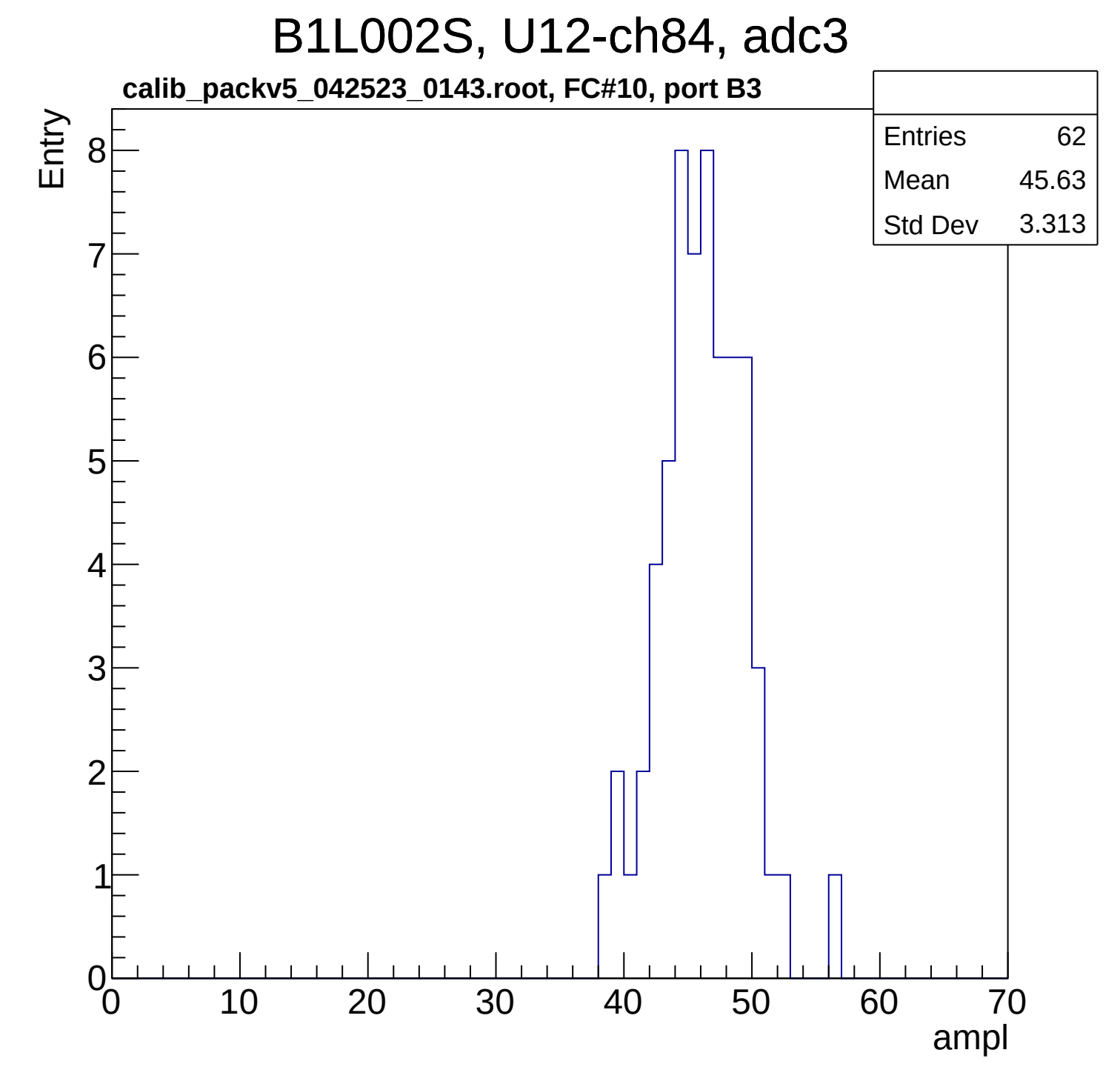

B1L002S, U12-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.14
Std Dev 1.457
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L002S, U12-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 26.1
Std Dev 4.455
Gaus mean : 26.4340
Gaus Width: 3.0731
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 32.93
Std Dev 3.193
Gaus mean : 33.5655
Gaus Width: 3.6197
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 39.35
Std Dev 2.825
Gaus mean : 39.8945
Gaus Width: 3.1061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 45.63
Std Dev 3.313
Entry
ampl
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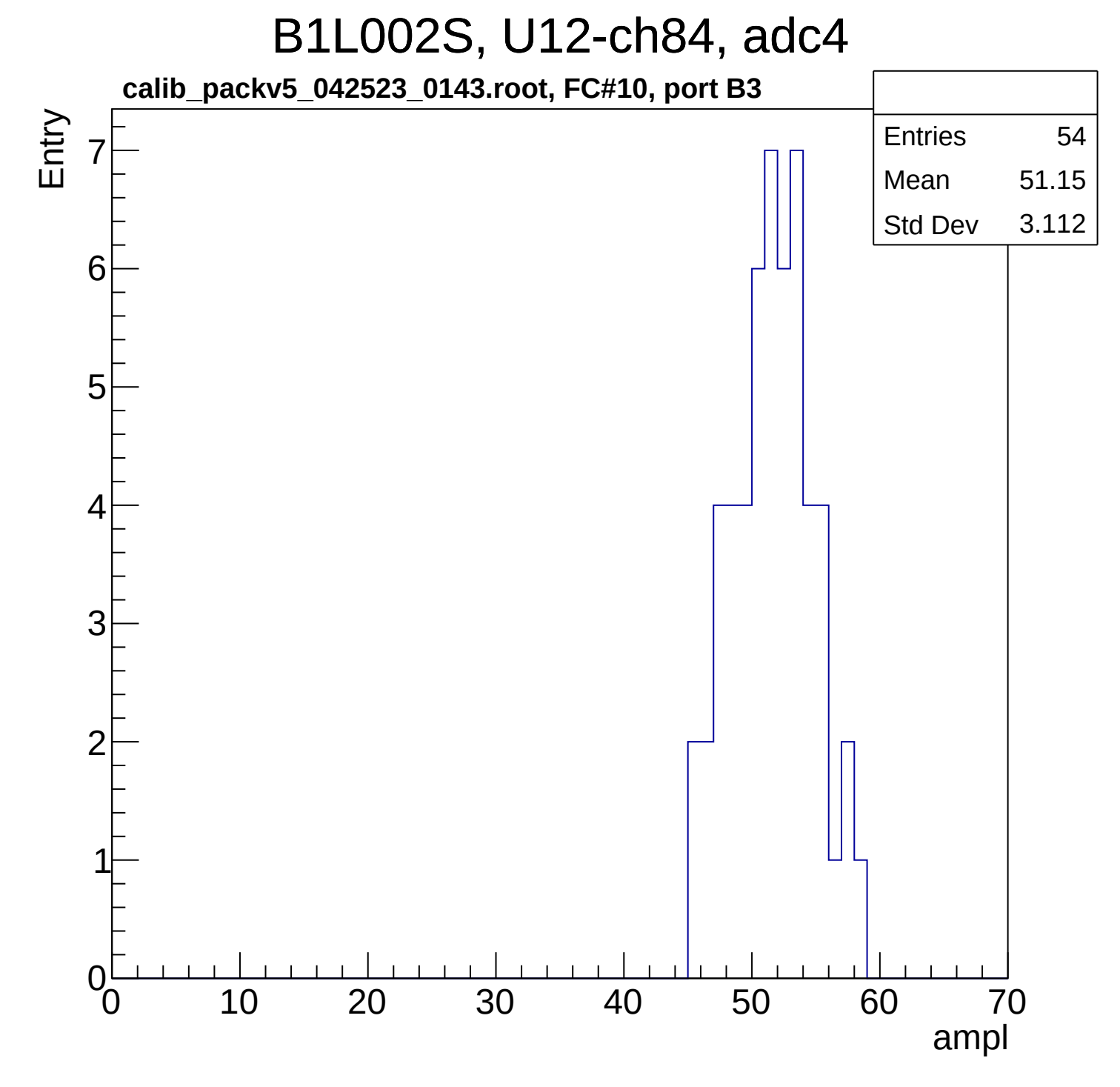
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.15
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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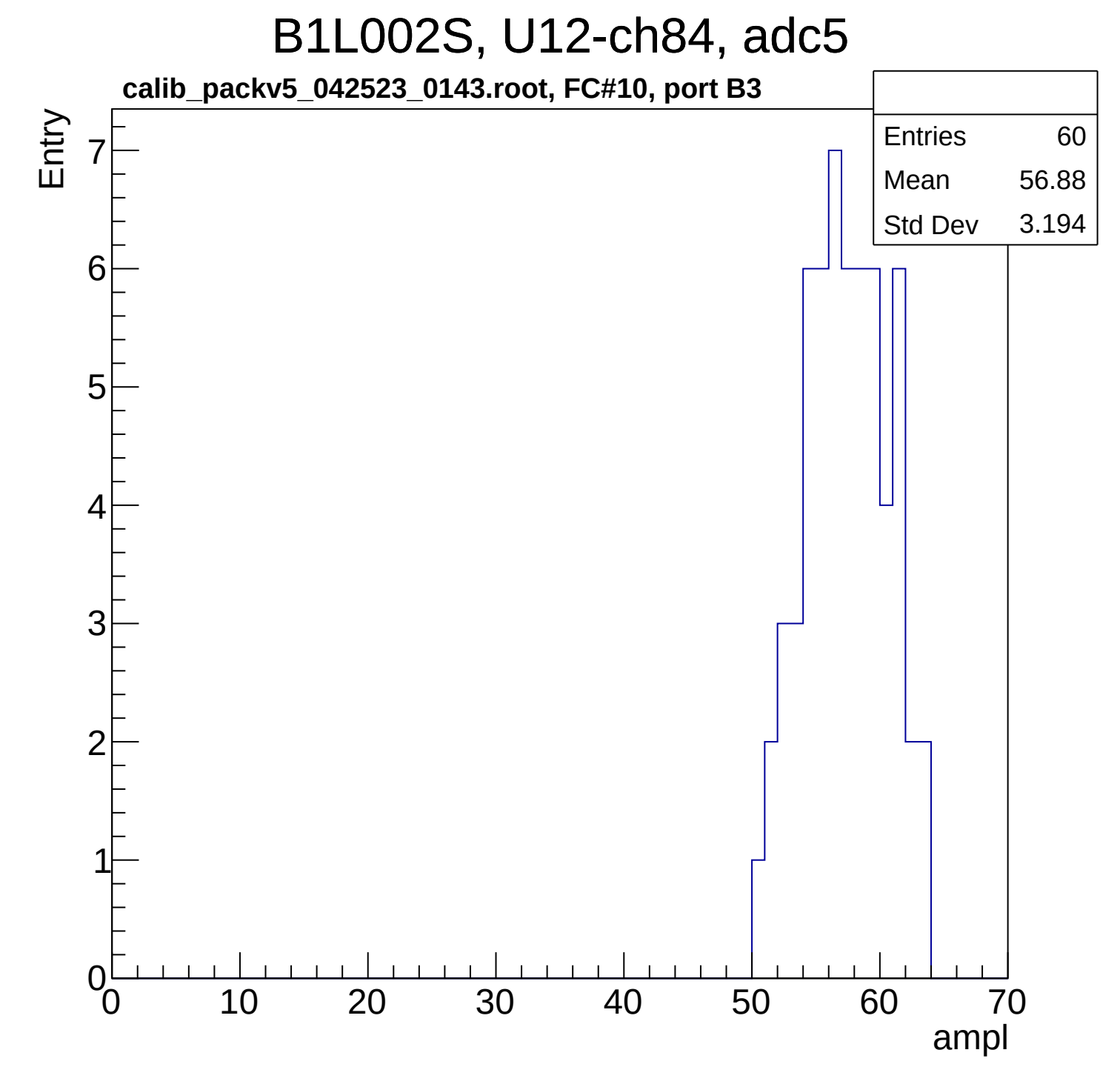
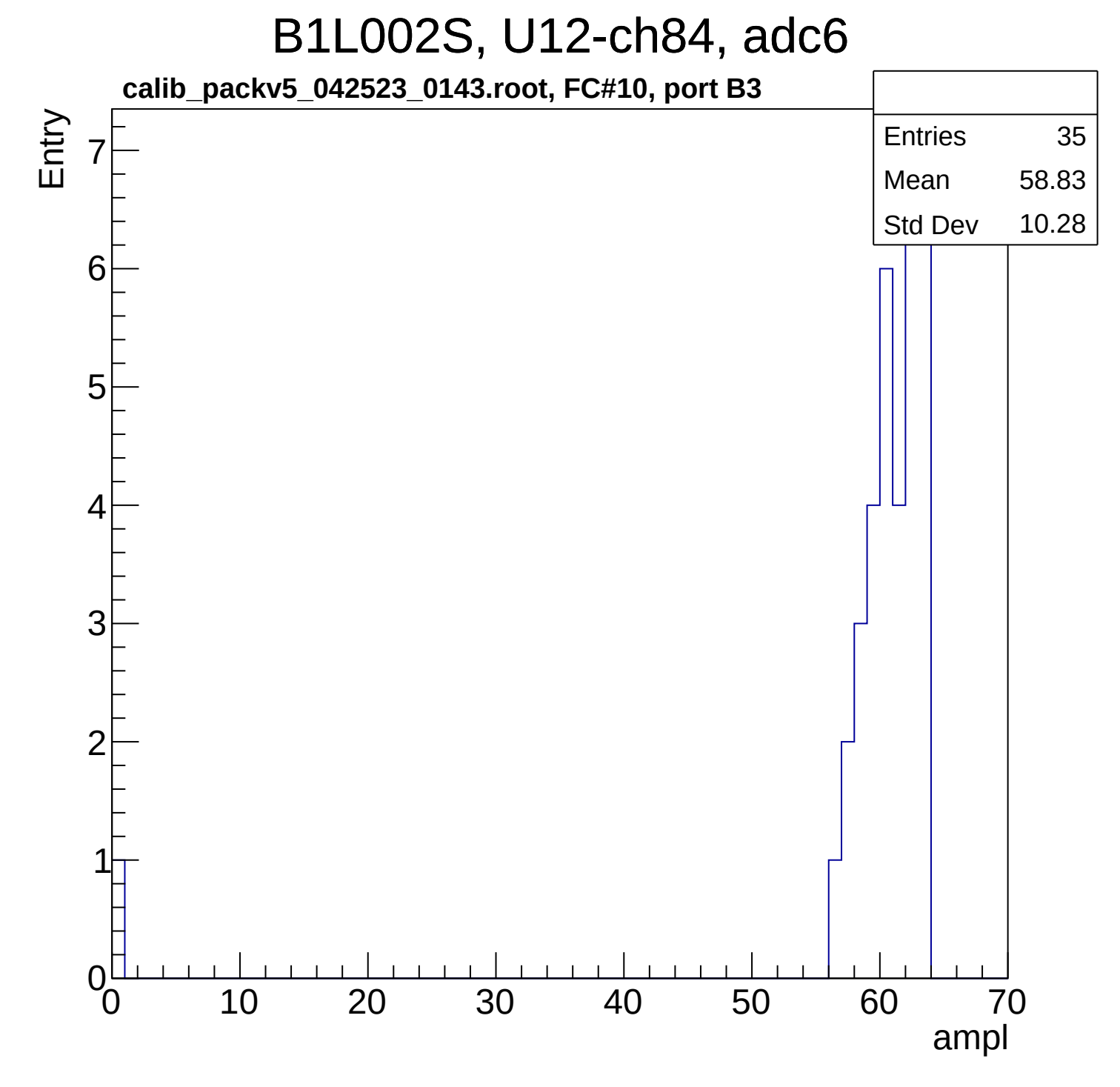

B1L002S, U12-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 56.88
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.83
Std Dev 10.28
Entry
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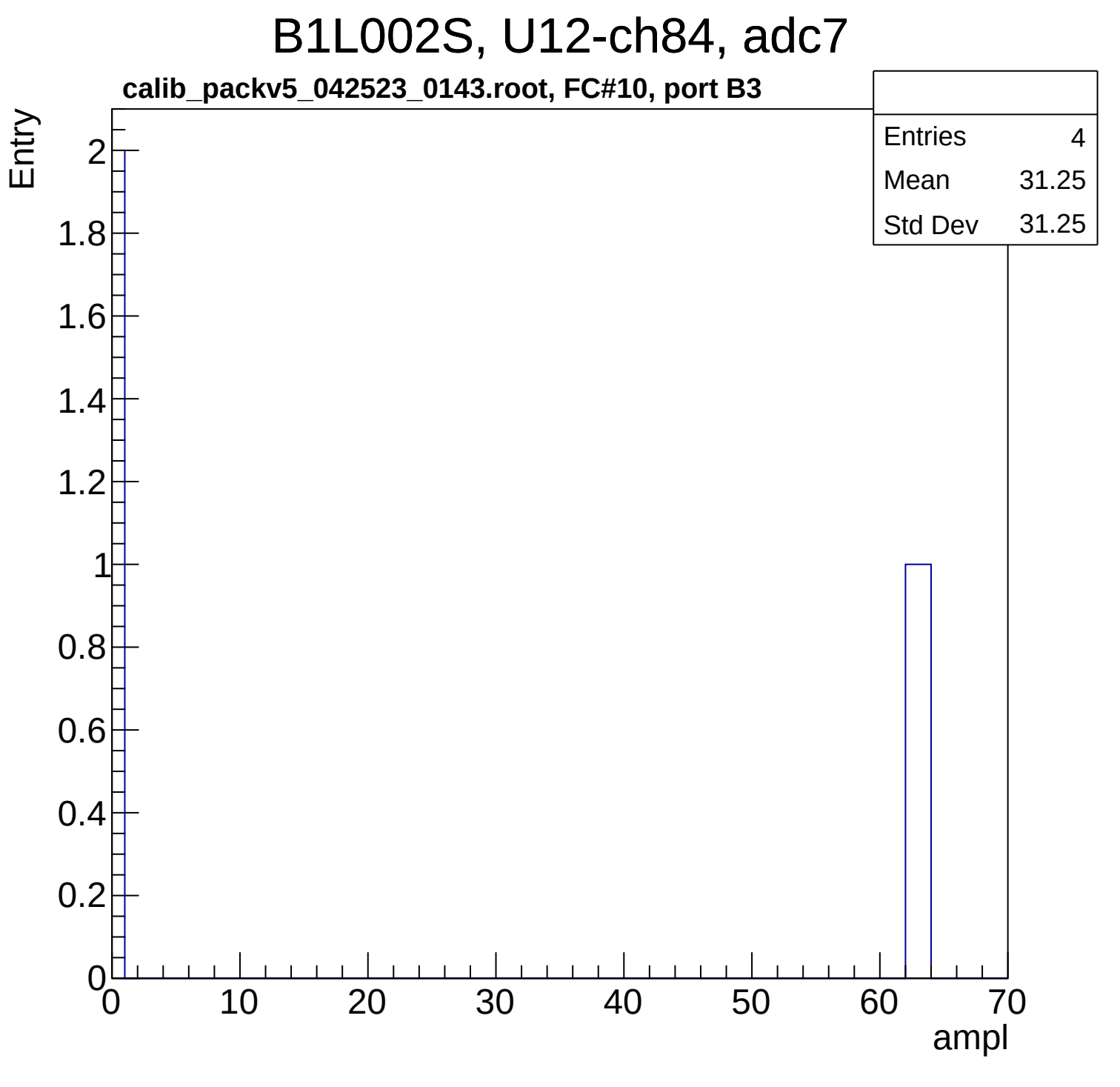
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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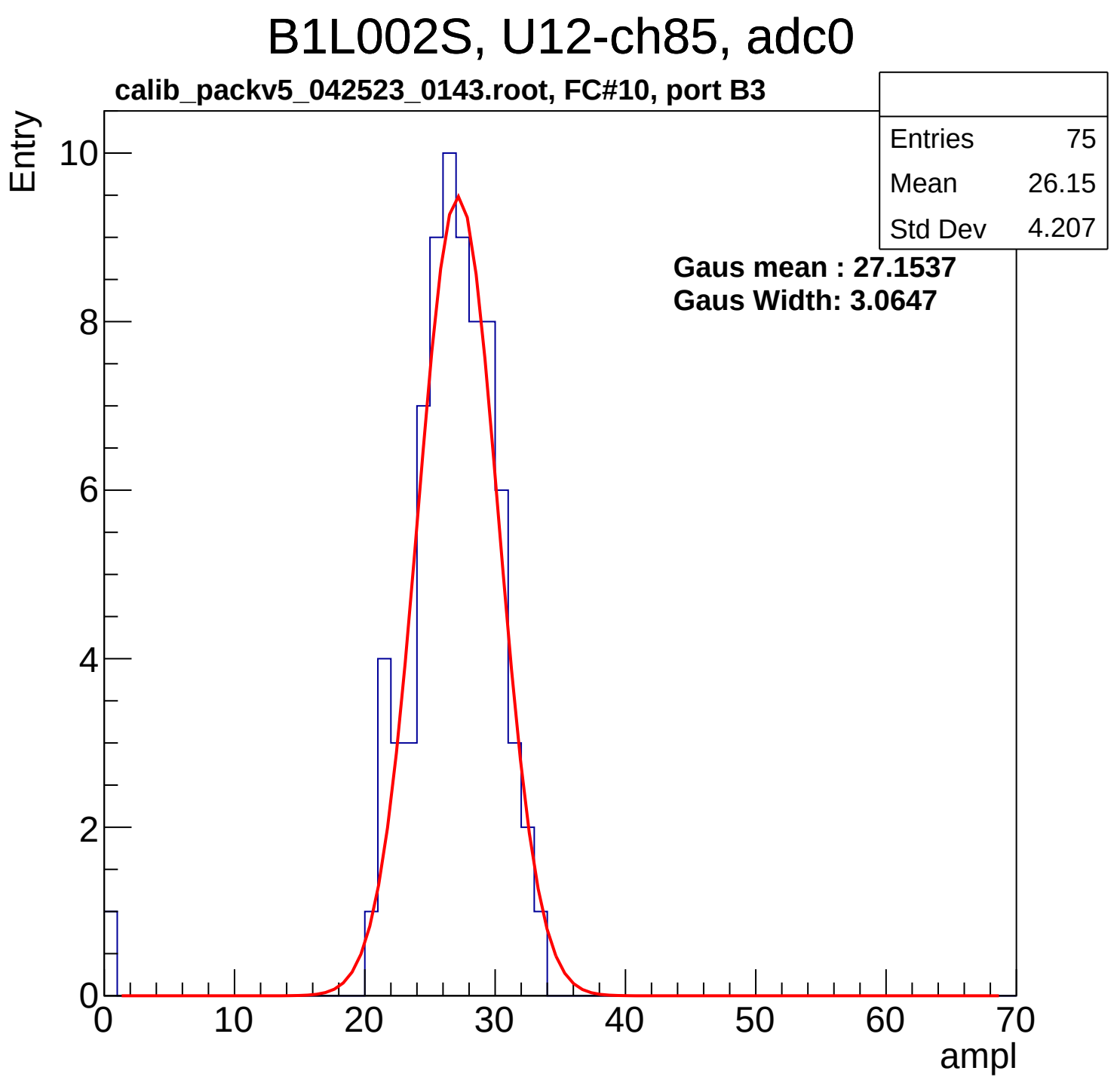
B1L002S, U12-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 26.15
Std Dev 4.207
Gaus mean : 27.1537
Gaus Width: 3.0647
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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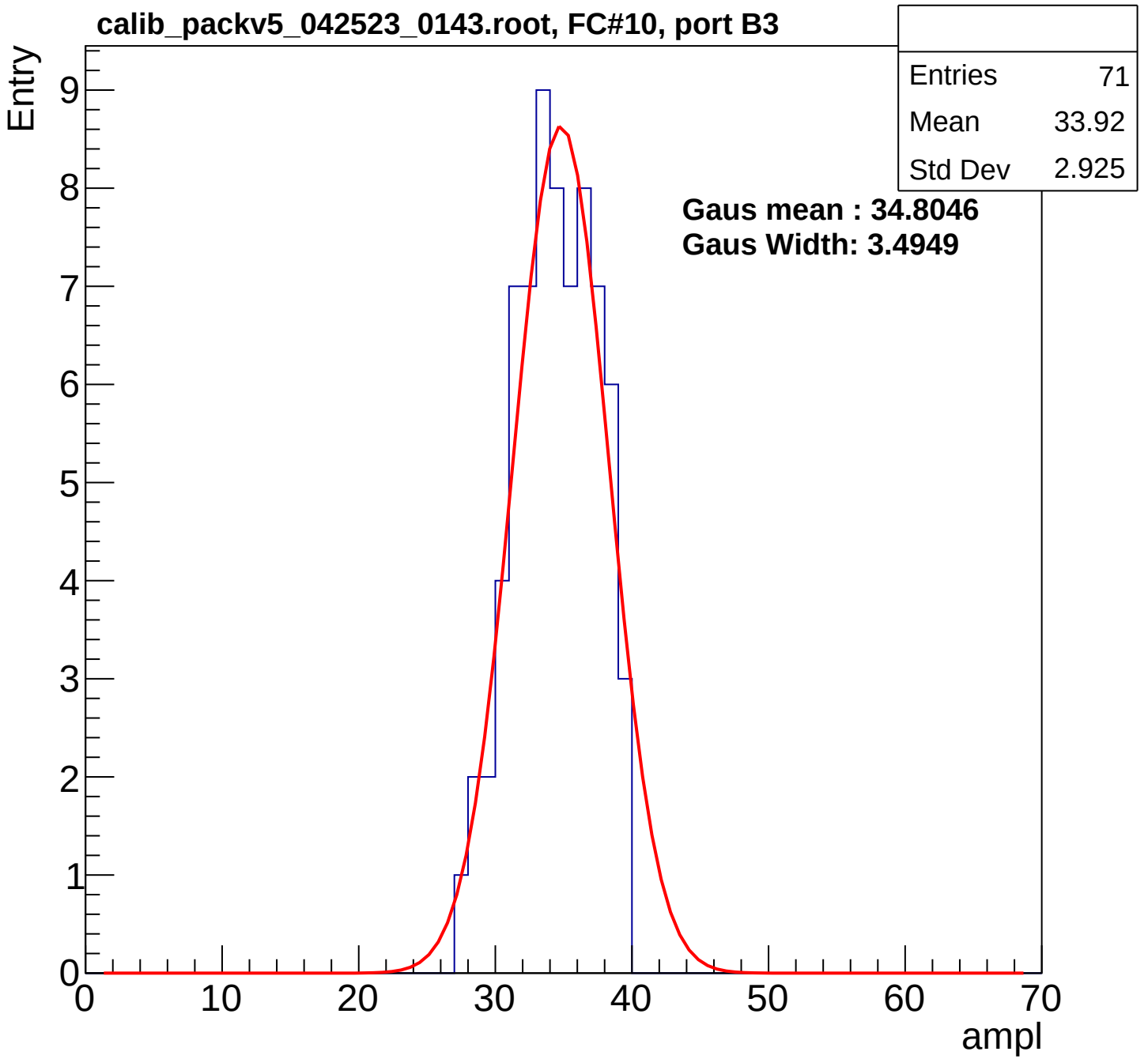
B1L002S, U12-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.92
Std Dev 2.925
Gaus mean : 34.8046
Gaus Width: 3.4949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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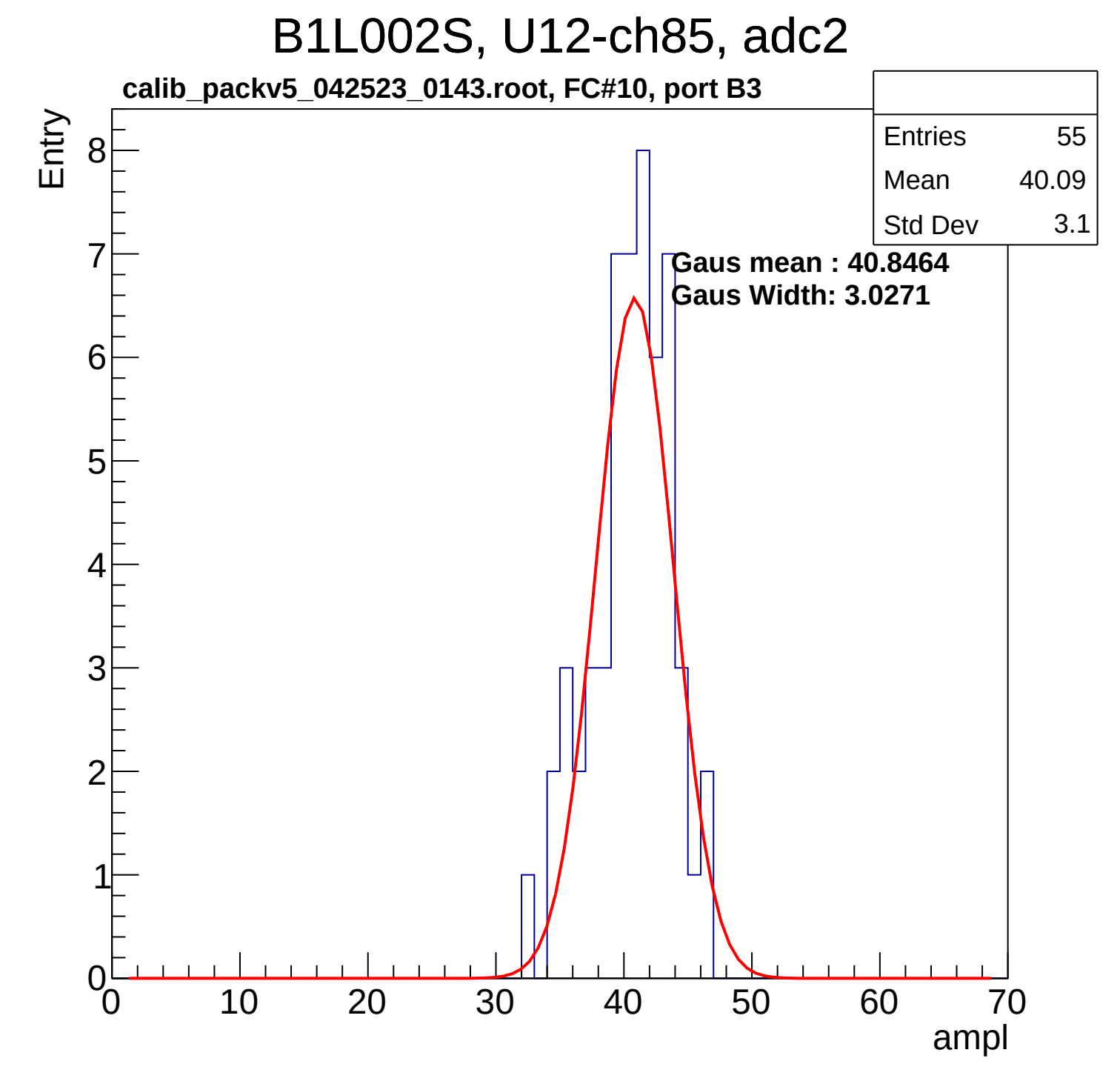
B1L002S, U12-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.09
Std Dev 3.1
Gaus mean : 40.8464
Gaus Width: 3.0271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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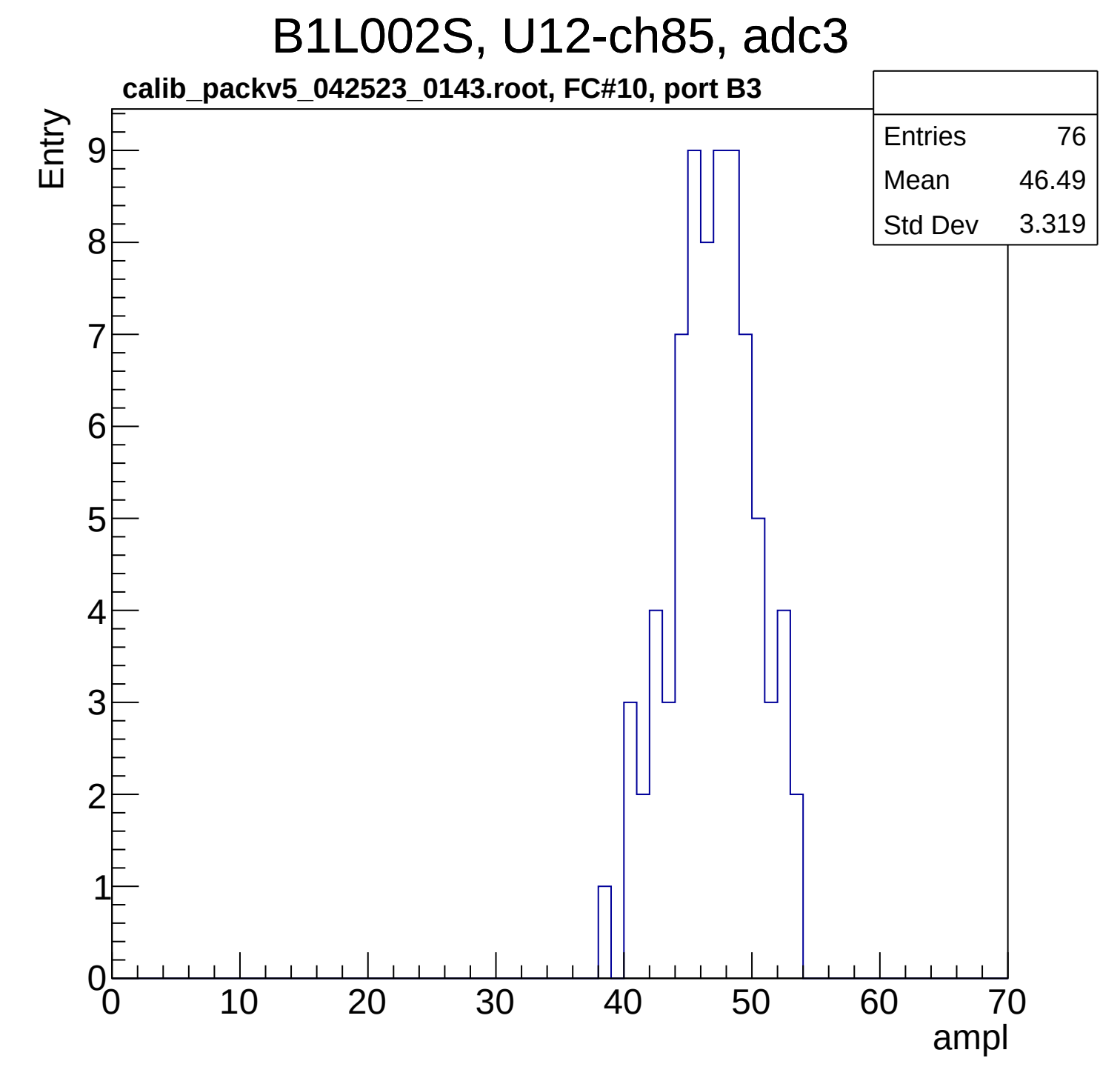
B1L002S, U12-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.49
Std Dev 3.319
Entry
ampl
0
1
2
3
4
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6
7
8
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0
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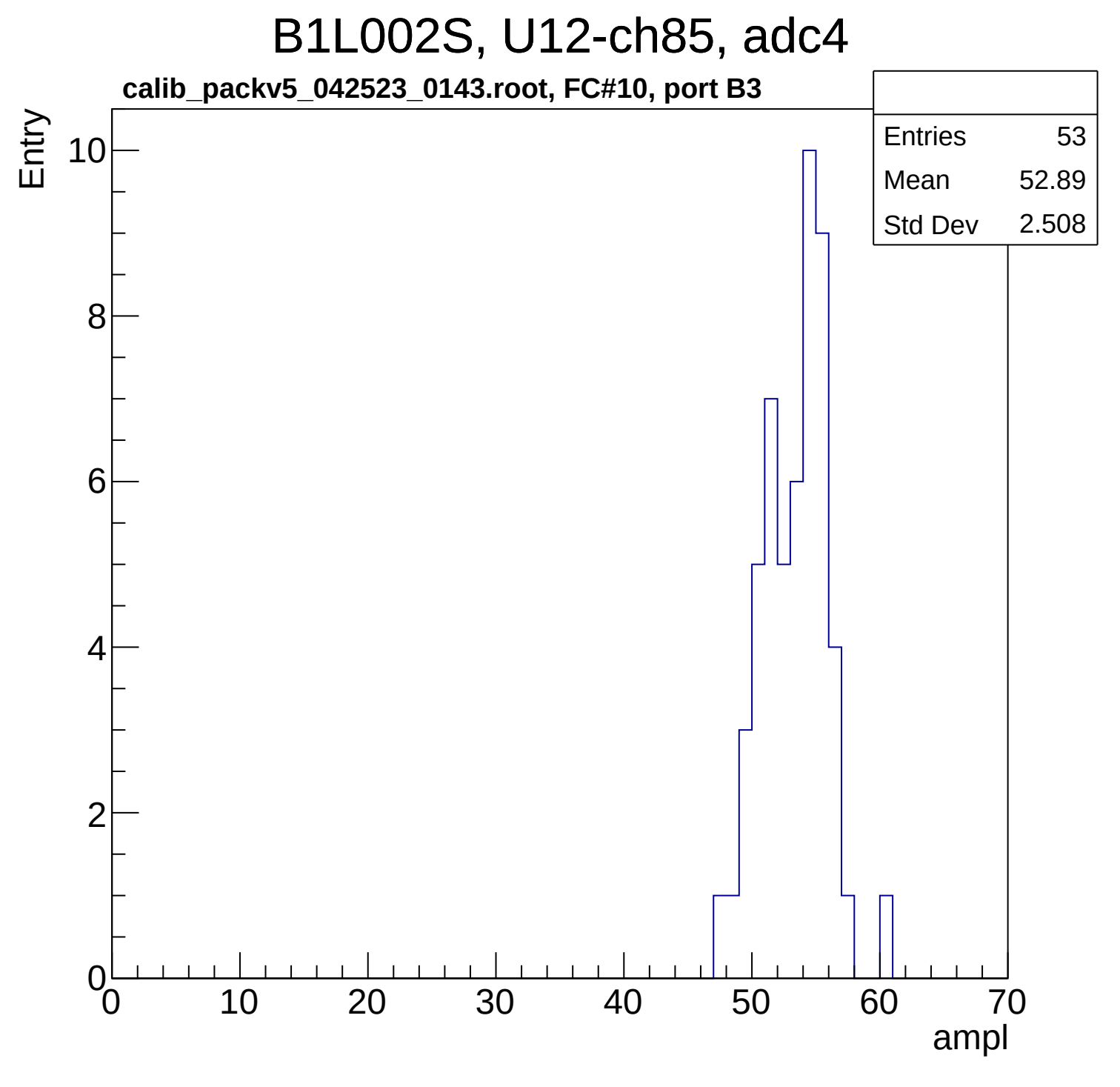
B1L002S, U12-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.89
Std Dev 2.508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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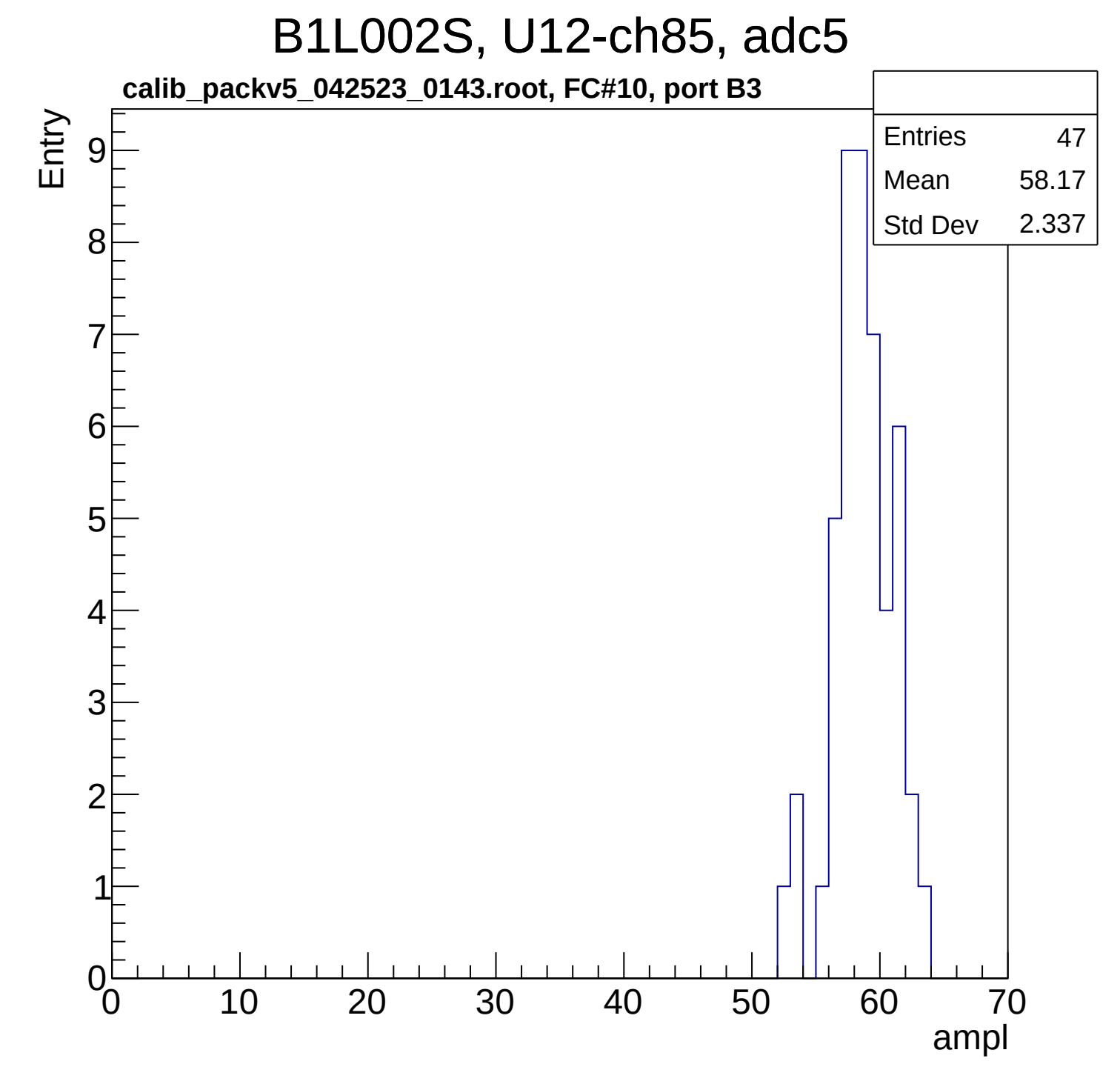
B1L002S, U12-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.17
Std Dev 2.337
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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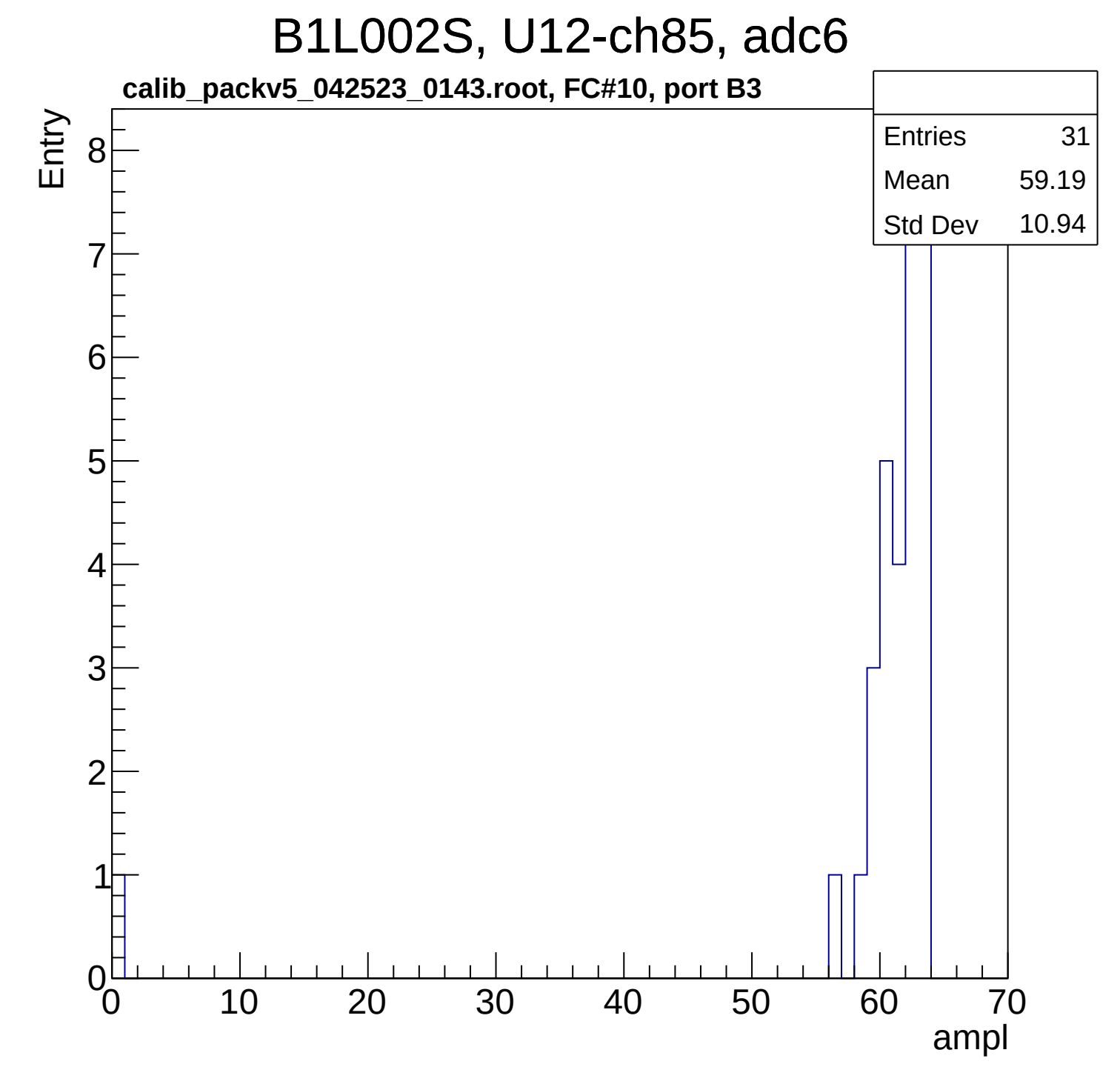
B1L002S, U12-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.19
Std Dev 10.94
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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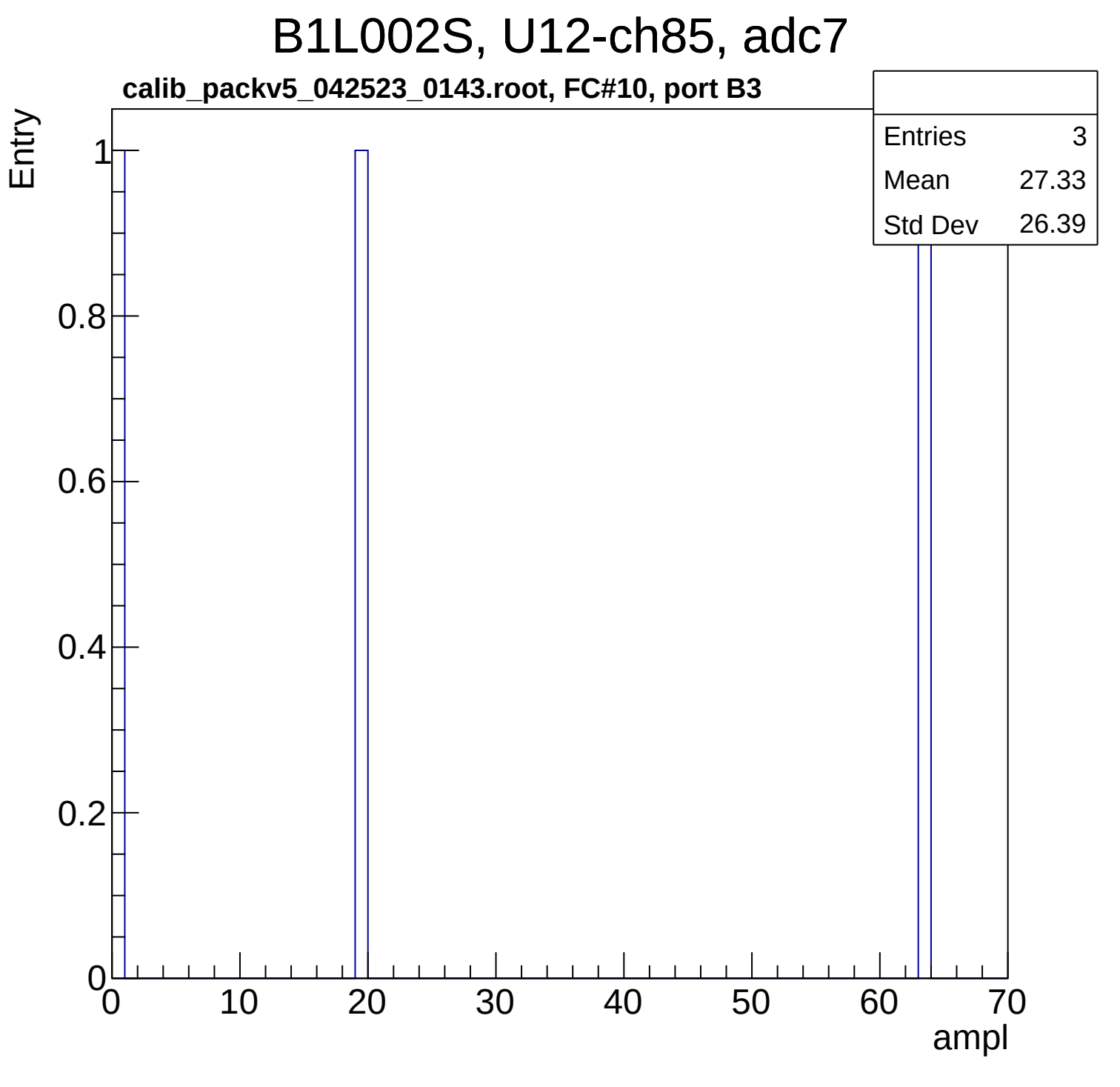
B1L002S, U12-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 26.39
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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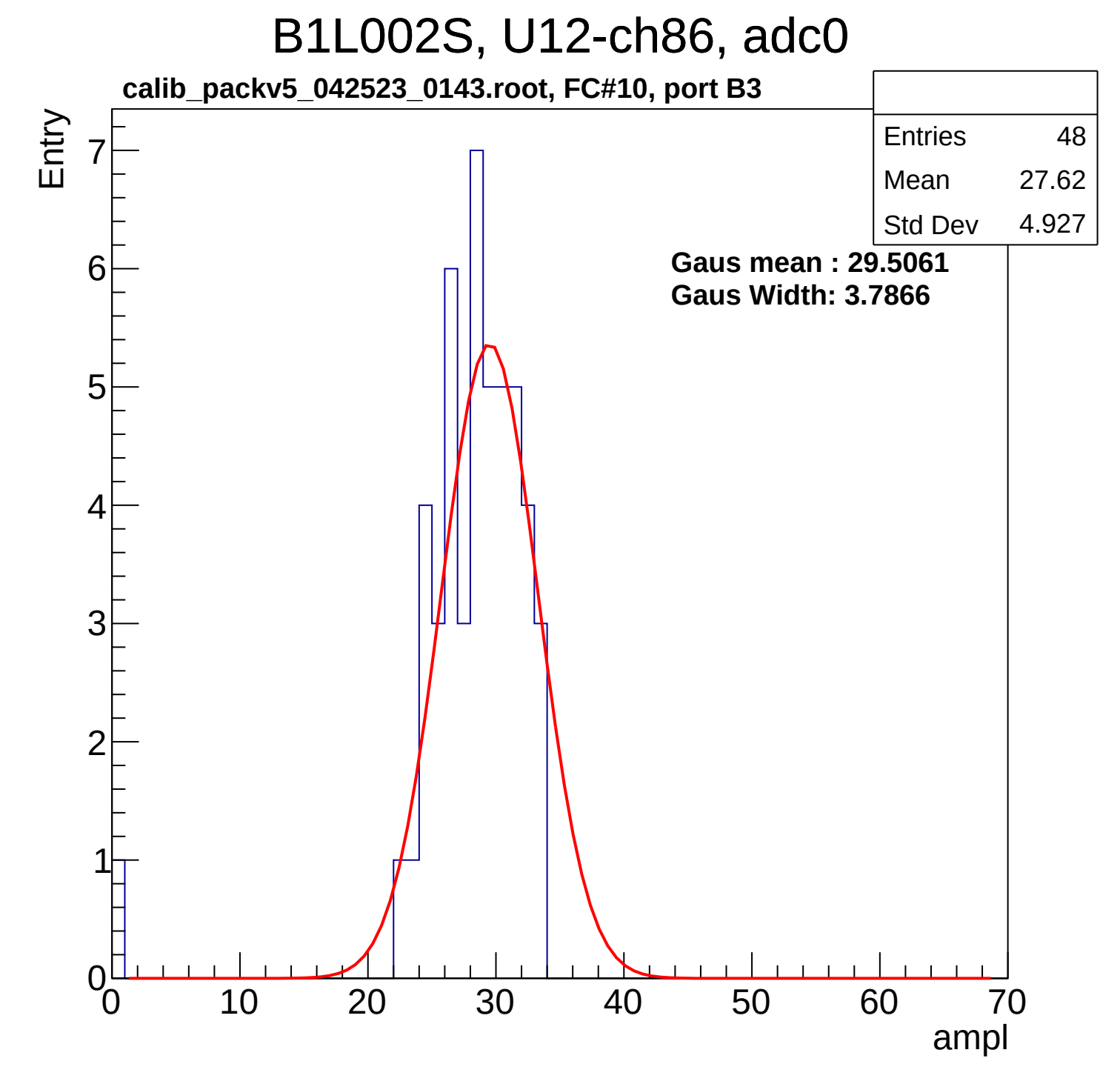
B1L002S, U12-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 27.62
Std Dev 4.927
Gaus mean : 29.5061
Gaus Width: 3.7866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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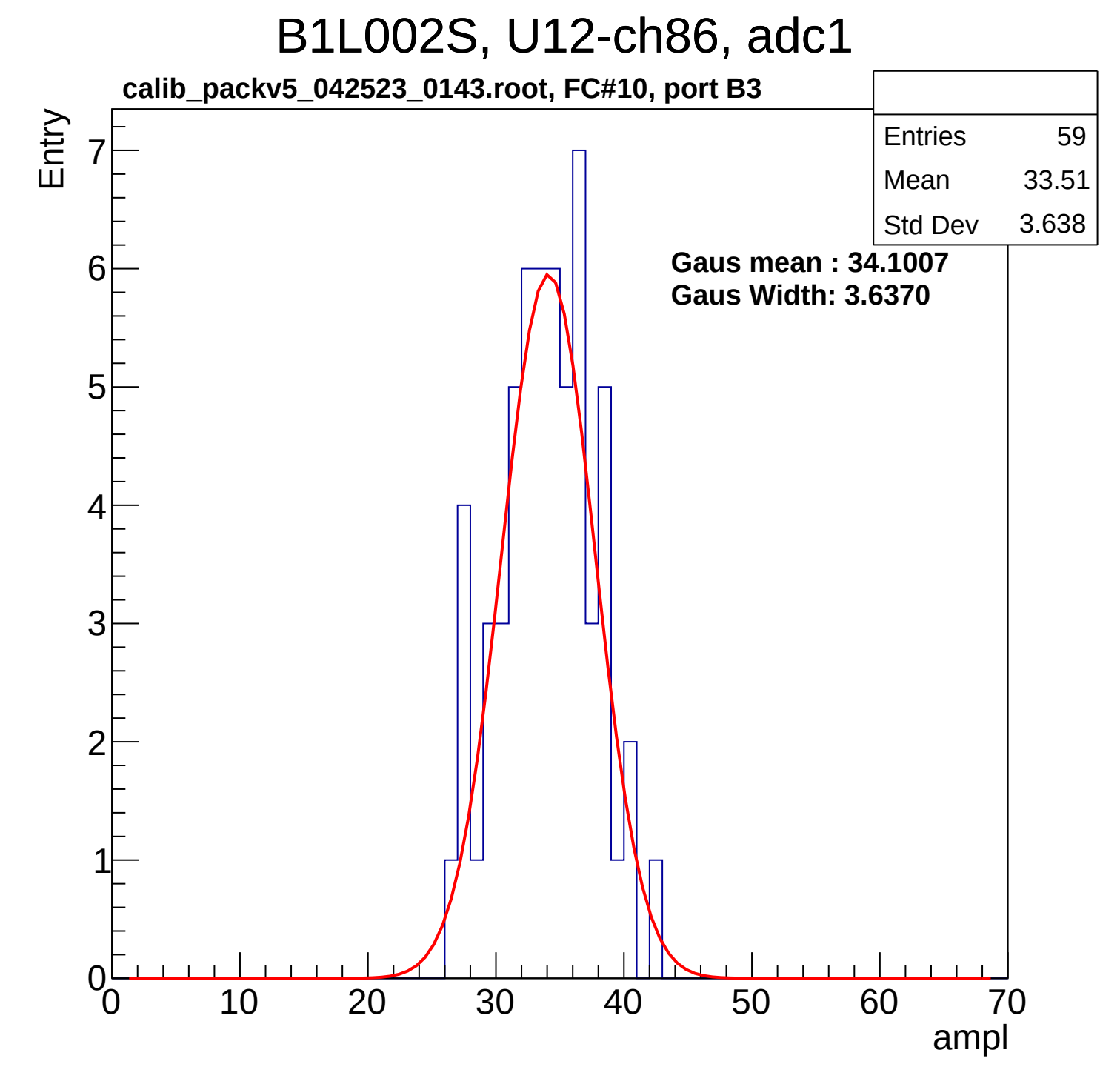
B1L002S, U12-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 33.51
Std Dev 3.638
Gaus mean : 34.1007
Gaus Width: 3.6370
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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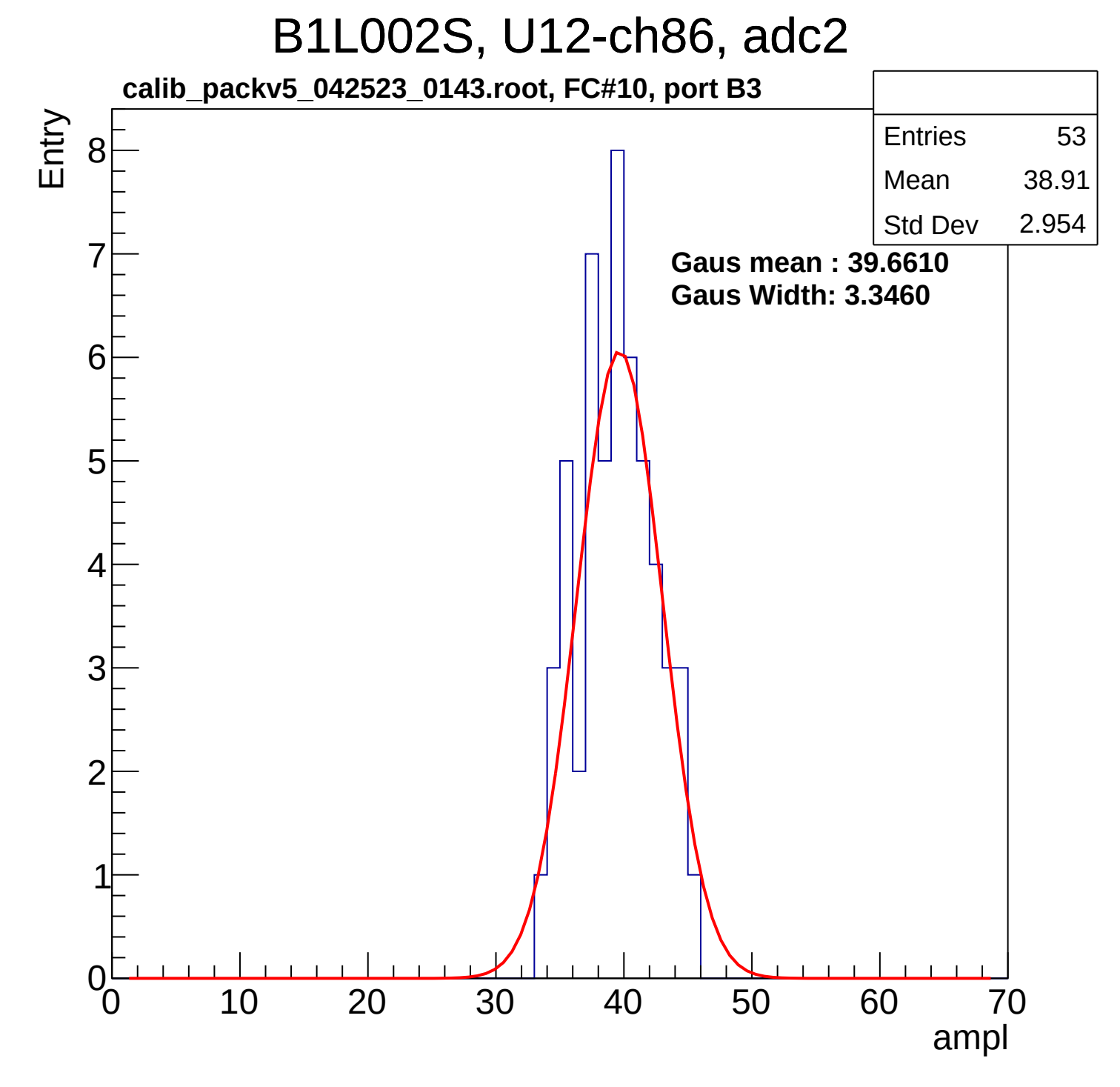
B1L002S, U12-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 38.91
Std Dev 2.954
Gaus mean : 39.6610
Gaus Width: 3.3460
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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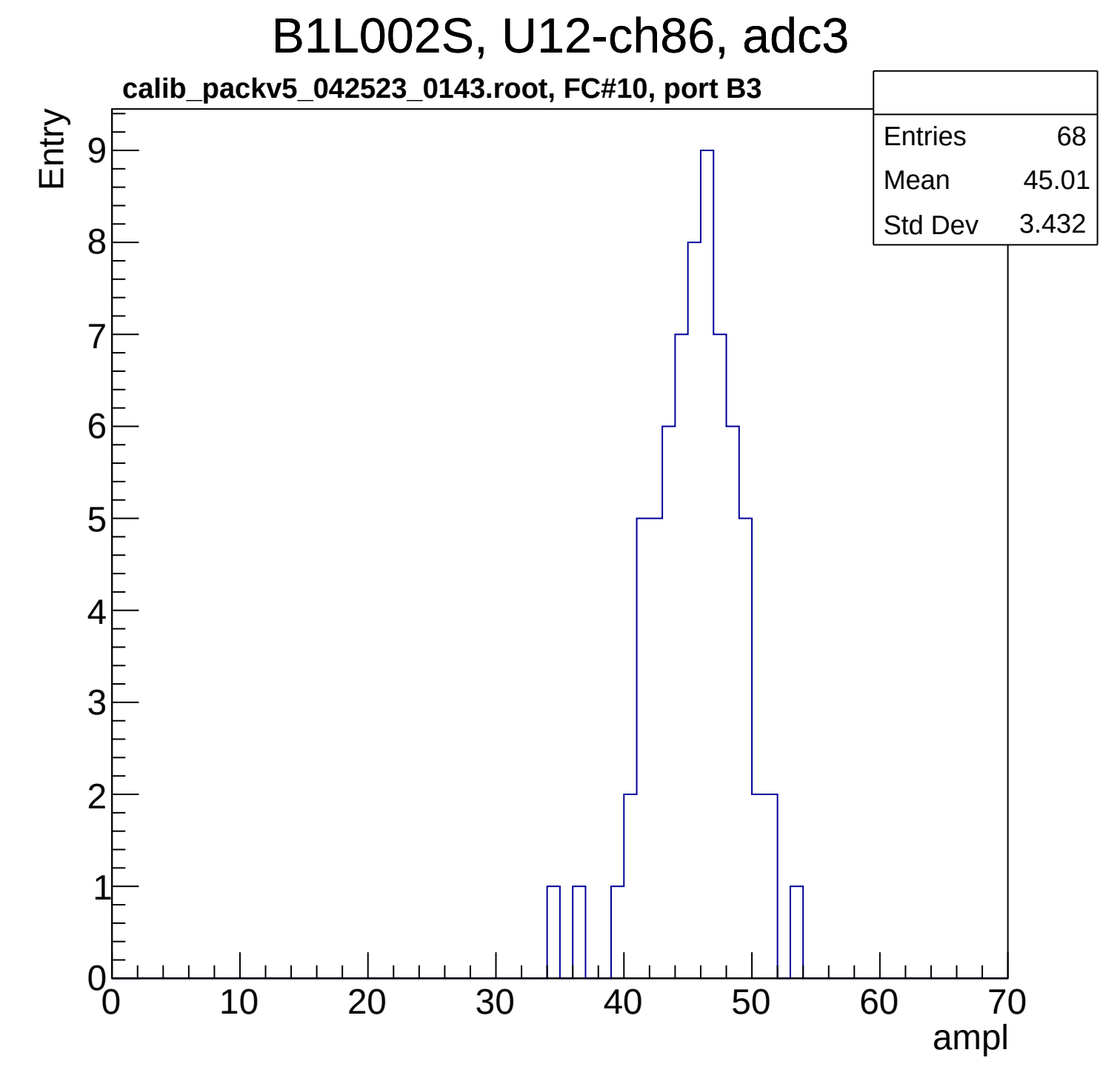
B1L002S, U12-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.01
Std Dev 3.432
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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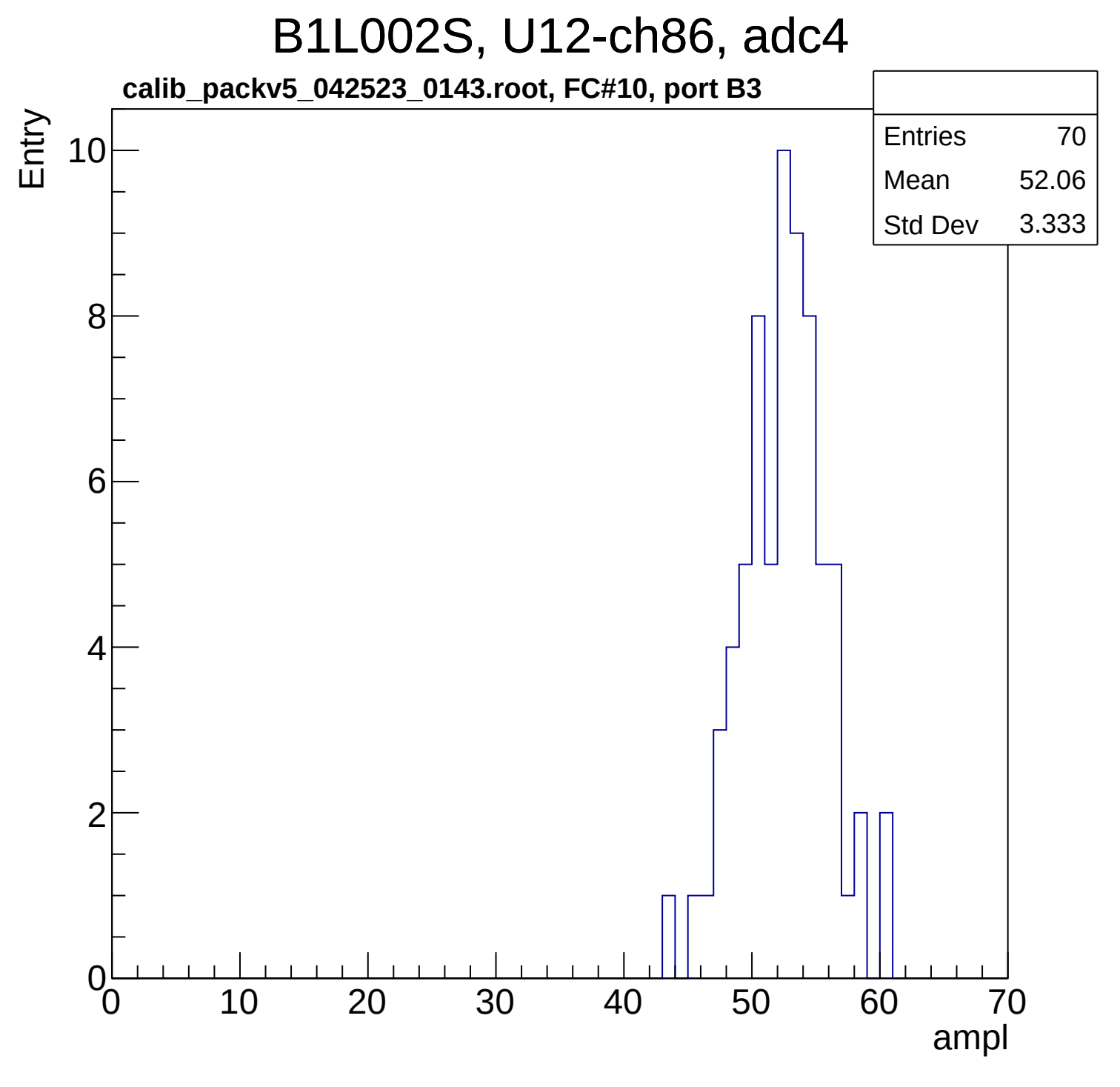
B1L002S, U12-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 52.06
Std Dev 3.333
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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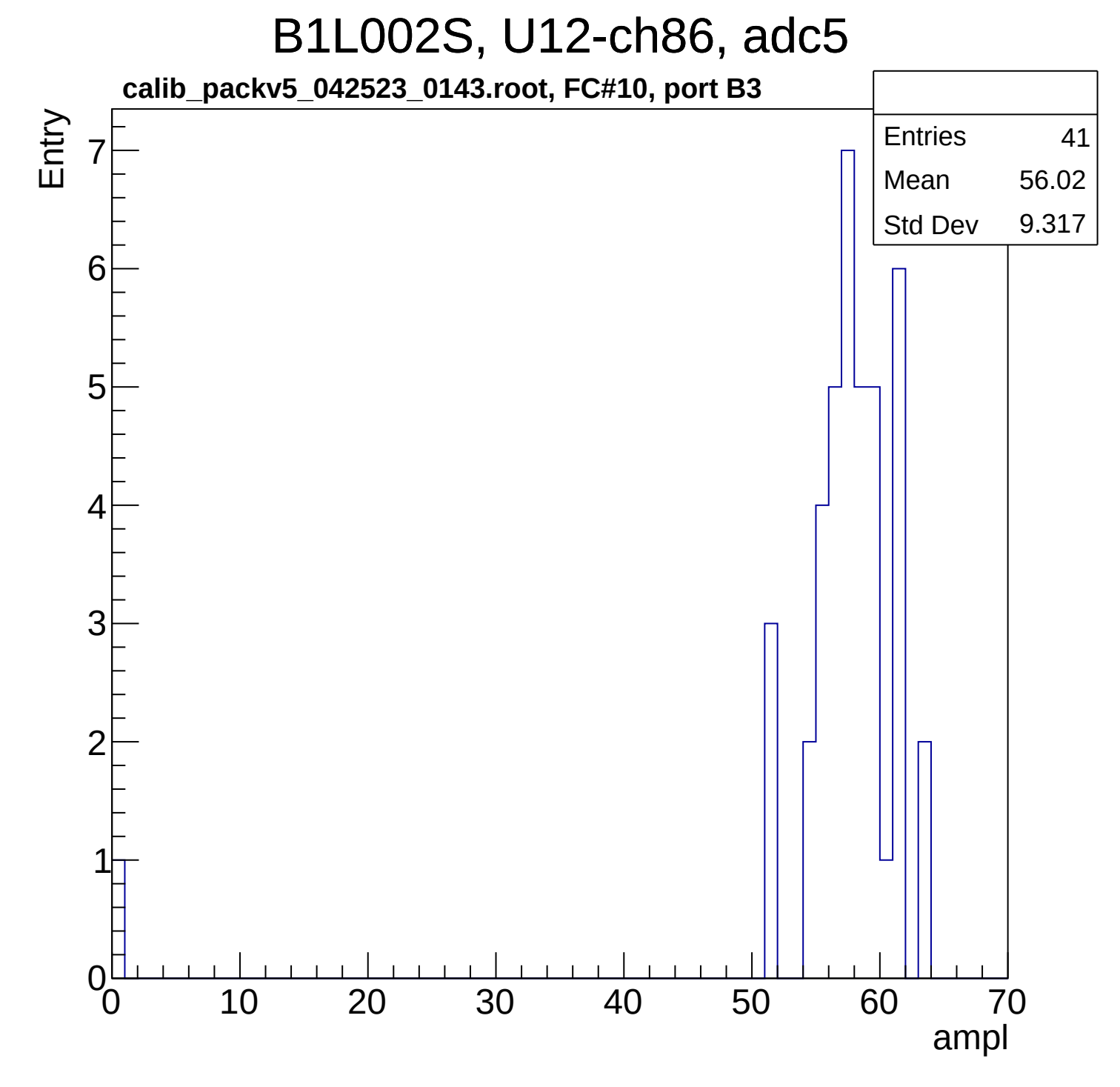
B1L002S, U12-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 56.02
Std Dev 9.317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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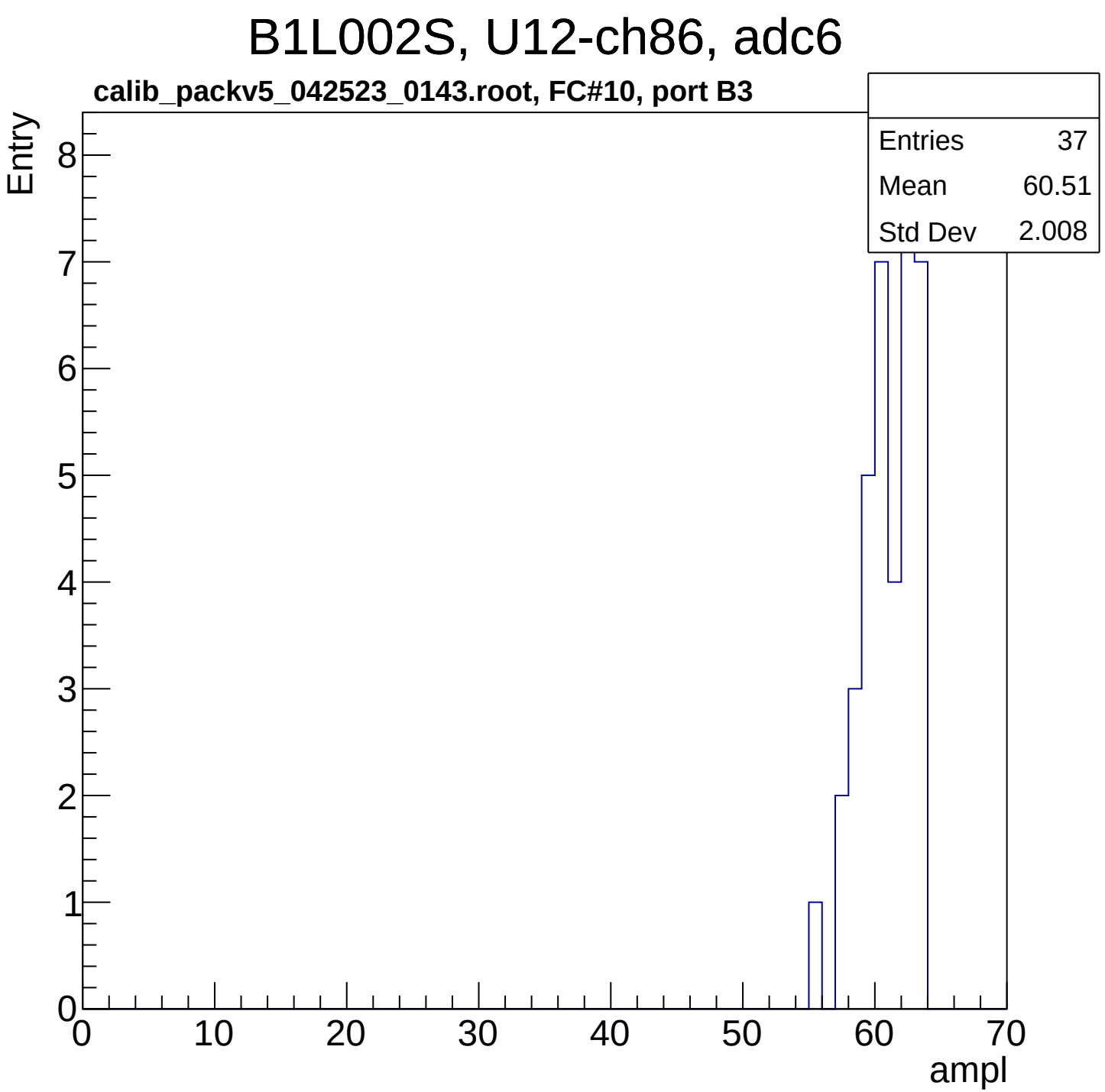
B1L002S, U12-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 60.51
Std Dev 2.008
Entry
ampl
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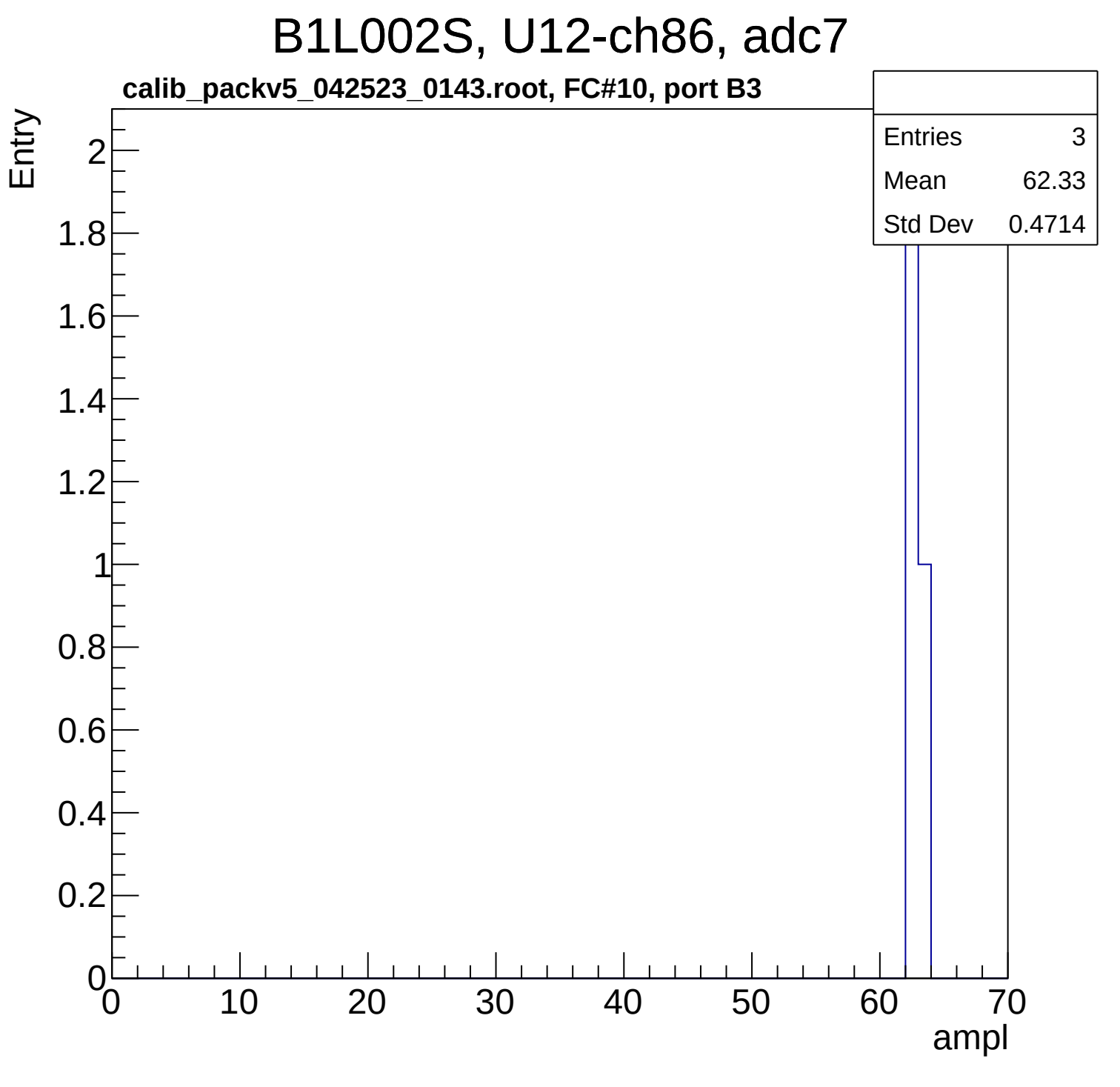
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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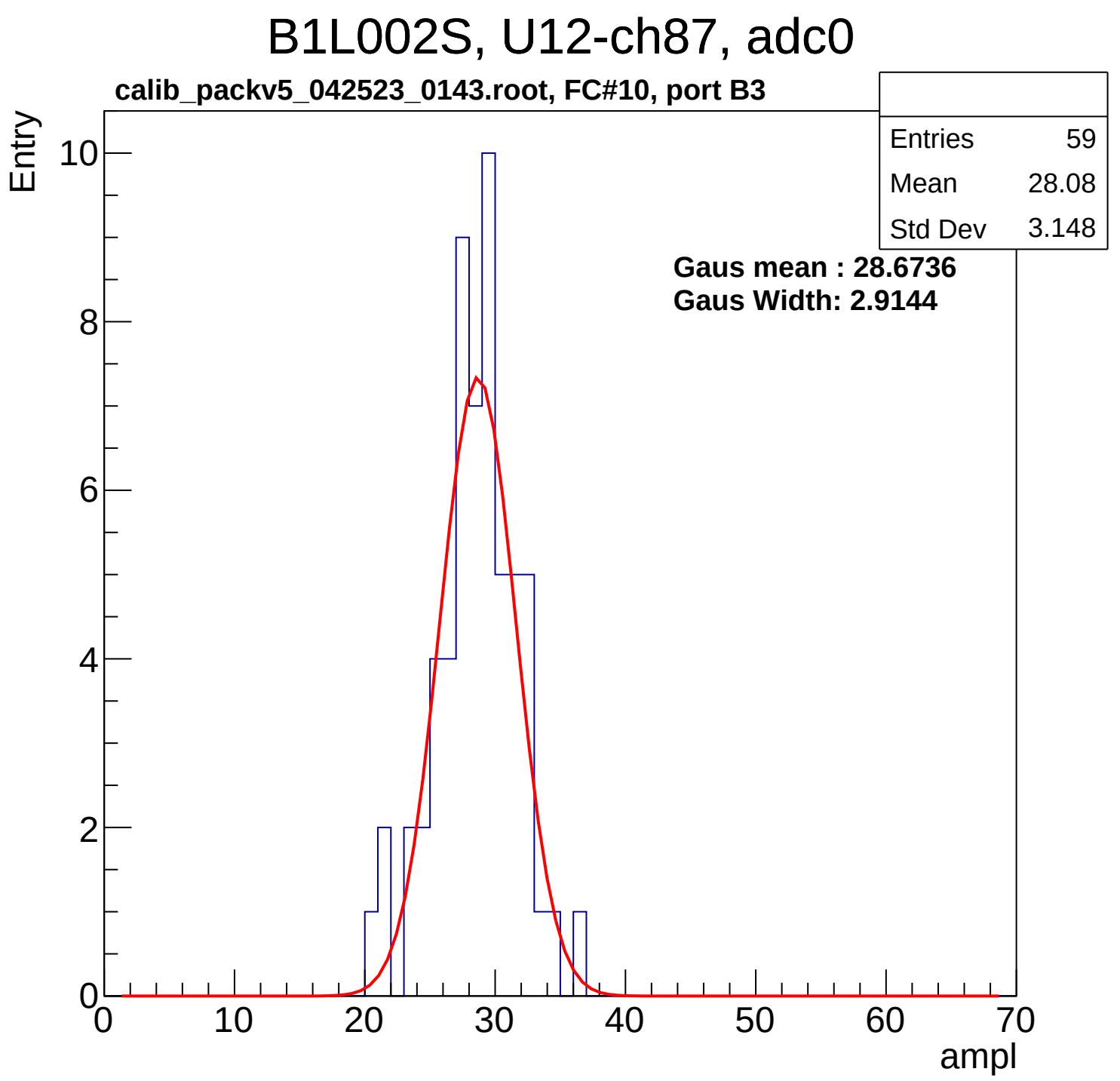
B1L002S, U12-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 28.08
Std Dev 3.148
Gaus mean : 28.6736
Gaus Width: 2.9144
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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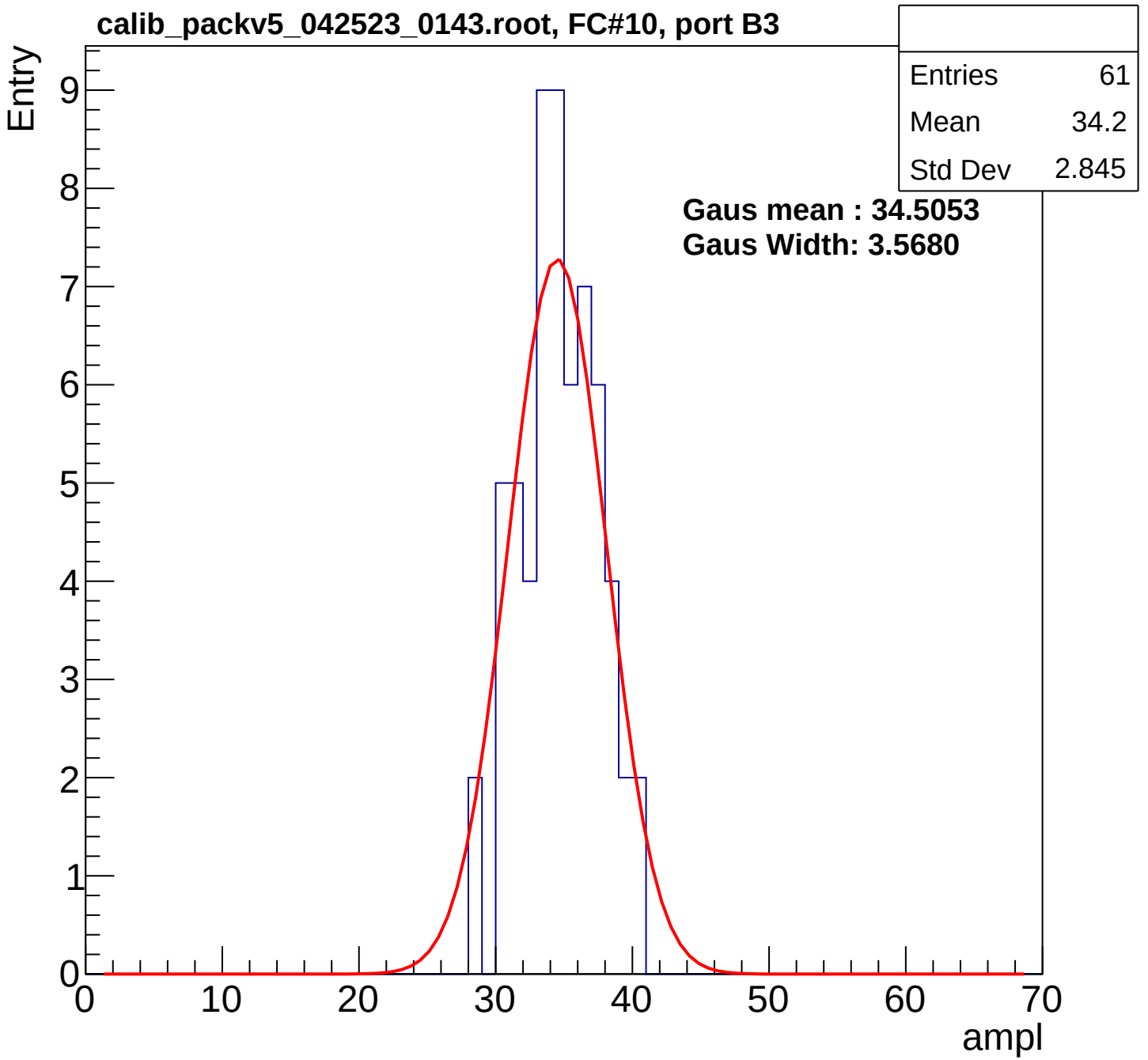
B1L002S, U12-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.2
Std Dev 2.845
Gaus mean : 34.5053
Gaus Width: 3.5680
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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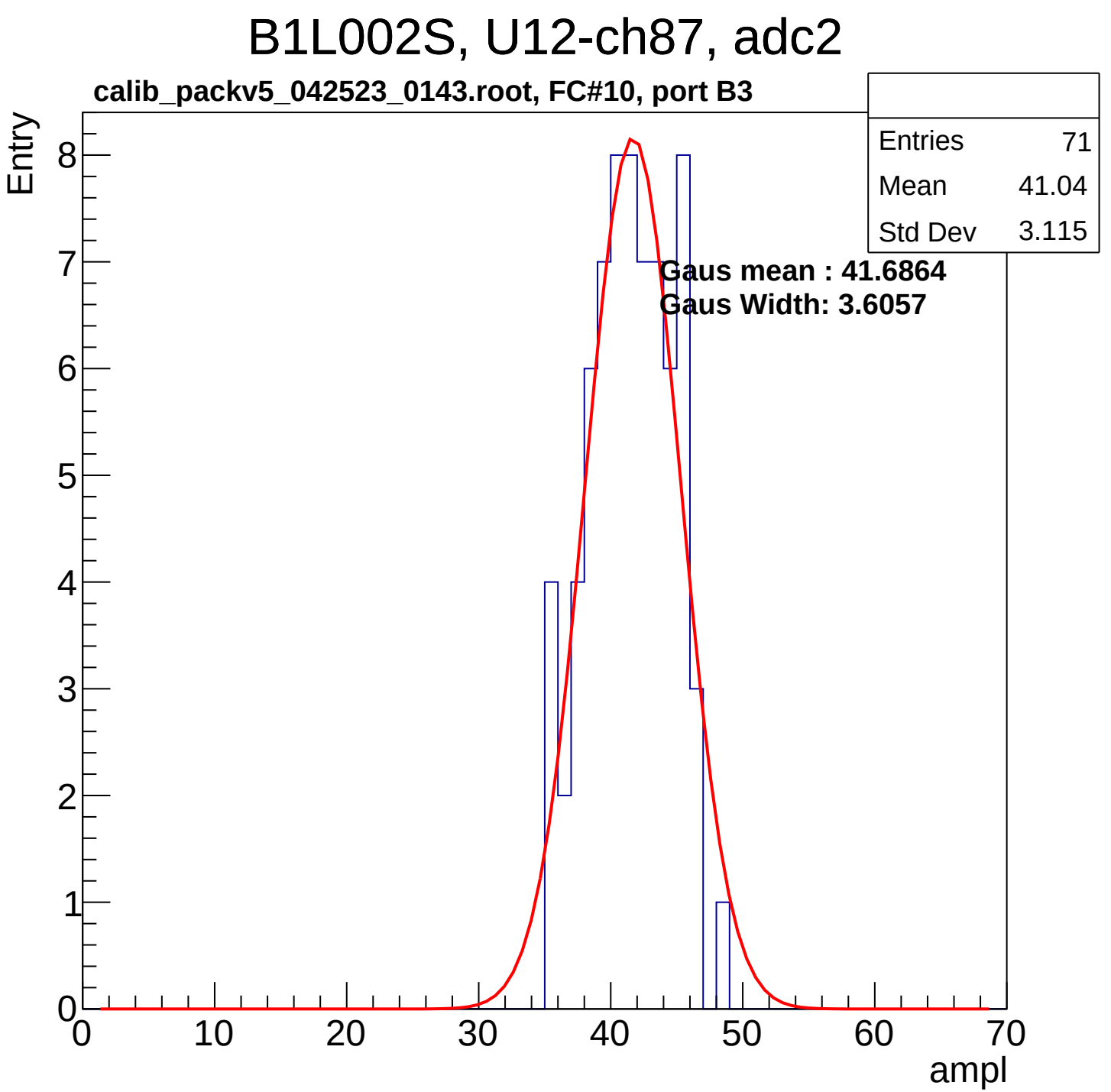
B1L002S, U12-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.04
Std Dev 3.115
Gaus mean : 41.6864
Gaus Width: 3.6057
Entry
ampl
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2
3
4
5
6
7
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0
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60
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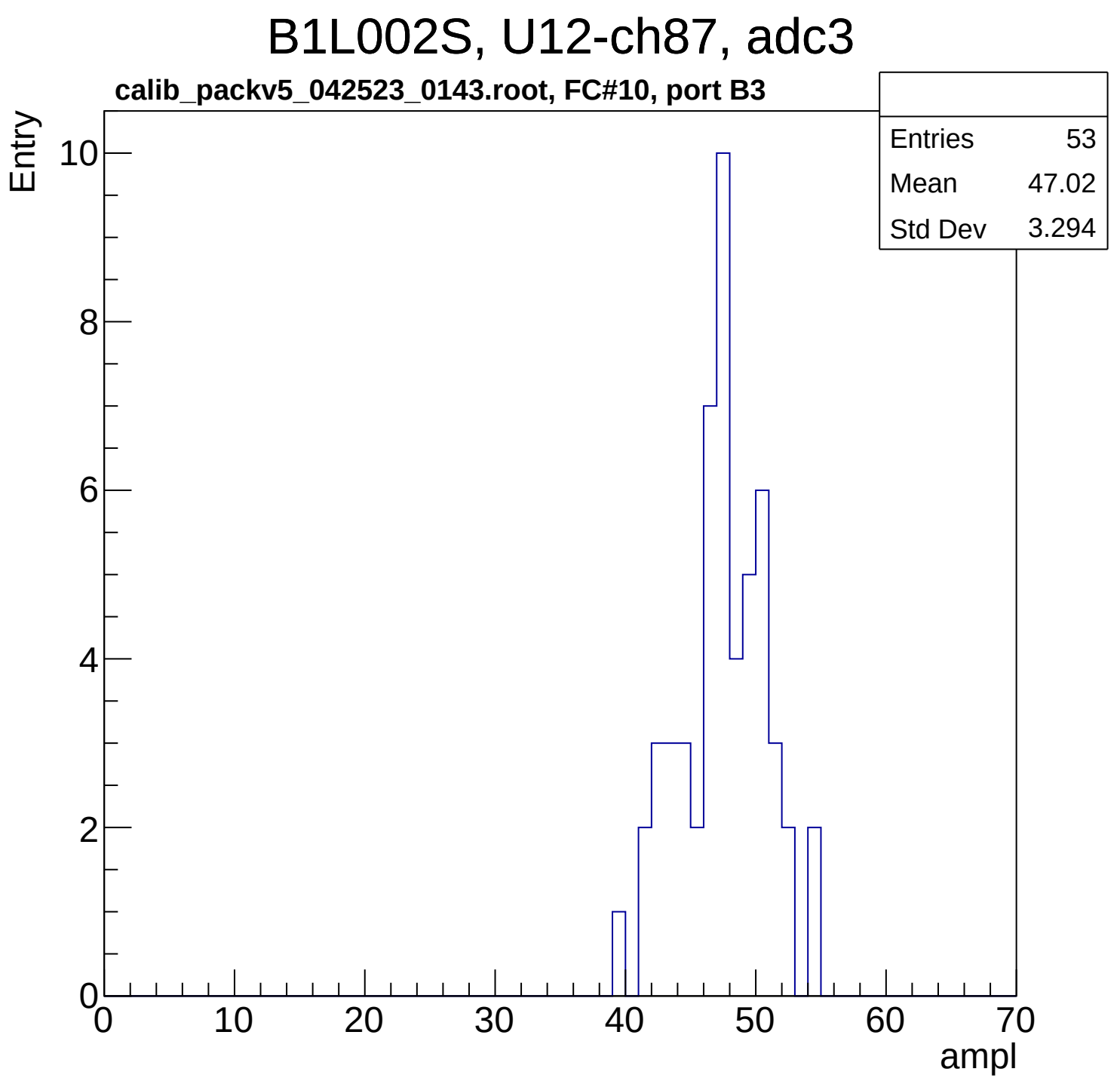
B1L002S, U12-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 47.02
Std Dev 3.294
Entry
0
2
4
6
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10
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10
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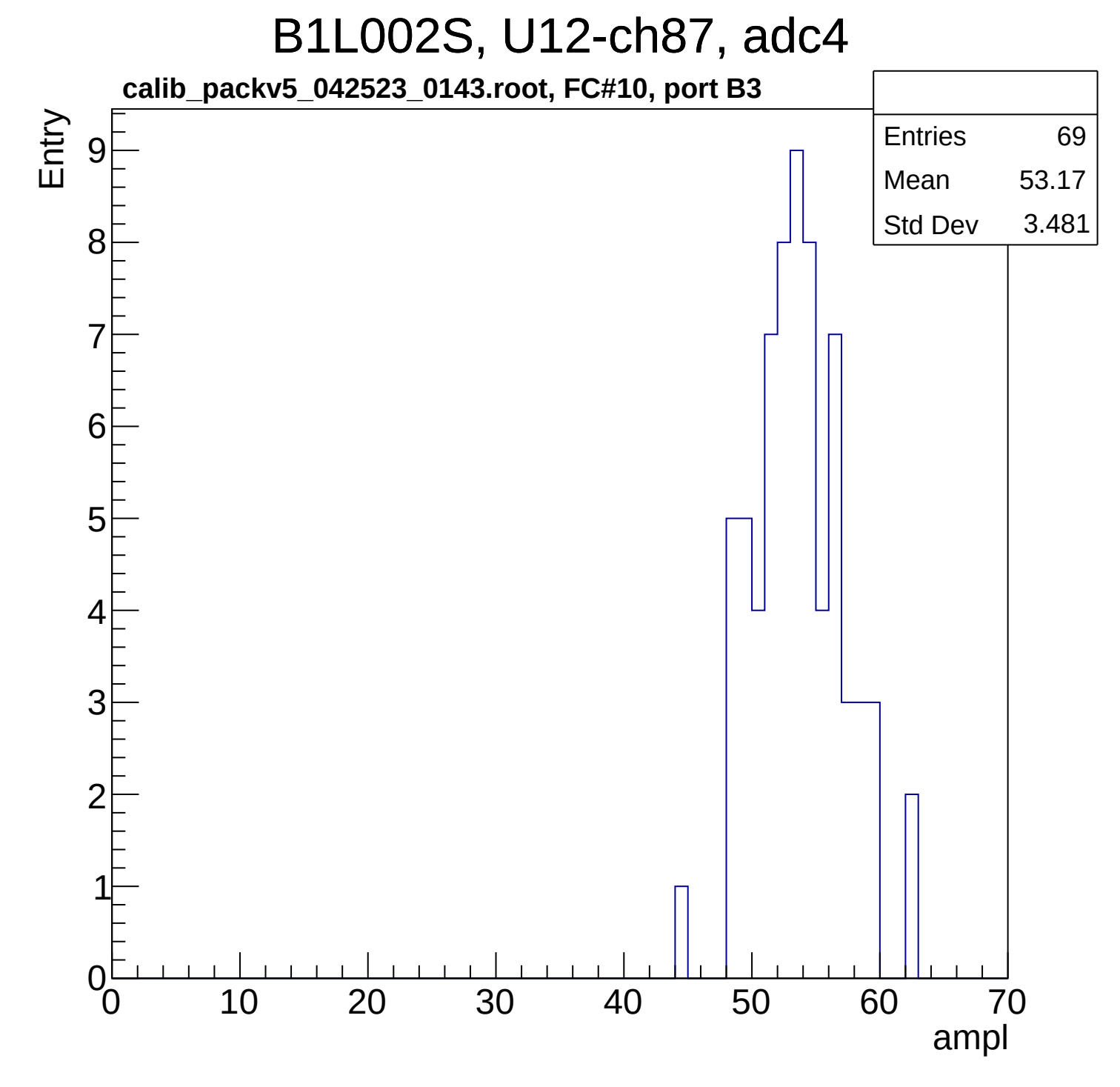
50
60
70
ampl


B1L002S, U12-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.17
Std Dev 3.481
Entry
ampl
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5
6
7
8
9
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40
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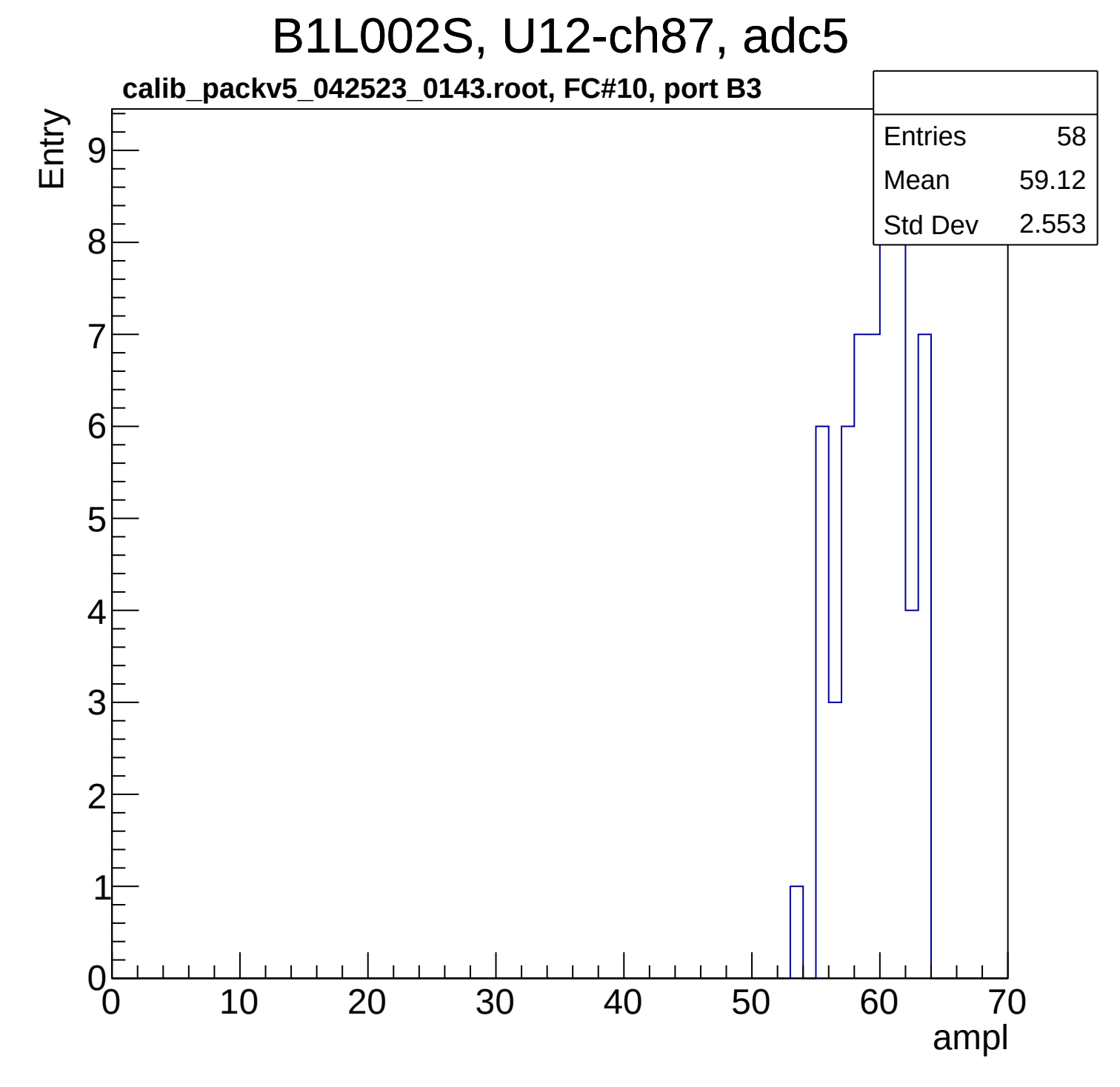
60
70


B1L002S, U12-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 59.12
Std Dev 2.553
Entry
ampl
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3
4
5
6
7
8
9
0
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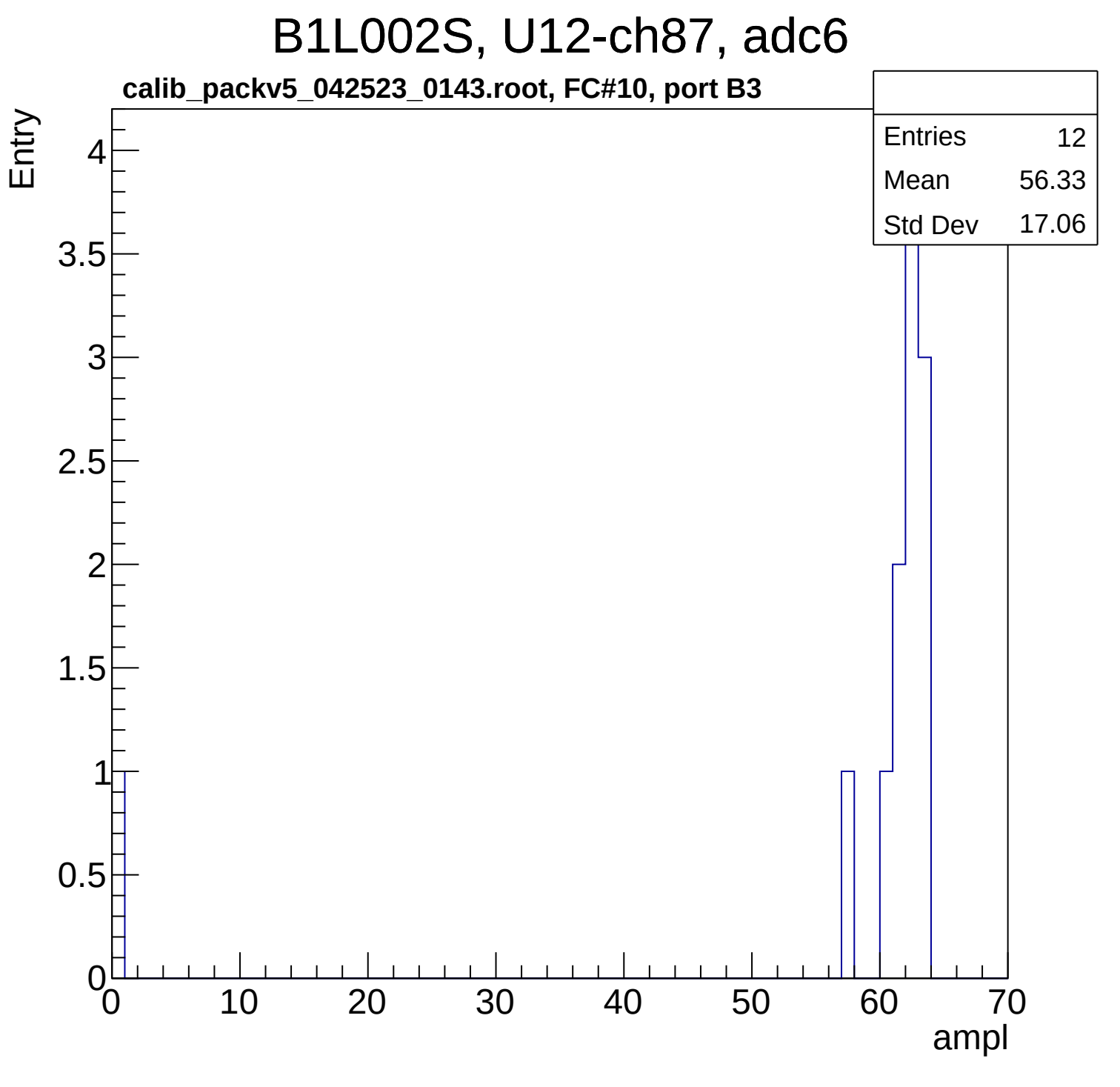
B1L002S, U12-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.33
Std Dev 17.06
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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60
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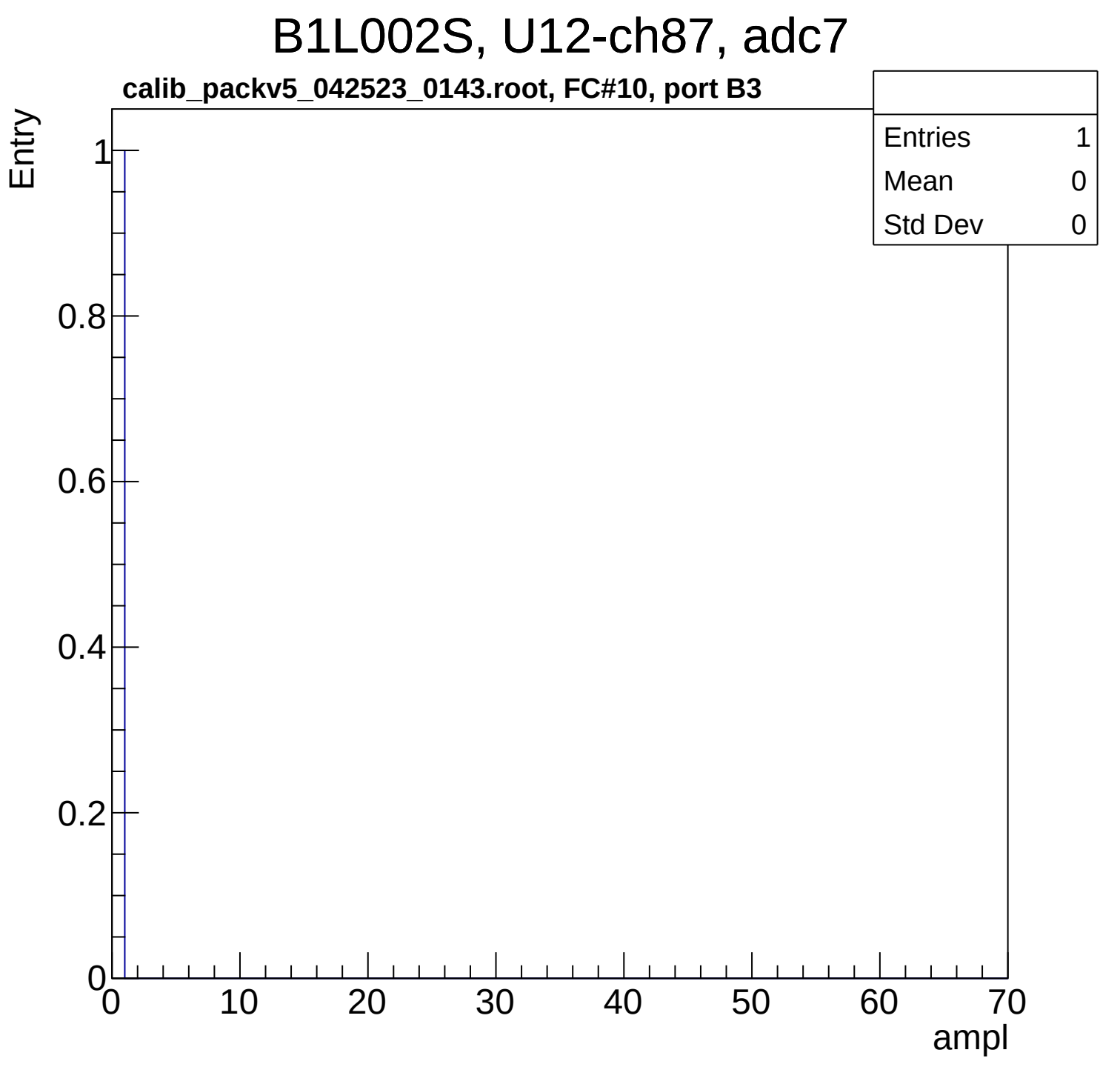
B1L002S, U12-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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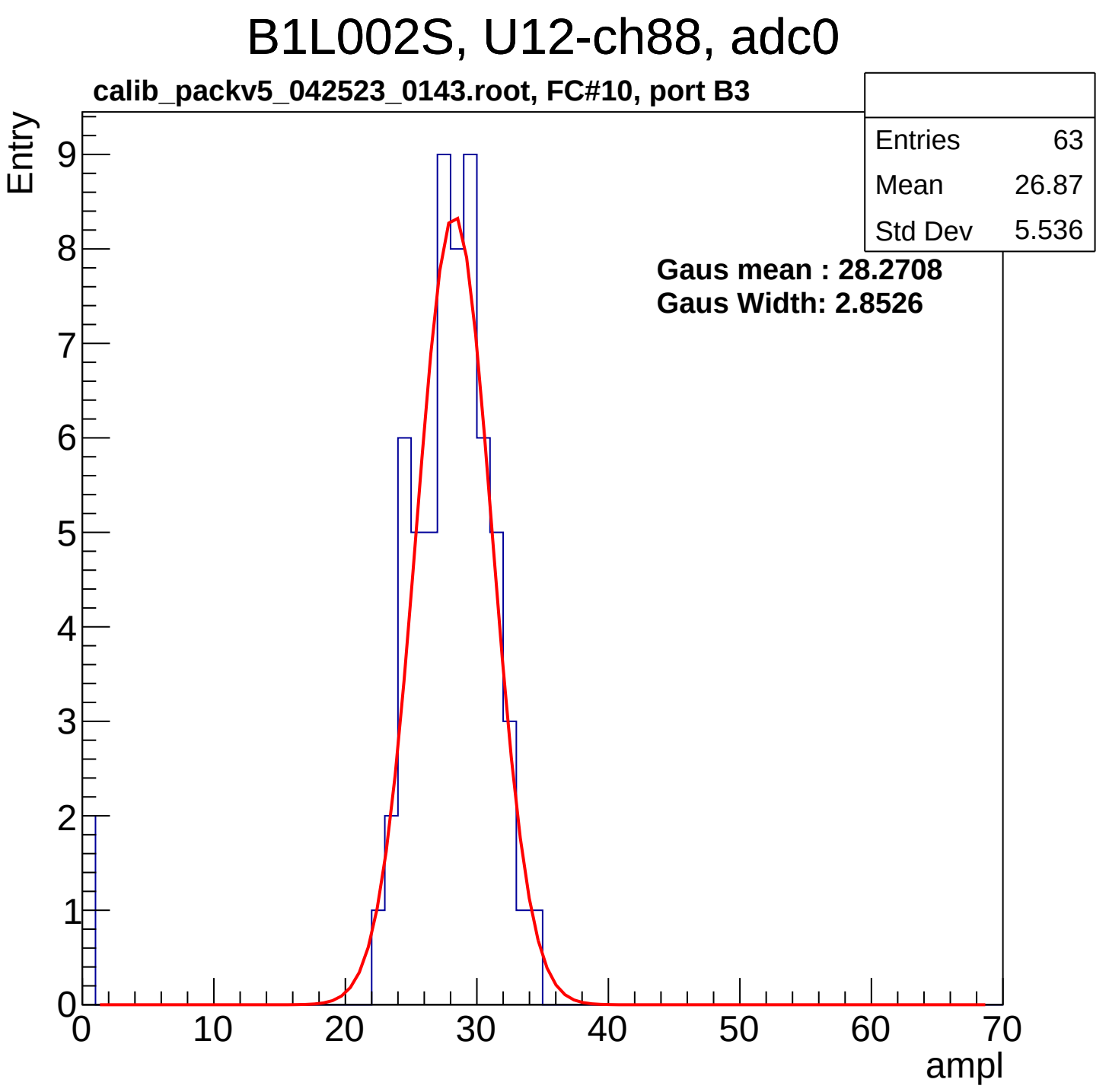
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 26.87
Std Dev 5.536
Gaus mean : 28.2708
Gaus Width: 2.8526
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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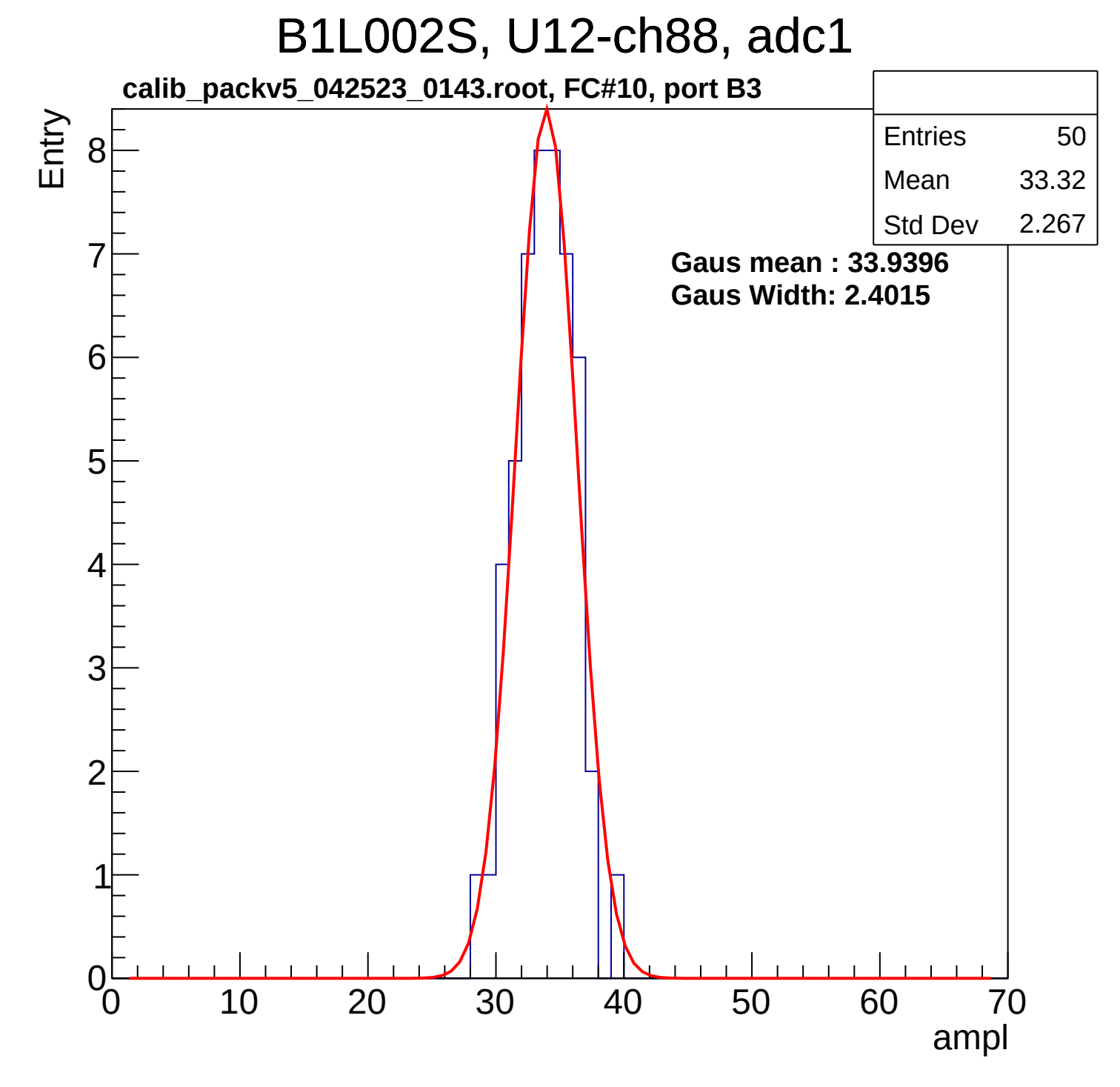
B1L002S, U12-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 33.32
Std Dev 2.267
Gaus mean : 33.9396
Gaus Width: 2.4015
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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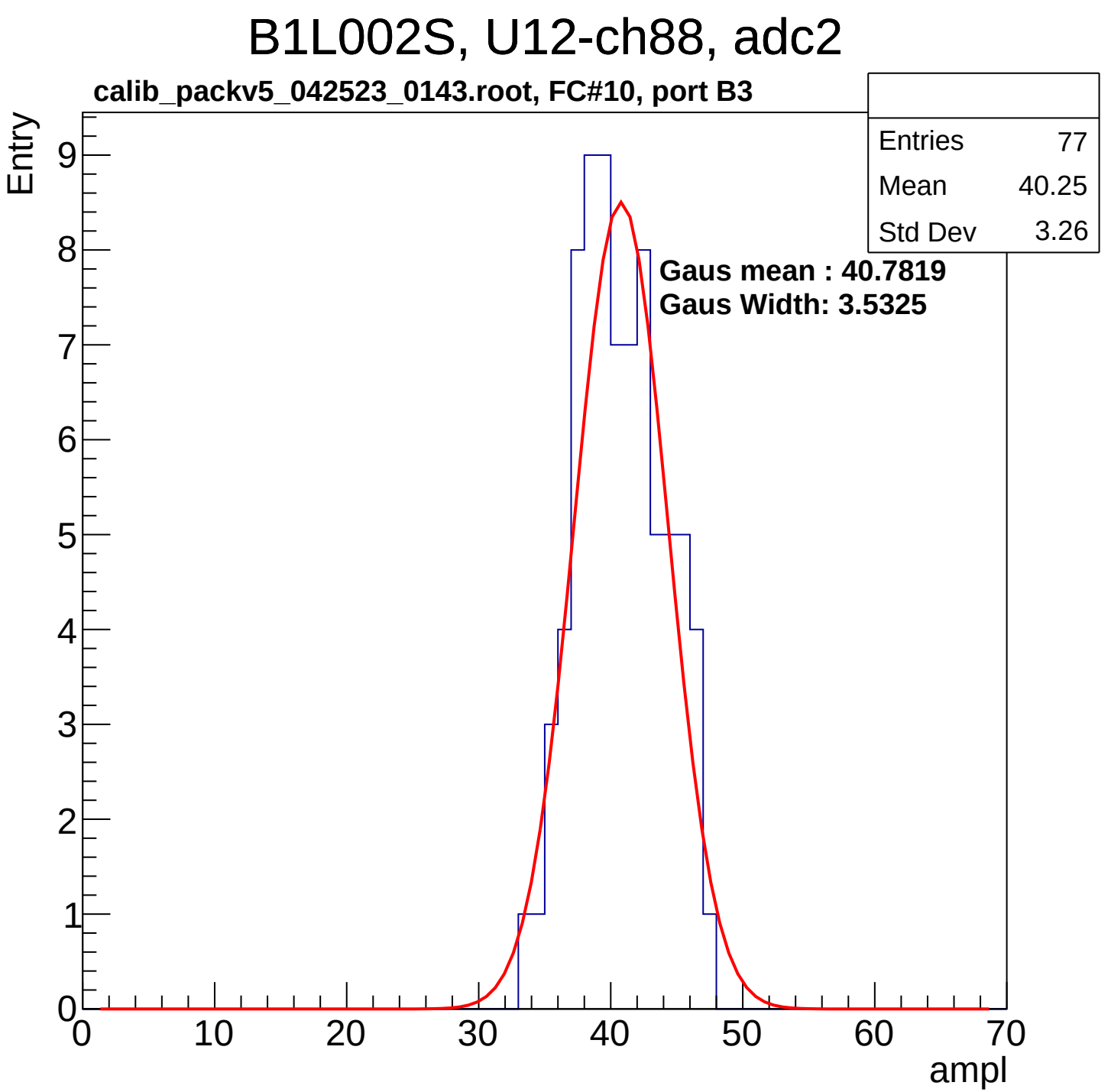
B1L002S, U12-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 40.25
Std Dev 3.26
Gaus mean : 40.7819
Gaus Width: 3.5325
Entry
ampl
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2
3
4
5
6
7
8
9
0
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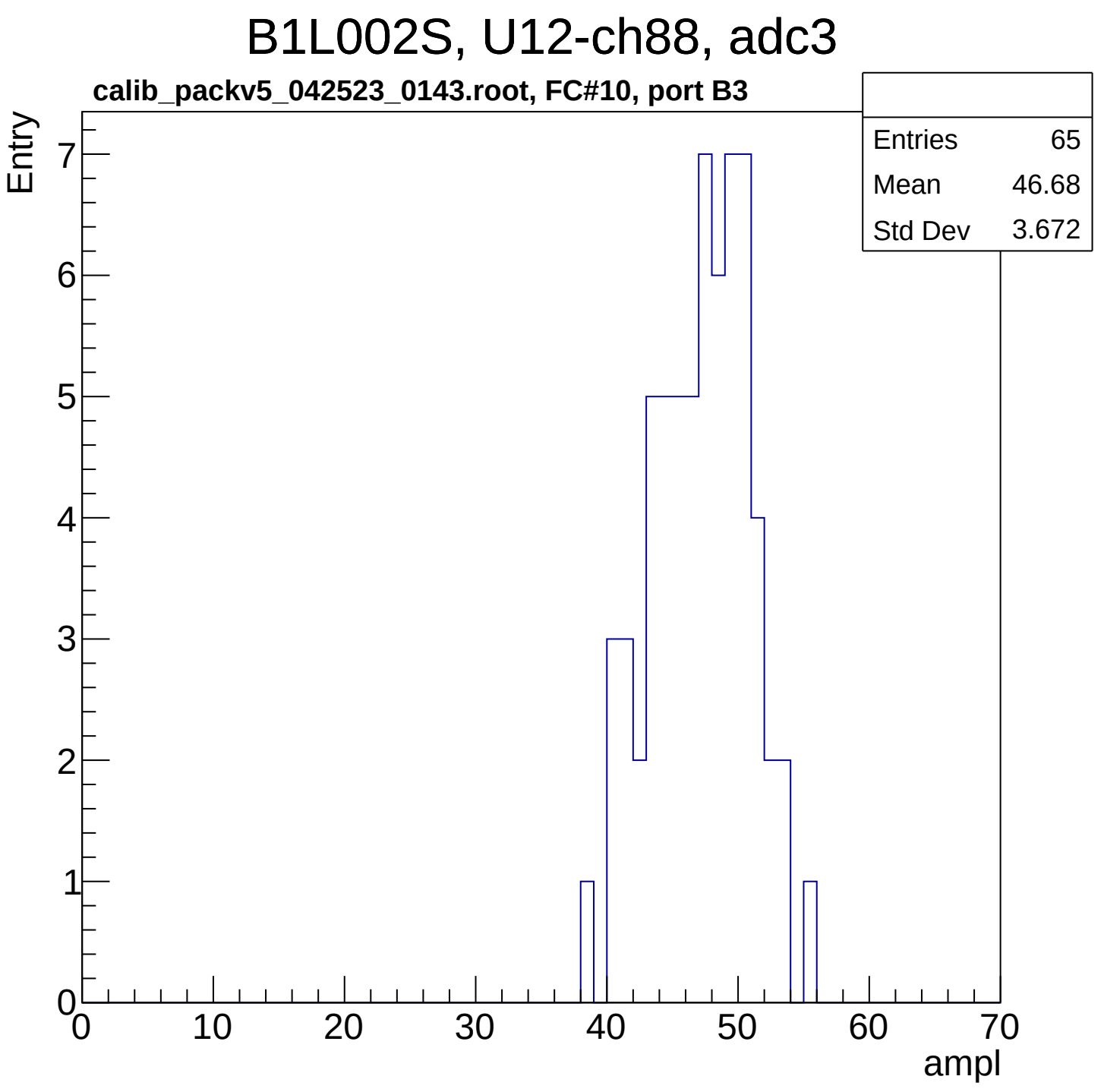
B1L002S, U12-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.68
Std Dev 3.672
Entry
ampl
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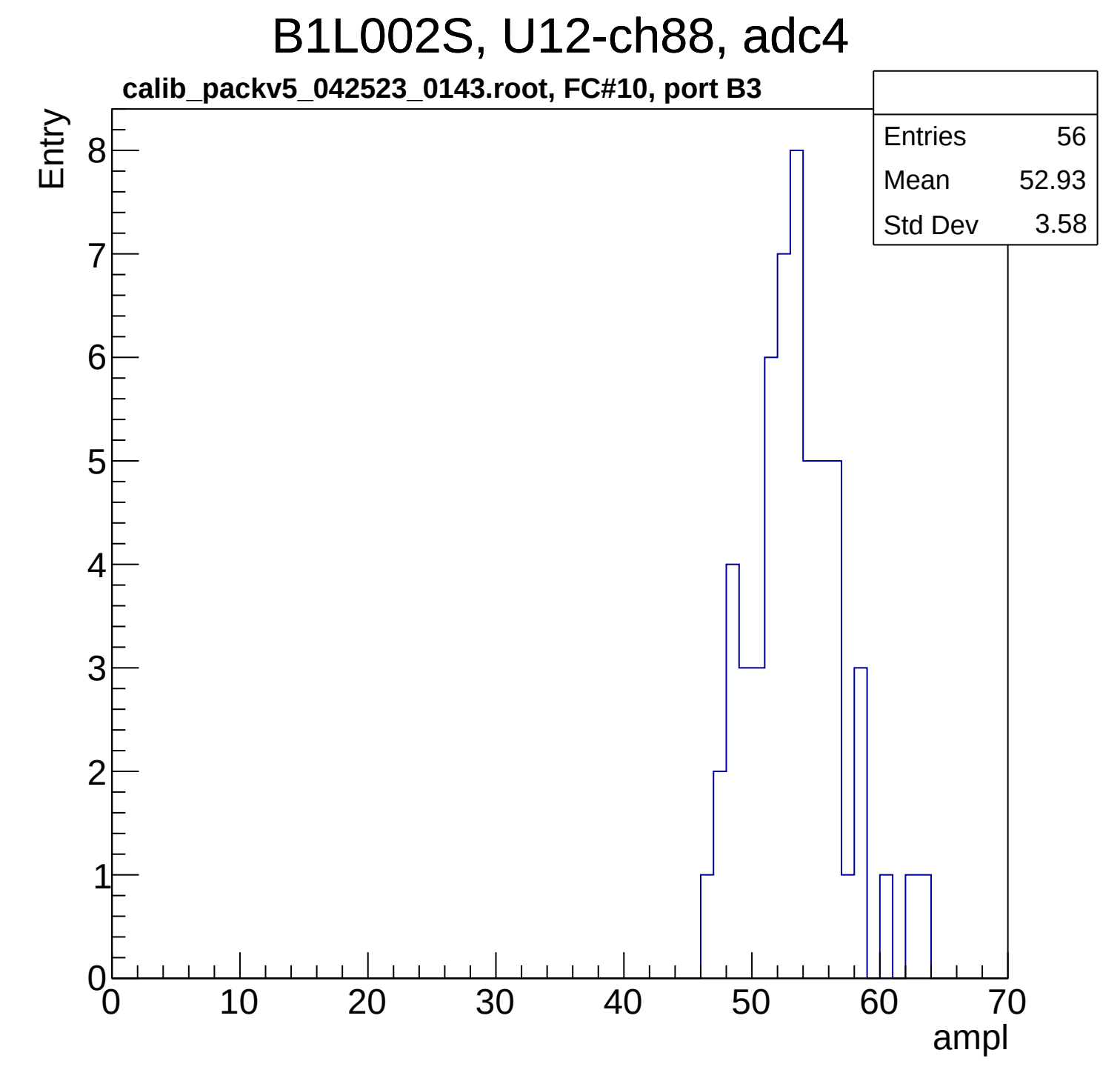
B1L002S, U12-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.93
Std Dev 3.58
Entry
ampl
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4
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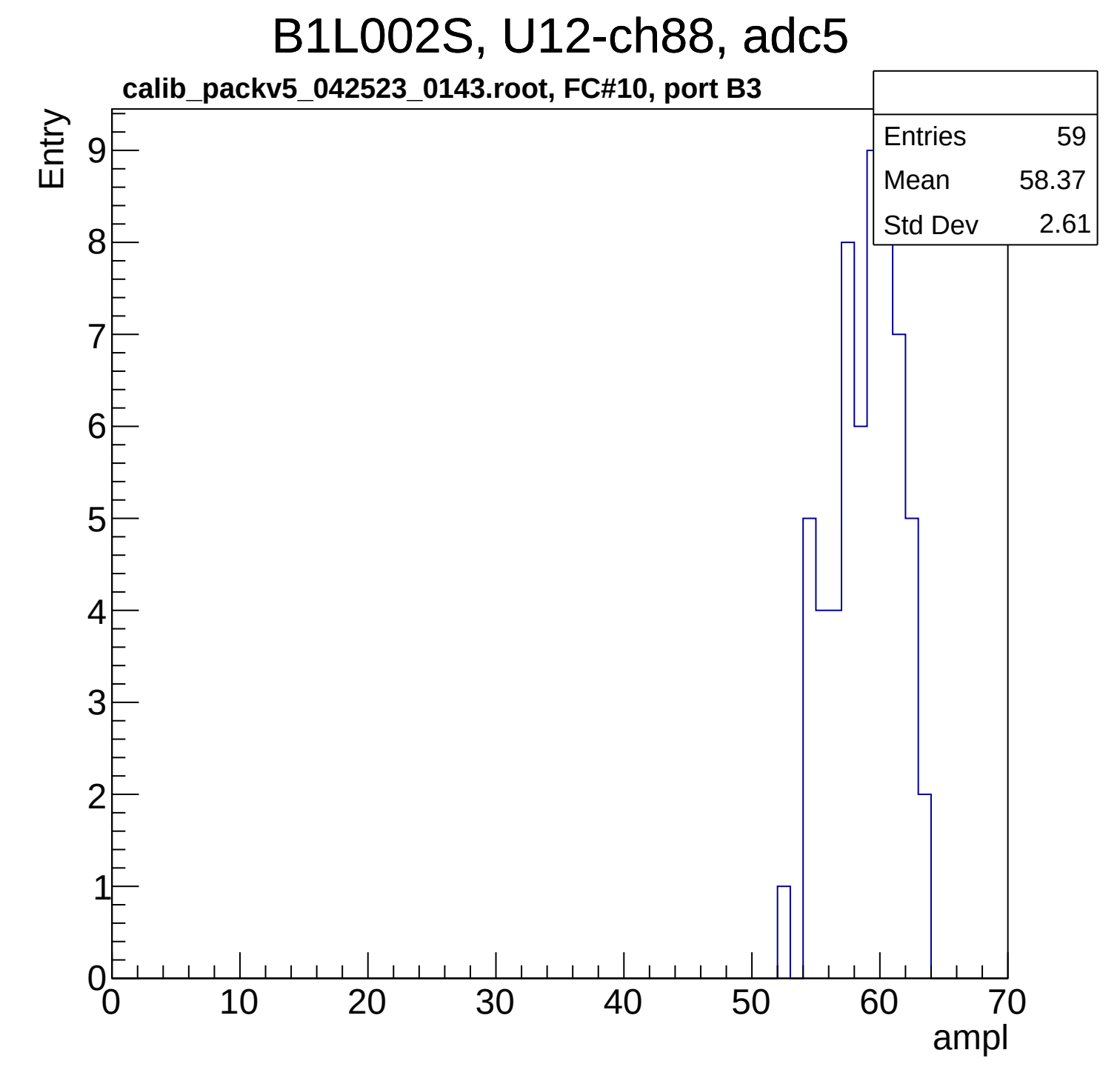
B1L002S, U12-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.37
Std Dev 2.61
Entry
ampl
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2
3
4
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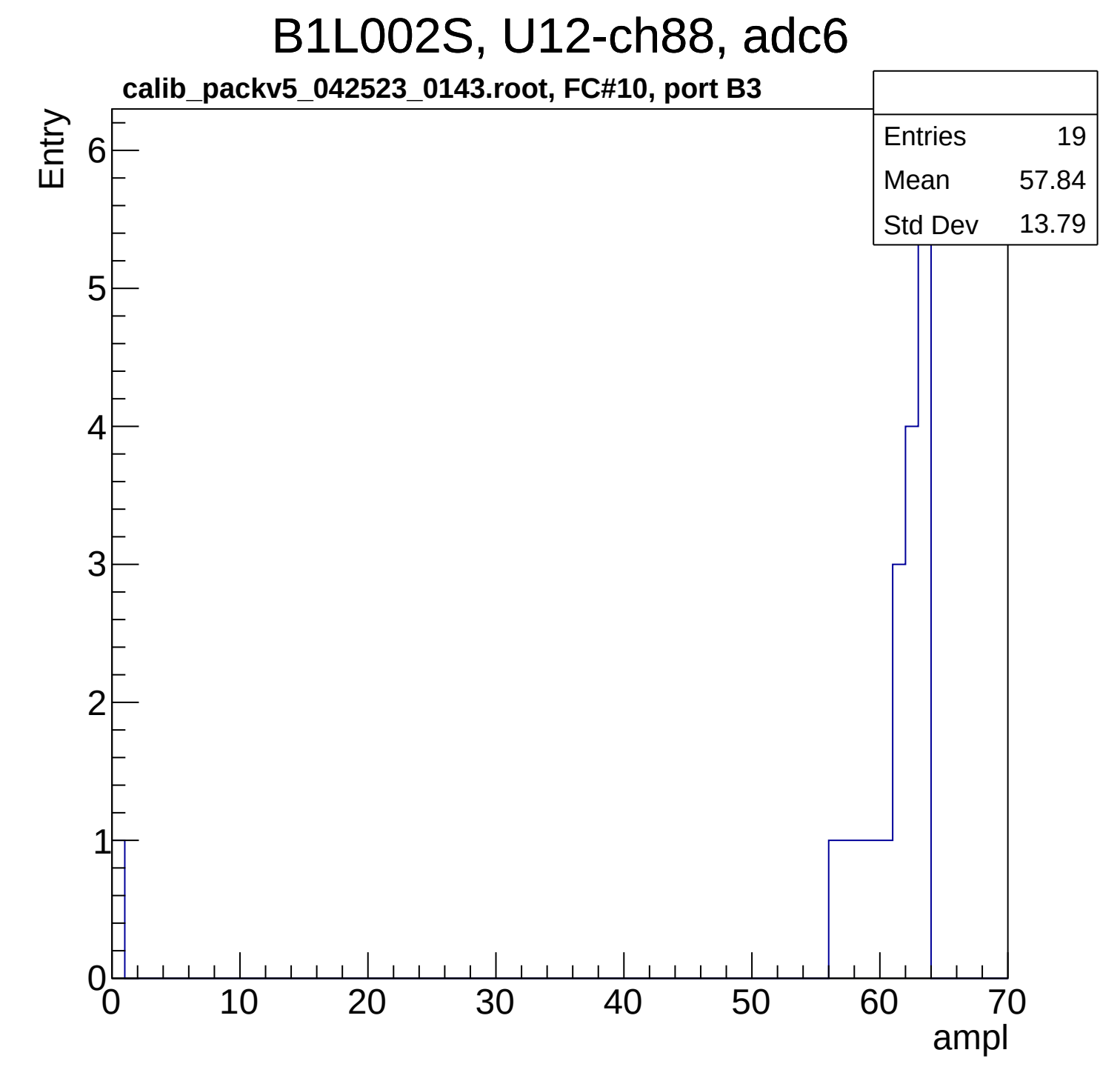
B1L002S, U12-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.84
Std Dev 13.79
Entry
0
1
2
3
4
5
6
0
10
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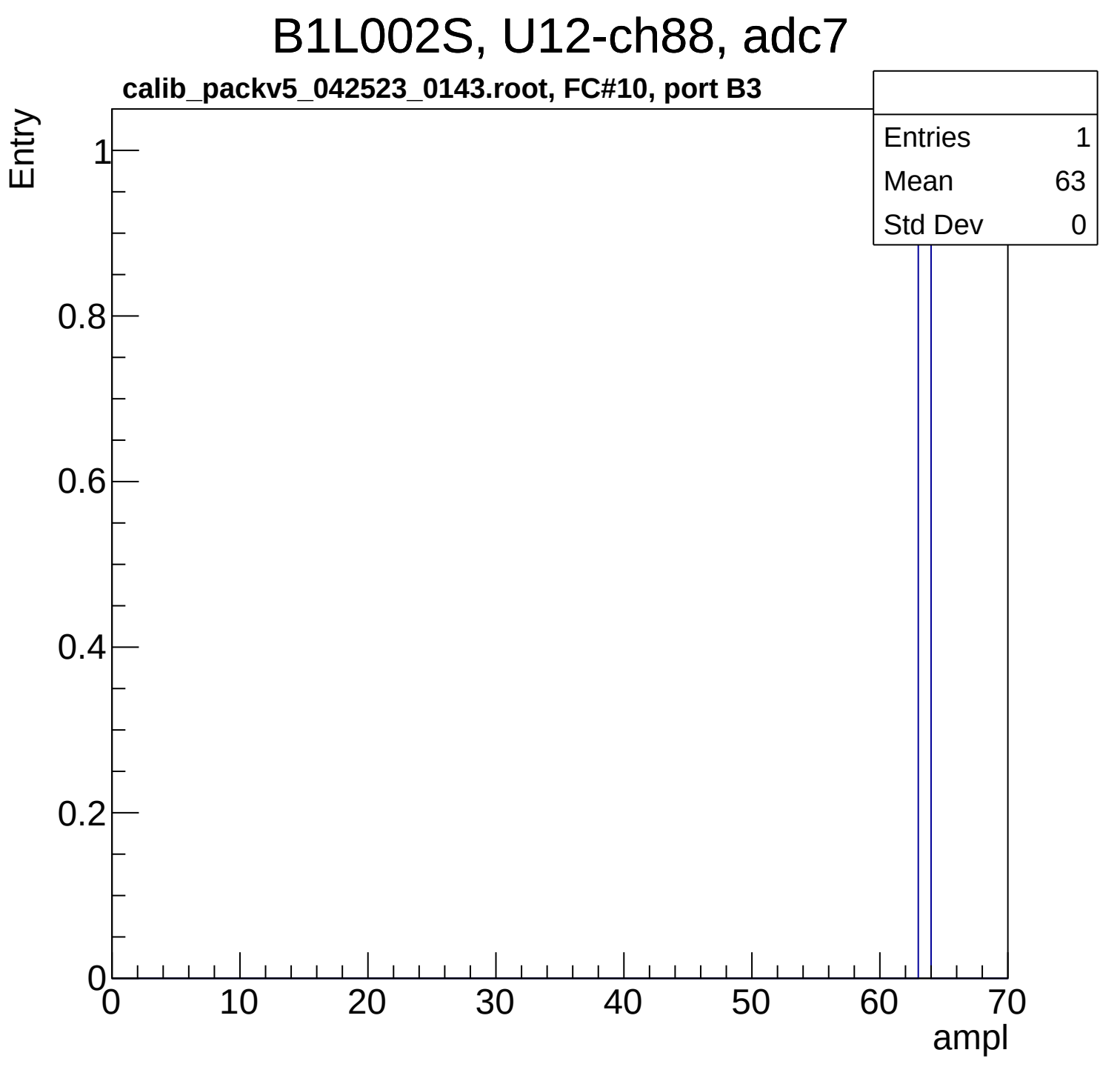
50
60
70
ampl


B1L002S, U12-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
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10
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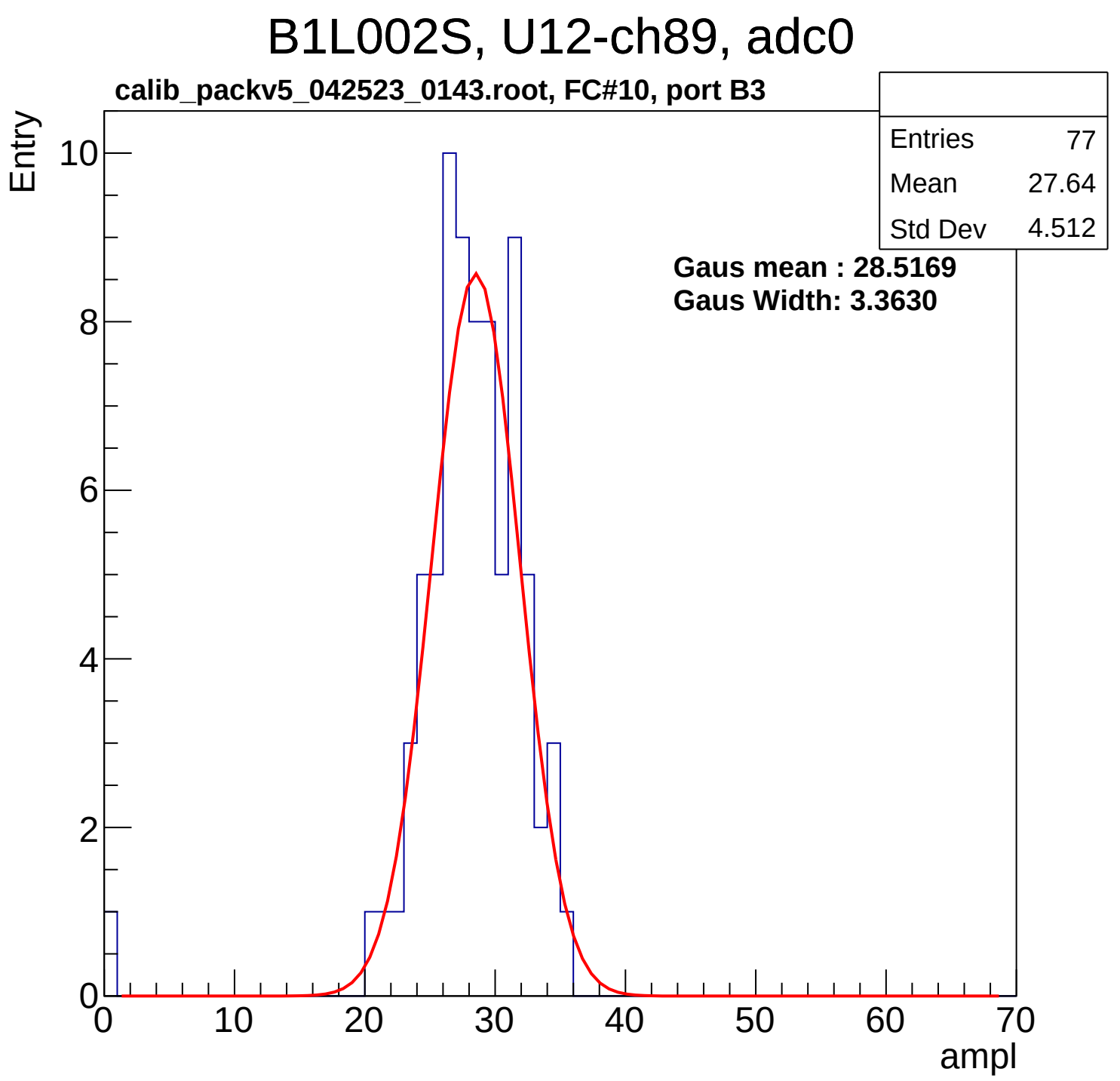
50
60
70
ampl


B1L002S, U12-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.64
Std Dev 4.512
Gaus mean : 28.5169
Gaus Width: 3.3630
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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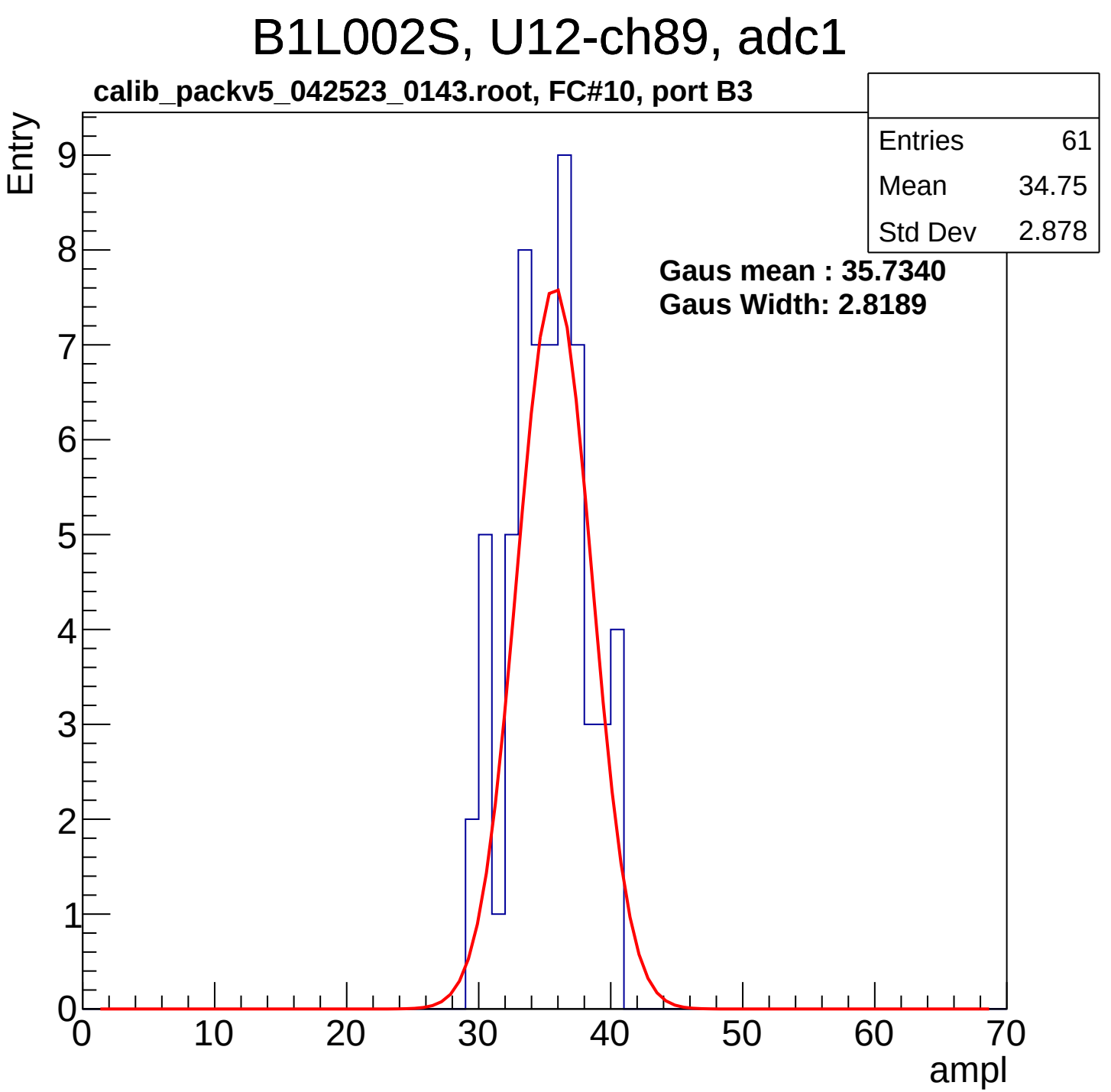
B1L002S, U12-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.75
Std Dev 2.878
Gaus mean : 35.7340
Gaus Width: 2.8189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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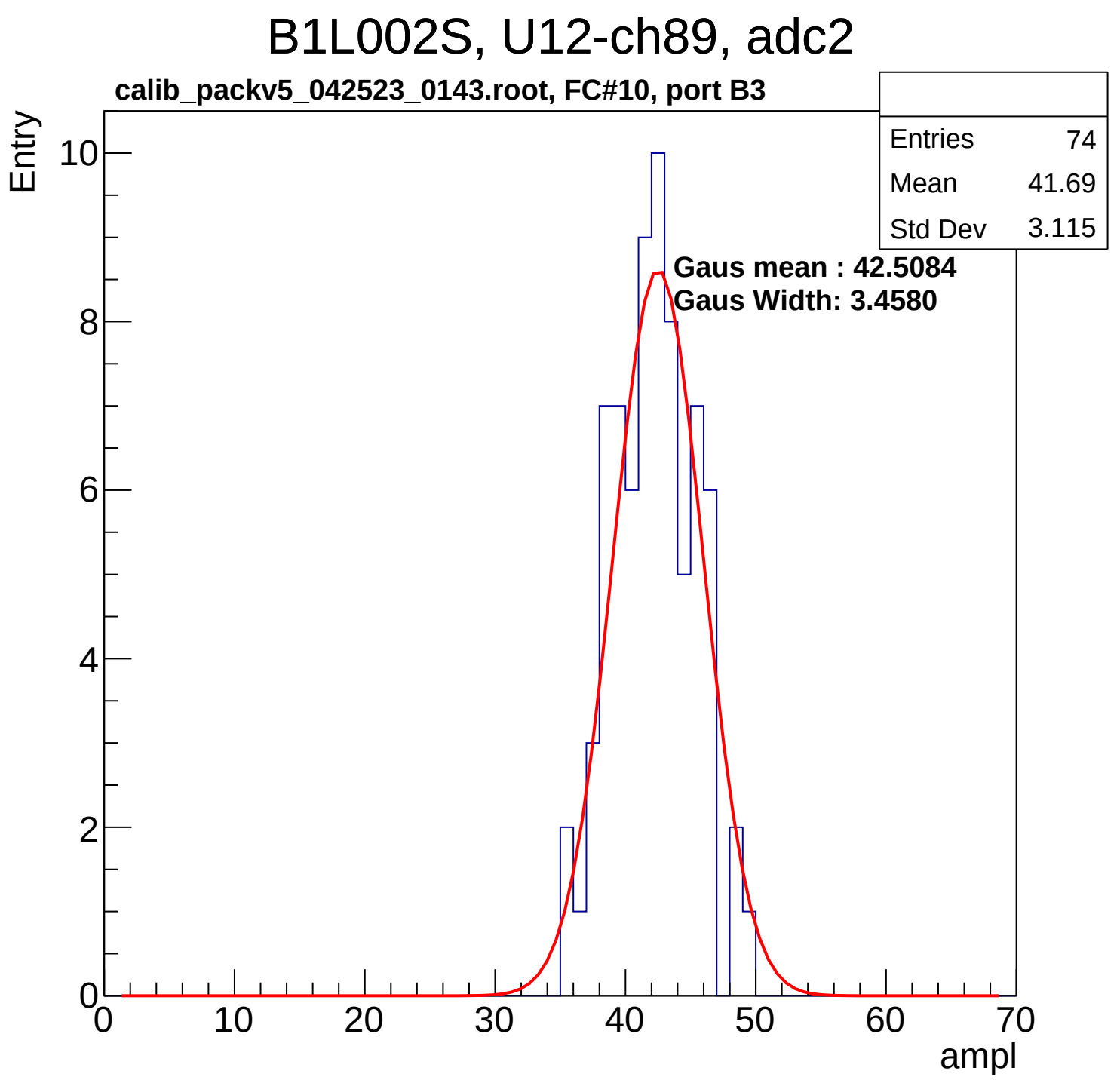
B1L002S, U12-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.69
Std Dev 3.115
Gaus mean : 42.5084
Gaus Width: 3.4580
Entry
ampl
0
2
4
6
8
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0
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40
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60
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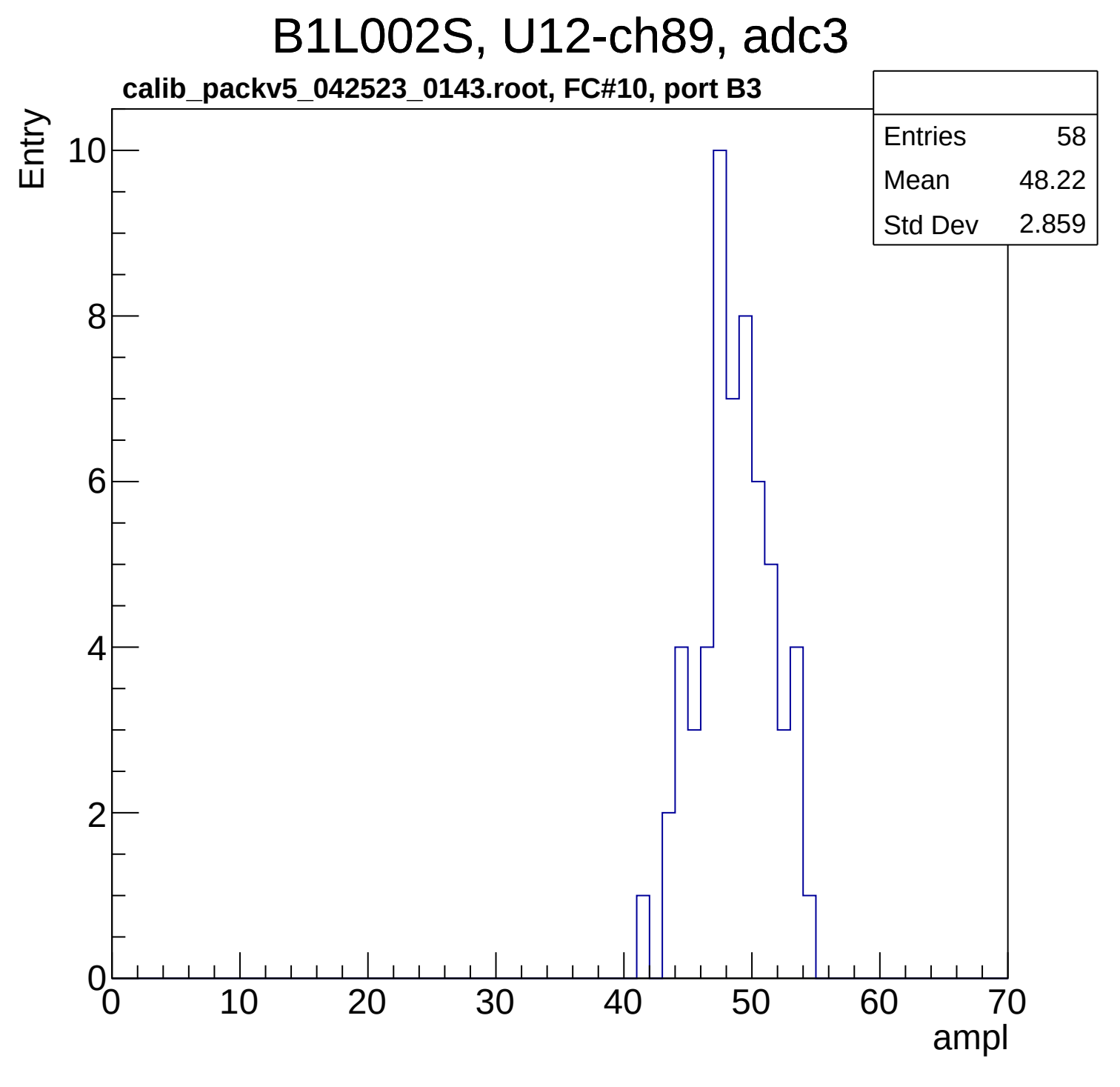
B1L002S, U12-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 48.22
Std Dev 2.859
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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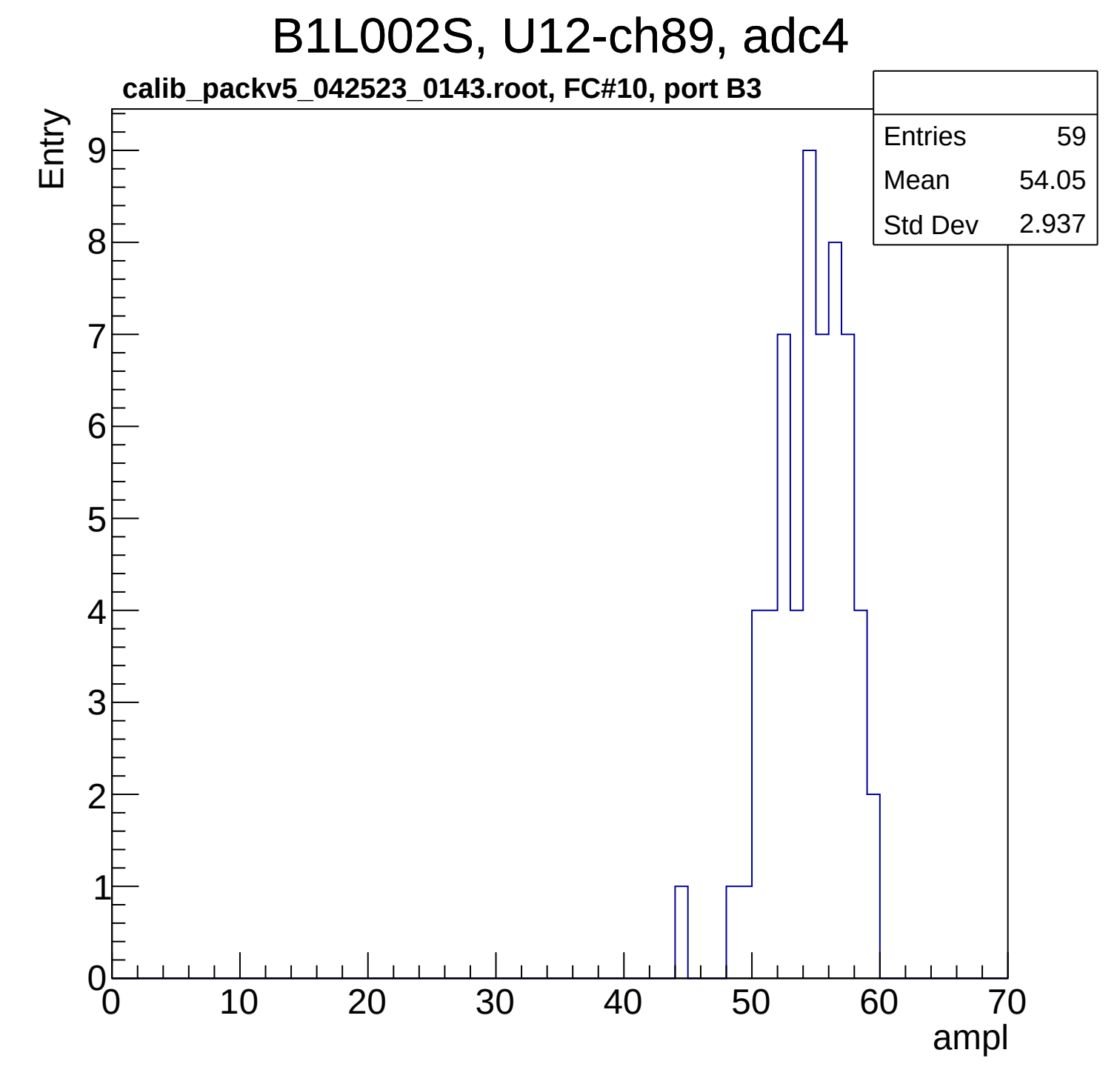
B1L002S, U12-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.05
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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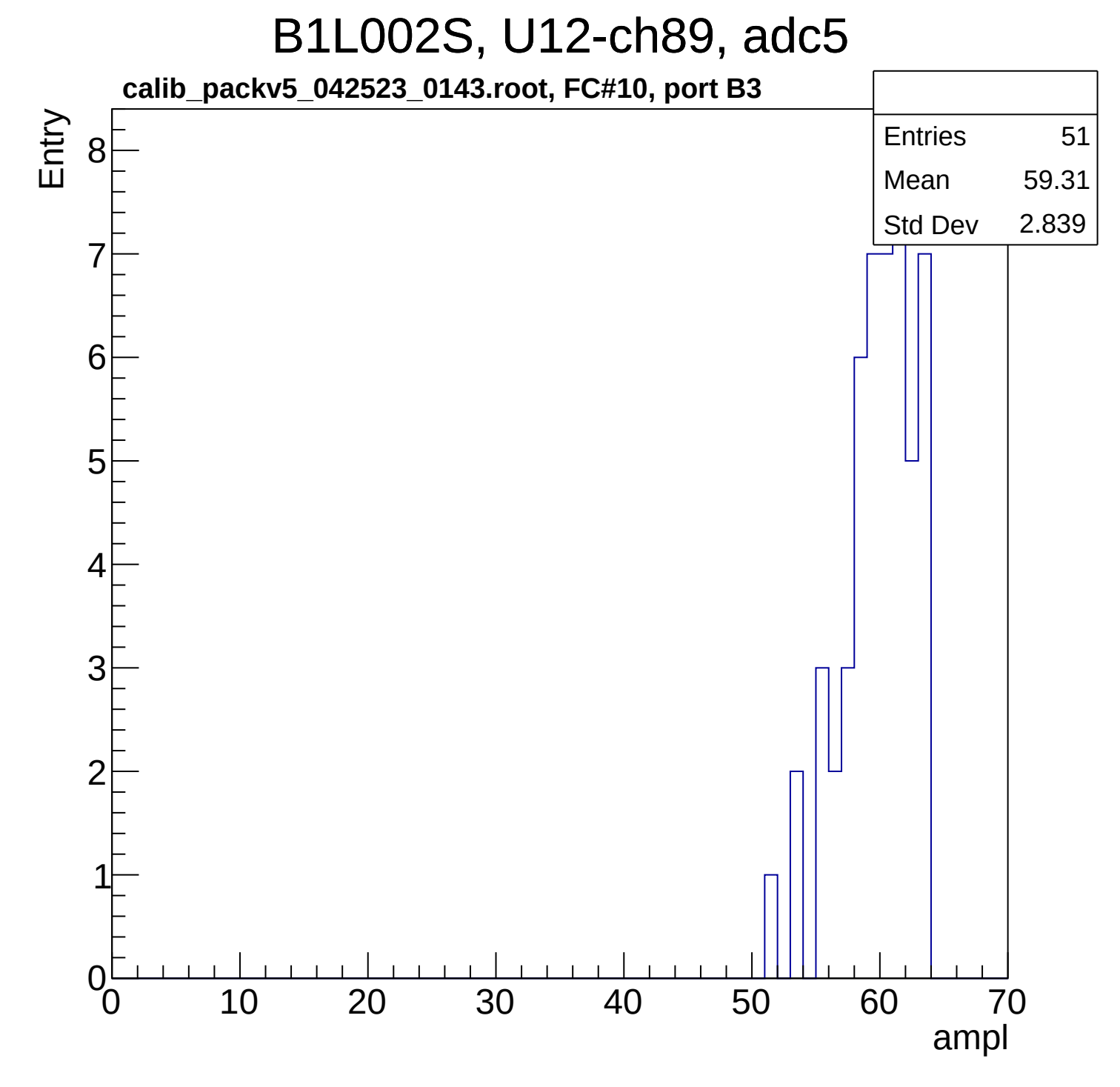
B1L002S, U12-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 59.31
Std Dev 2.839
Entry
ampl
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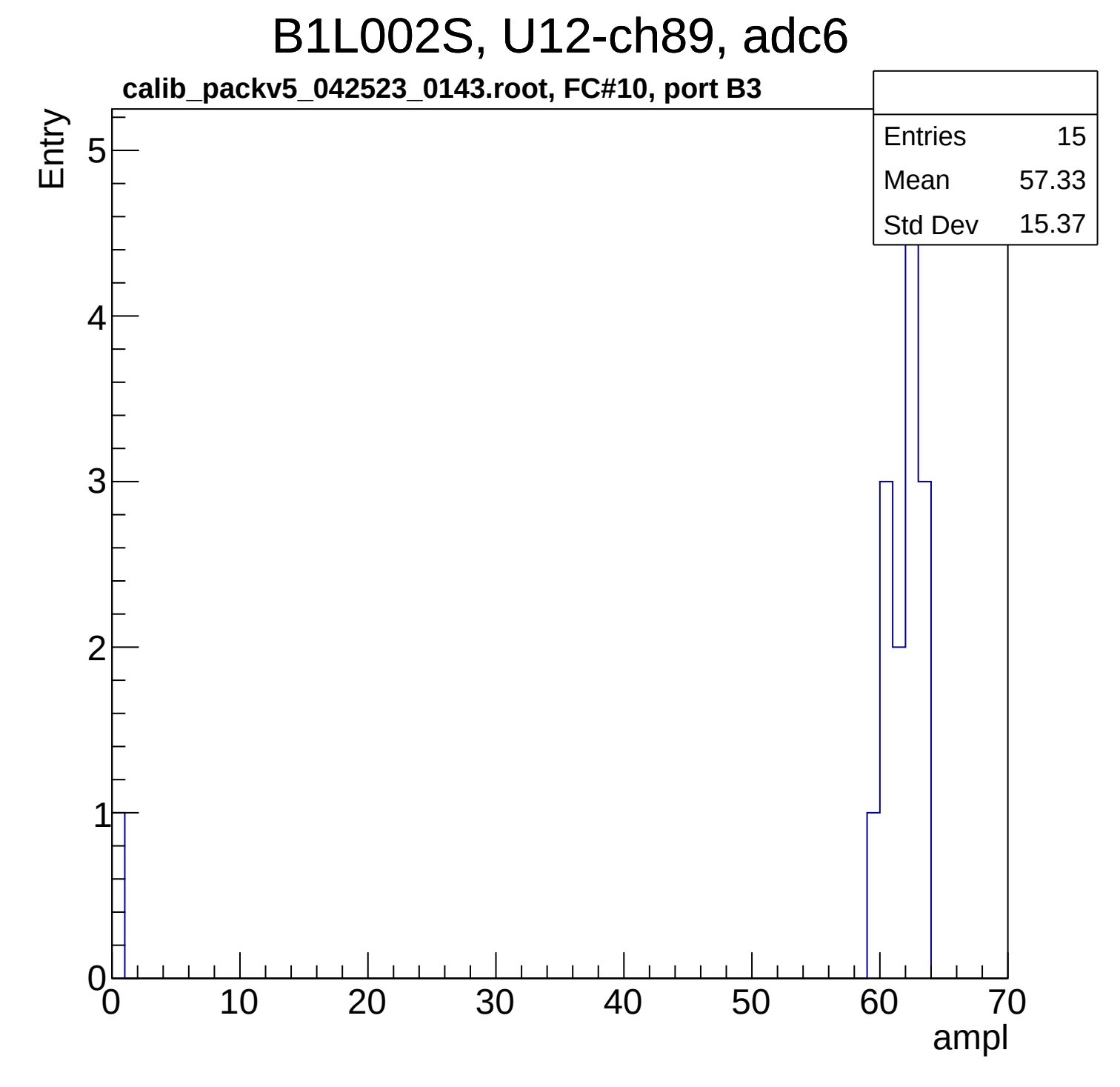
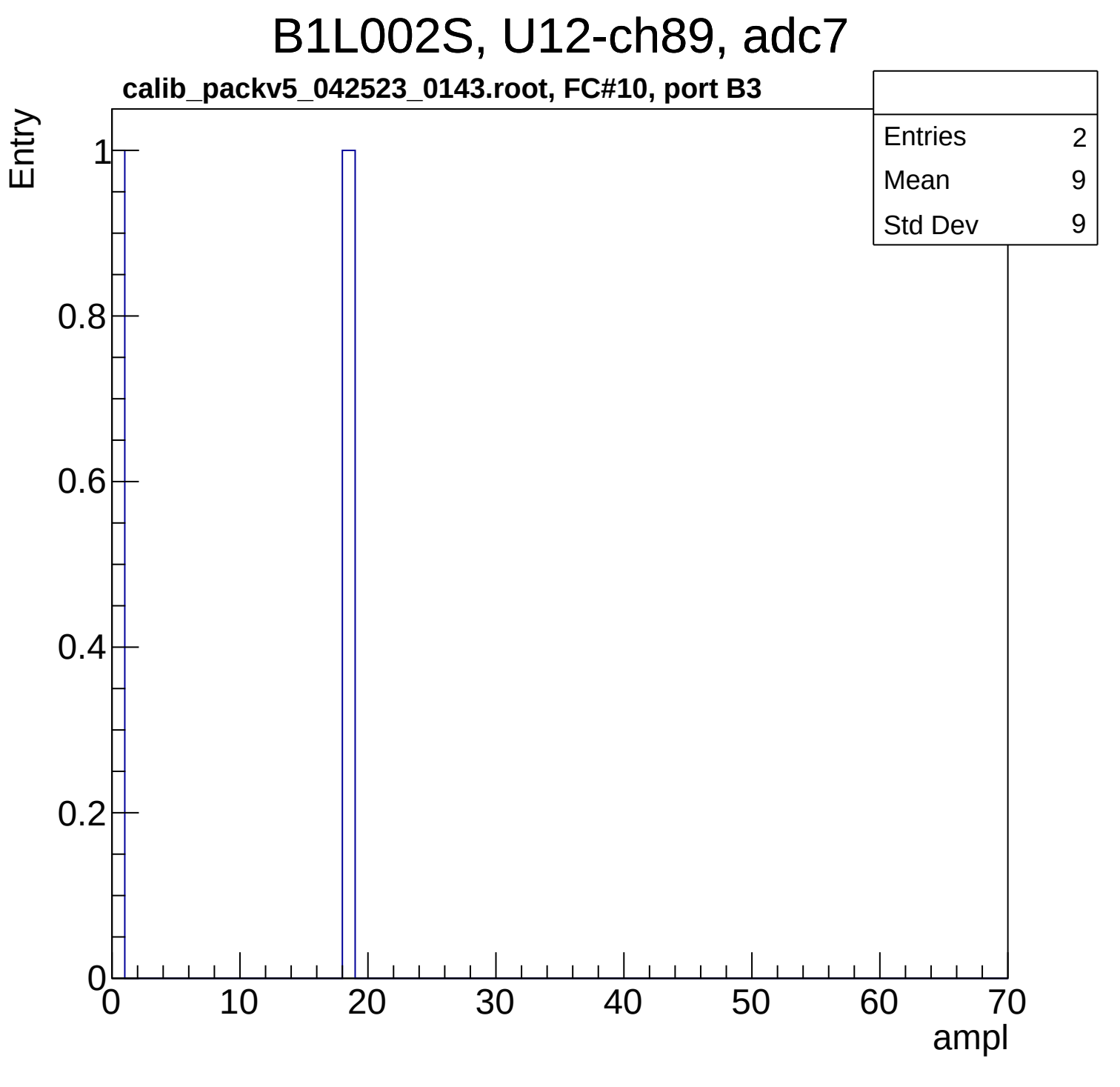
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.33
Std Dev 15.37
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9
Std Dev 9
Entry
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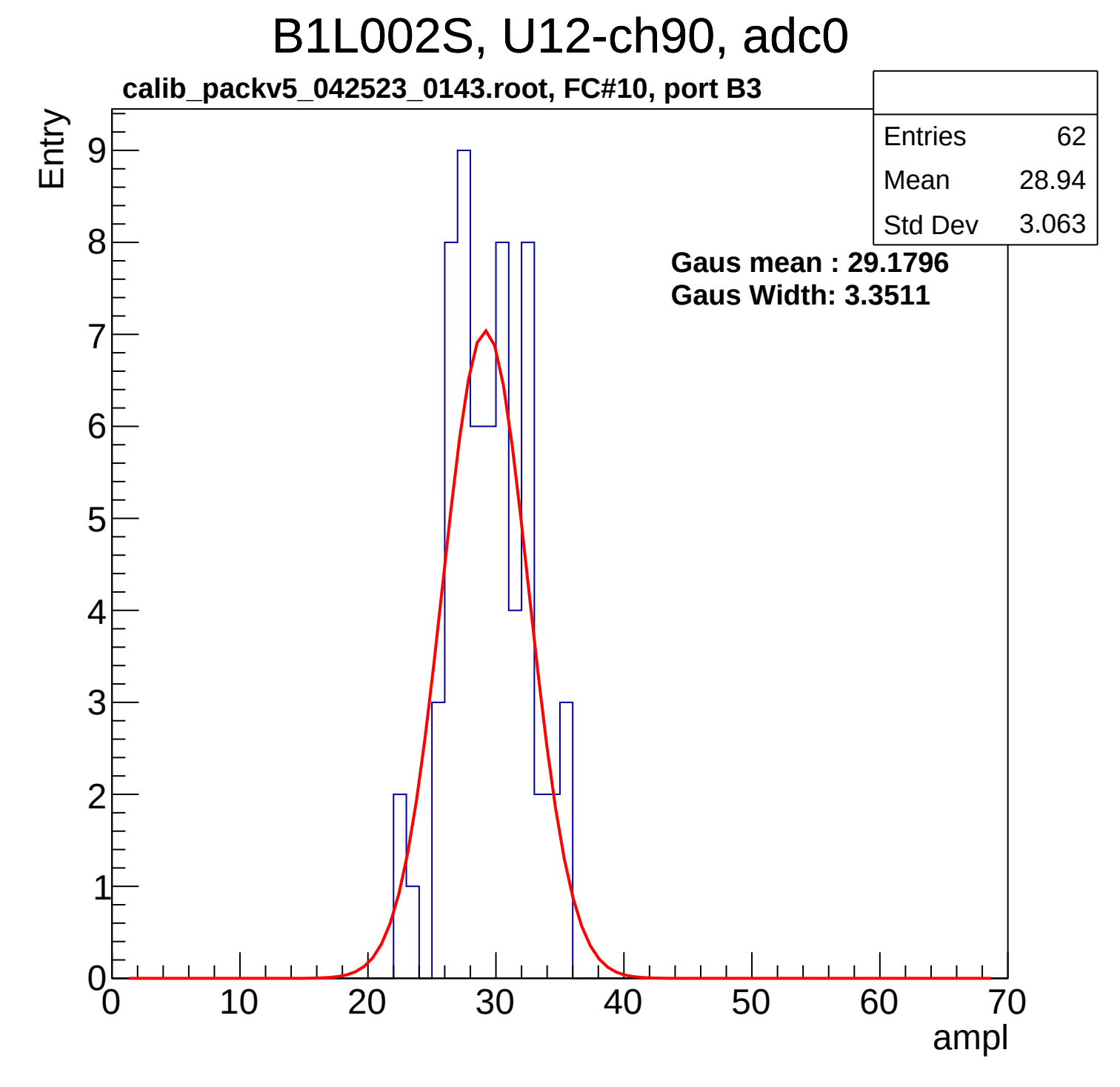
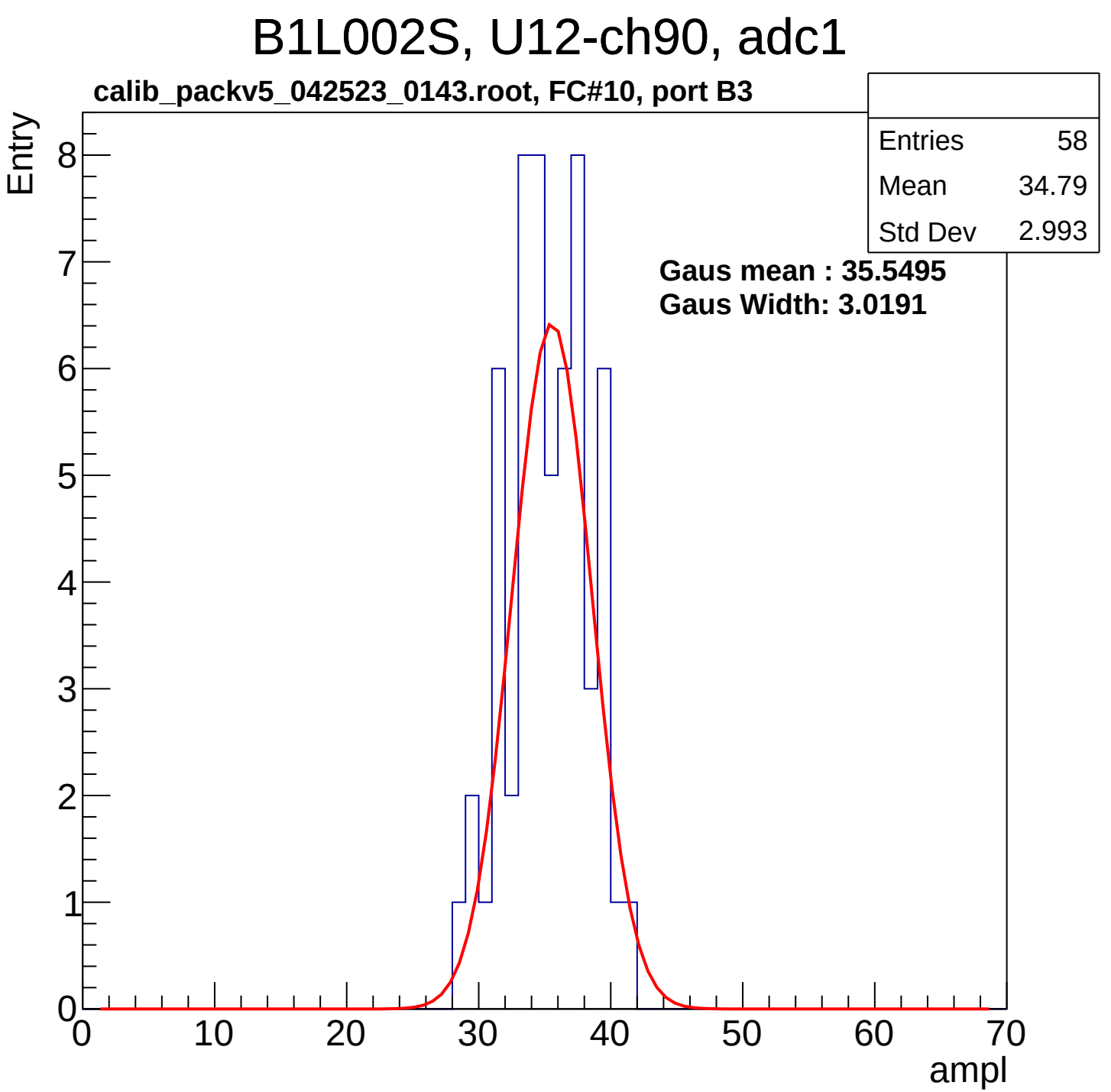
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 28.94
Std Dev 3.063
Gaus mean : 29.1796
Gaus Width: 3.3511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.79
Std Dev 2.993
Gaus mean : 35.5495
Gaus Width: 3.0191
Entry
ampl
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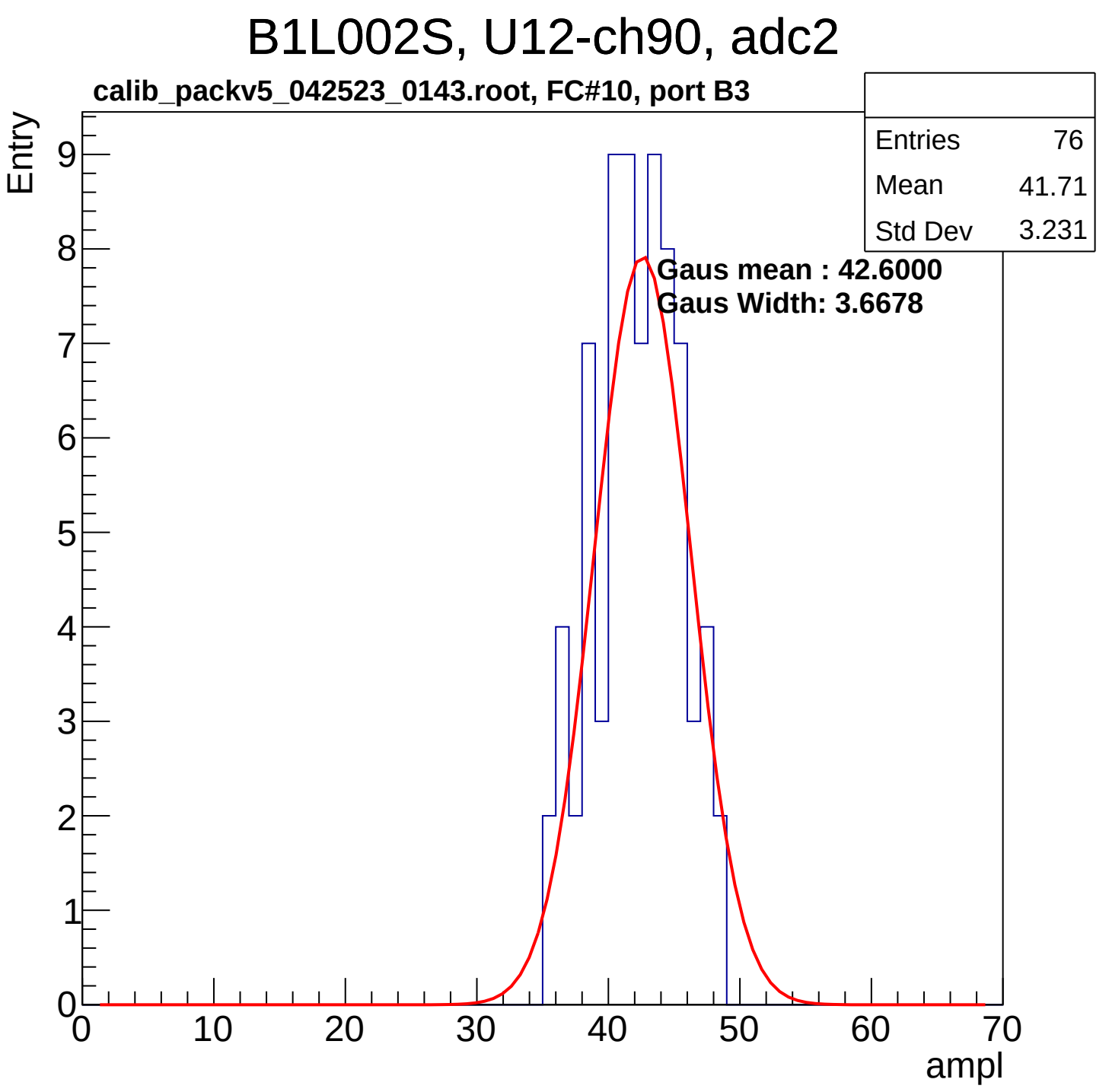
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 41.71
Std Dev 3.231
Gaus mean : 42.6000
Gaus Width: 3.6678
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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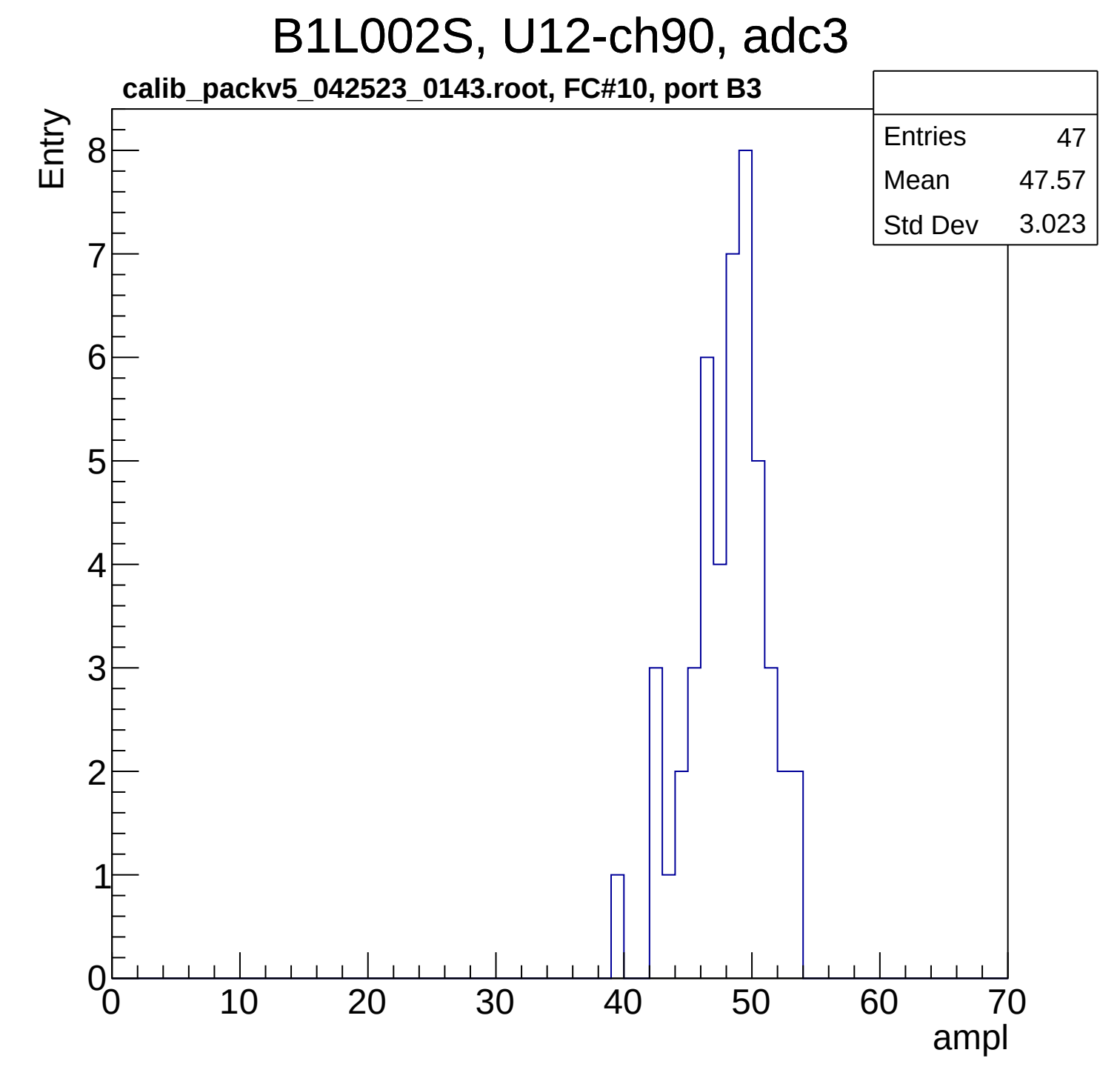
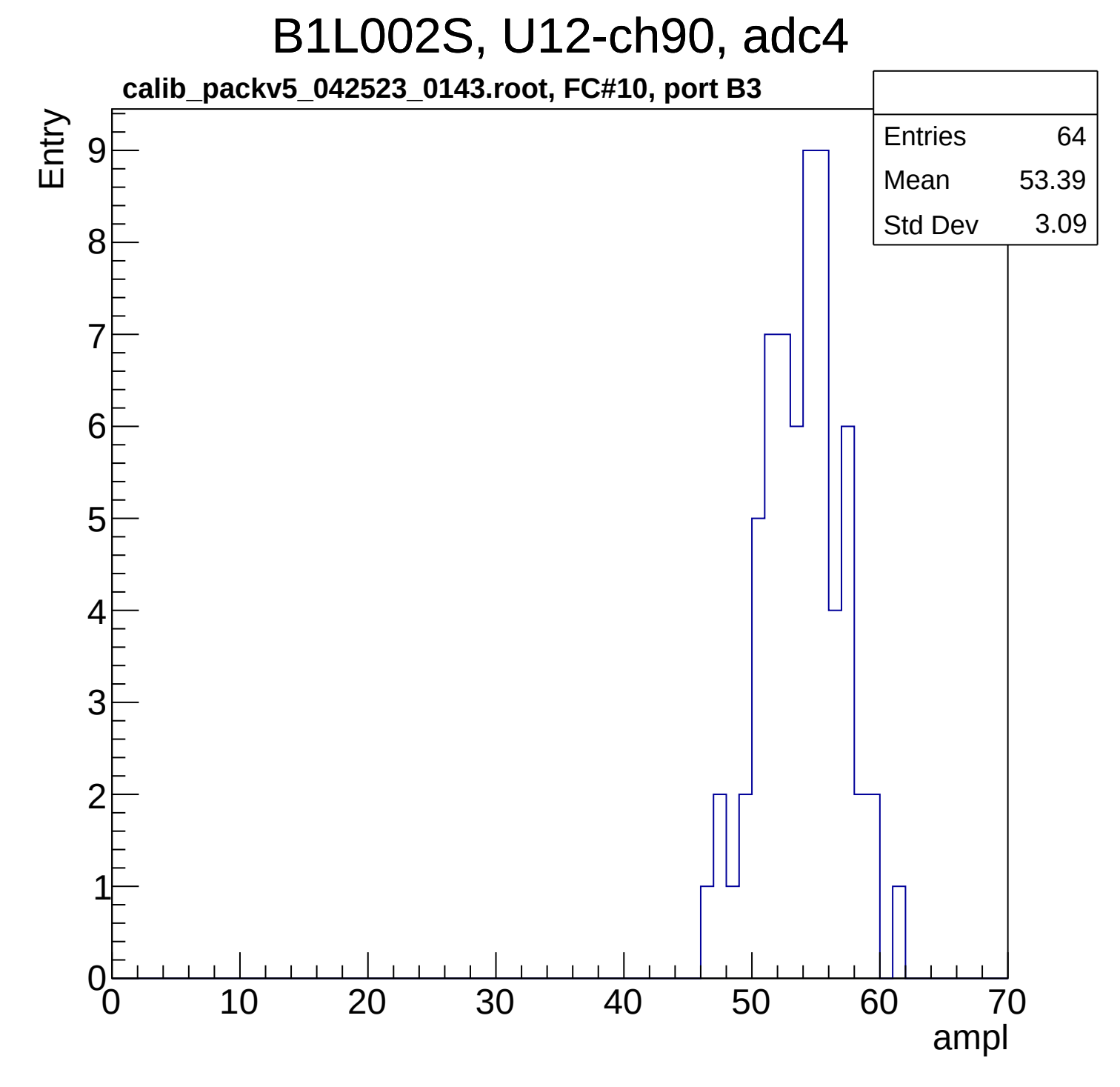

B1L002S, U12-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 47.57
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.39
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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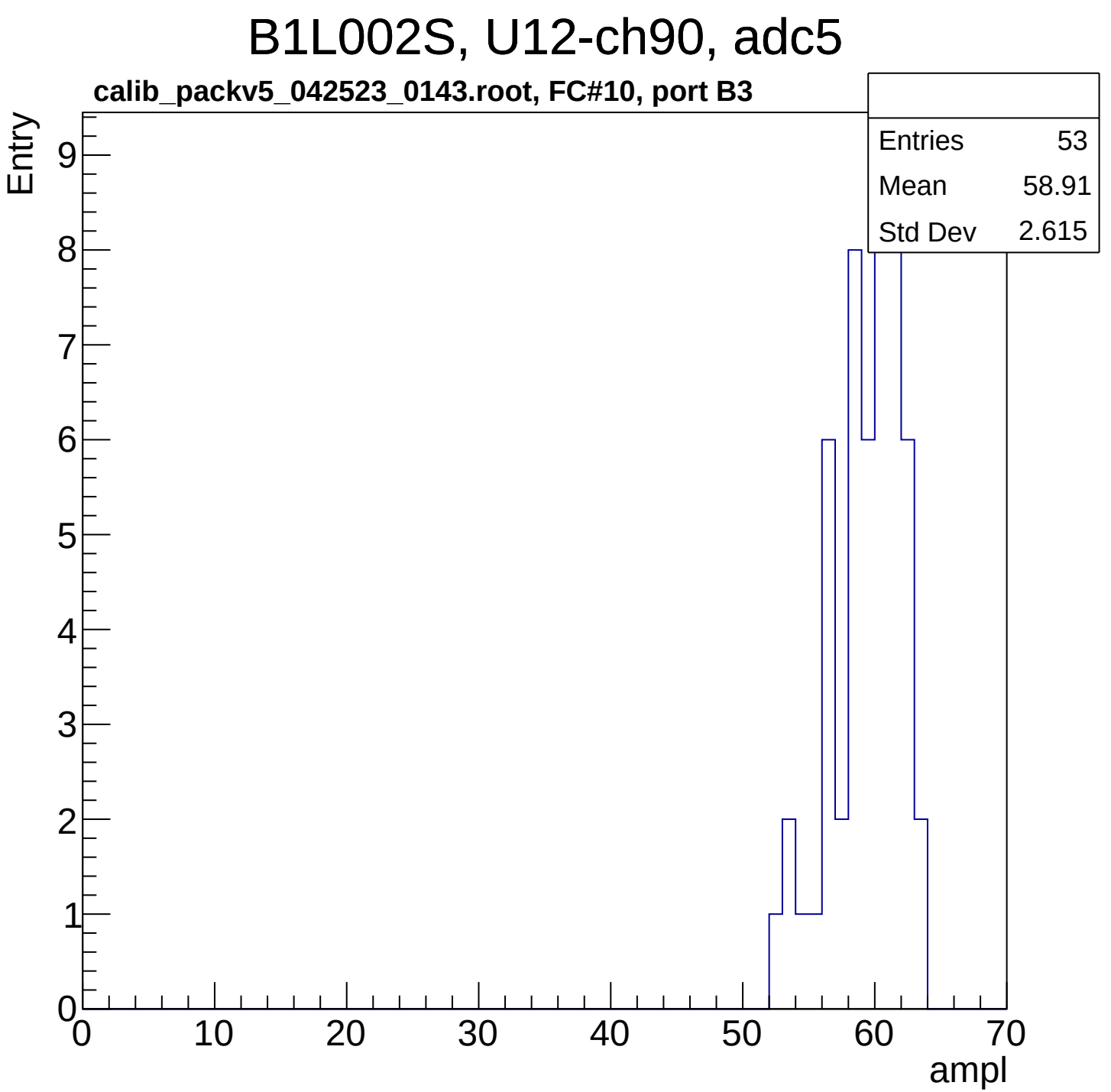
B1L002S, U12-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.91
Std Dev 2.615
Entry
ampl
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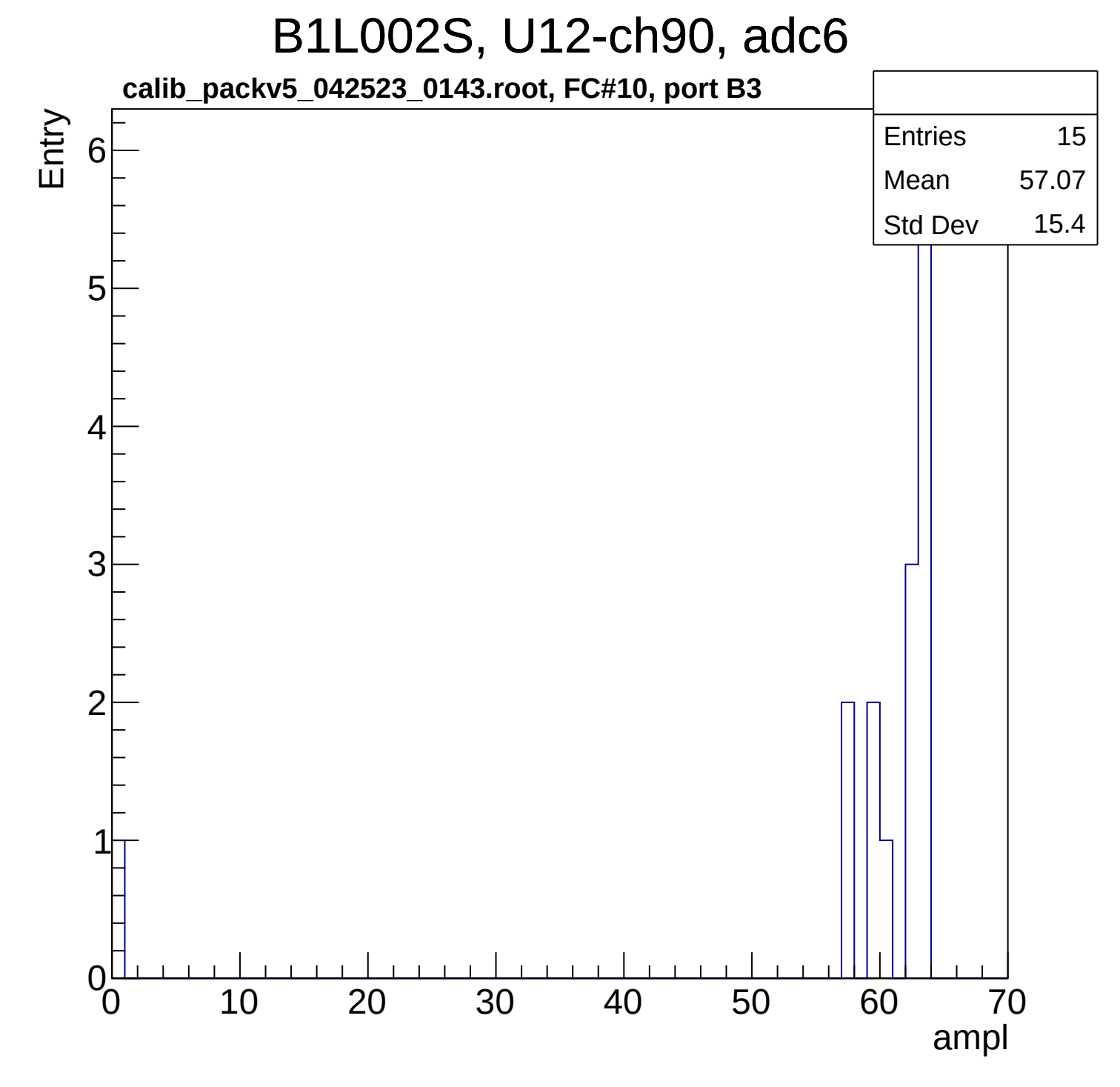
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.07
Std Dev 15.4
Entry
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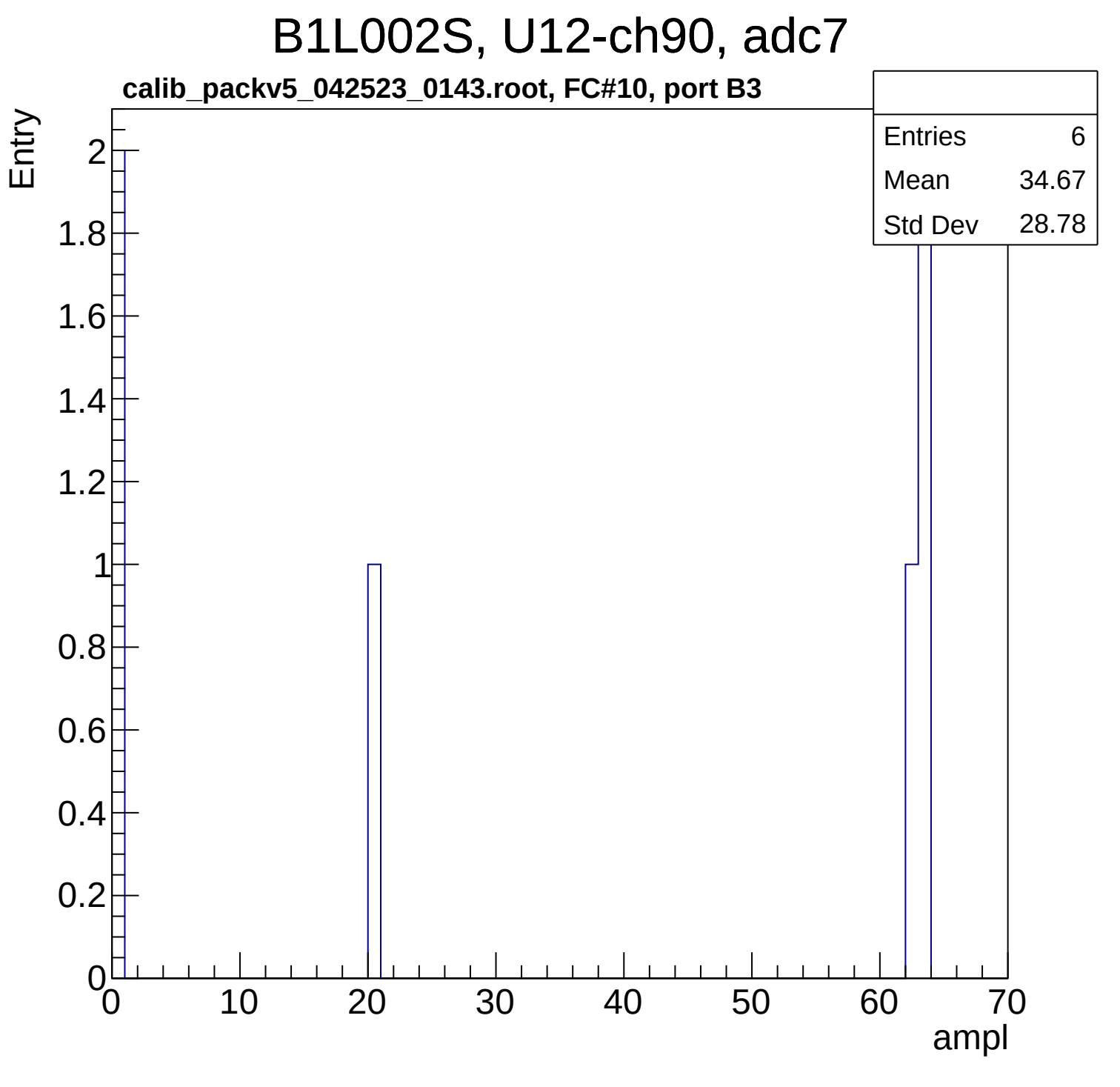
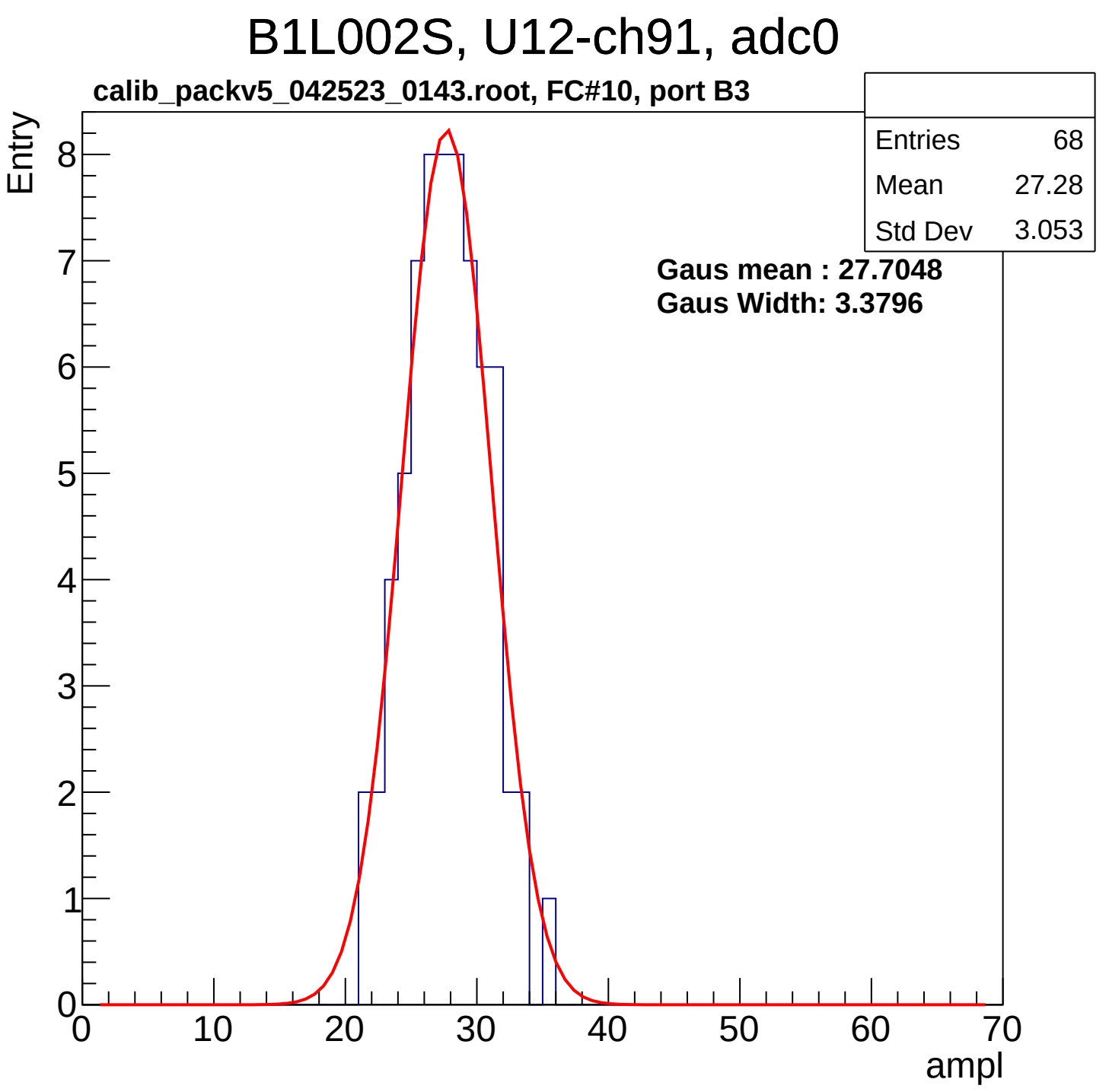
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 34.67
Std Dev 28.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.28
Std Dev 3.053
Gaus mean : 27.7048
Gaus Width: 3.3796
Entry
ampl
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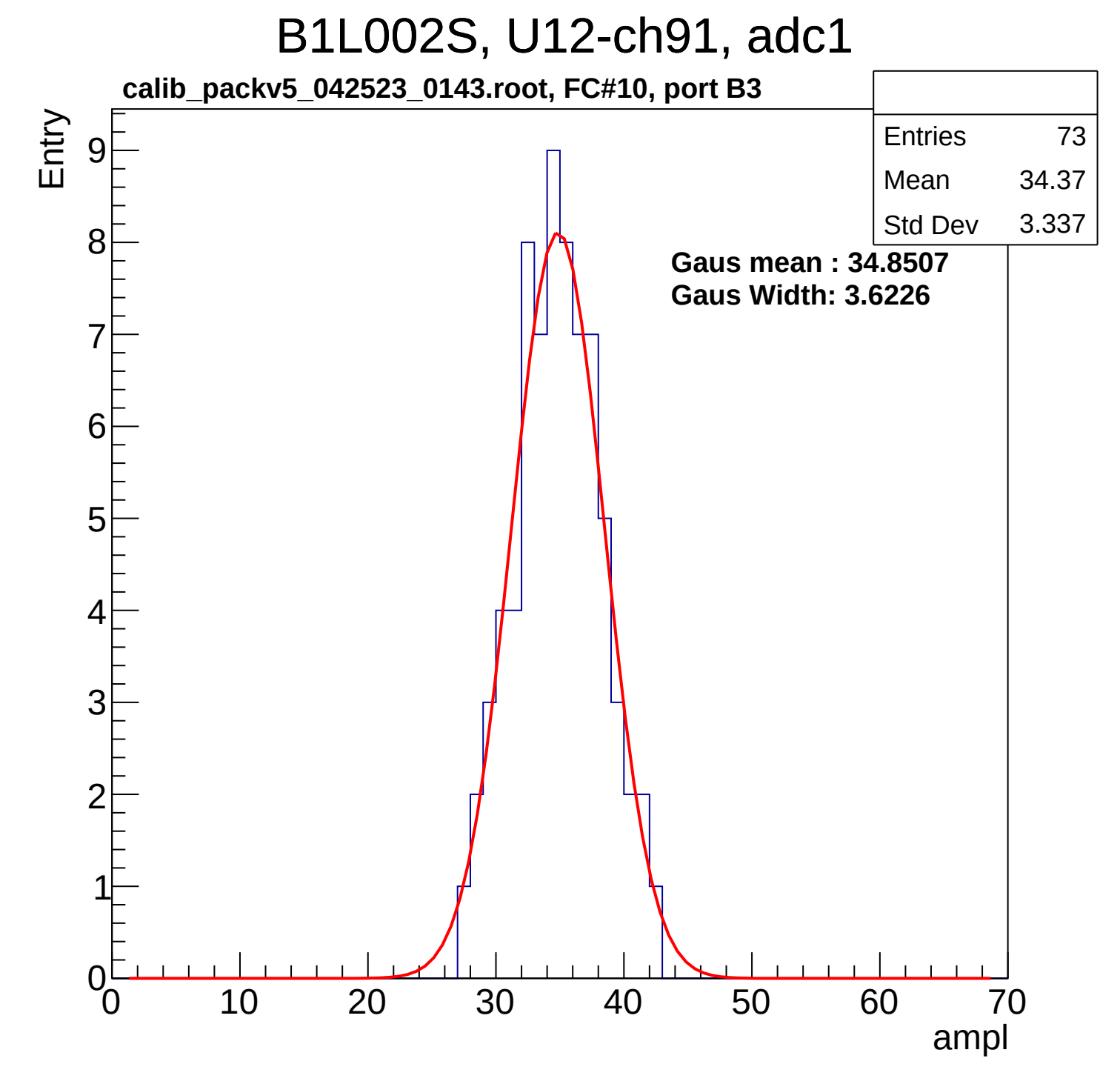
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.37
Std Dev 3.337
Gaus mean : 34.8507
Gaus Width: 3.6226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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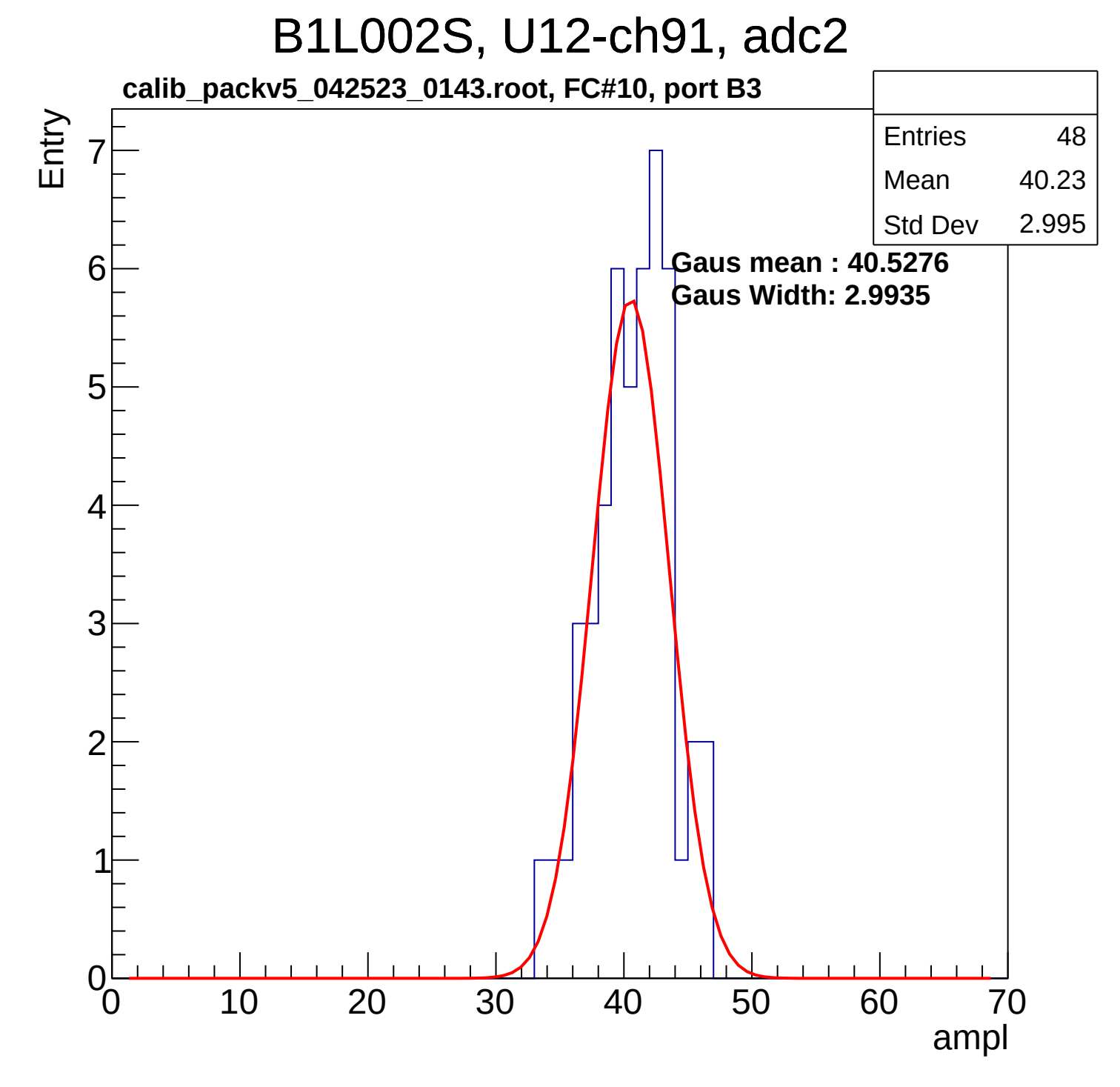
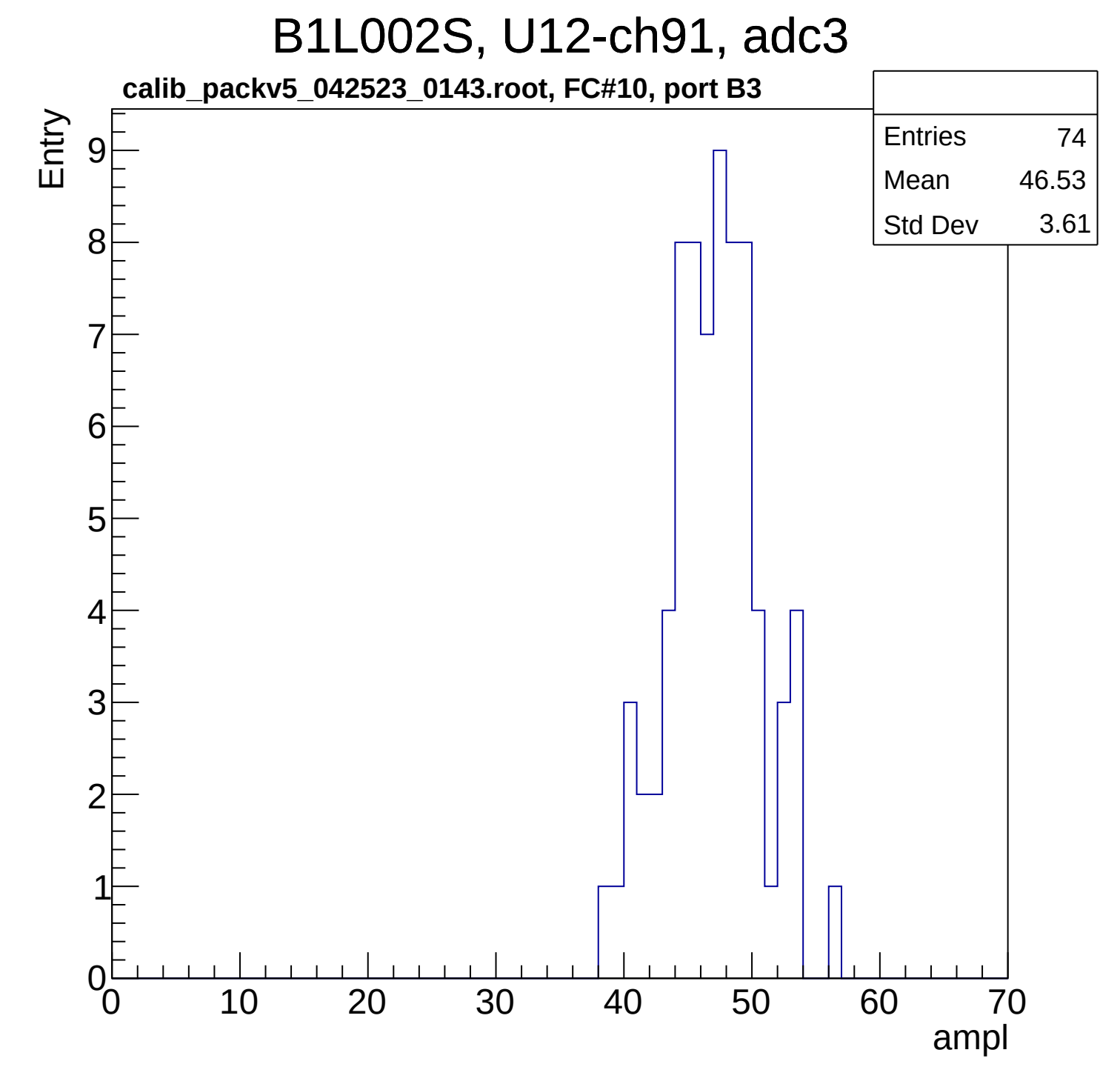

B1L002S, U12-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 40.23
Std Dev 2.995
Gaus mean : 40.5276
Gaus Width: 2.9935
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.53
Std Dev 3.61
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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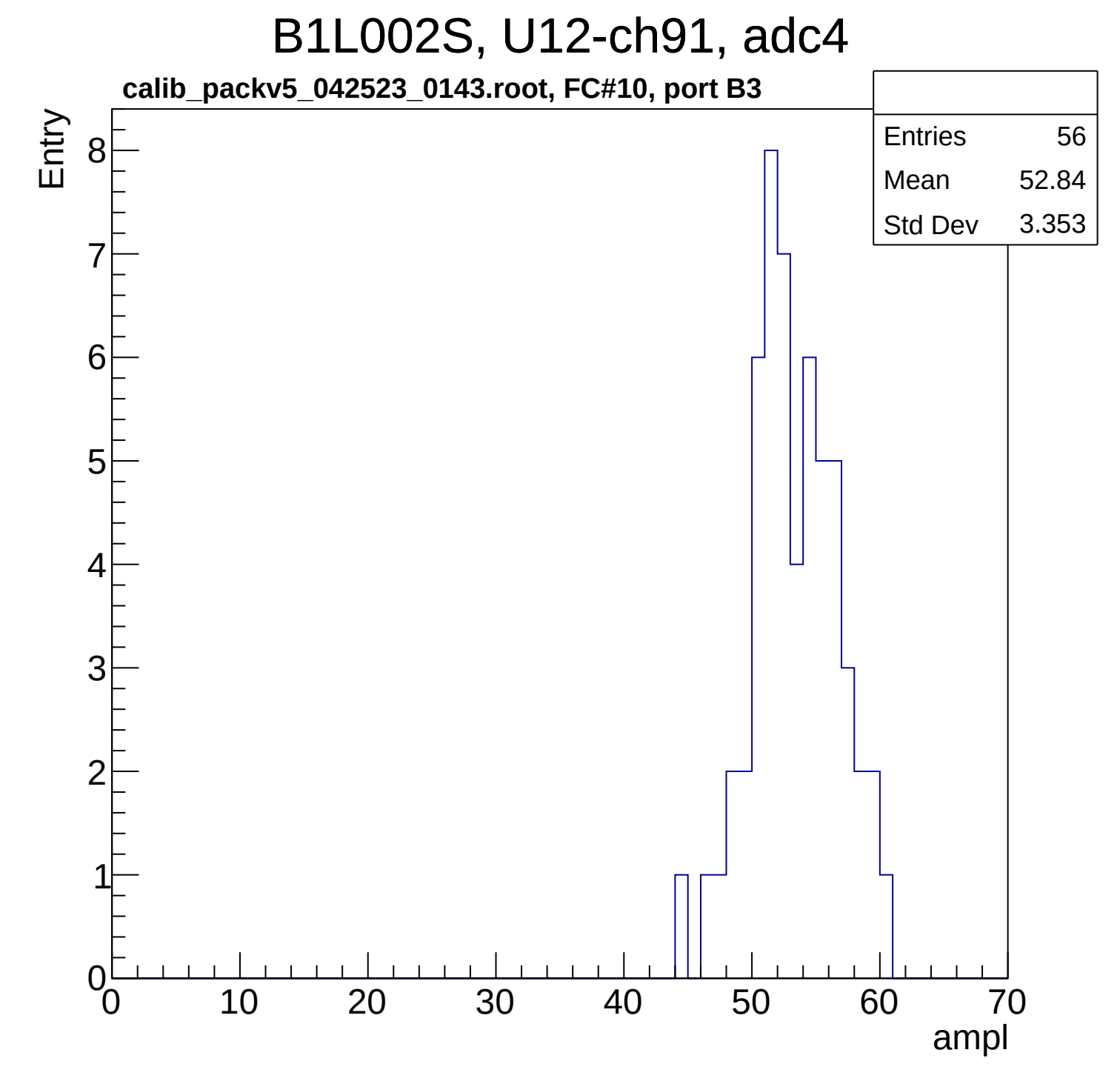
40
50
60
70


B1L002S, U12-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.84
Std Dev 3.353
Entry
ampl
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2
3
4
5
6
7
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0
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20
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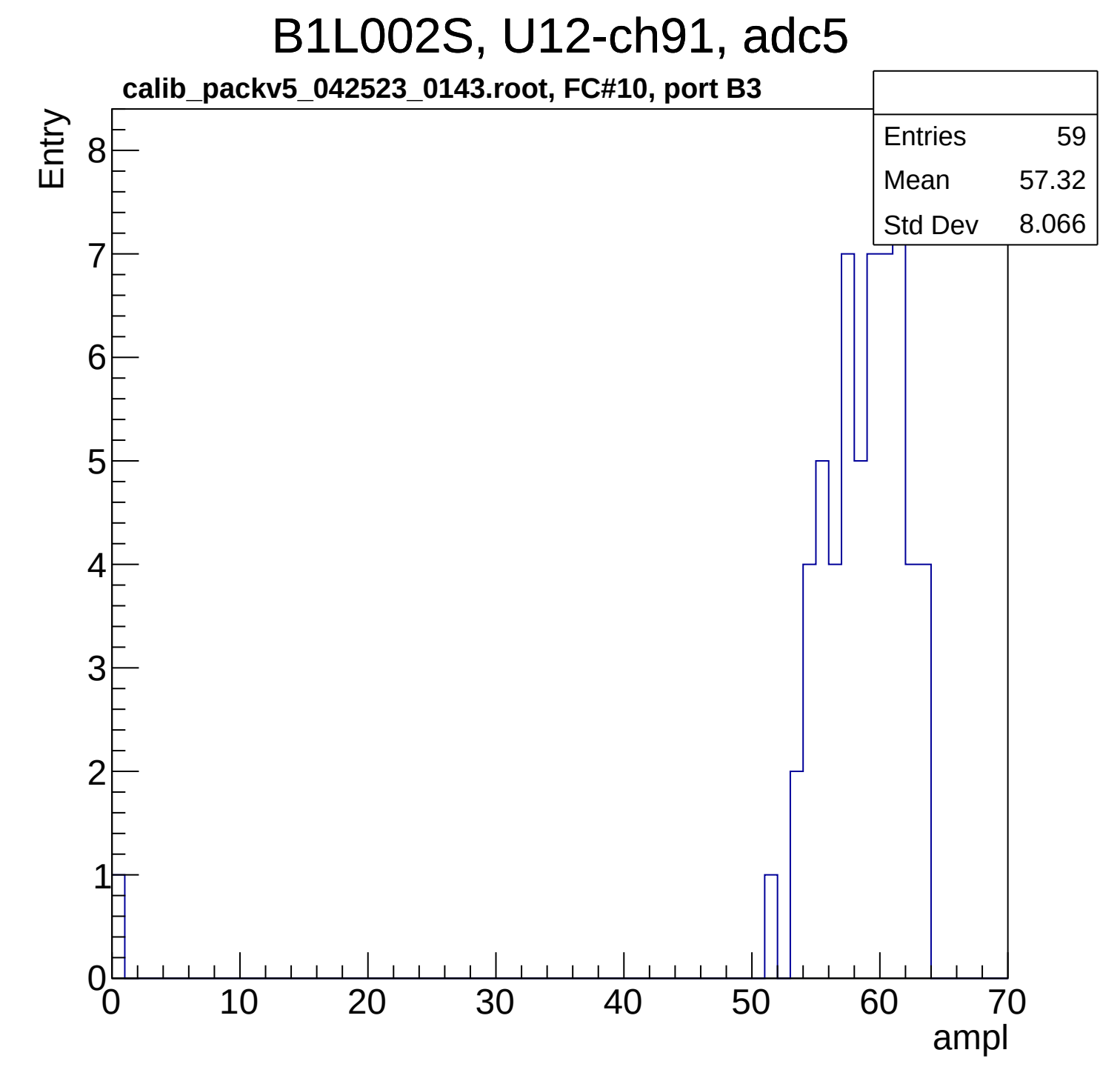
40
50
60
70


B1L002S, U12-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.32
Std Dev 8.066
Entry
ampl
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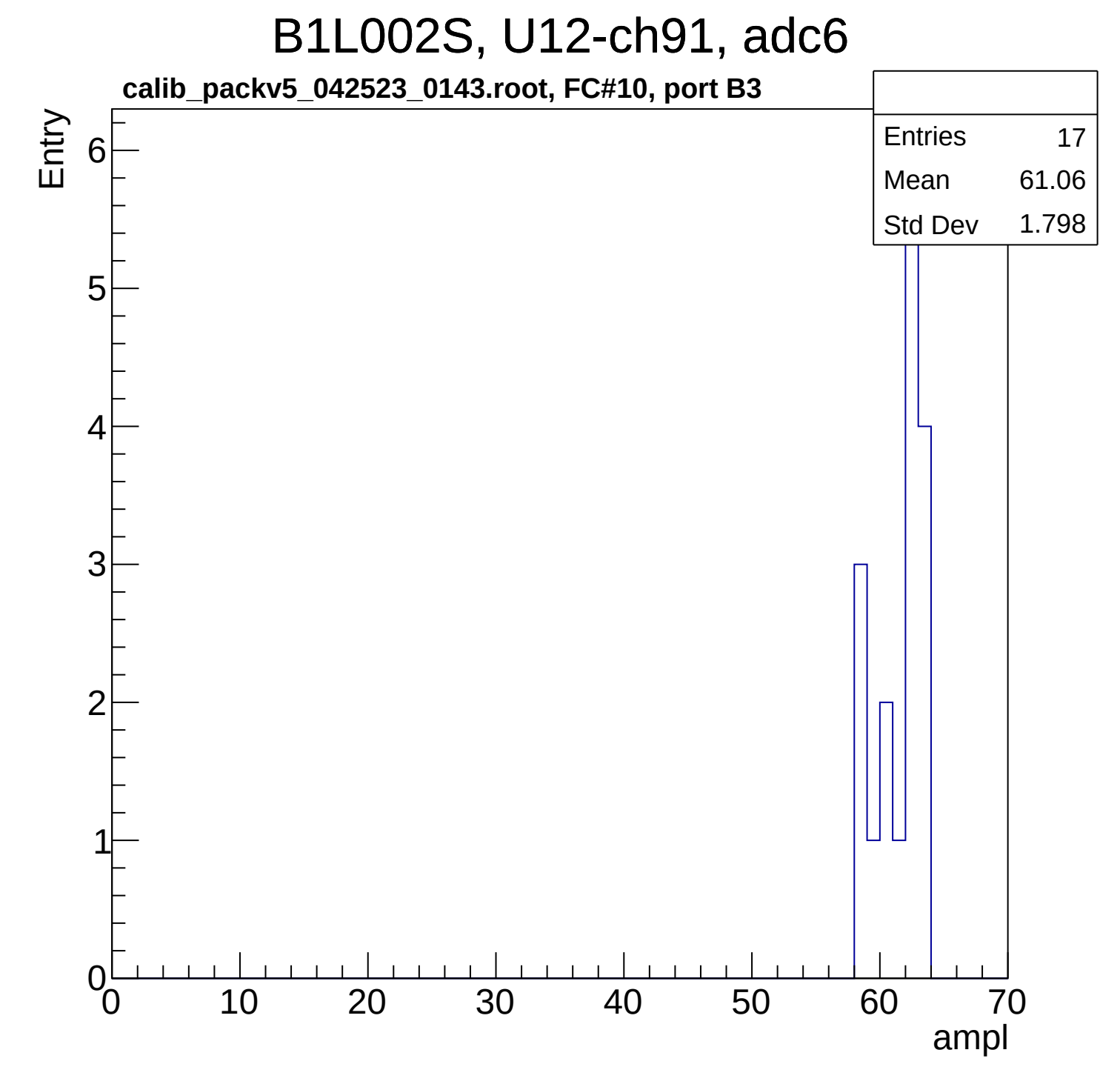
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.06
Std Dev 1.798
Entry
ampl
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3
4
5
6
0
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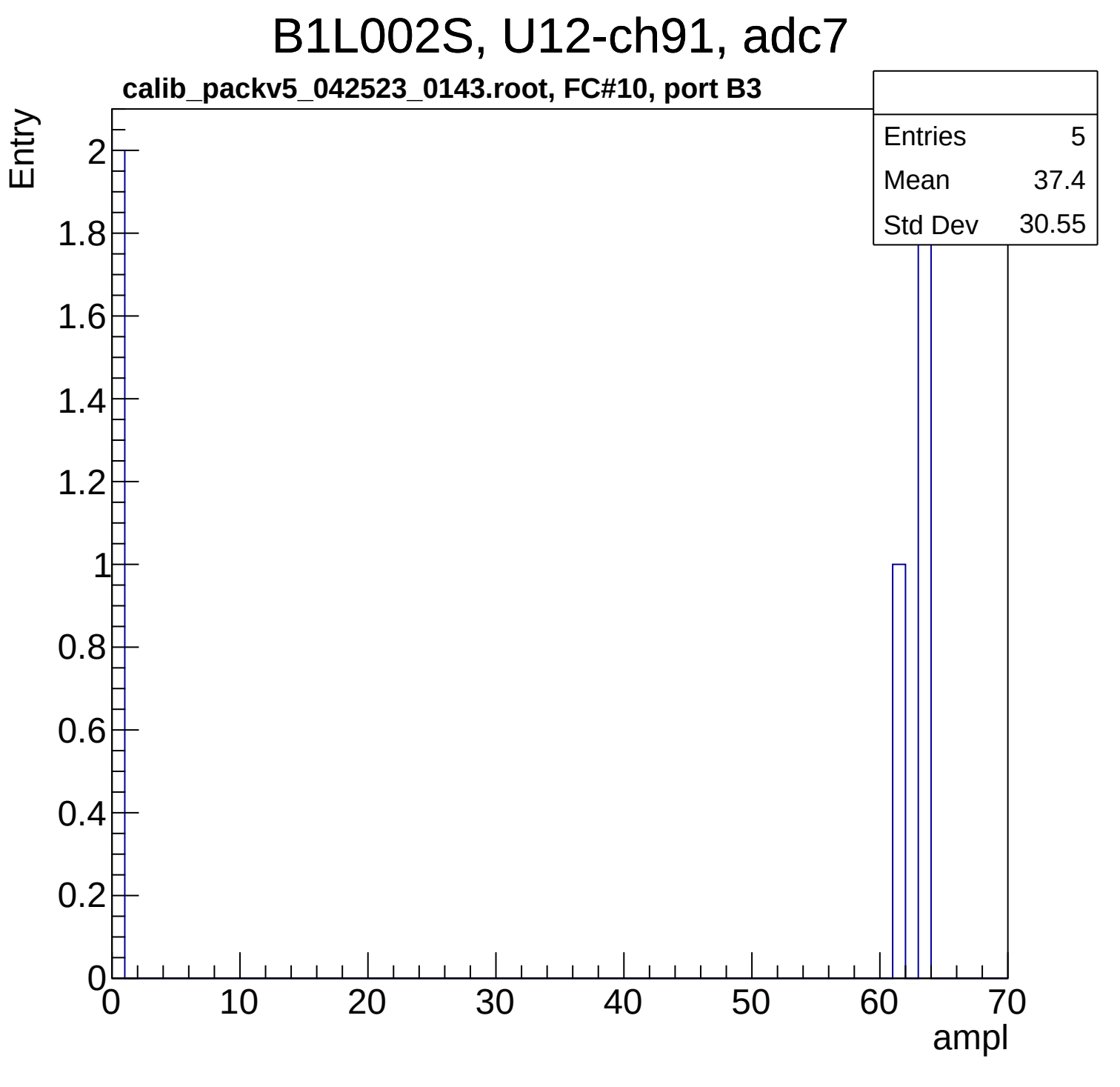
20
30
40
50
60
70


B1L002S, U12-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.4
Std Dev 30.55
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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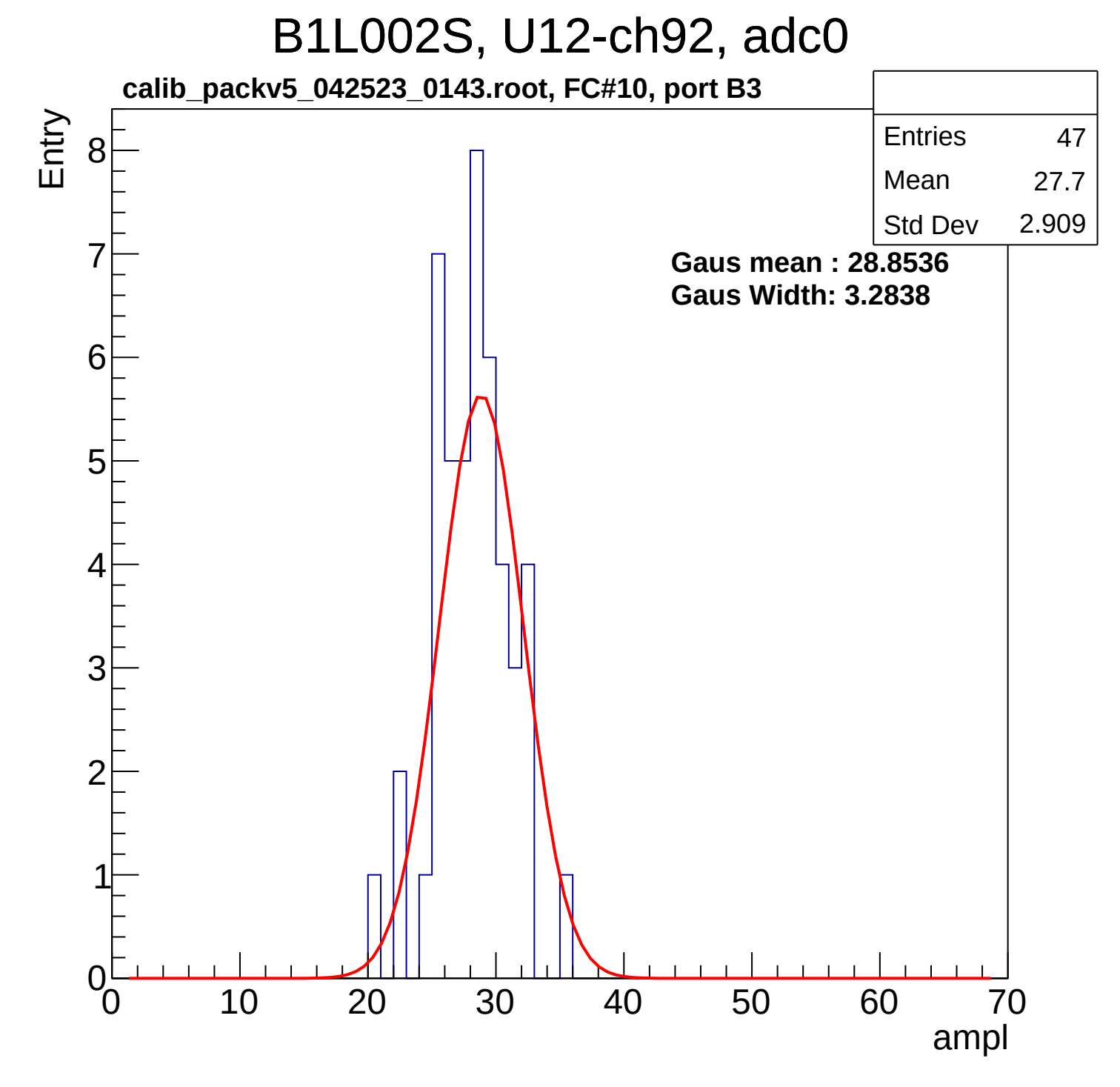
B1L002S, U12-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 27.7
Std Dev 2.909
Gaus mean : 28.8536
Gaus Width: 3.2838
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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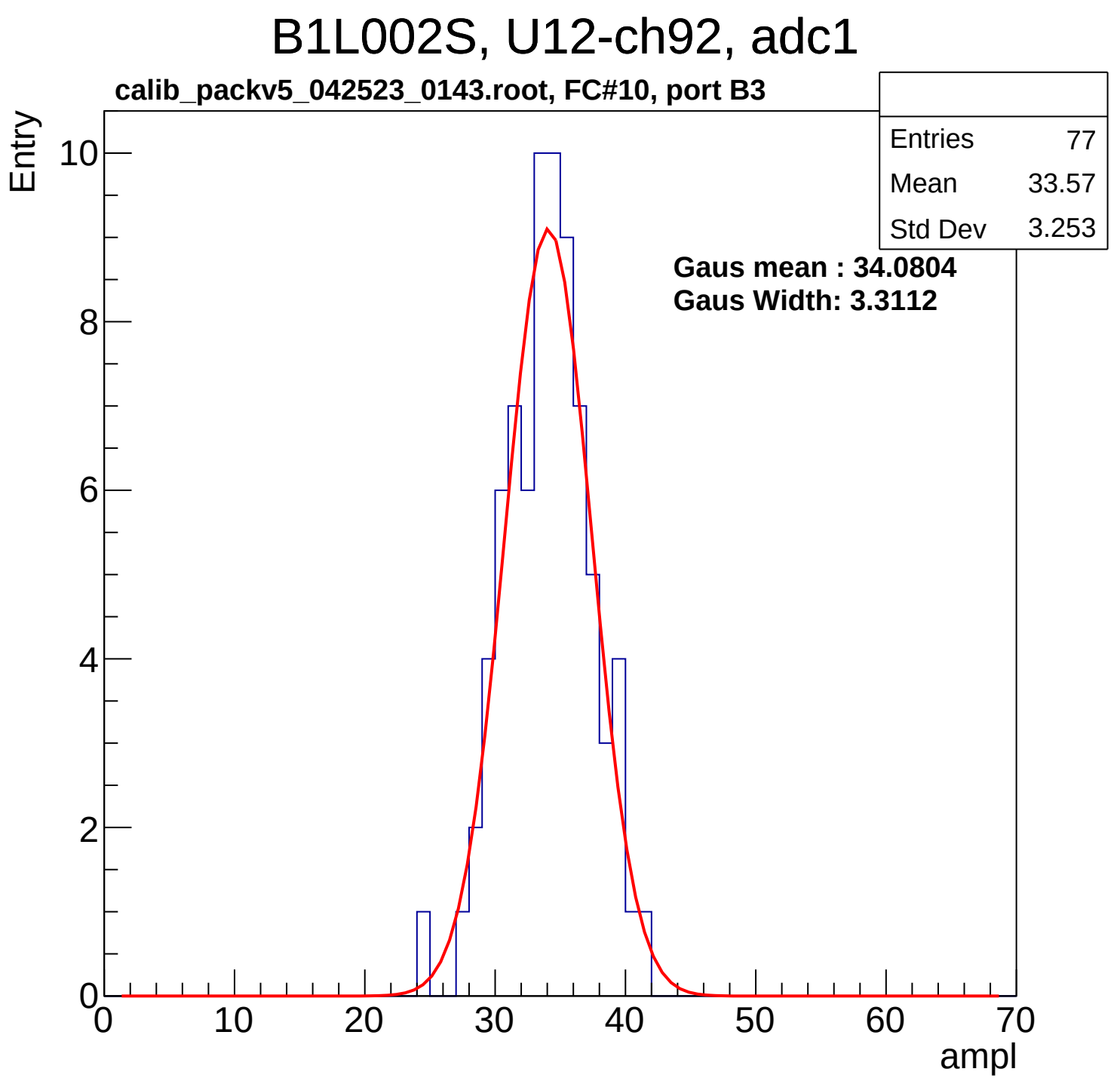
B1L002S, U12-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 33.57
Std Dev 3.253
Gaus mean : 34.0804
Gaus Width: 3.3112
Entry
ampl
0
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4
6
8
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0
10
20
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40
50
60
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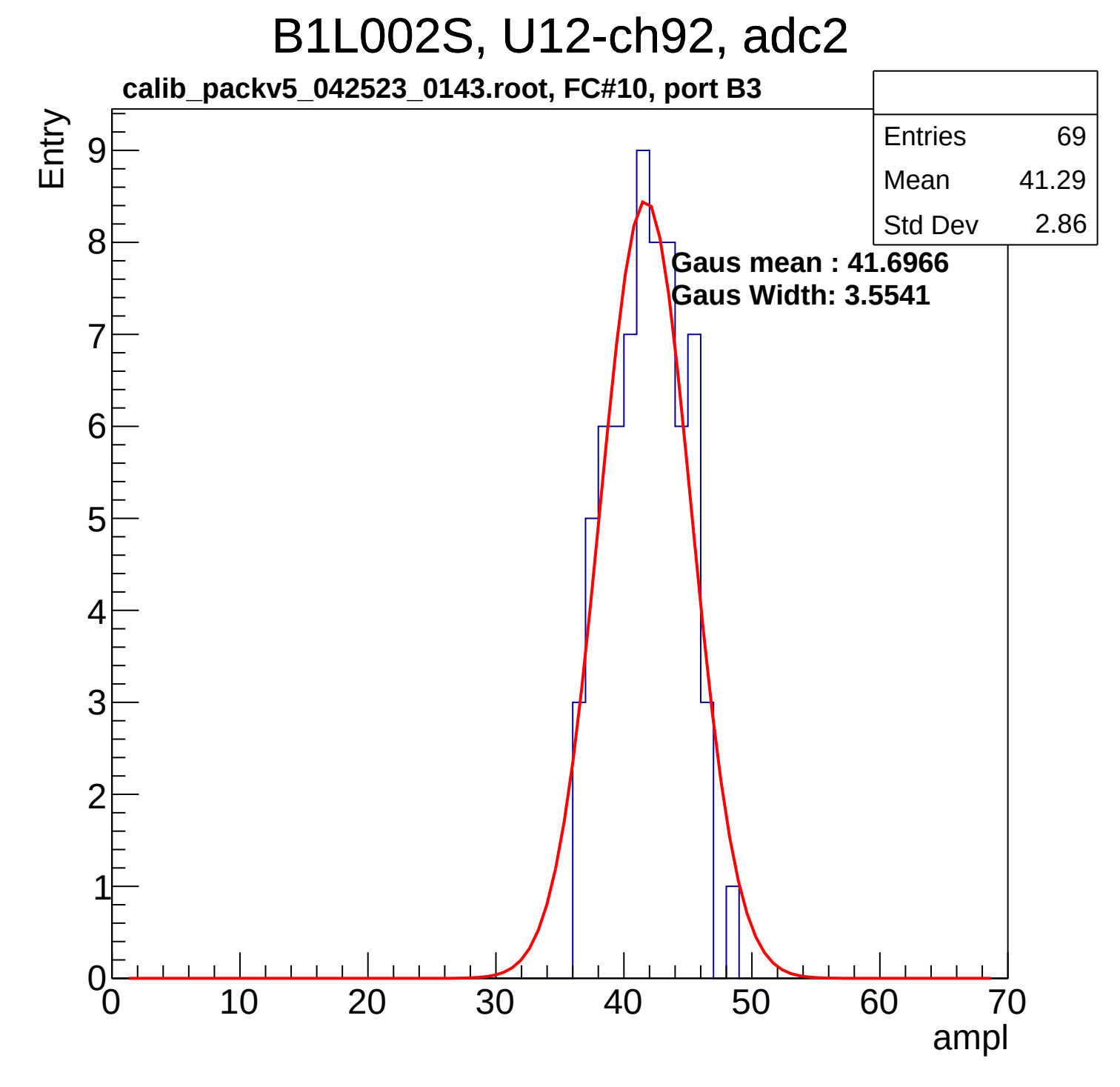
B1L002S, U12-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.29
Std Dev 2.86
Gaus mean : 41.6966
Gaus Width: 3.5541
Entry
ampl
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1
2
3
4
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7
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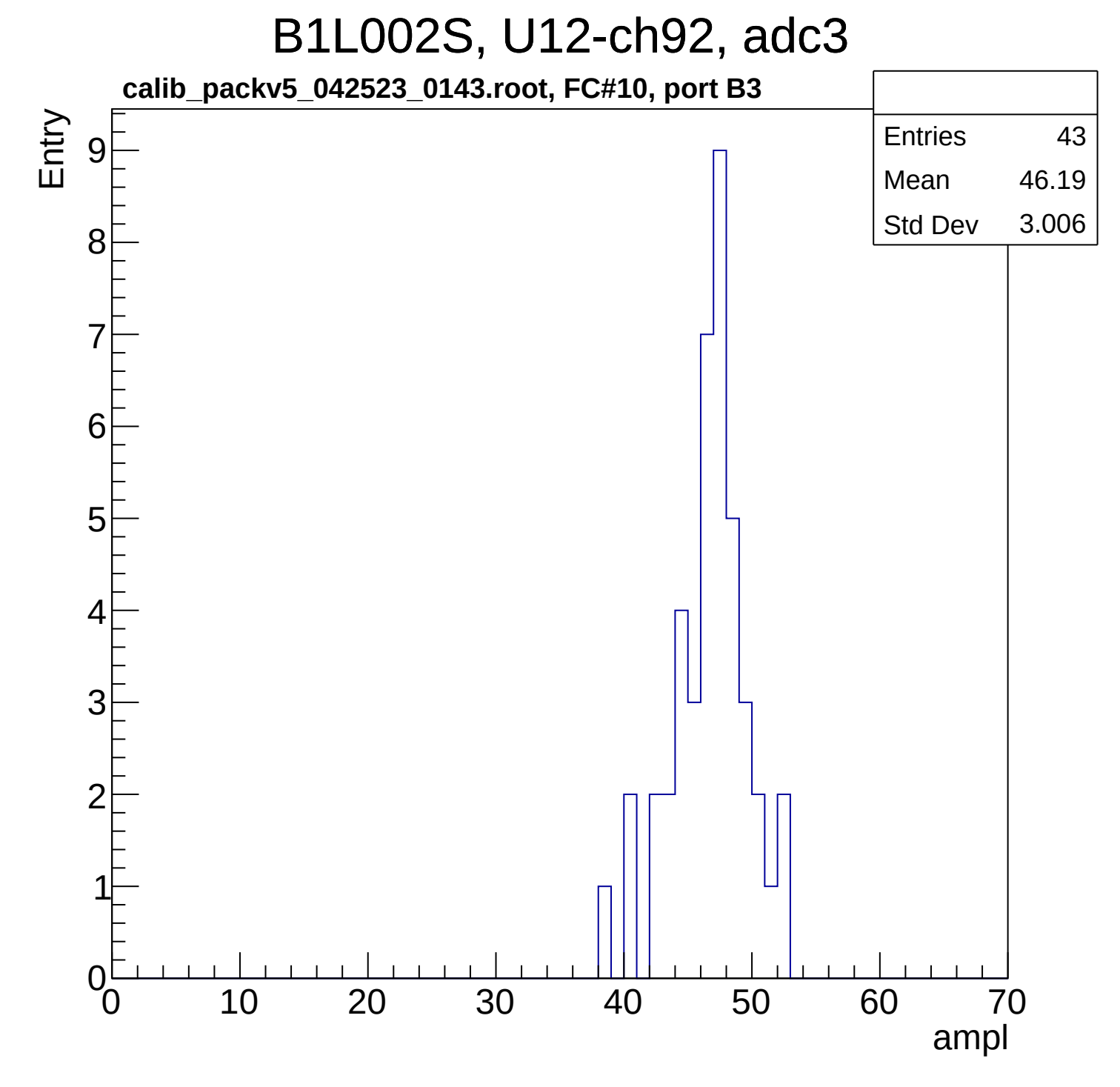
B1L002S, U12-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 46.19
Std Dev 3.006
Entry
ampl
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6
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8
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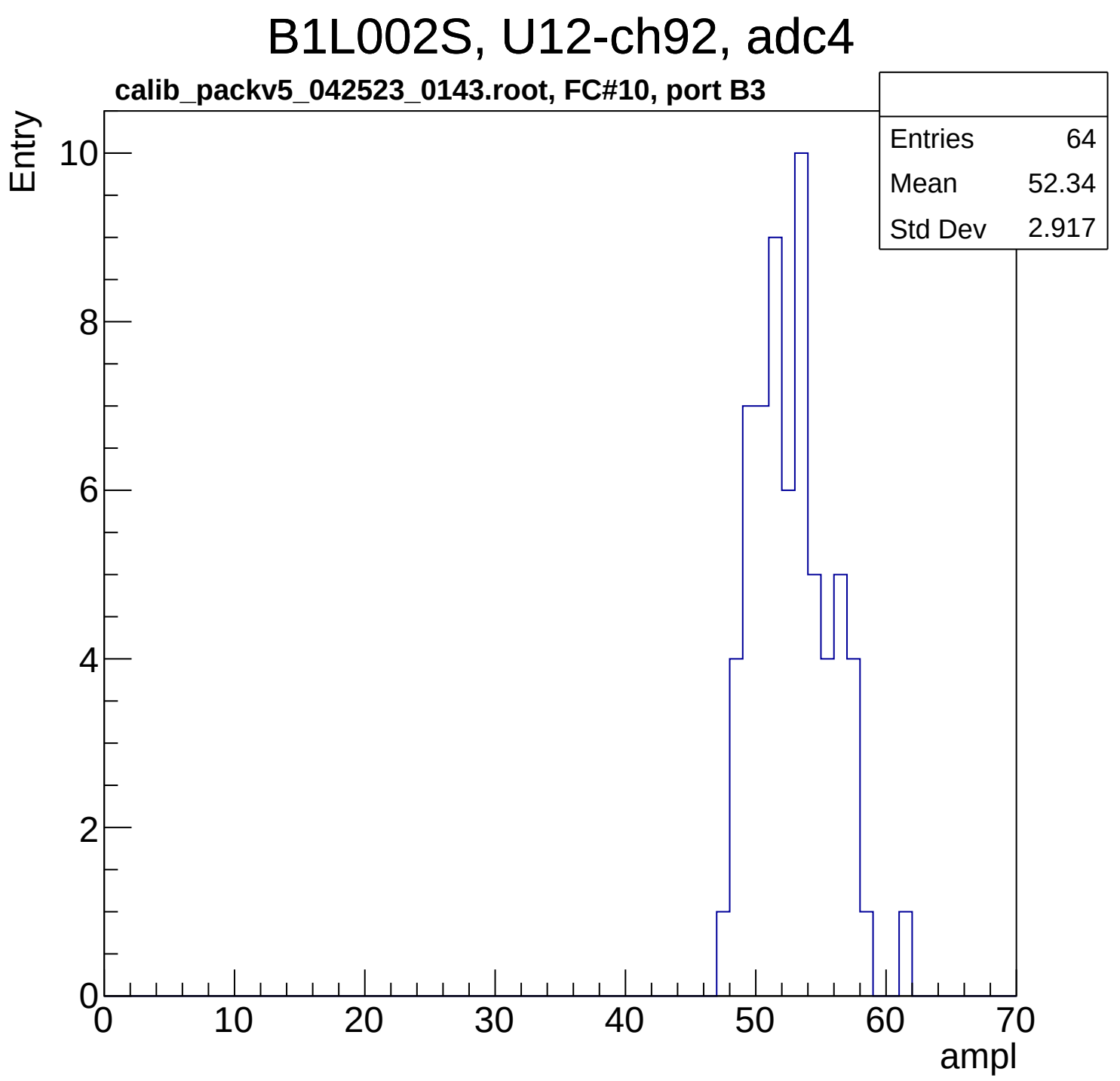
B1L002S, U12-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.34
Std Dev 2.917
Entry
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10
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30
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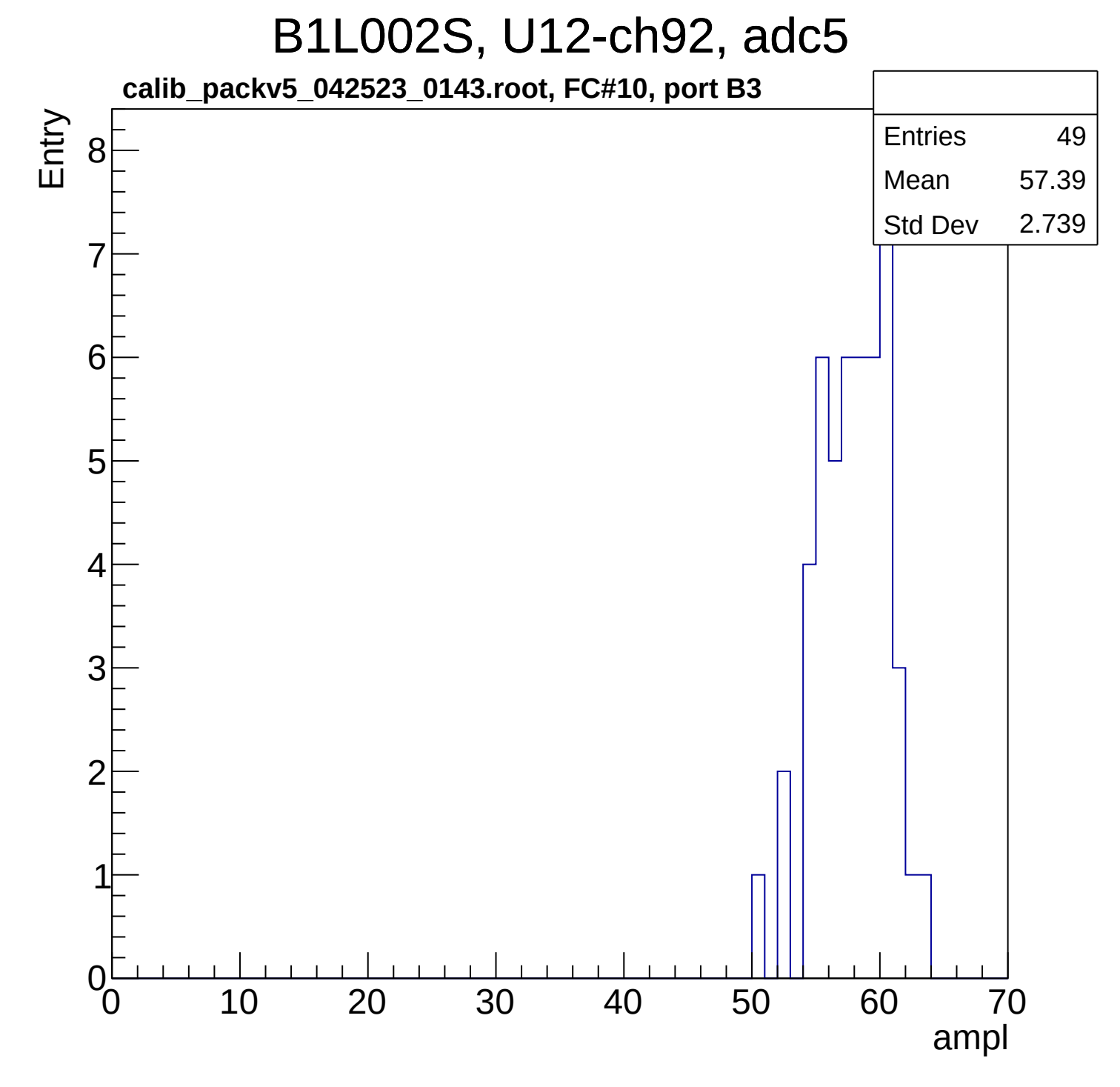
50
60
70
ampl


B1L002S, U12-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.39
Std Dev 2.739
Entry
ampl
0
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2
3
4
5
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7
8
0
10
20
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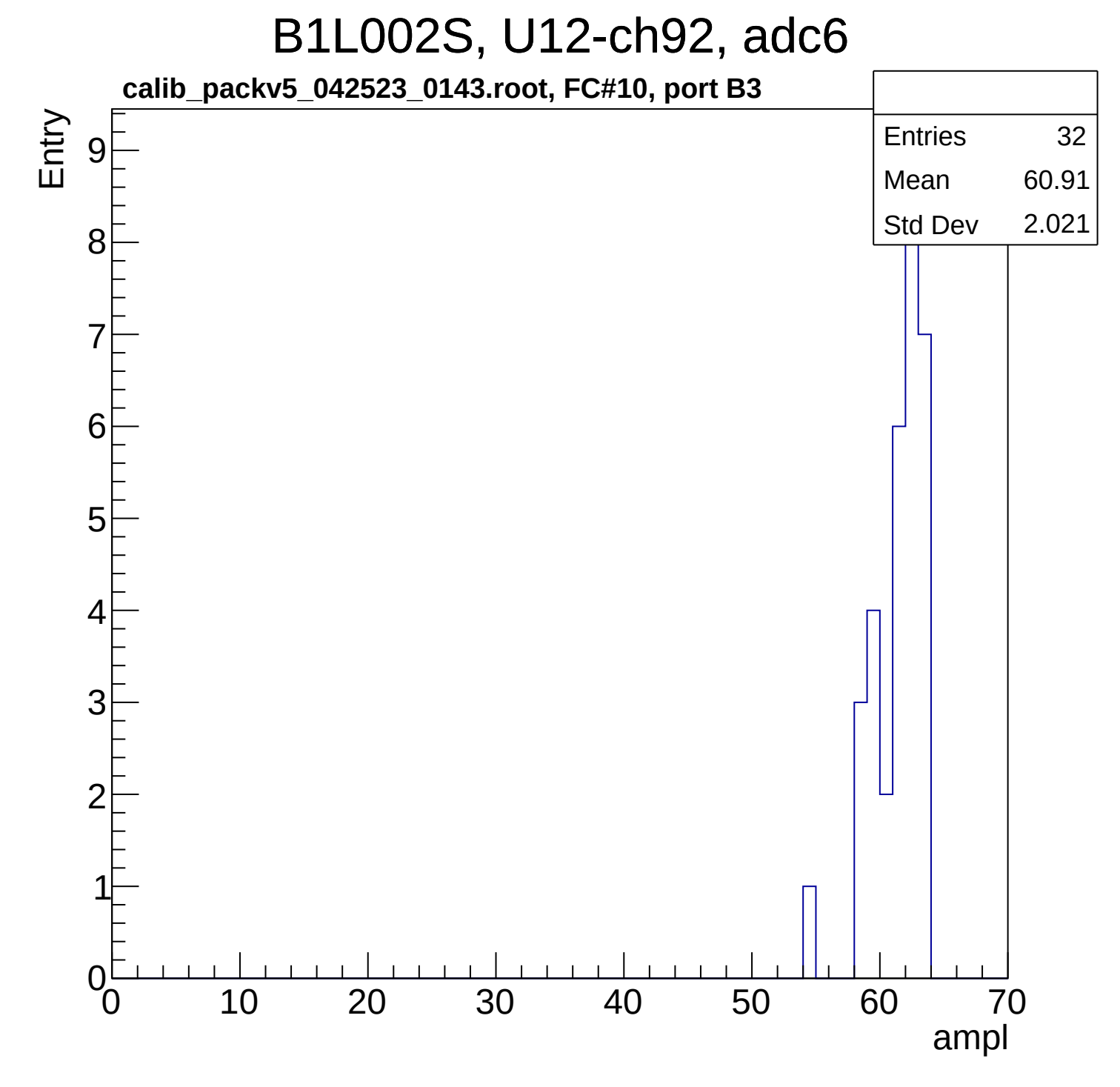
40
50
60
70


B1L002S, U12-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 60.91
Std Dev 2.021
Entry
ampl
0
1
2
3
4
5
6
7
8
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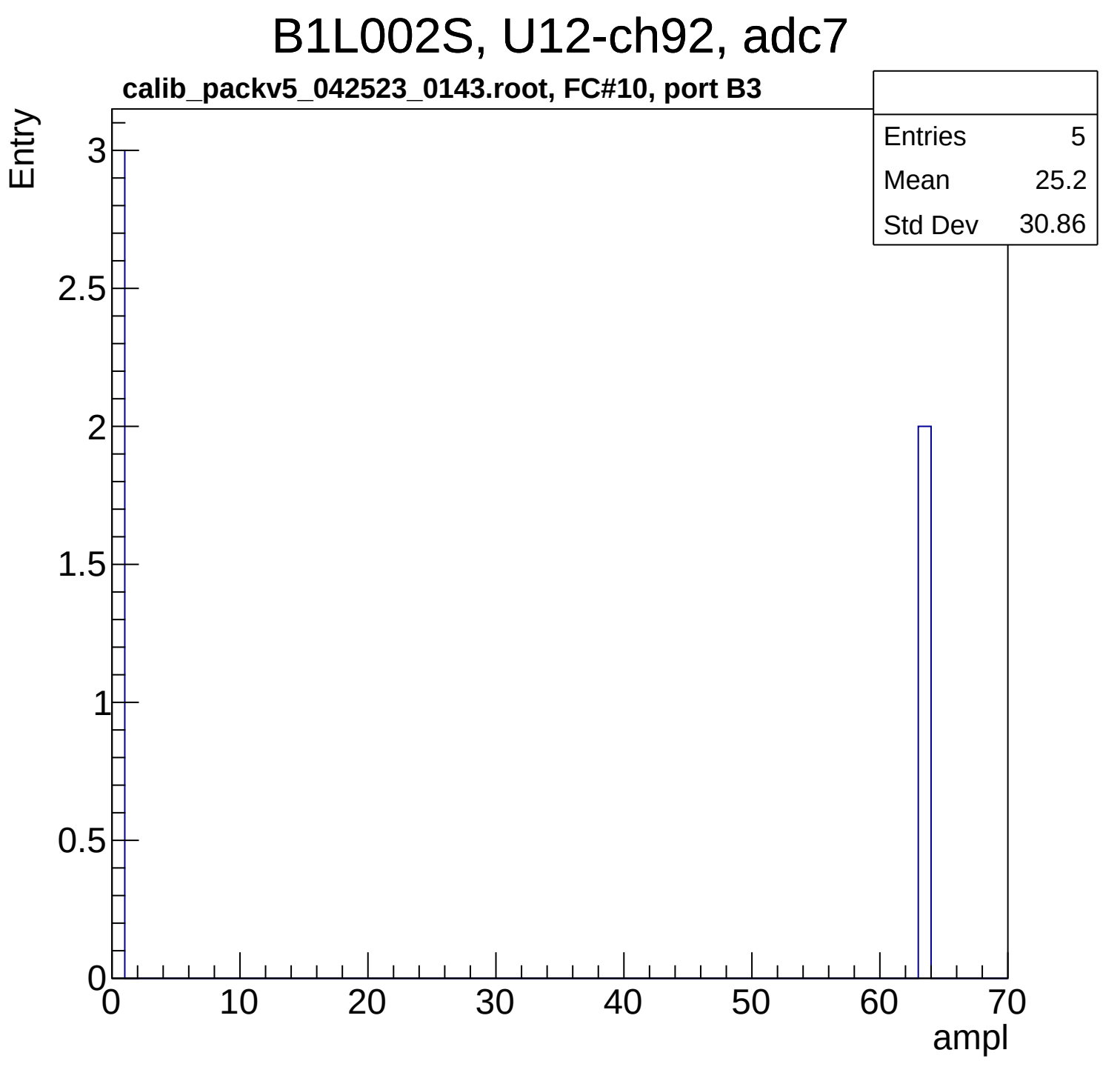
B1L002S, U12-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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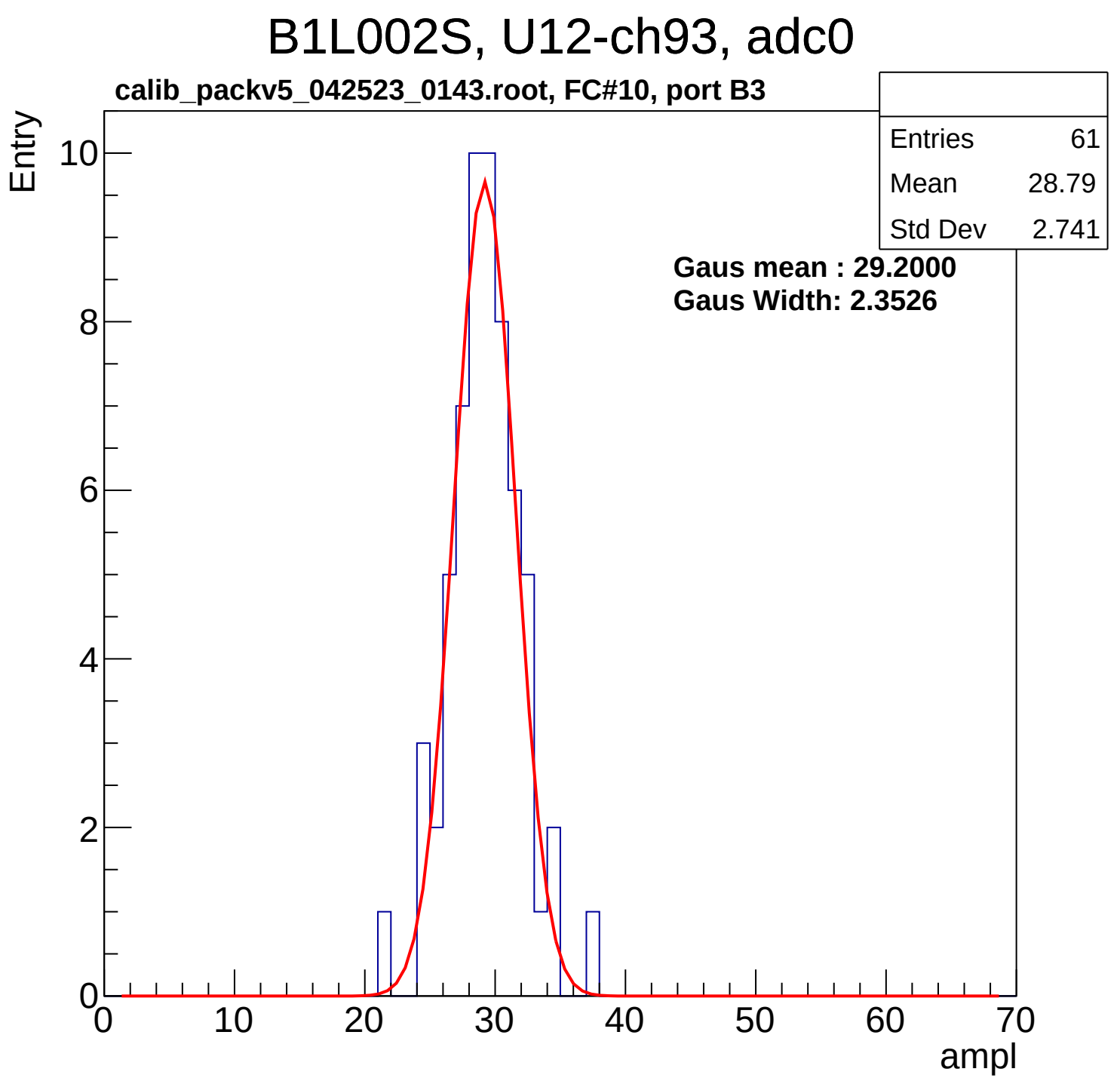
B1L002S, U12-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.79
Std Dev 2.741
Gaus mean : 29.2000
Gaus Width: 2.3526
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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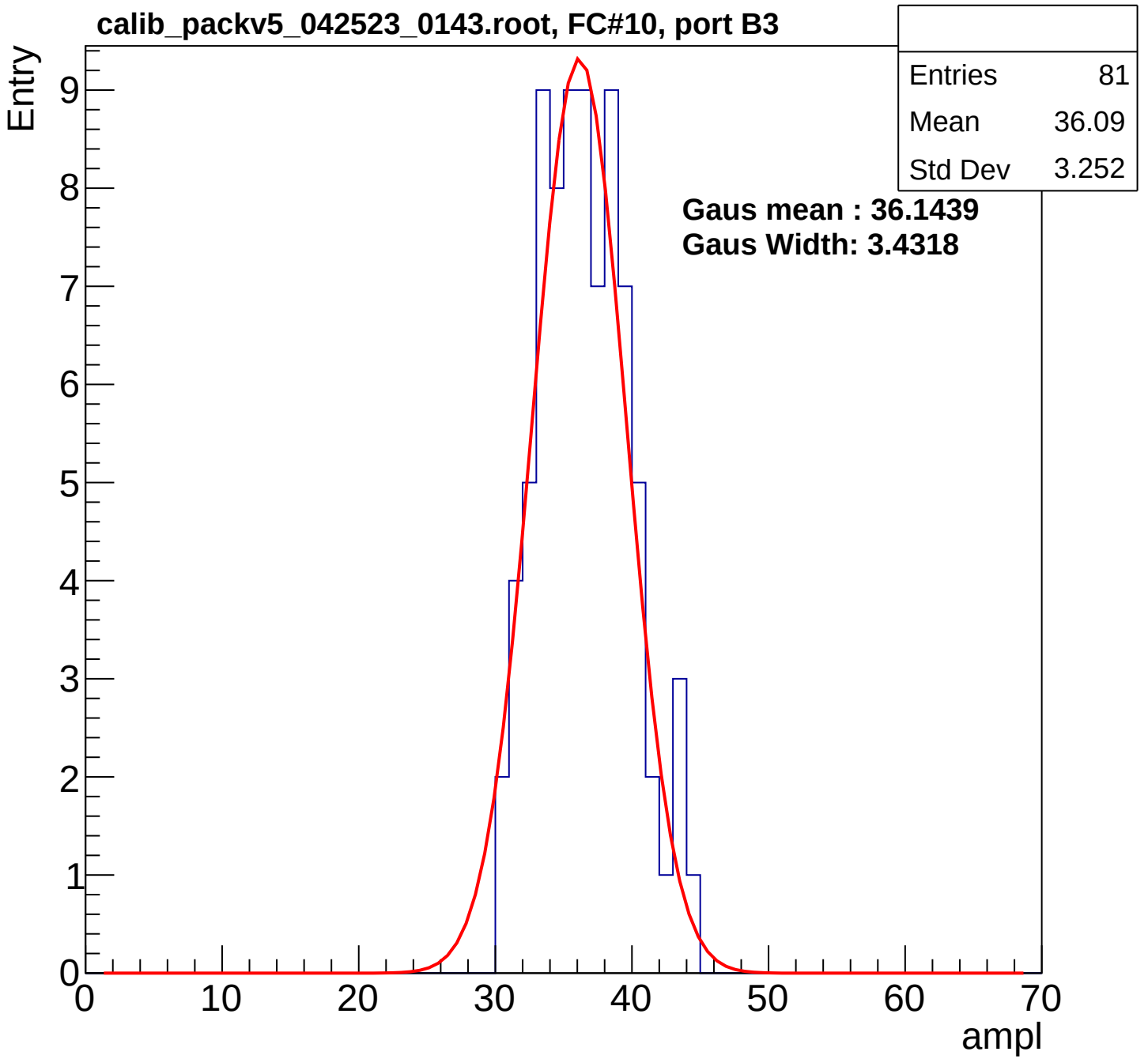
B1L002S, U12-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 36.09
Std Dev 3.252
Gaus mean : 36.1439
Gaus Width: 3.4318
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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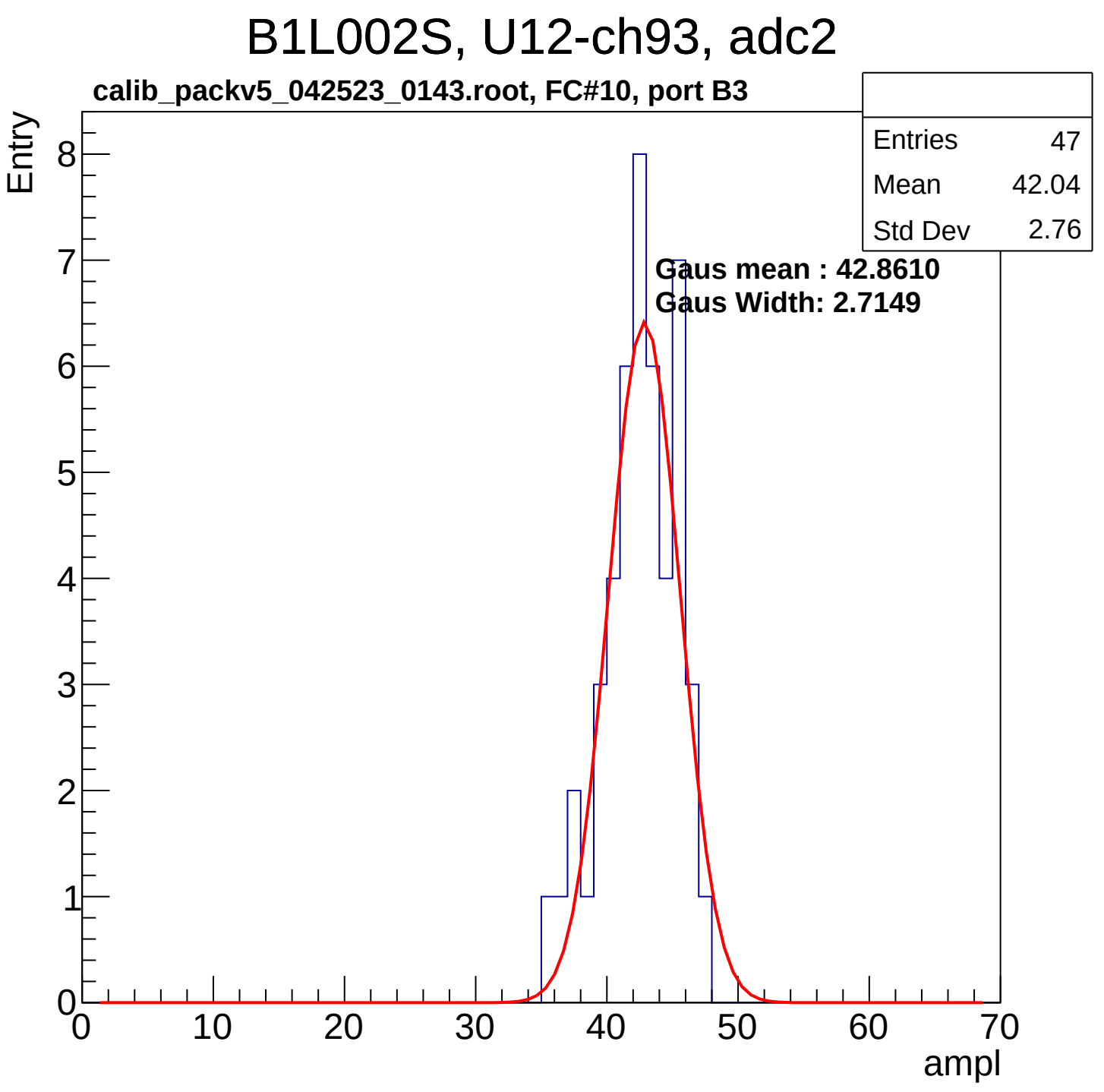
B1L002S, U12-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 42.04
Std Dev 2.76
Gaus mean : 42.8610
Gaus Width: 2.7149
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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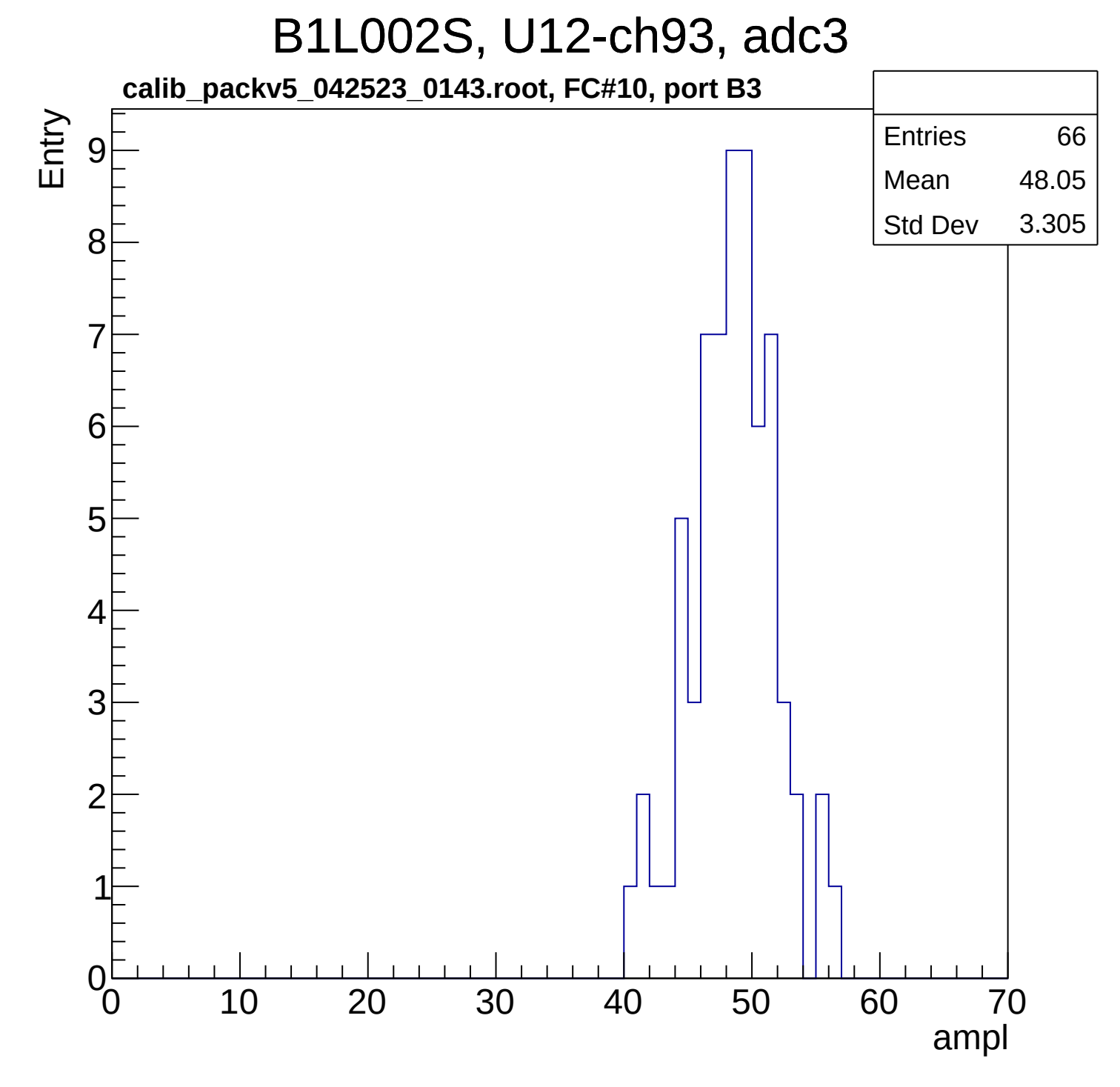
B1L002S, U12-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.05
Std Dev 3.305
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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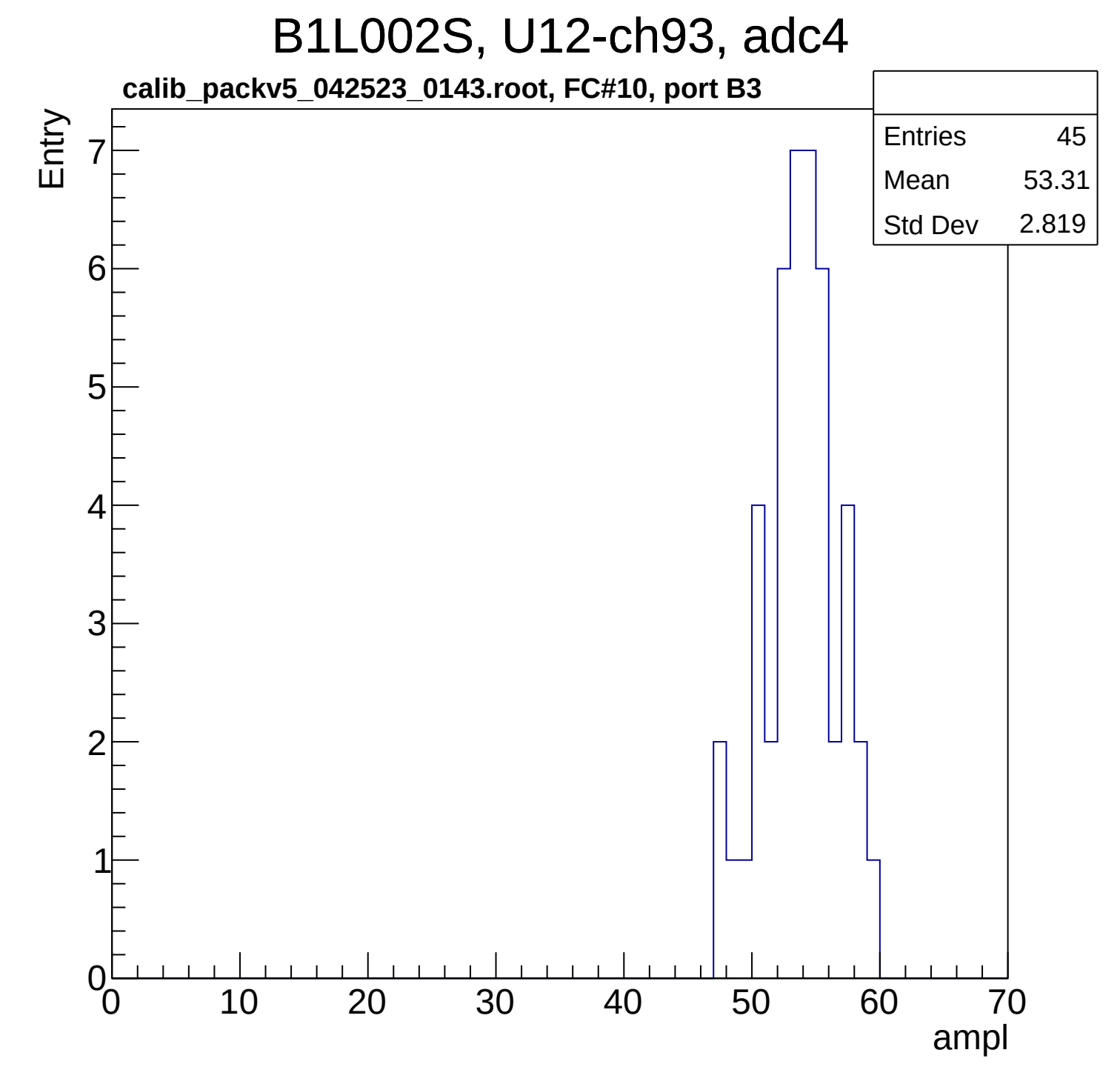
B1L002S, U12-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53.31
Std Dev 2.819
Entry
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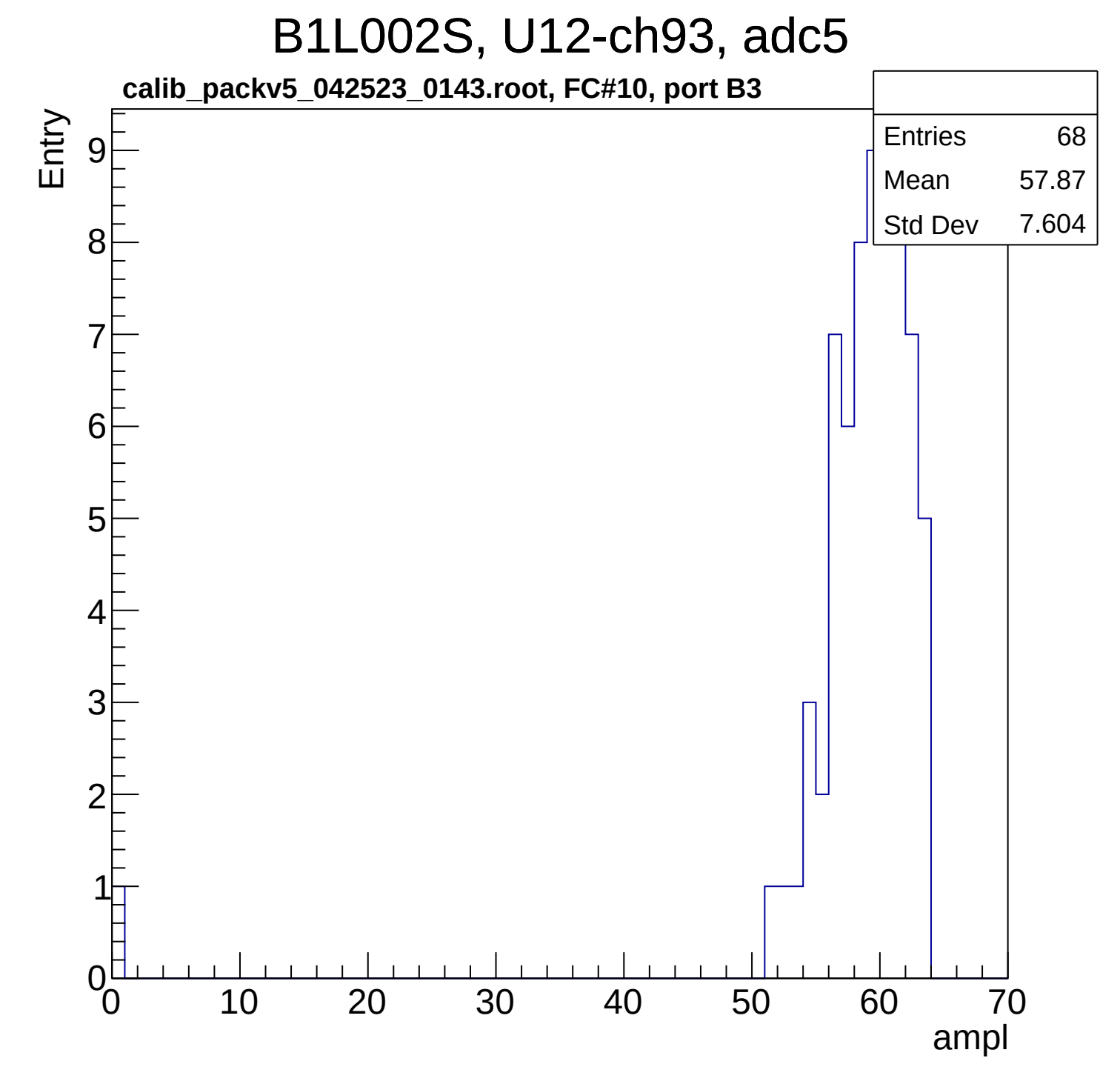
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.87
Std Dev 7.604
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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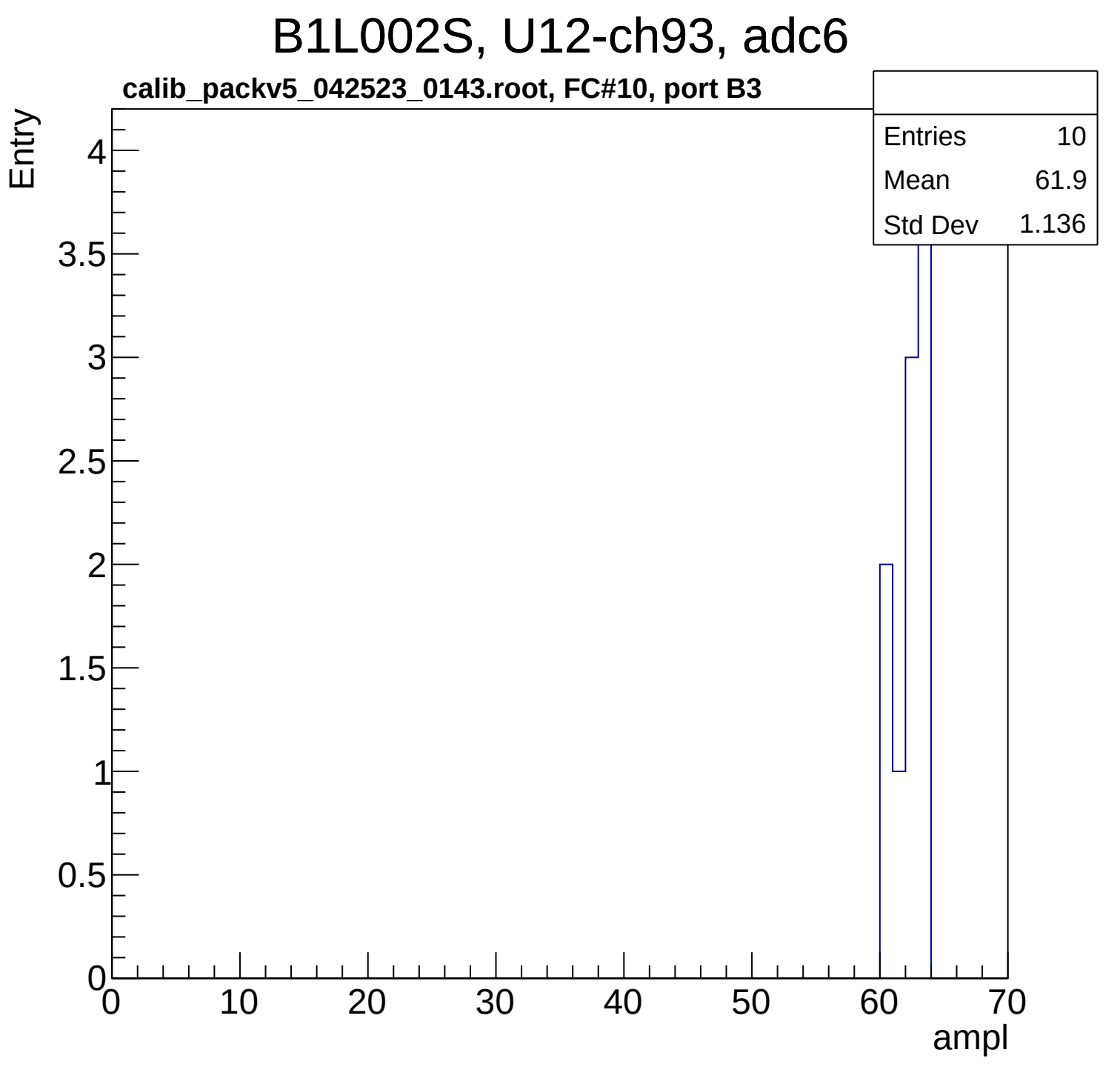
B1L002S, U12-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.9
Std Dev 1.136
Entry
ampl
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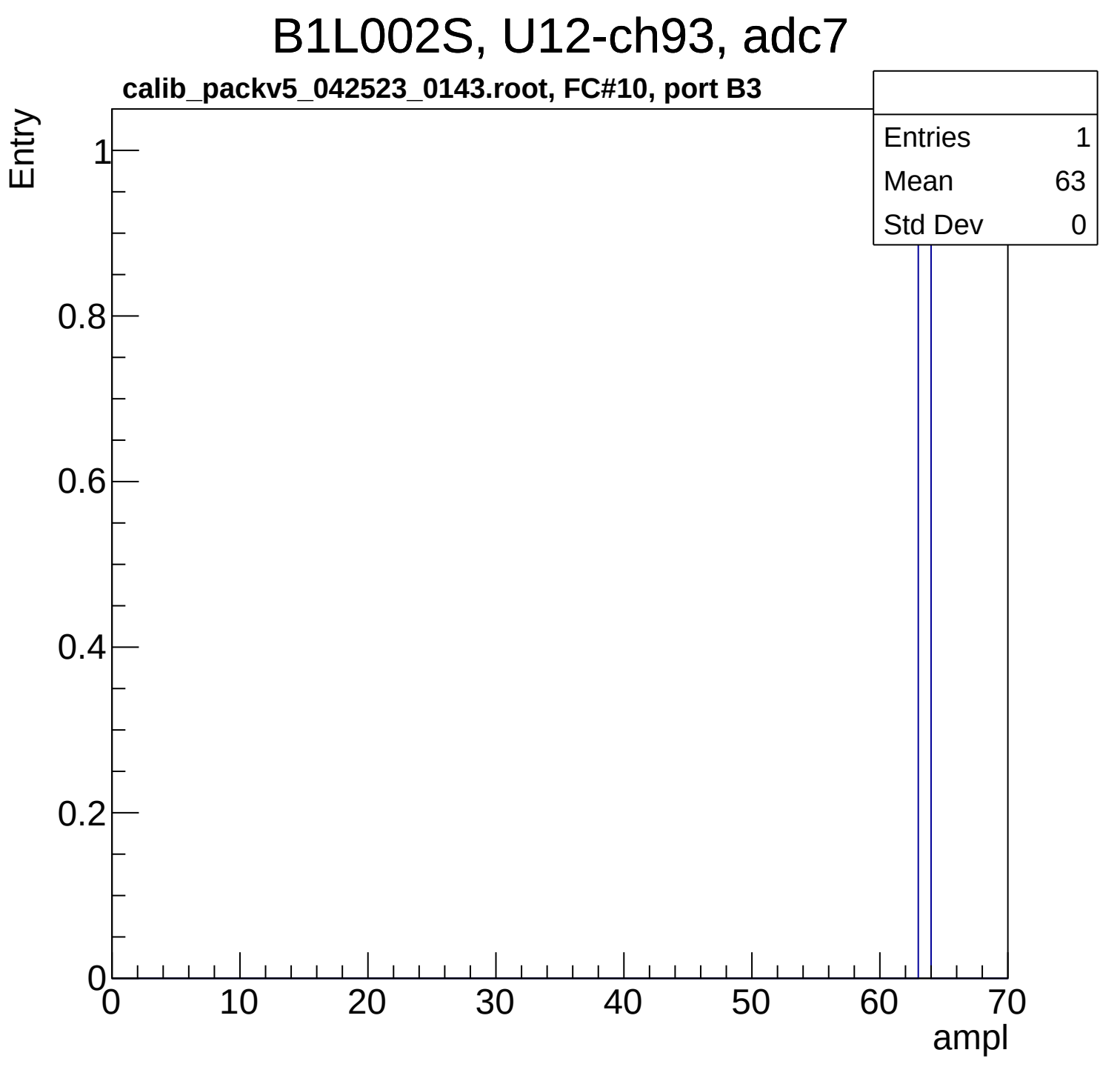
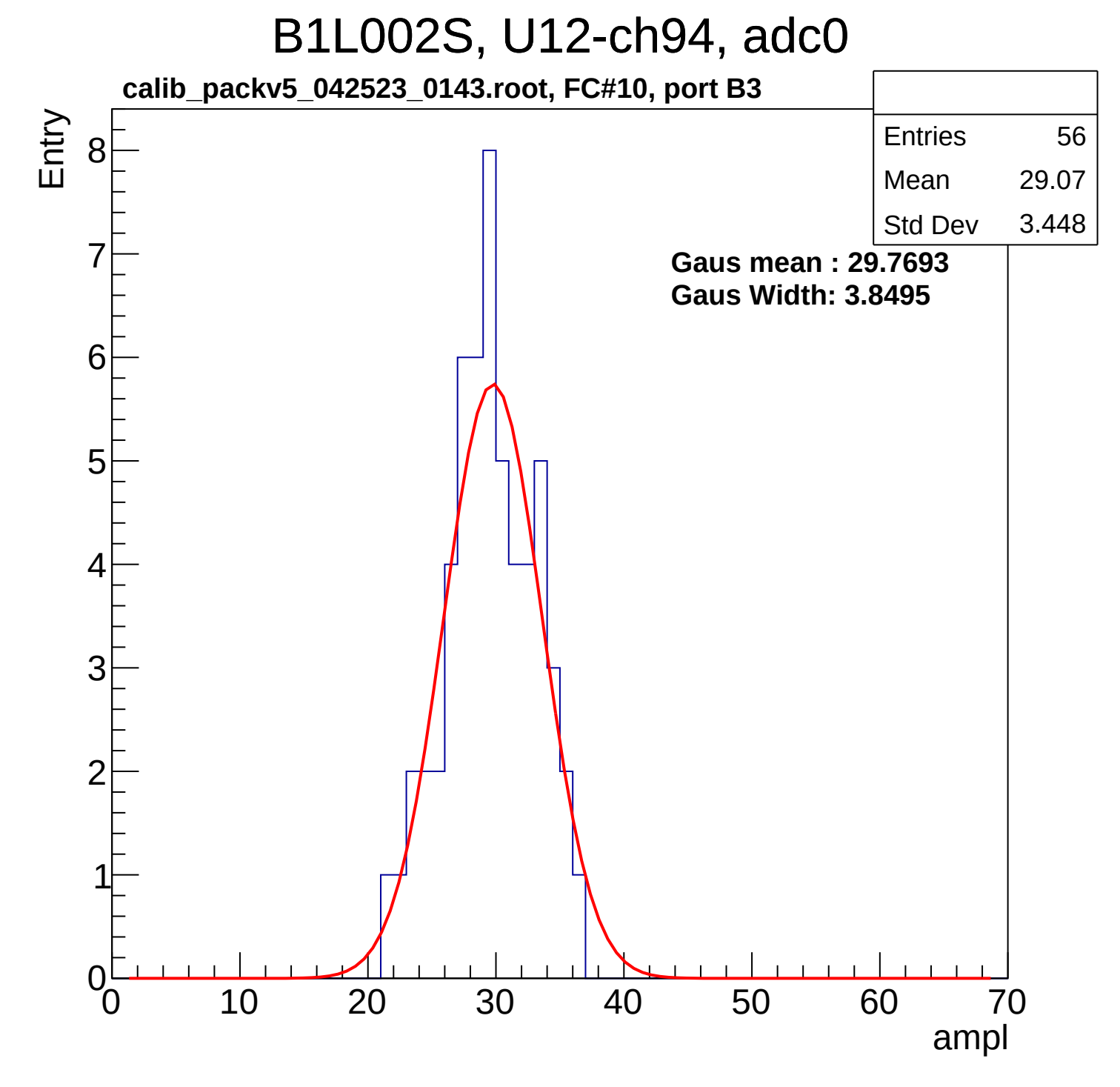
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 29.07
Std Dev 3.448
Gaus mean : 29.7693
Gaus Width: 3.8495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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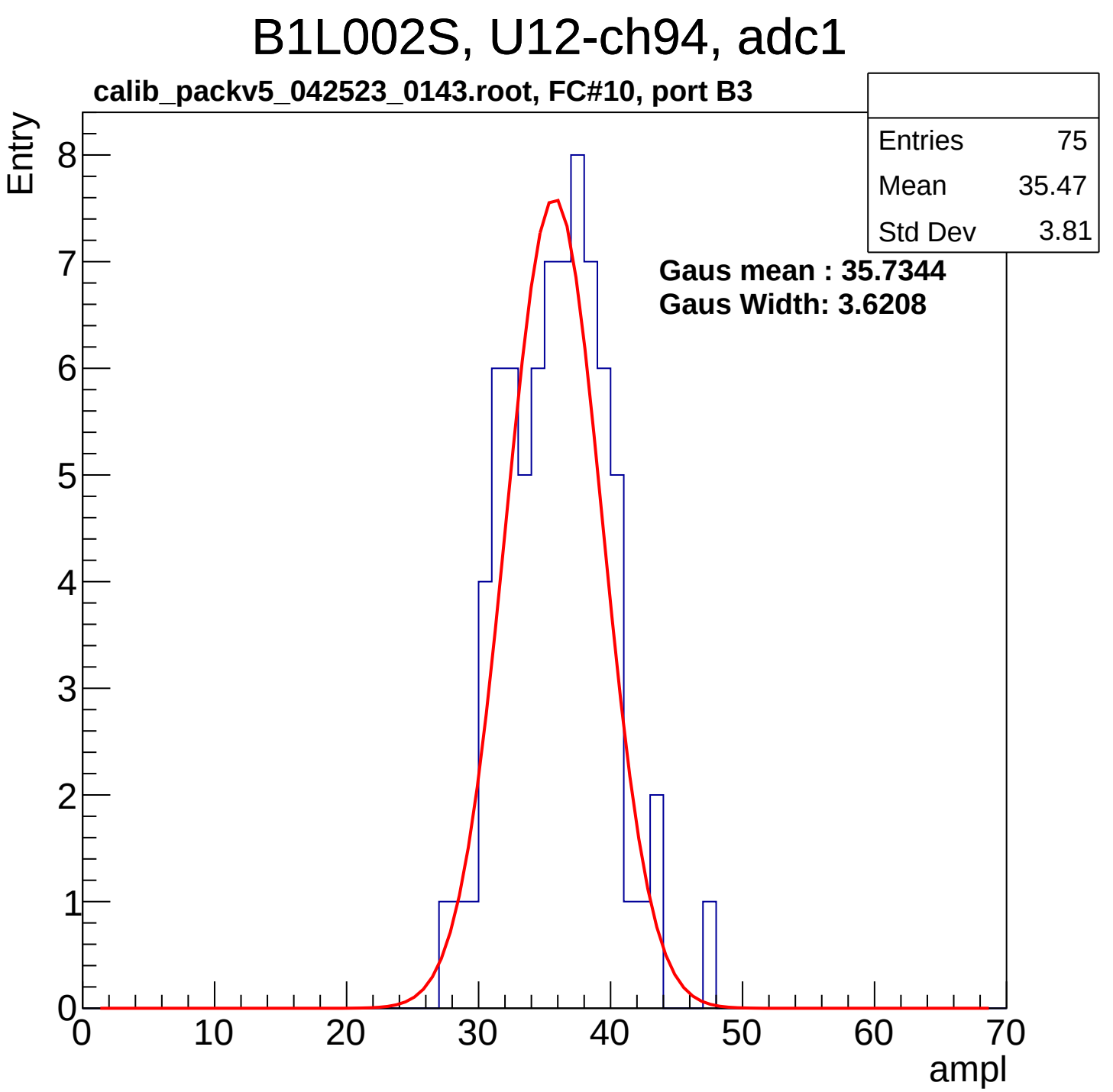
B1L002S, U12-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 35.47
Std Dev 3.81
Gaus mean : 35.7344
Gaus Width: 3.6208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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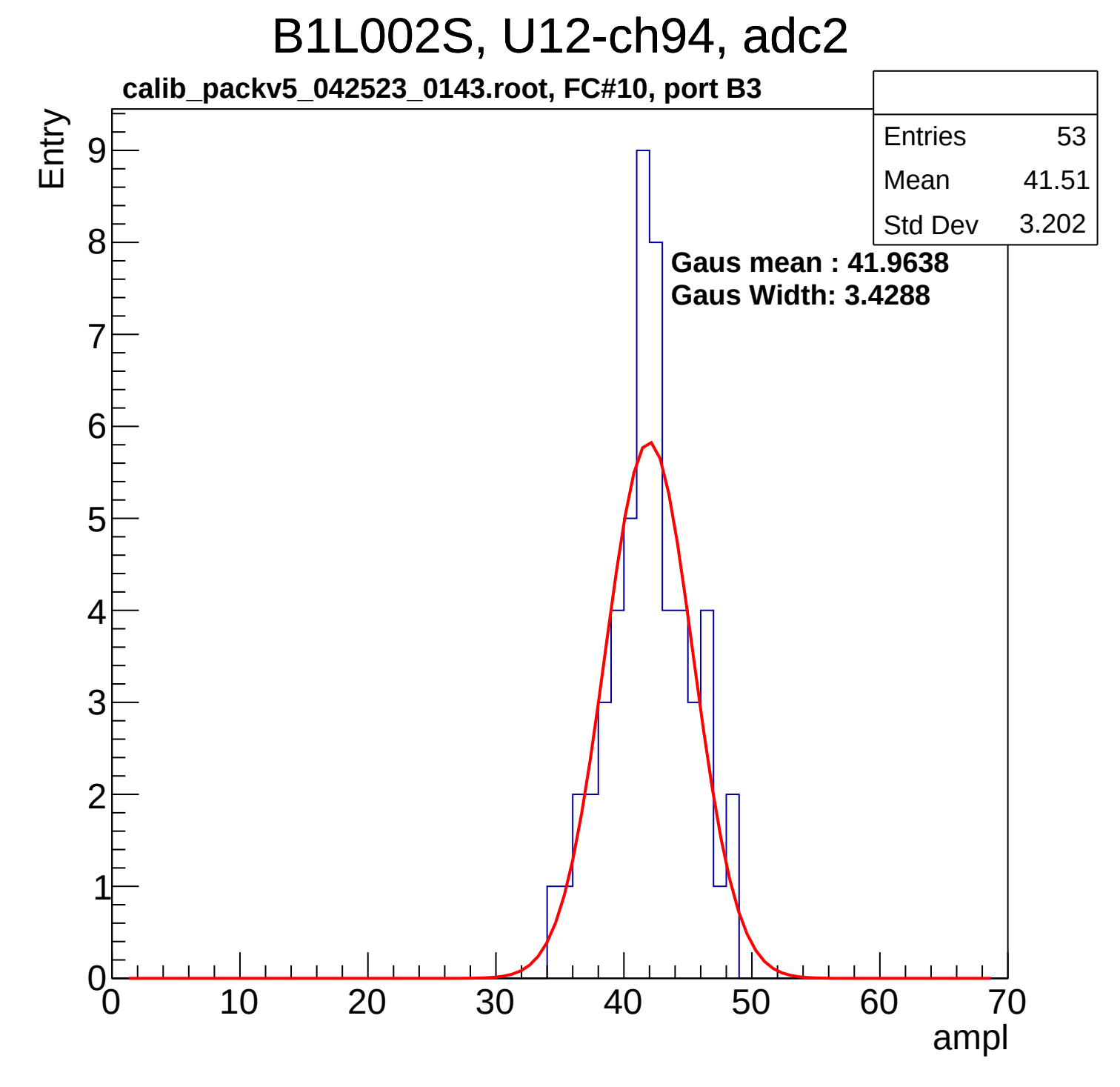
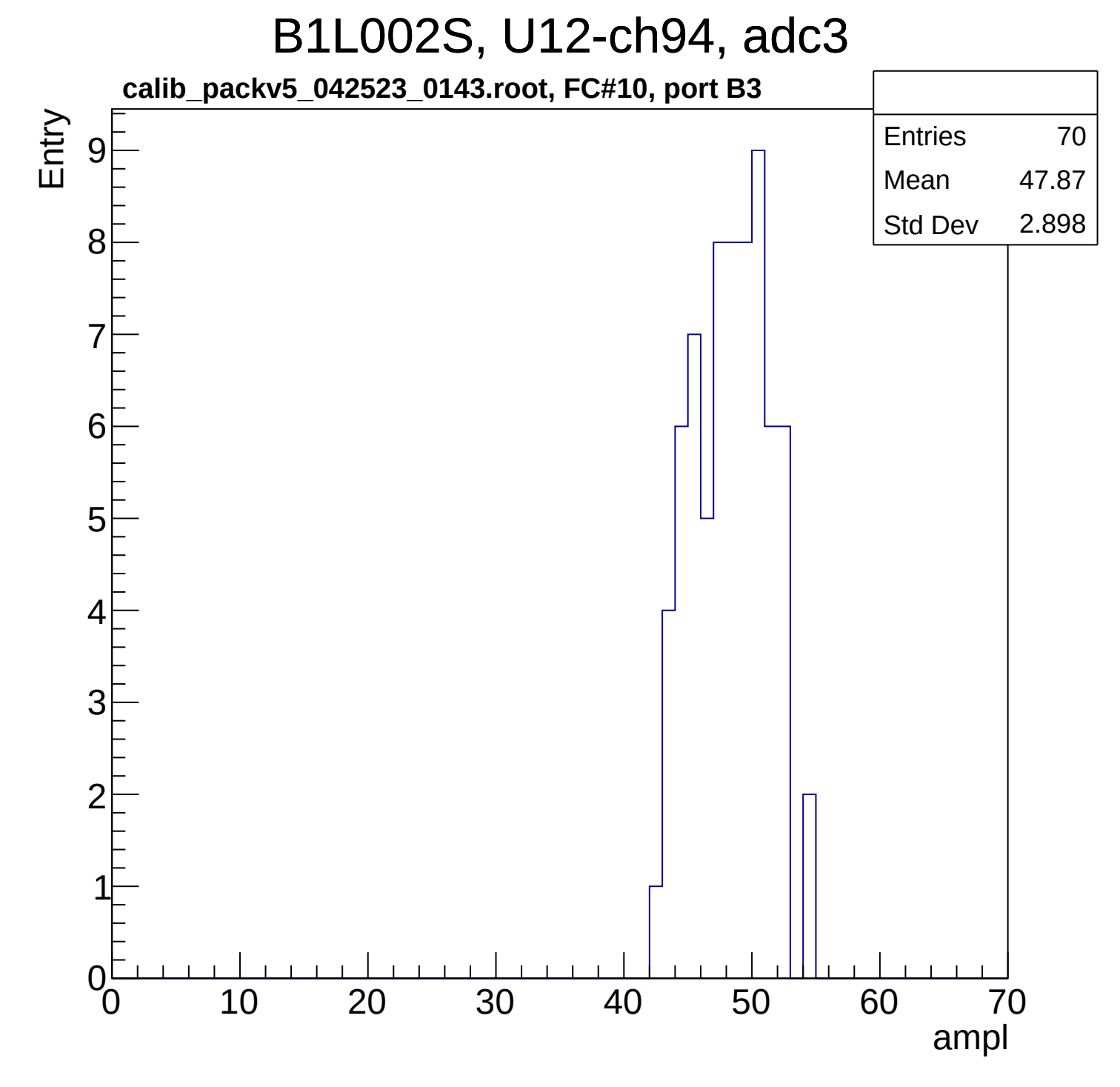

B1L002S, U12-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.51
Std Dev 3.202
Gaus mean : 41.9638
Gaus Width: 3.4288
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.87
Std Dev 2.898
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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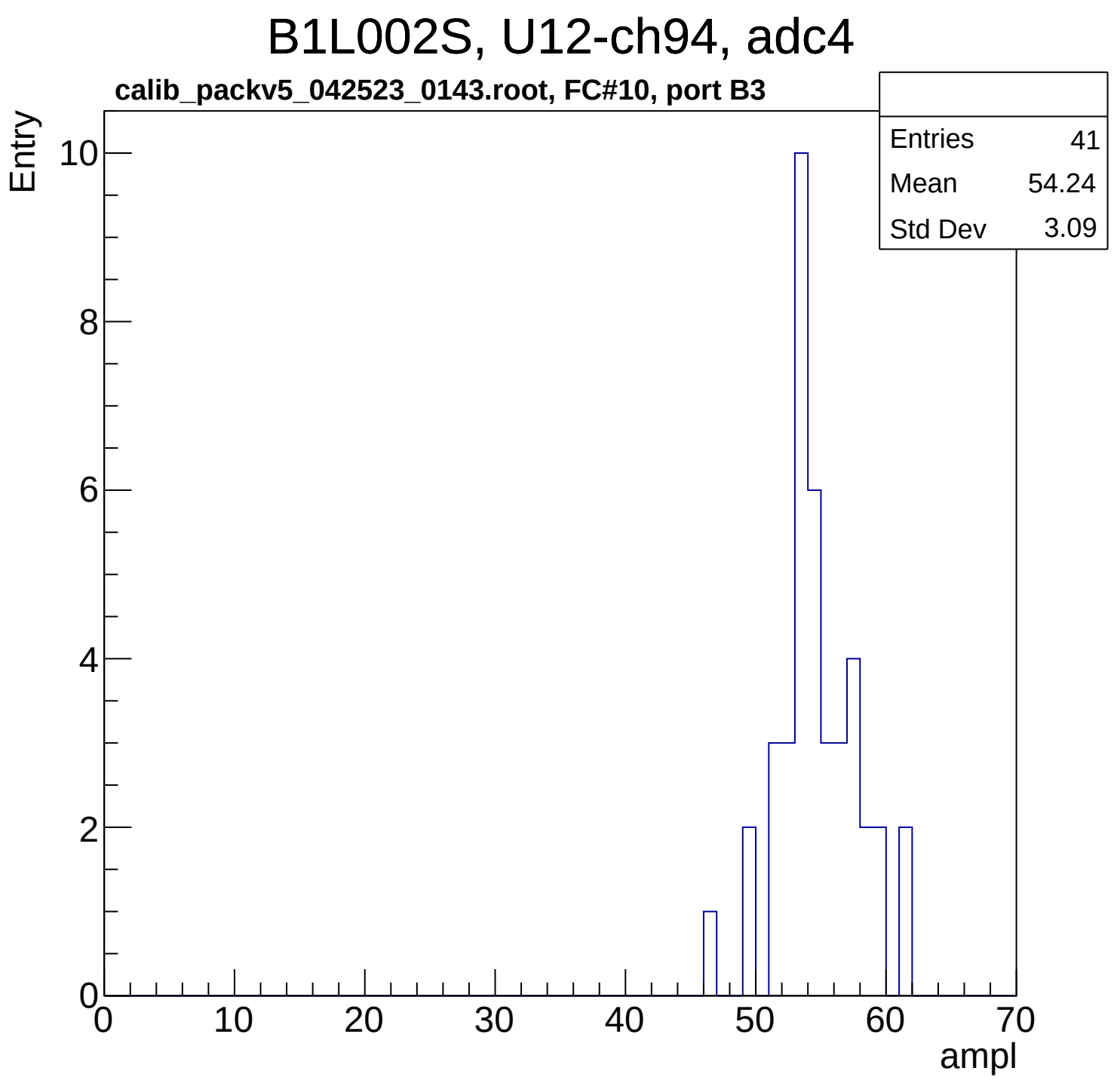
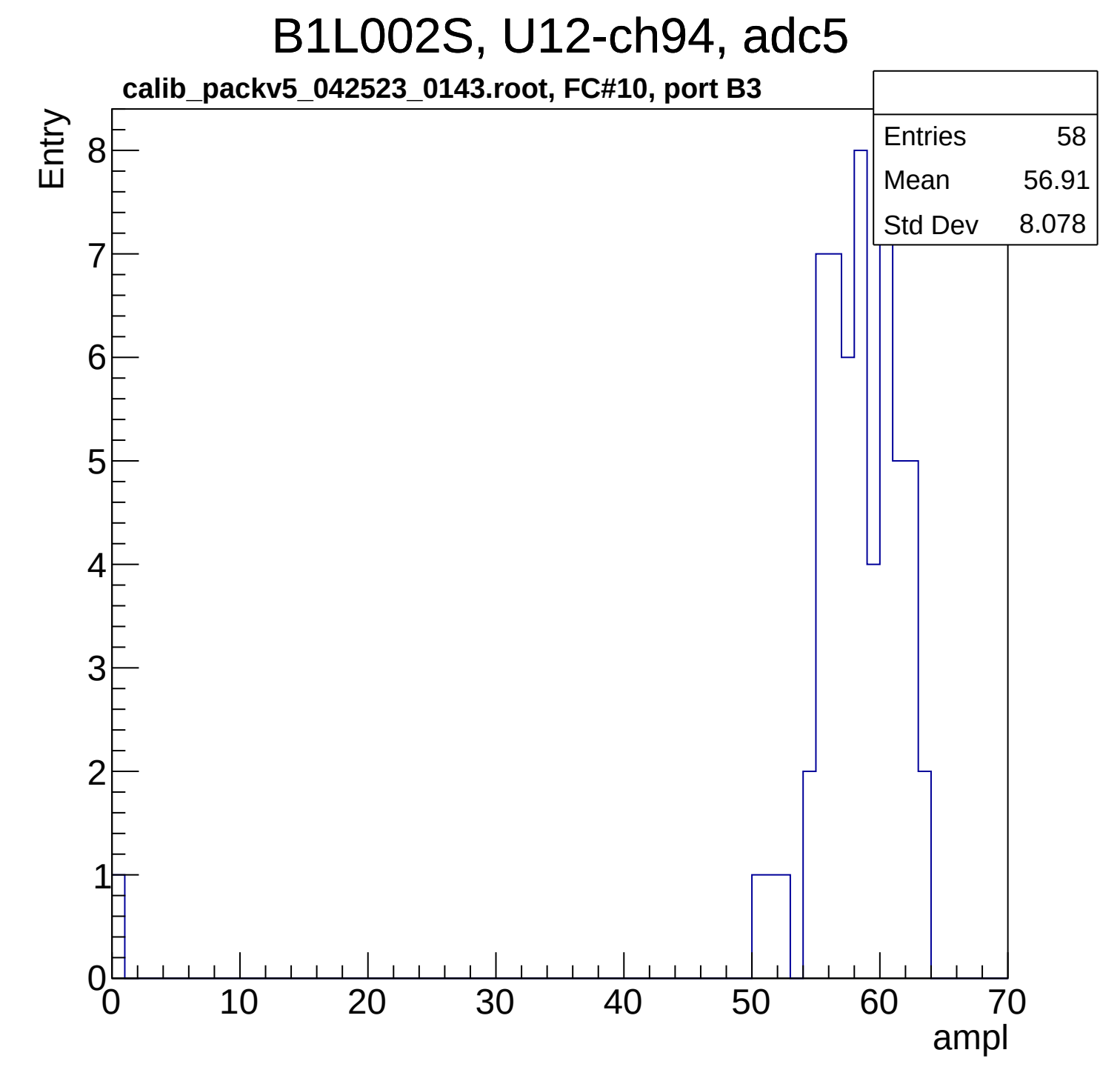

B1L002S, U12-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 54.24
Std Dev 3.09
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 56.91
Std Dev 8.078
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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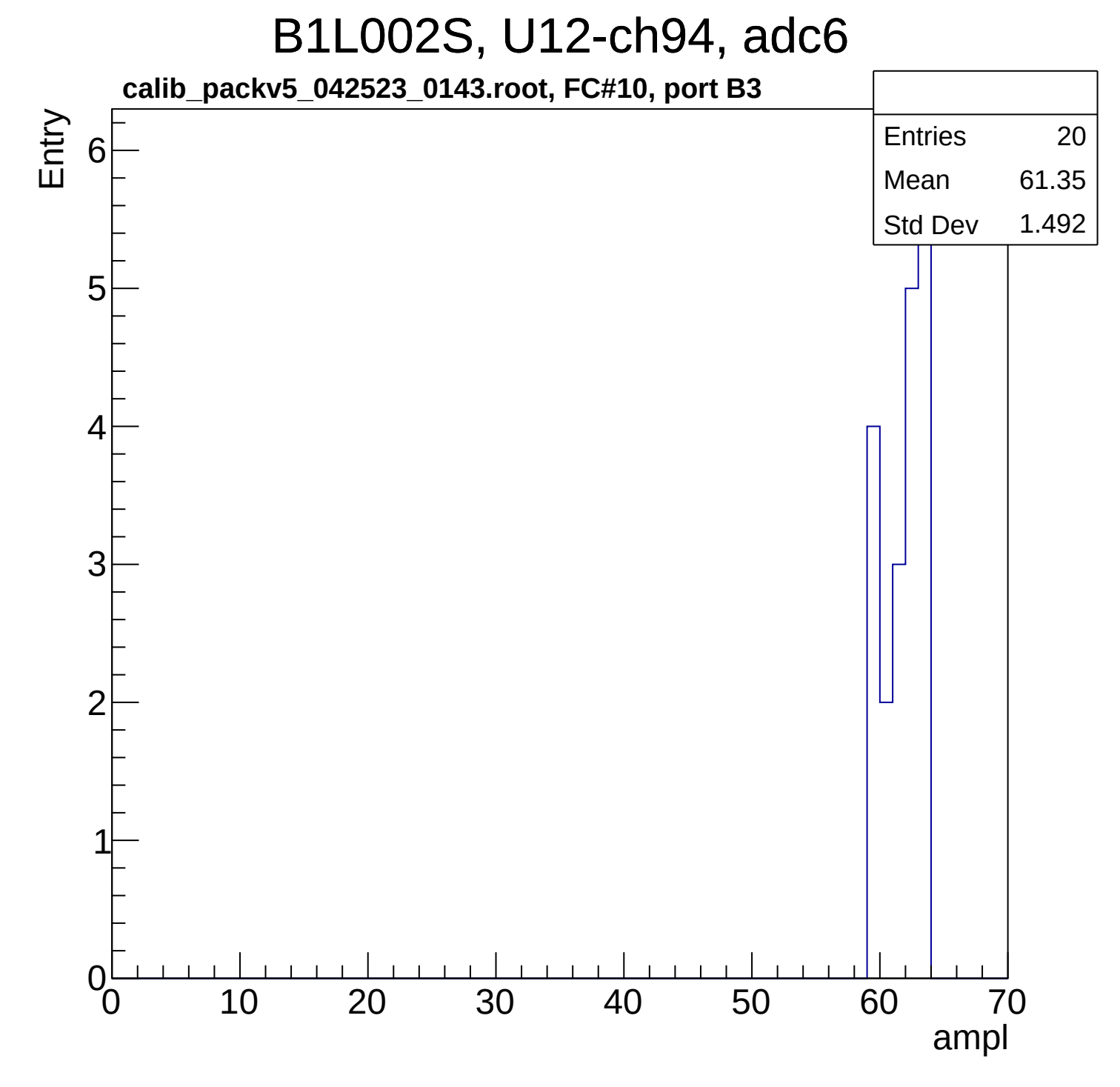
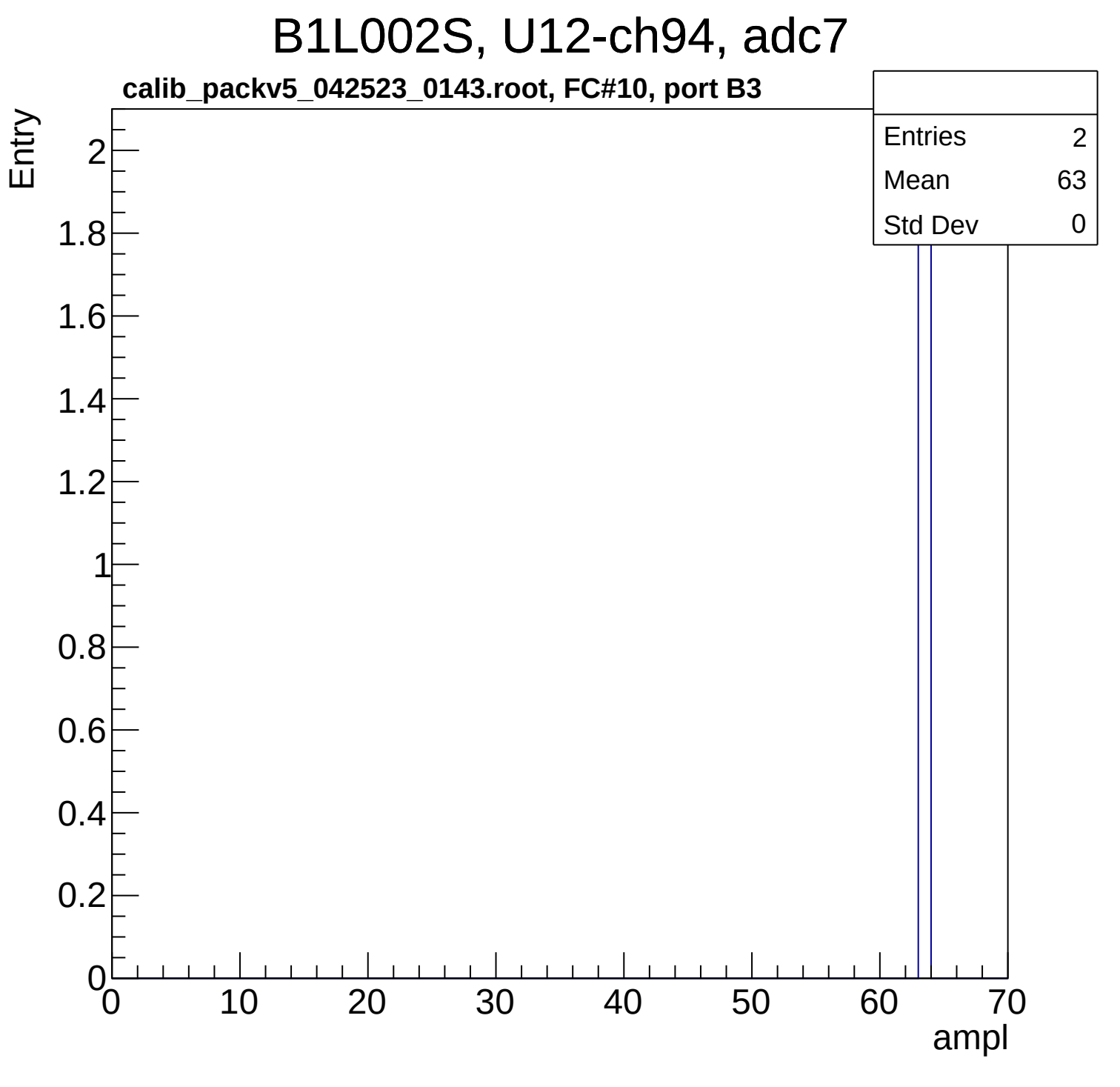

B1L002S, U12-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.35
Std Dev 1.492
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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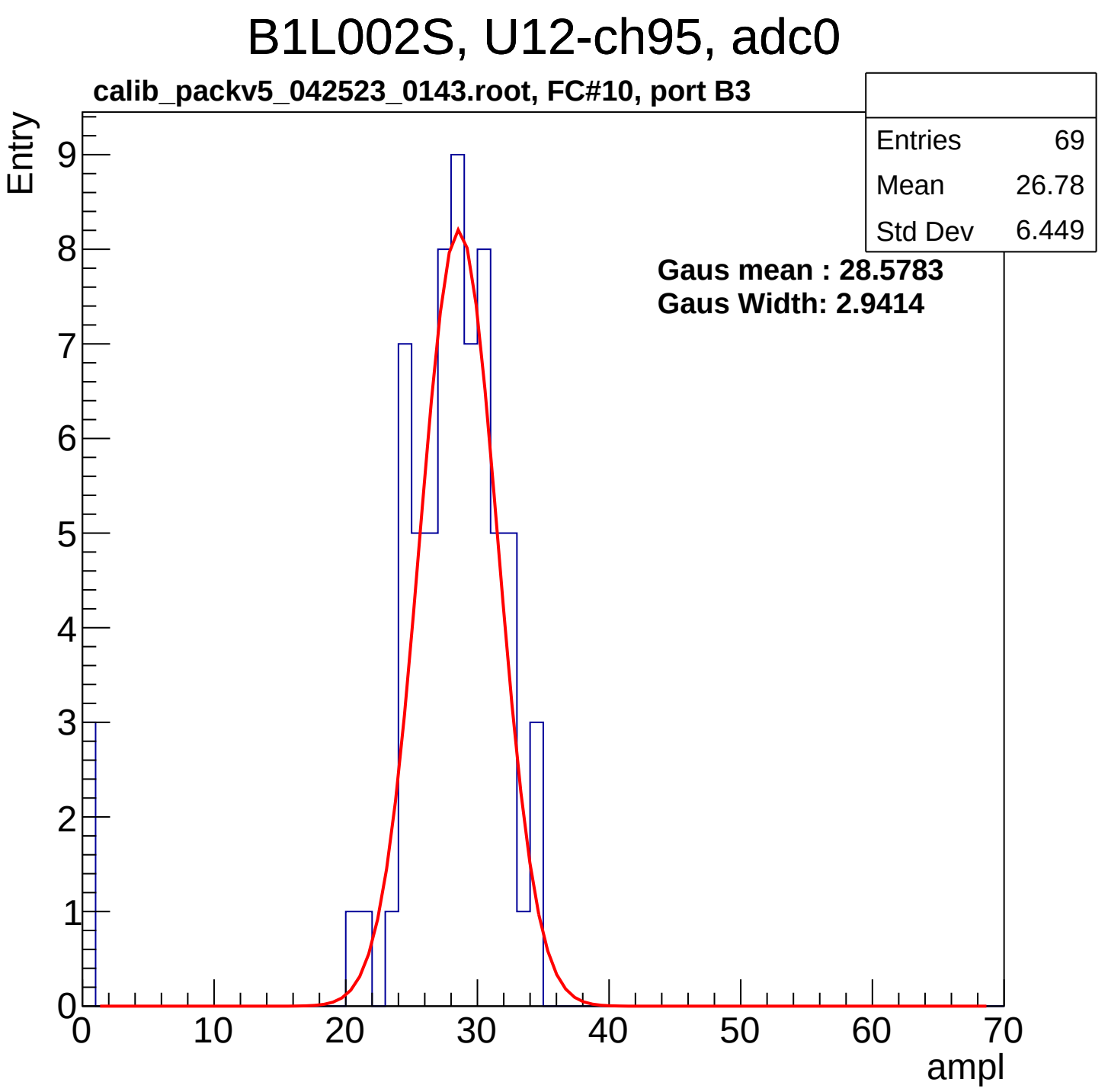
B1L002S, U12-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 26.78
Std Dev 6.449
Gaus mean : 28.5783
Gaus Width: 2.9414
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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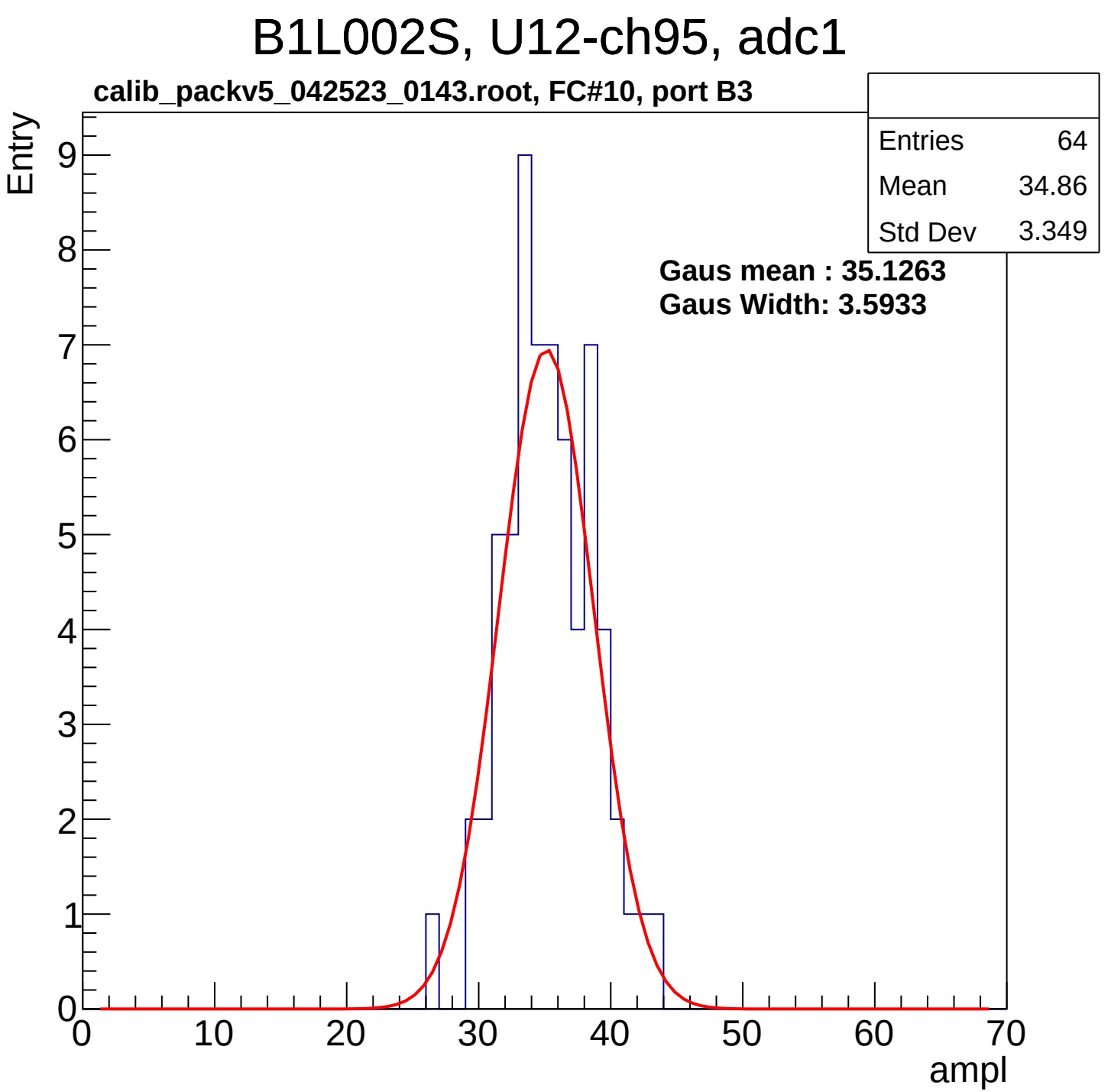
B1L002S, U12-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.86
Std Dev 3.349
Gaus mean : 35.1263
Gaus Width: 3.5933
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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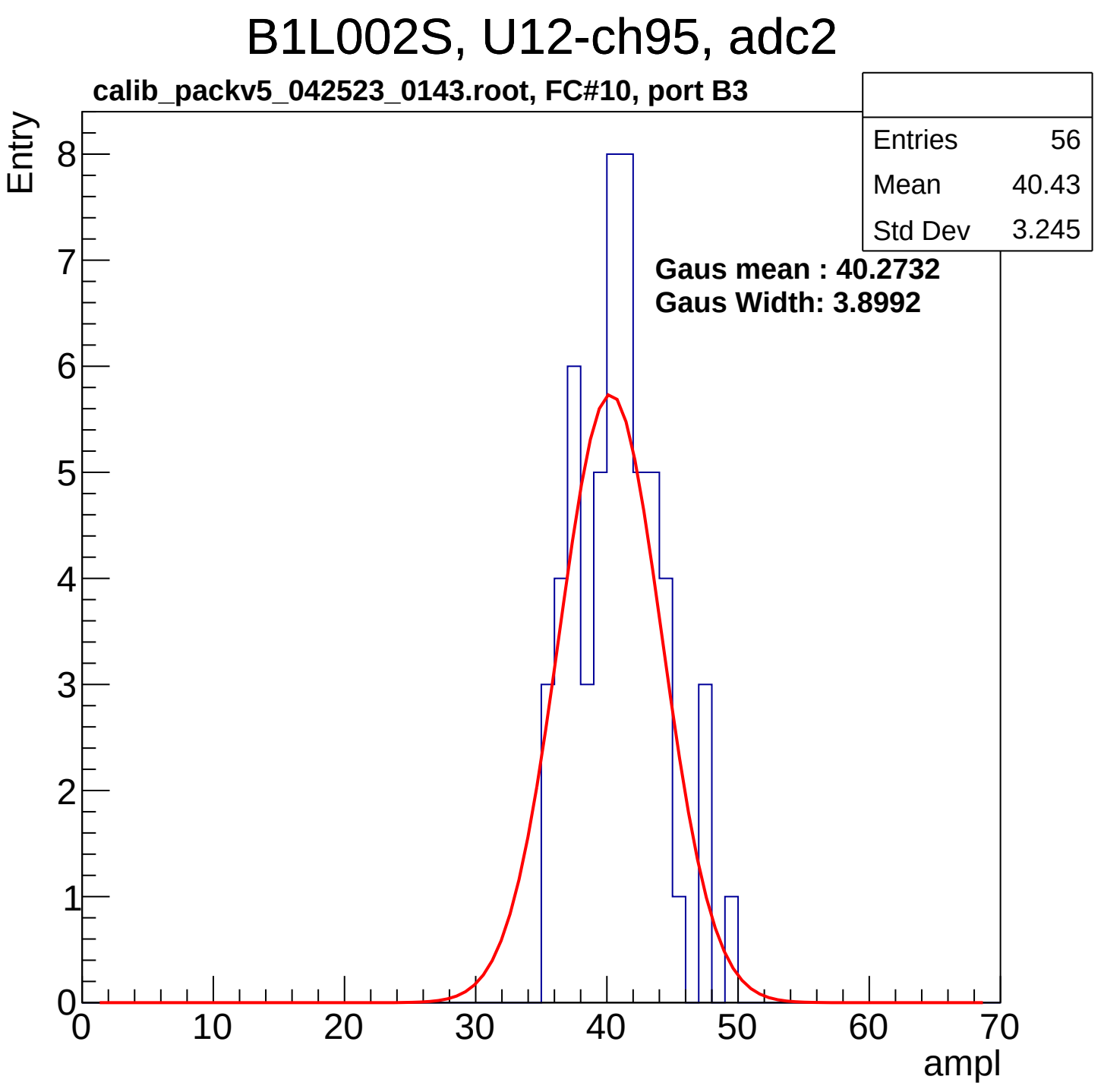
B1L002S, U12-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.43
Std Dev 3.245
Gaus mean : 40.2732
Gaus Width: 3.8992
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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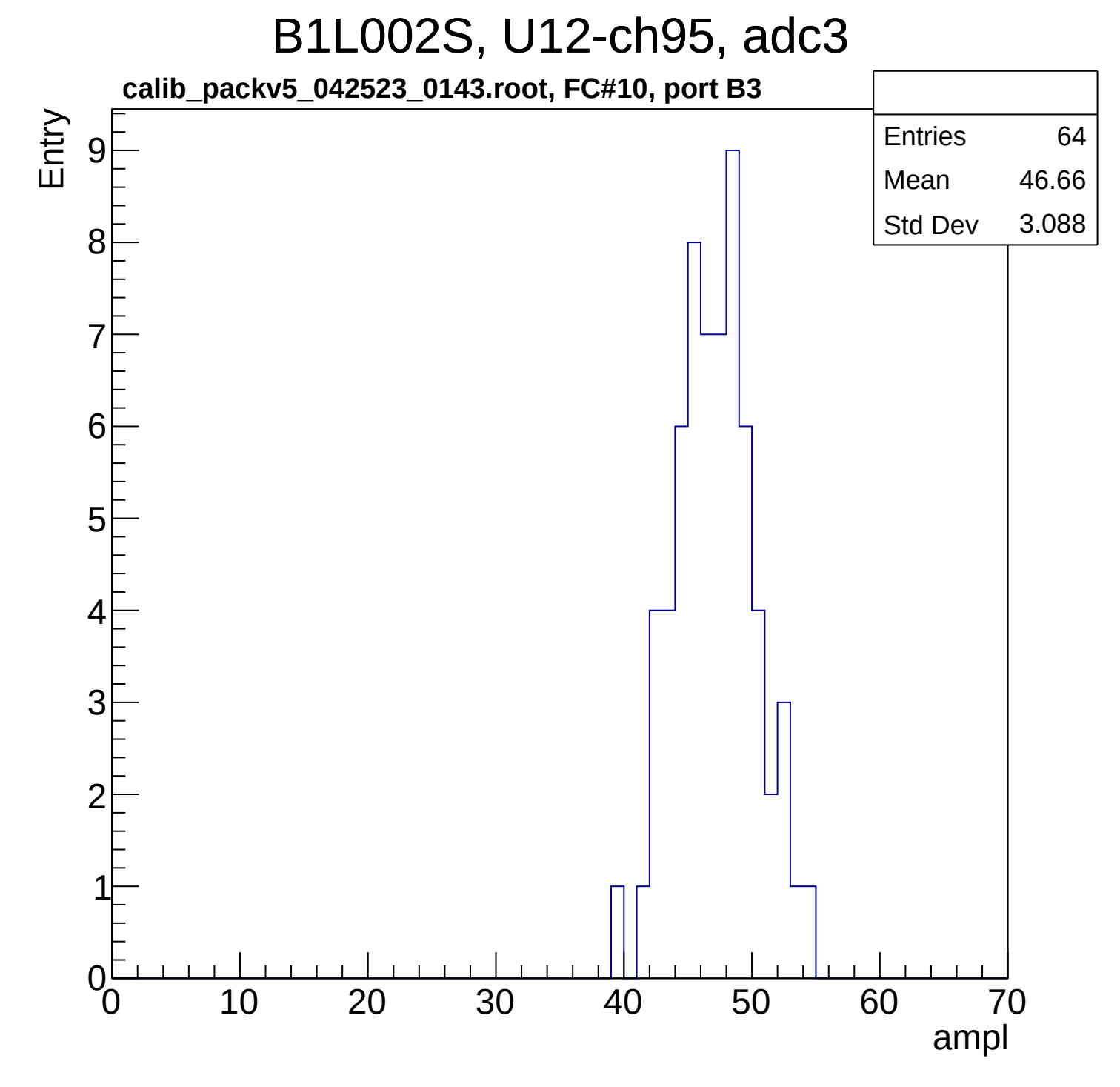
B1L002S, U12-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.66
Std Dev 3.088
Entry
ampl
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6
7
8
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0
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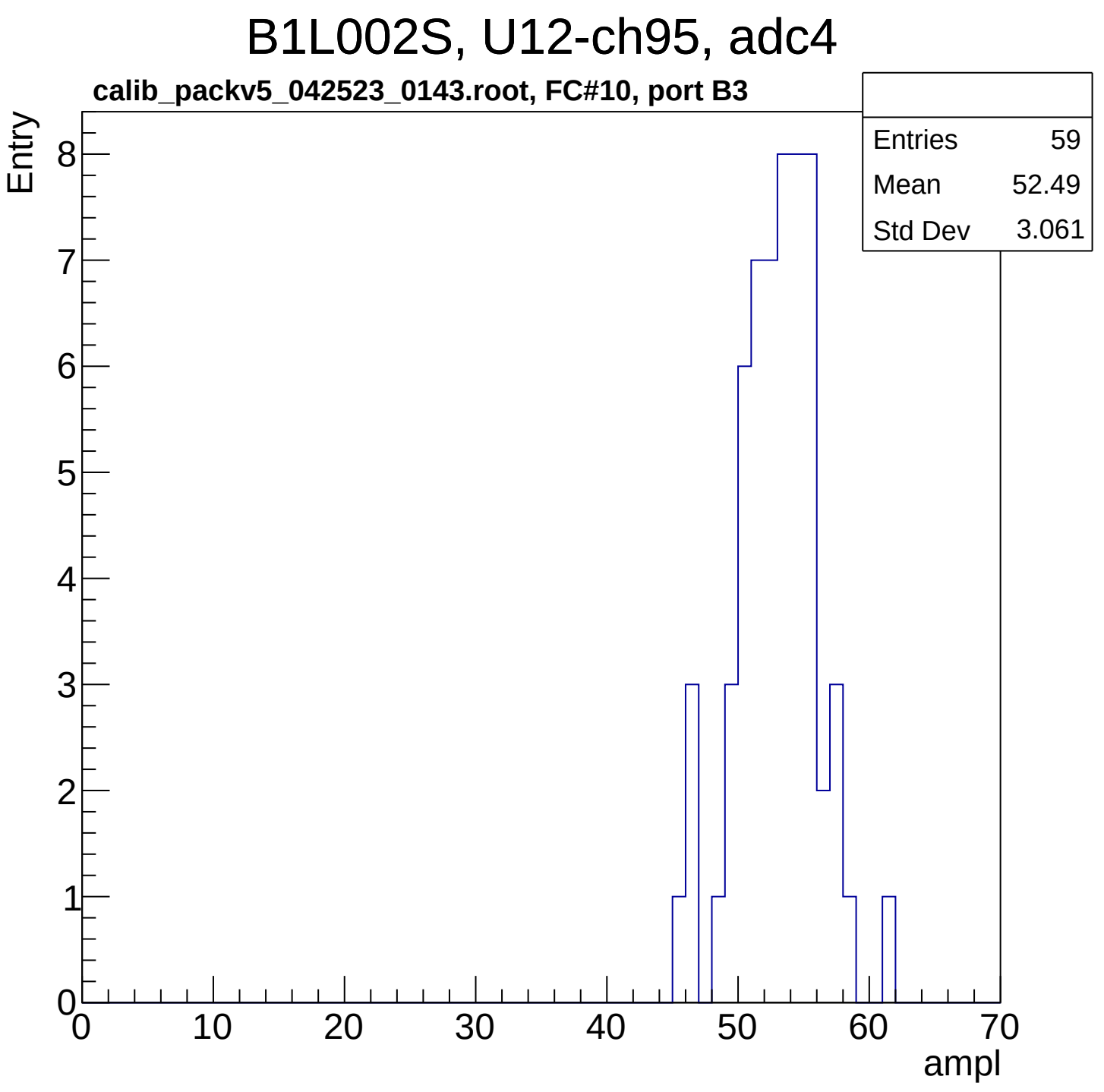
B1L002S, U12-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.49
Std Dev 3.061
Entry
ampl
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7
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0
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60
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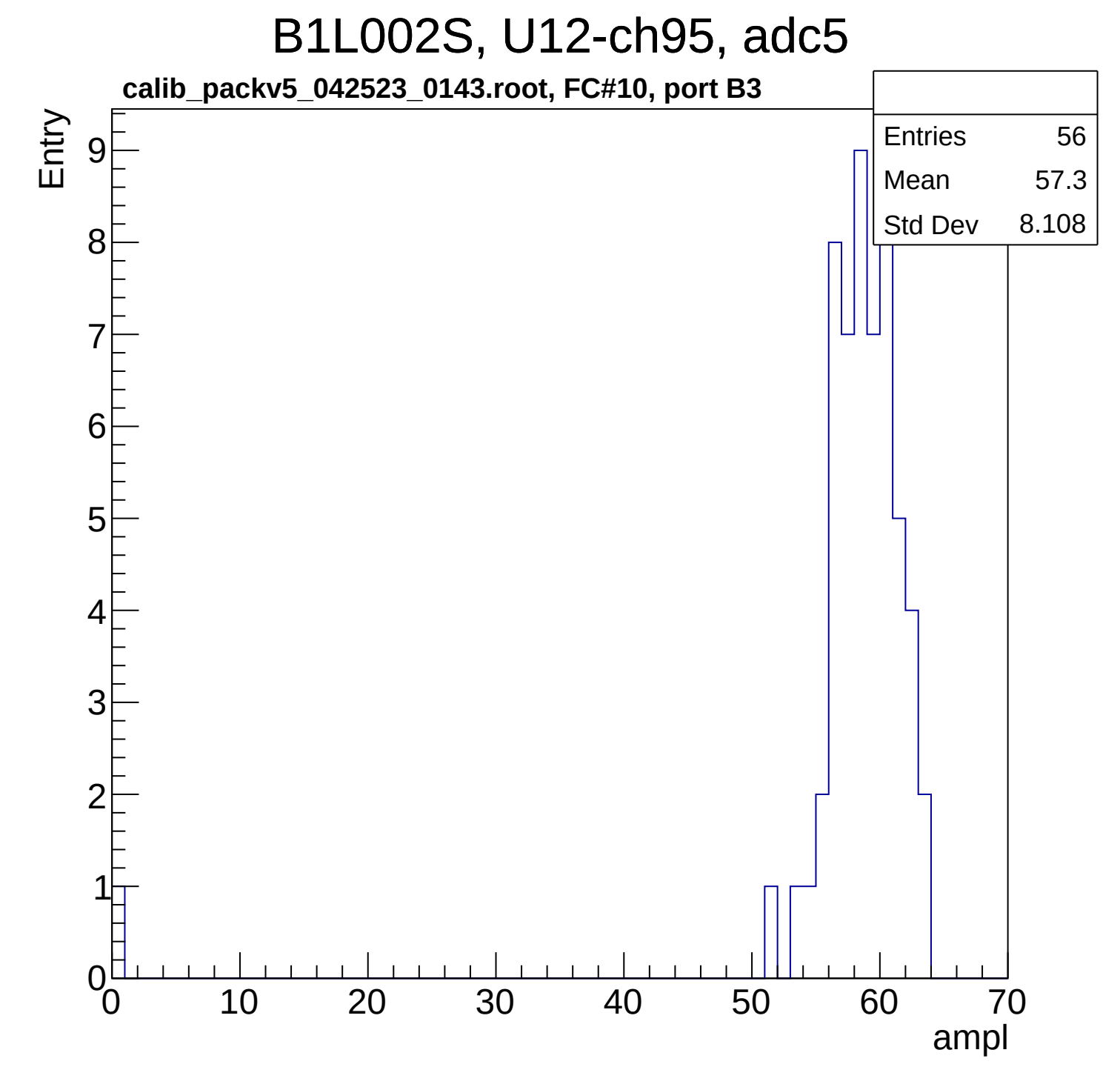
B1L002S, U12-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.3
Std Dev 8.108
Entry
ampl
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3
4
5
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7
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0
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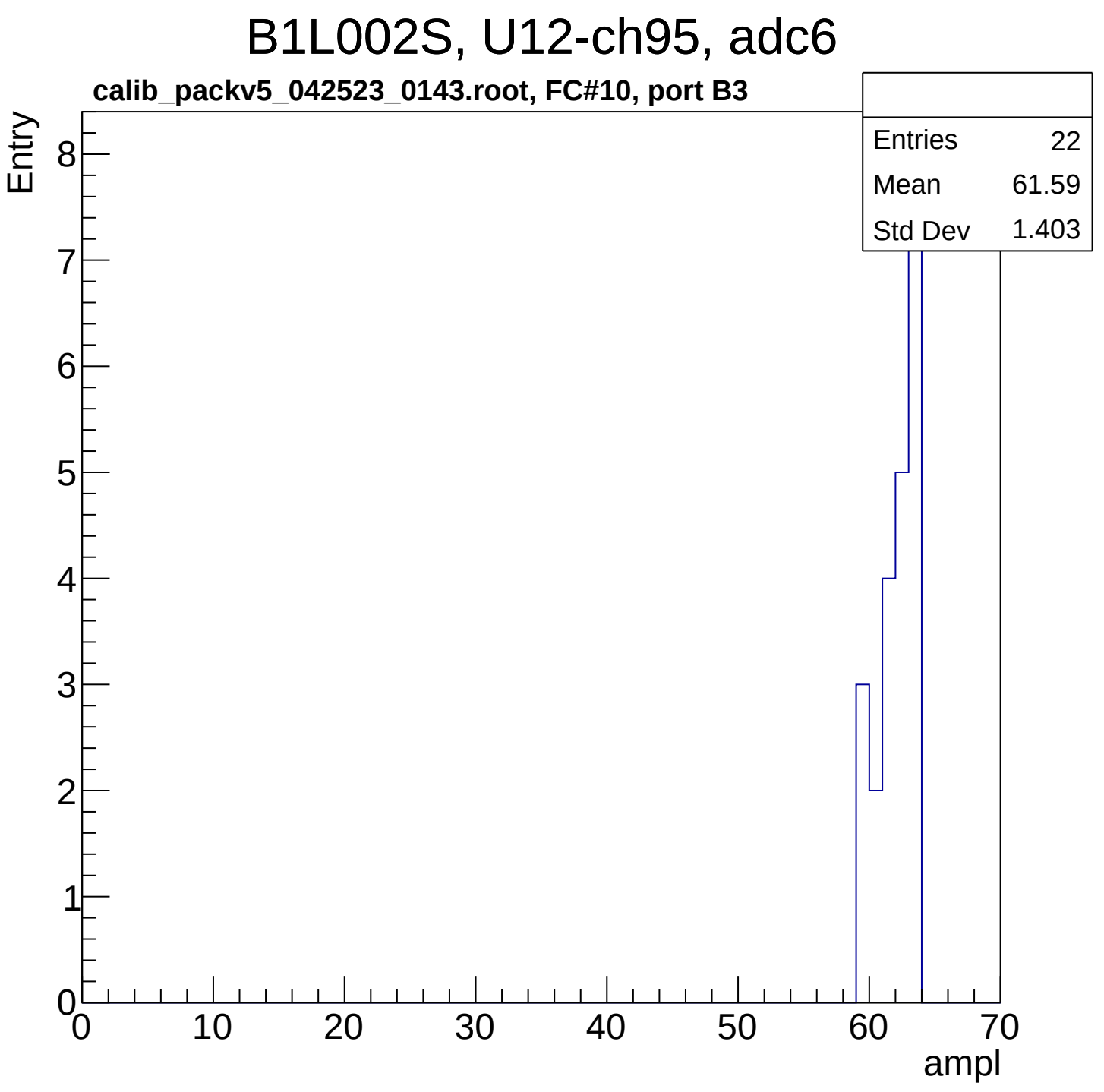
B1L002S, U12-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.59
Std Dev 1.403
Entry
ampl
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1
2
3
4
5
6
7
8
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10
20
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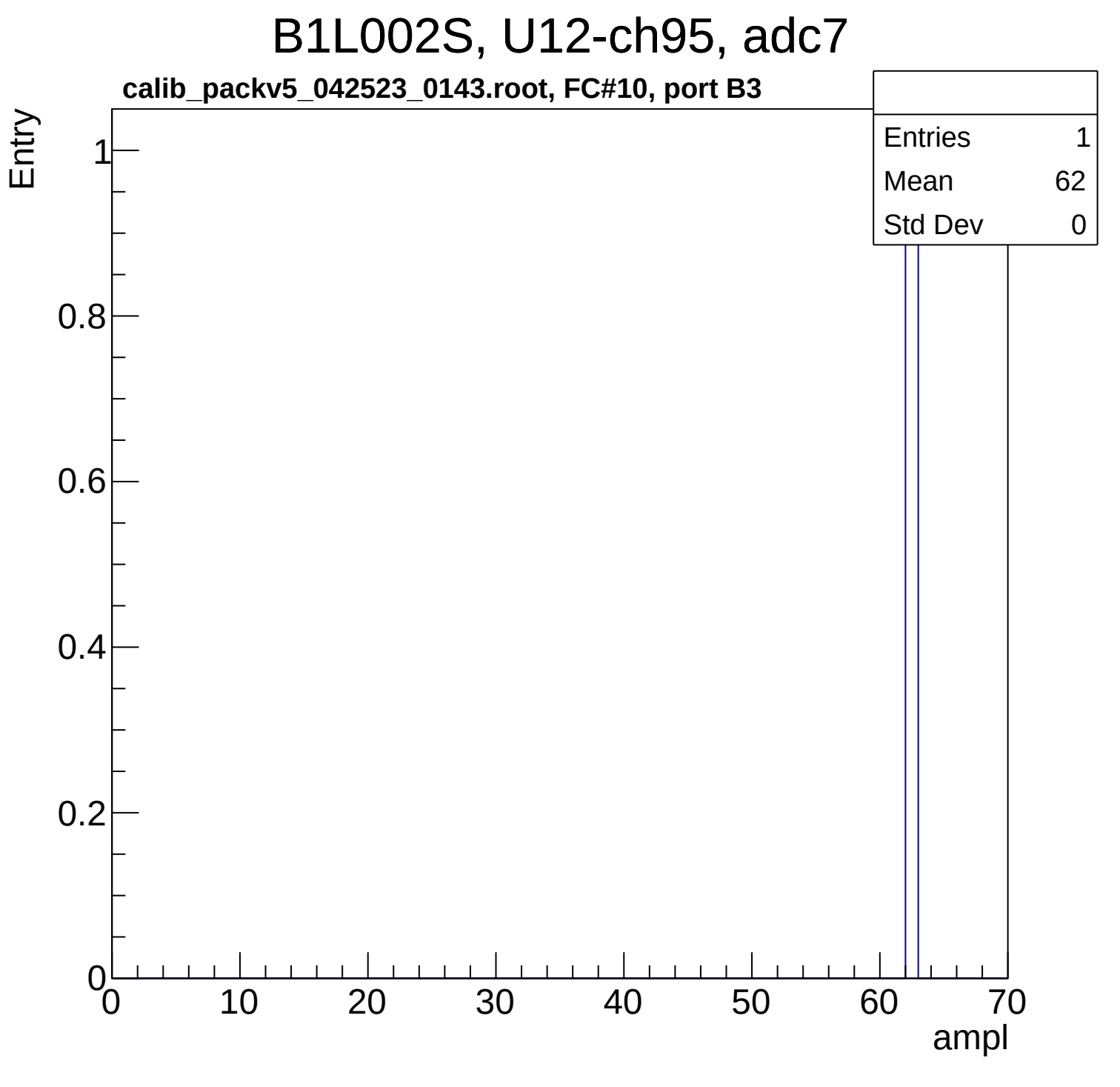
60
70


B1L002S, U12-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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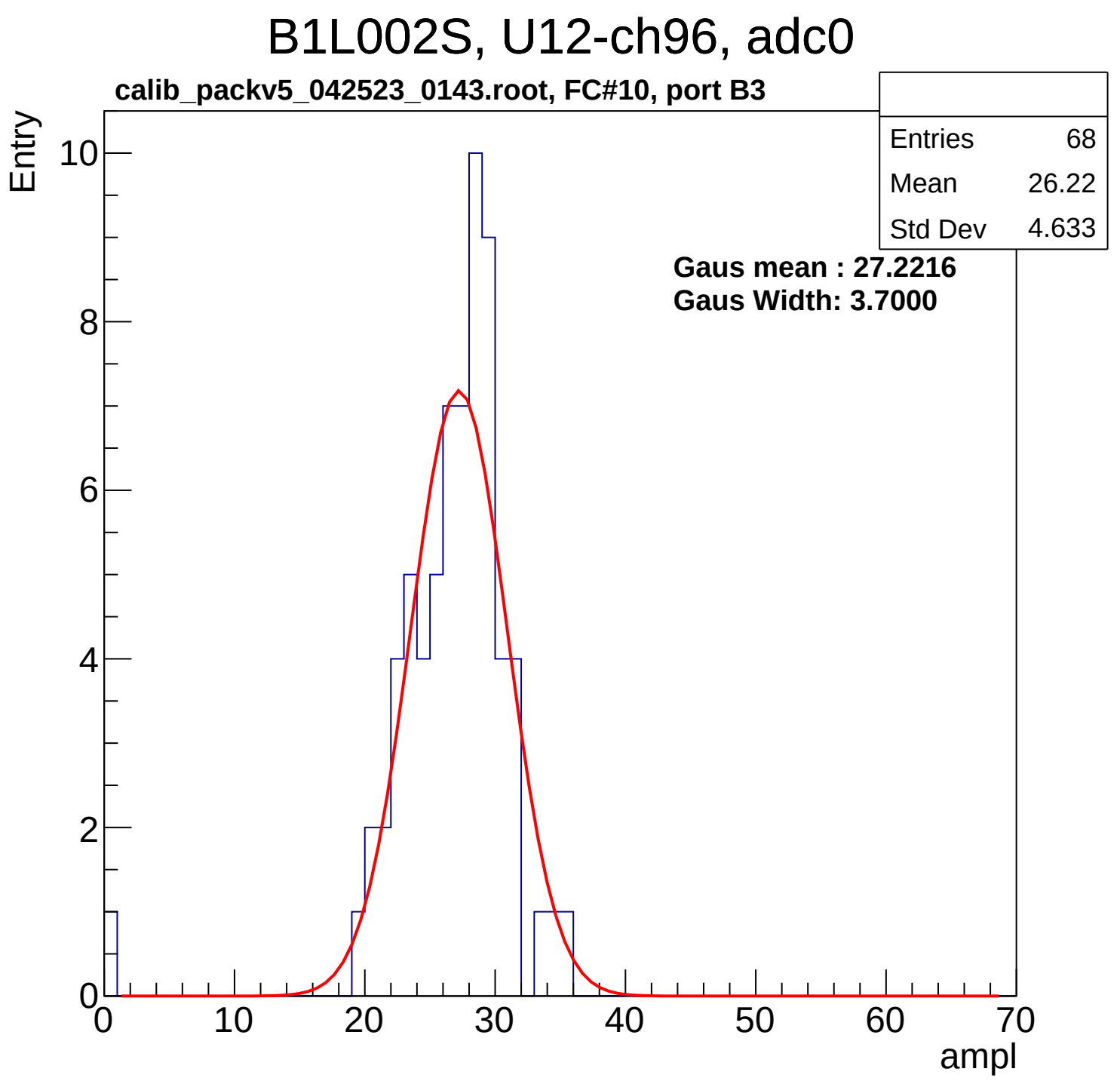
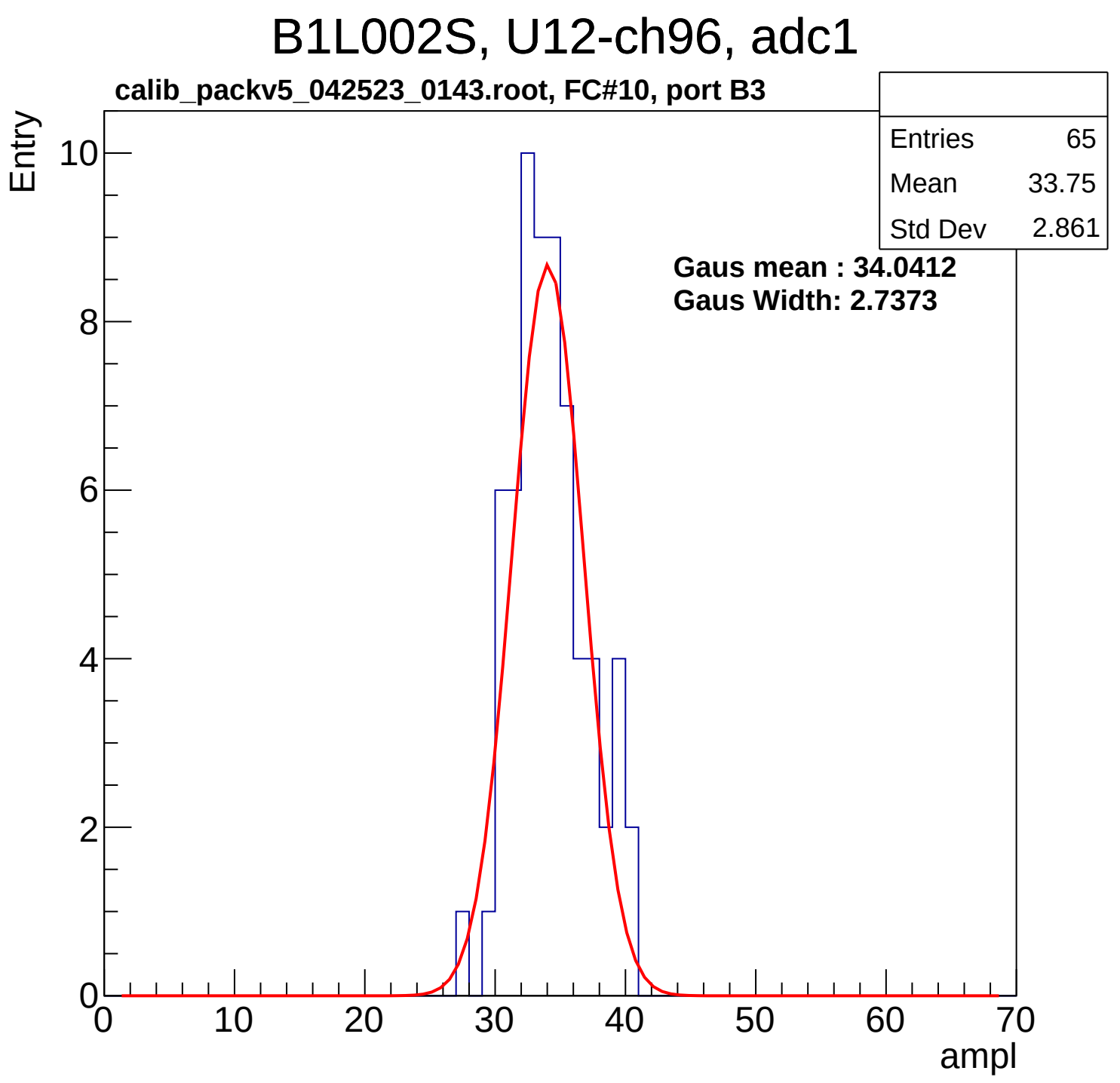
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 26.22
Std Dev 4.633
Gaus mean : 27.2216
Gaus Width: 3.7000
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.75
Std Dev 2.861
Gaus mean : 34.0412
Gaus Width: 2.7373
Entry
ampl
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6
8
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0
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40
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60
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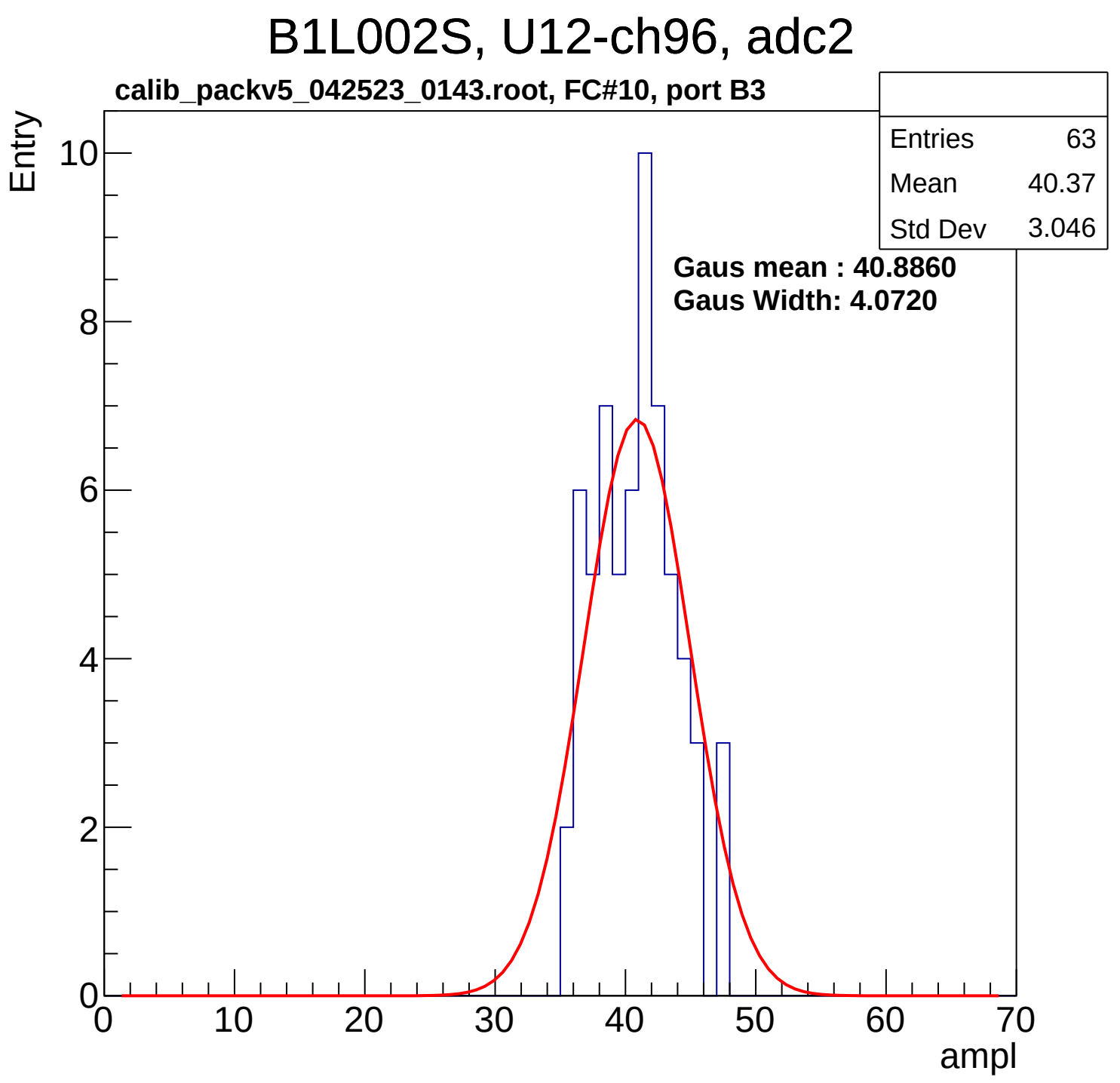
B1L002S, U12-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.37
Std Dev 3.046
Gaus mean : 40.8860
Gaus Width: 4.0720
Entry
ampl
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4
6
8
10
0
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60
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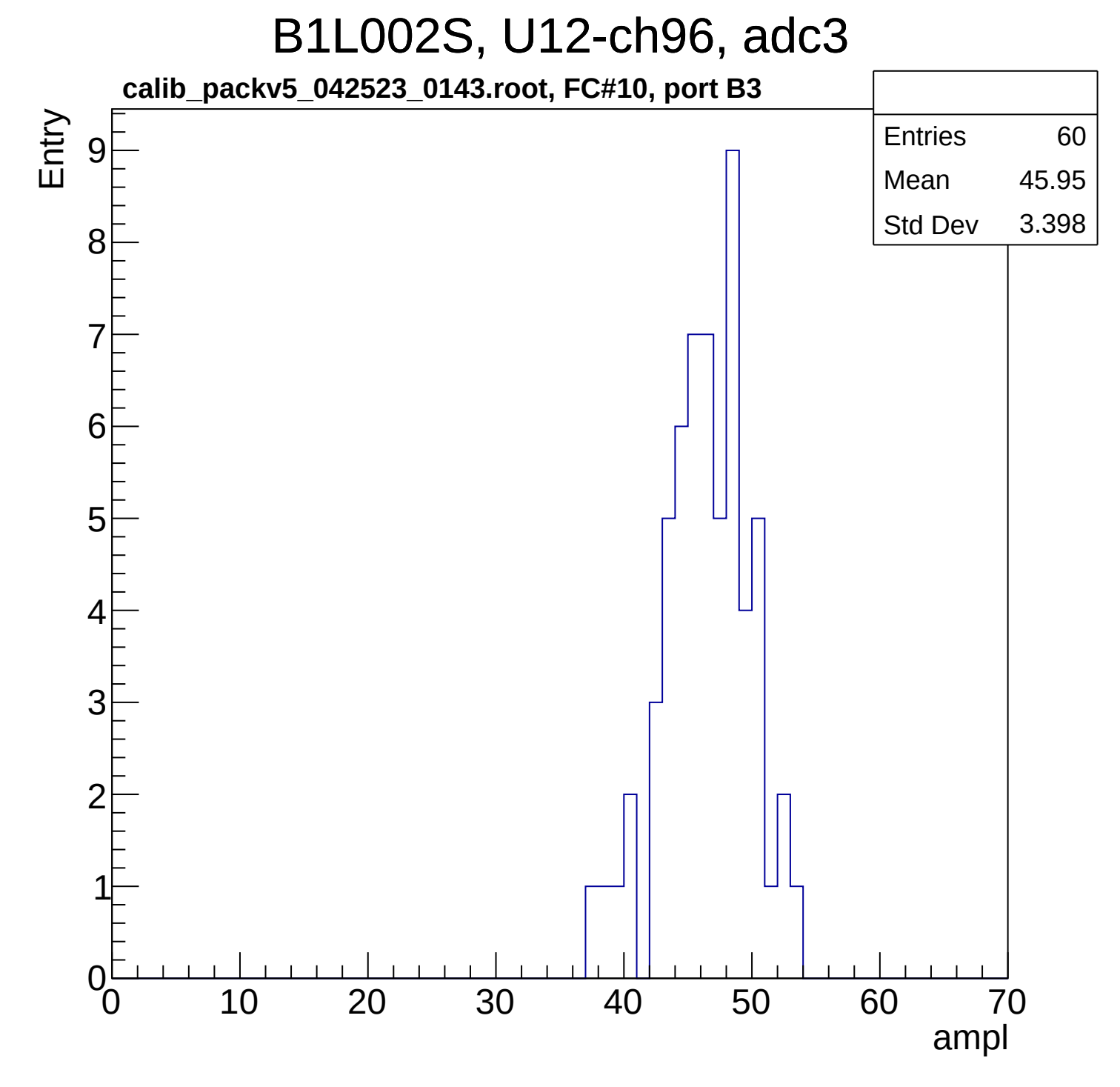
B1L002S, U12-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 45.95
Std Dev 3.398
Entry
ampl
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3
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6
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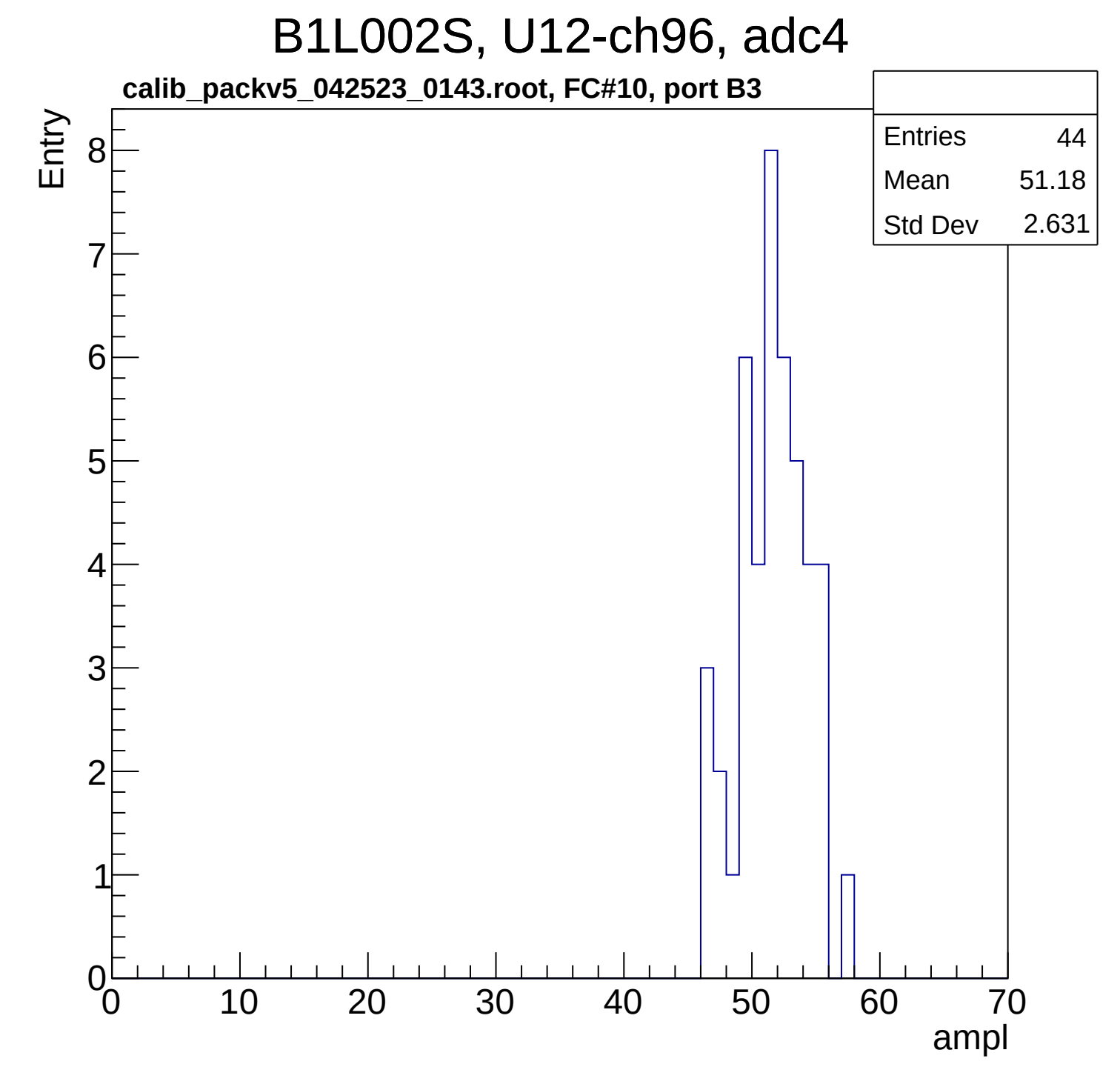
B1L002S, U12-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 51.18
Std Dev 2.631
Entry
ampl
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1
2
3
4
5
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7
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0
10
20
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40
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60
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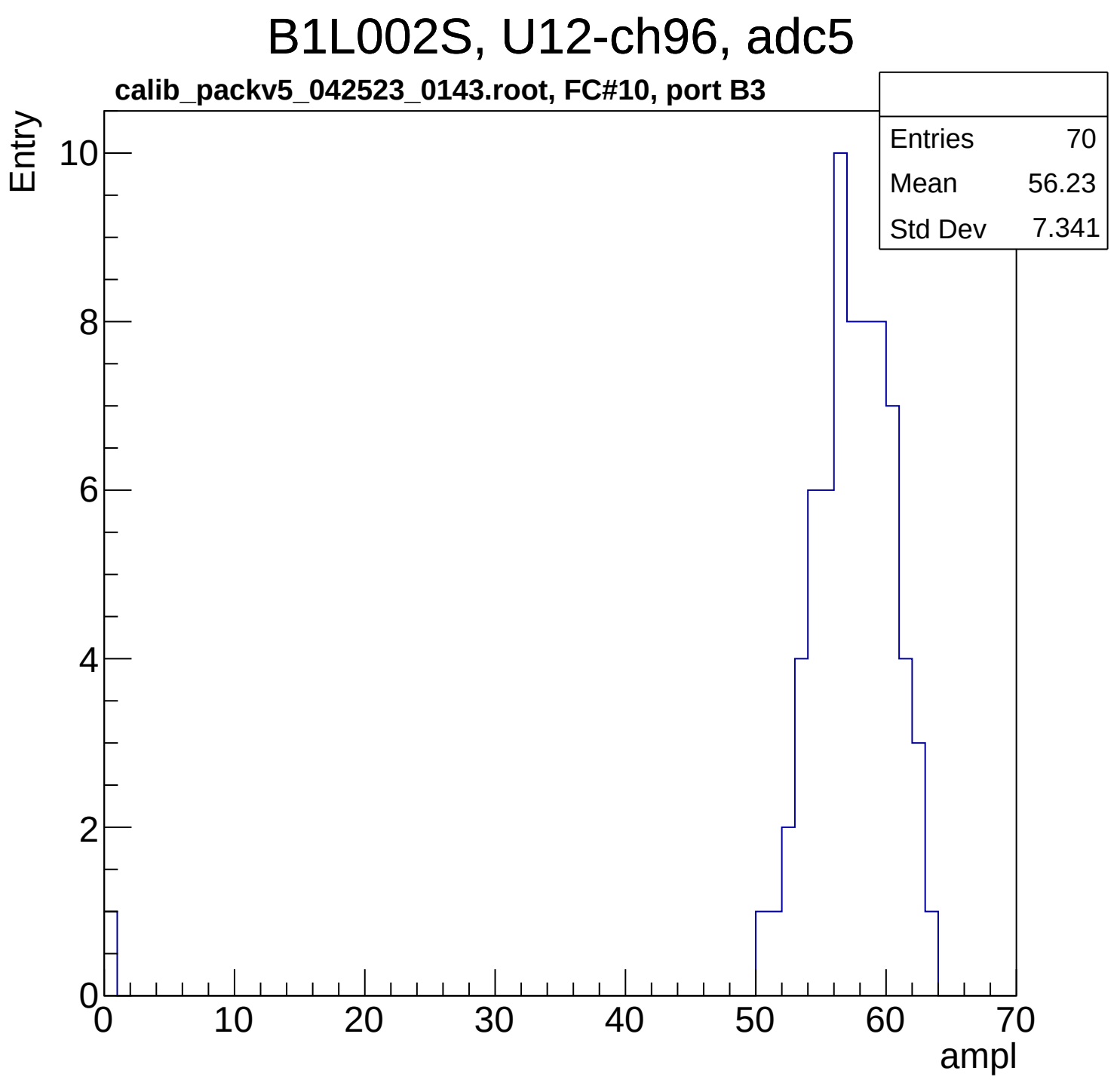
B1L002S, U12-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 56.23
Std Dev 7.341
Entry
0
2
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6
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10
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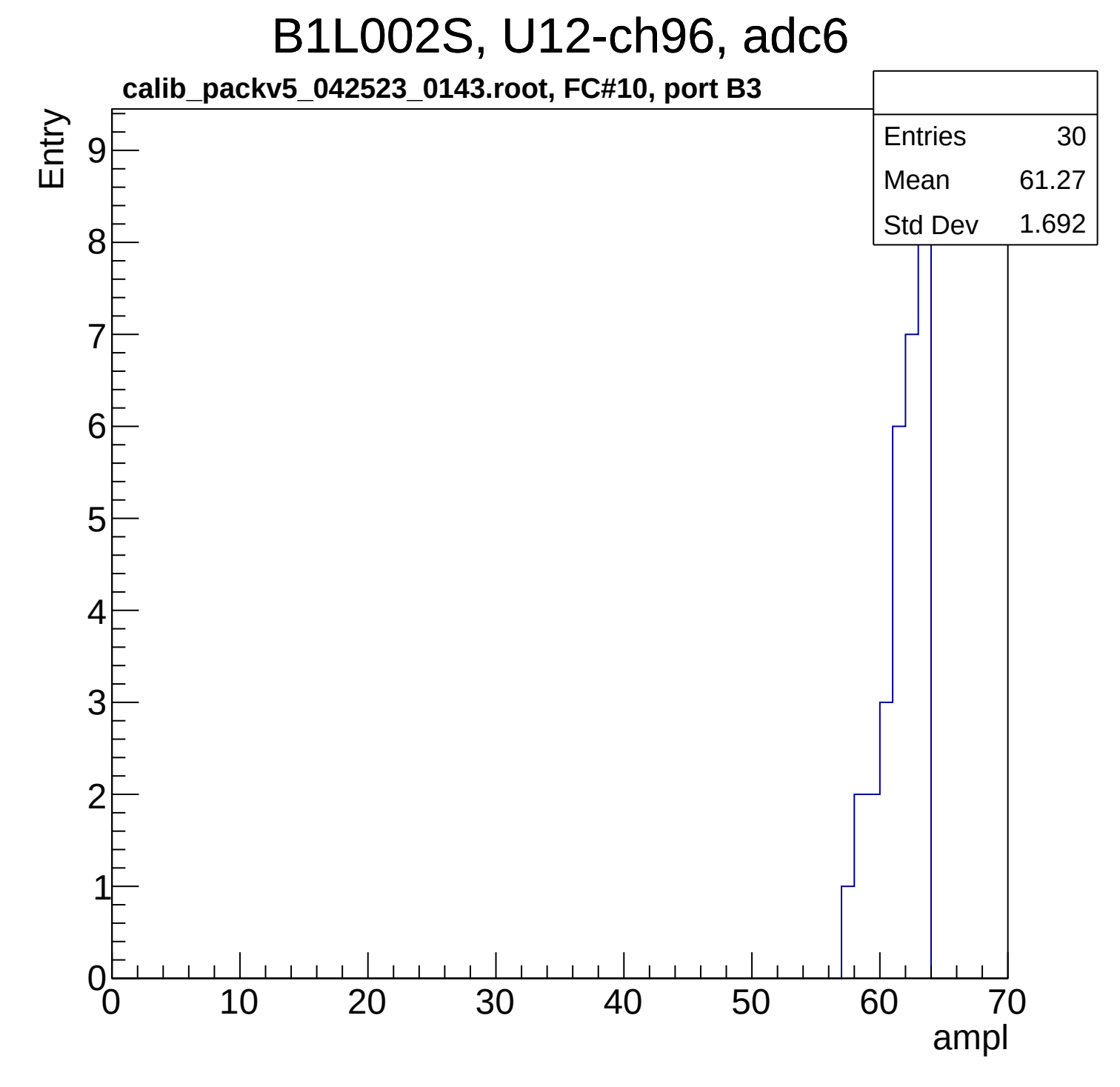
70
ampl


B1L002S, U12-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 61.27
Std Dev 1.692
Entry
ampl
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2
3
4
5
6
7
8
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0
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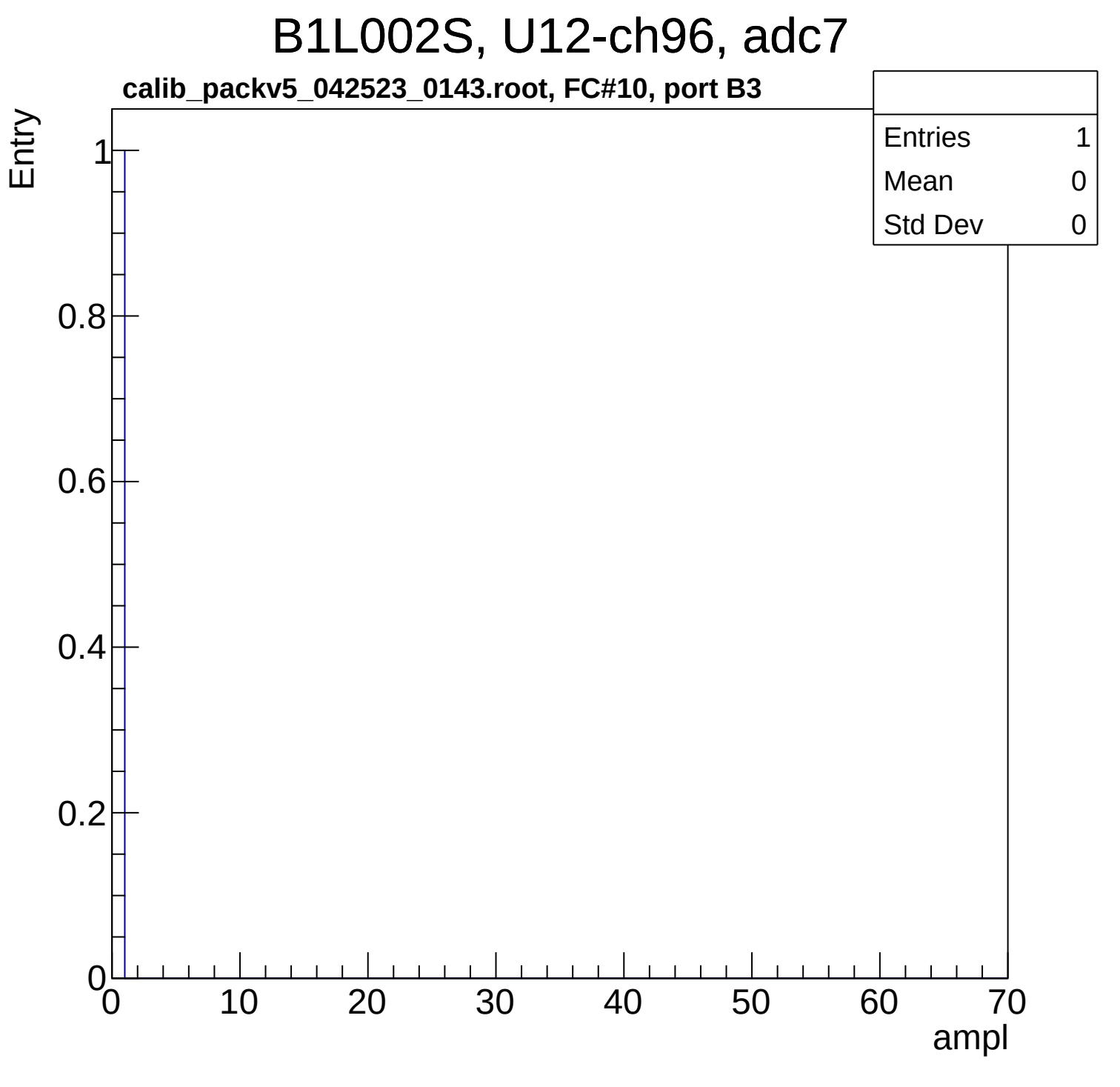
B1L002S, U12-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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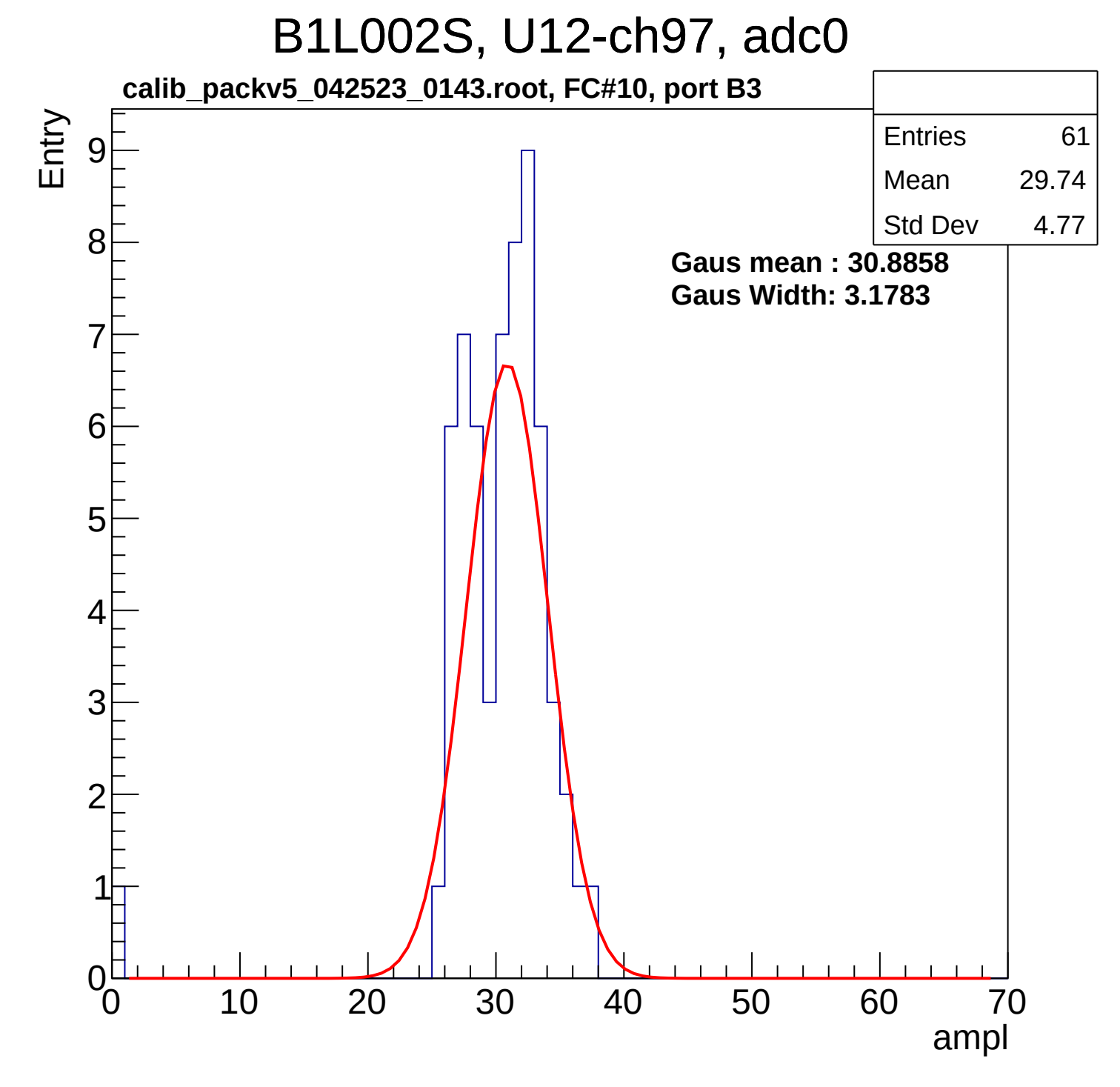
30
40
50
60
70
ampl


B1L002S, U12-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 29.74
Std Dev 4.77
Gaus mean : 30.8858
Gaus Width: 3.1783
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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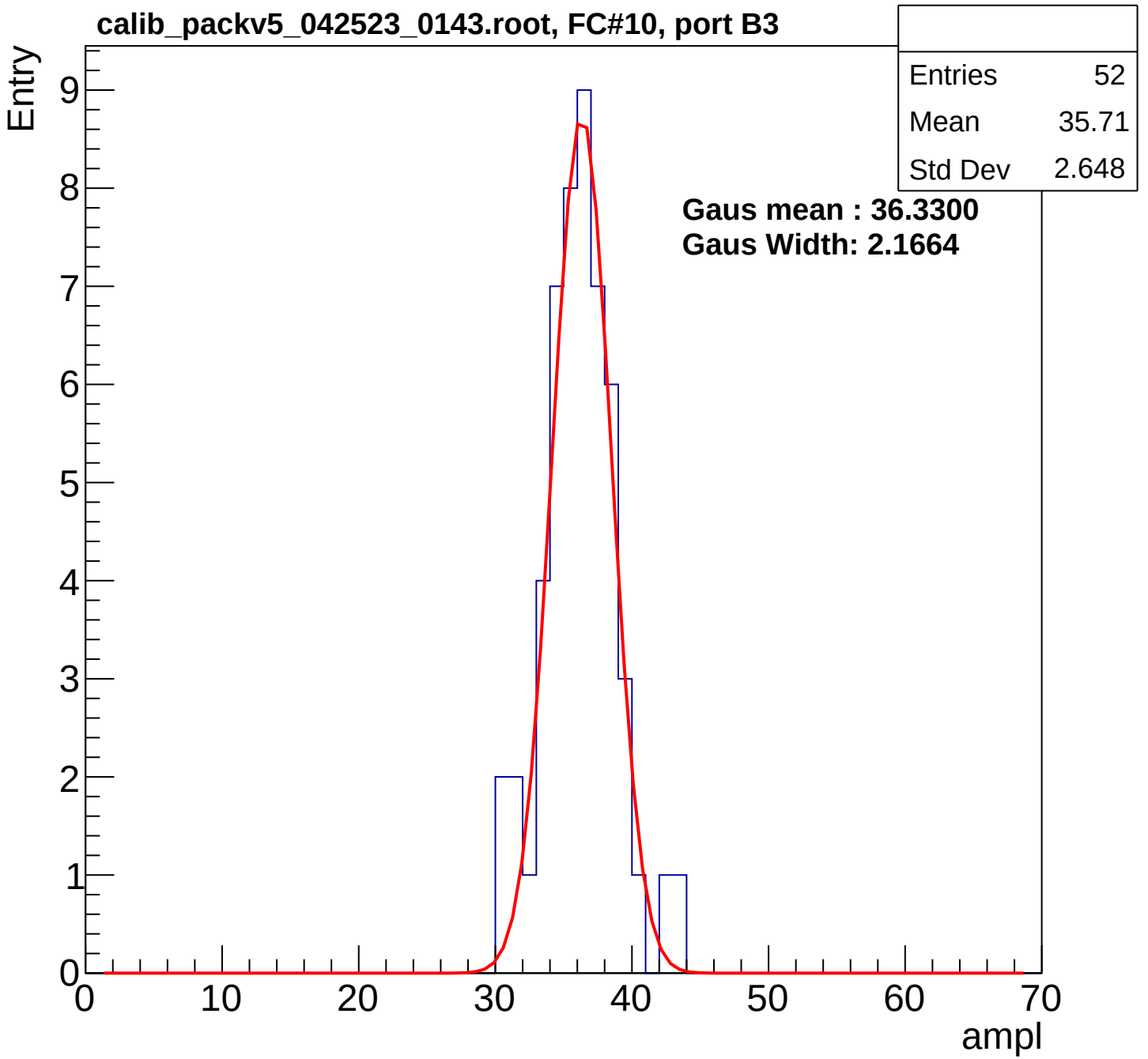
B1L002S, U12-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.71
Std Dev 2.648
Gaus mean : 36.3300
Gaus Width: 2.1664
Entry
ampl
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2
3
4
5
6
7
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9
0
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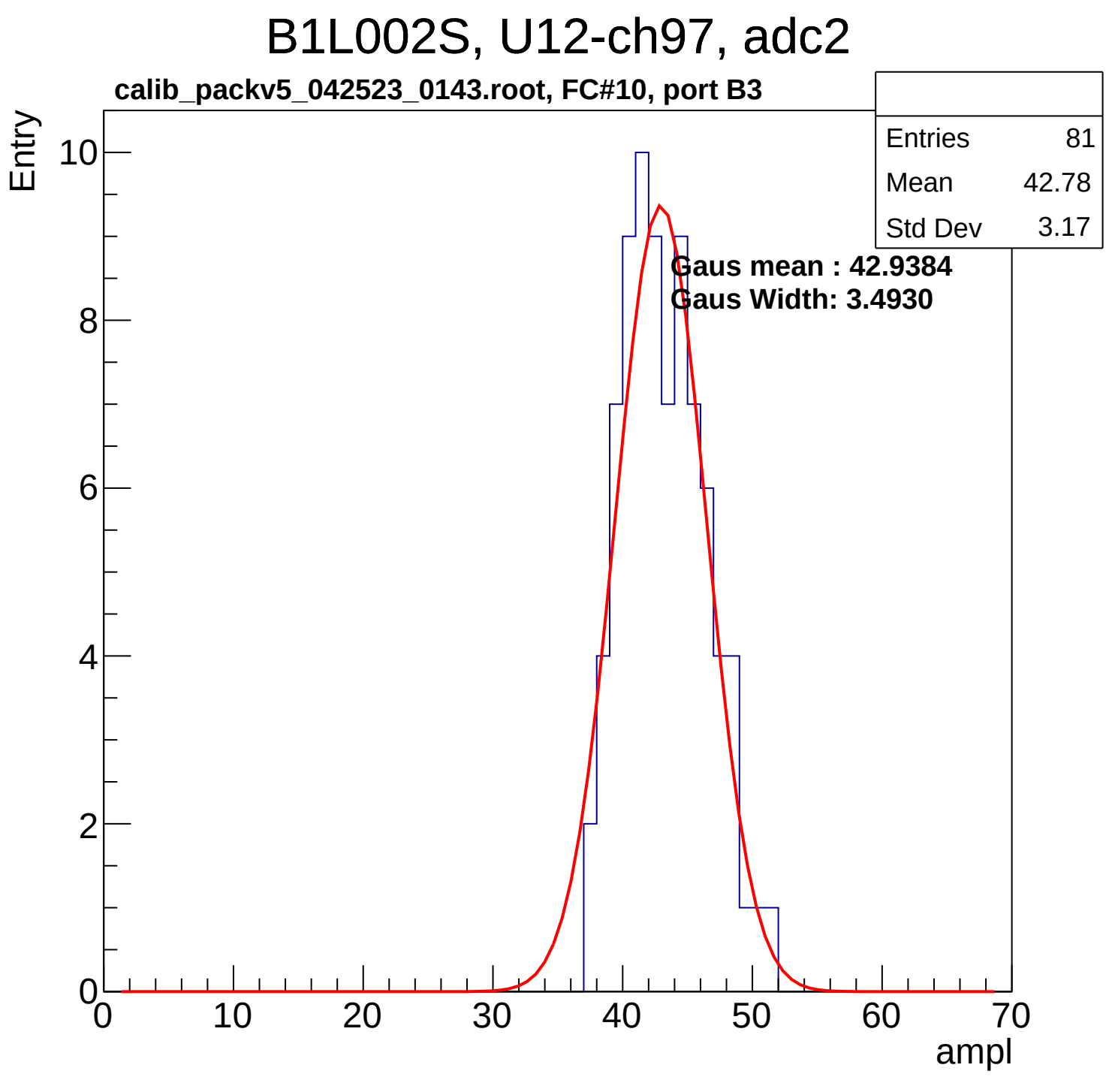
B1L002S, U12-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 42.78
Std Dev 3.17
Gaus mean : 42.9384
Gaus Width: 3.4930
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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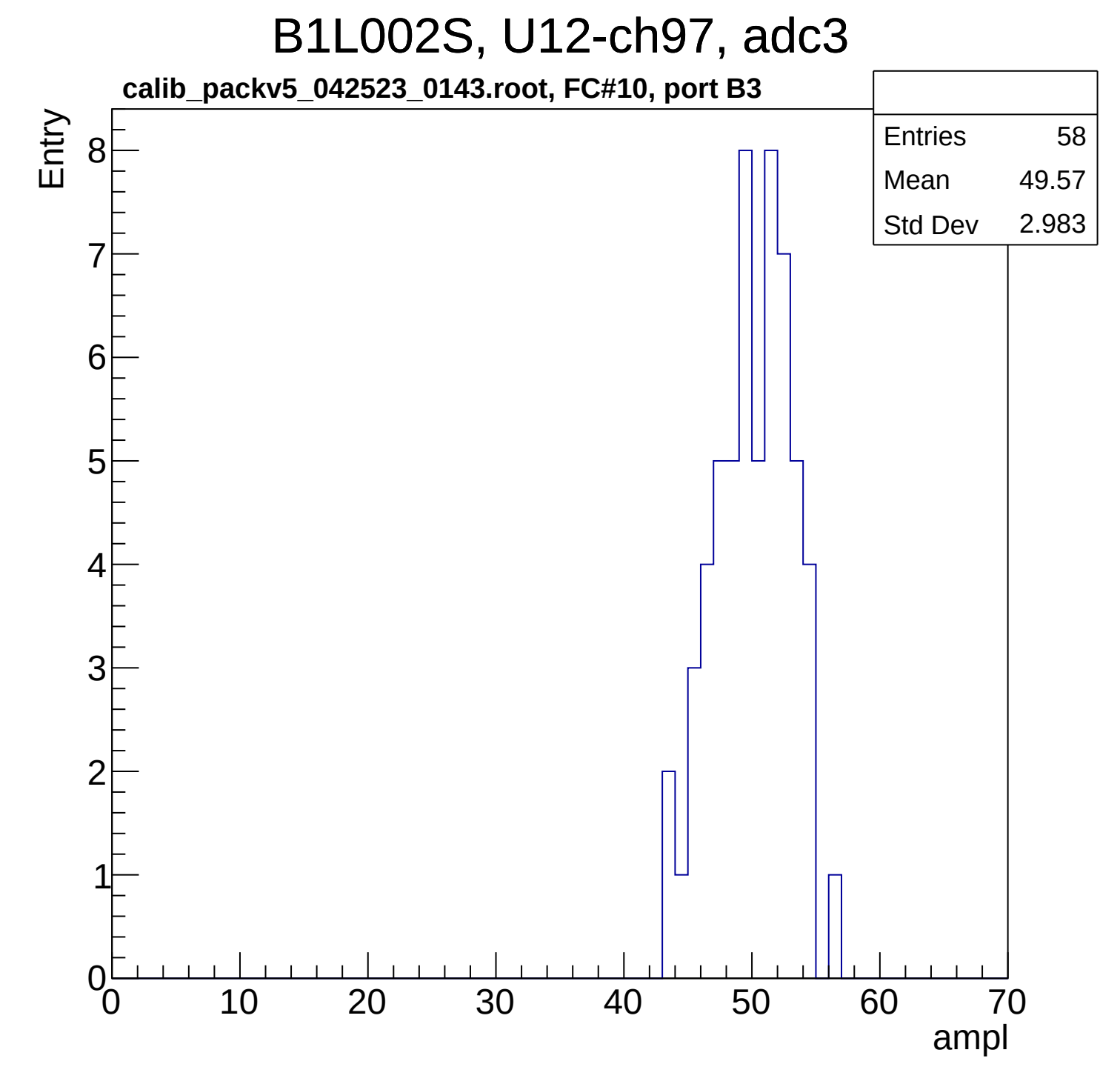
B1L002S, U12-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 49.57
Std Dev 2.983
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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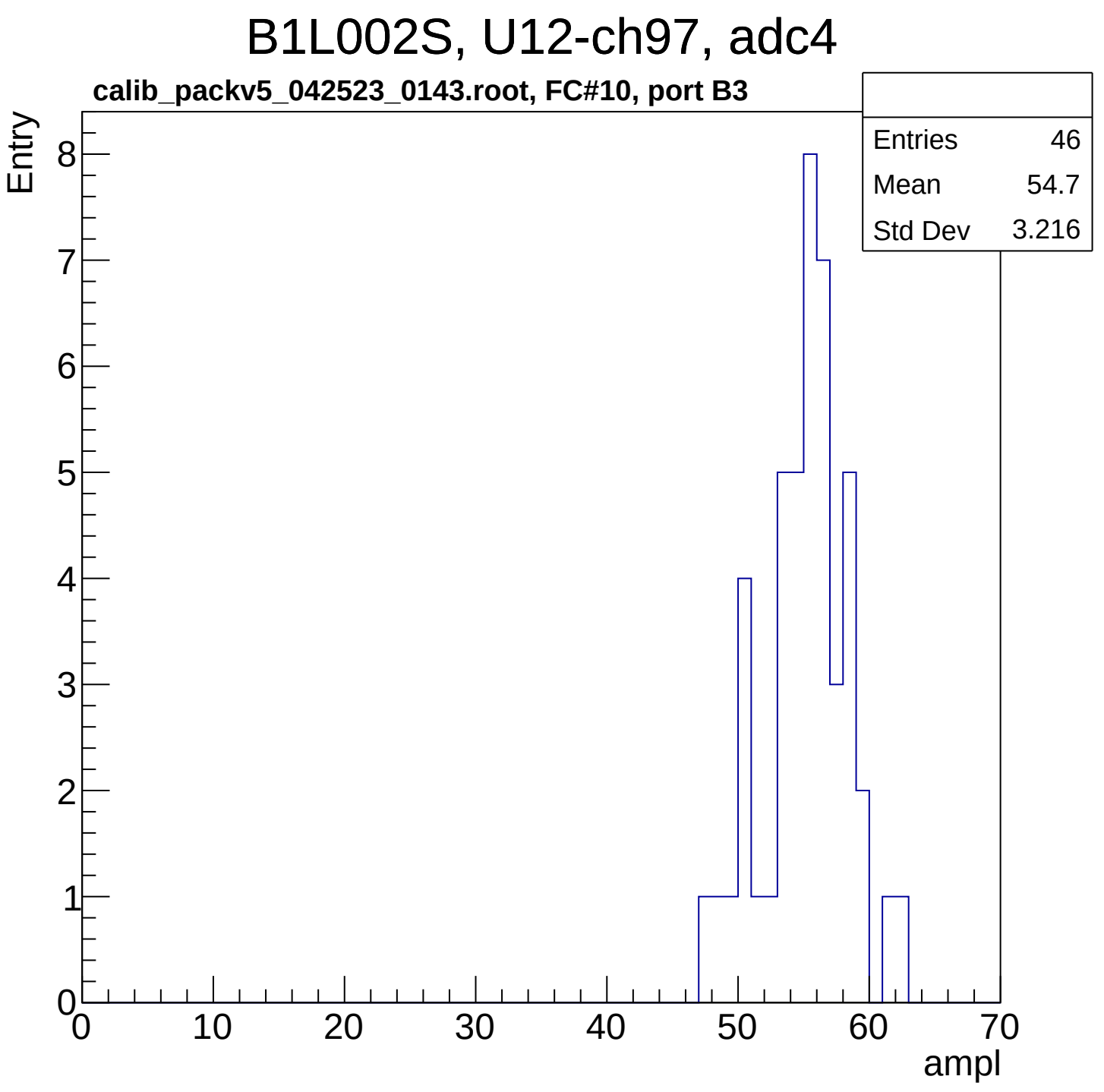
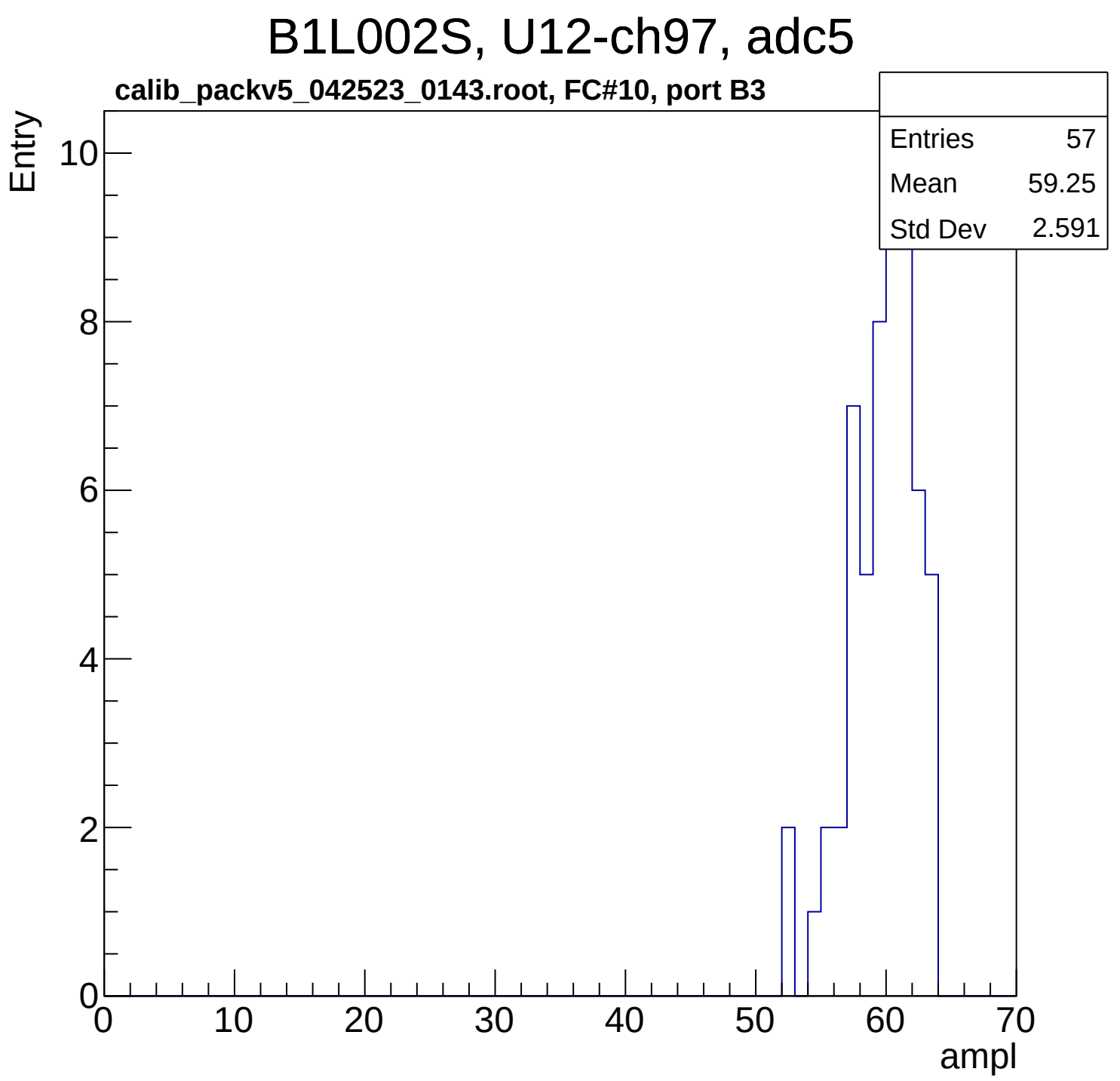
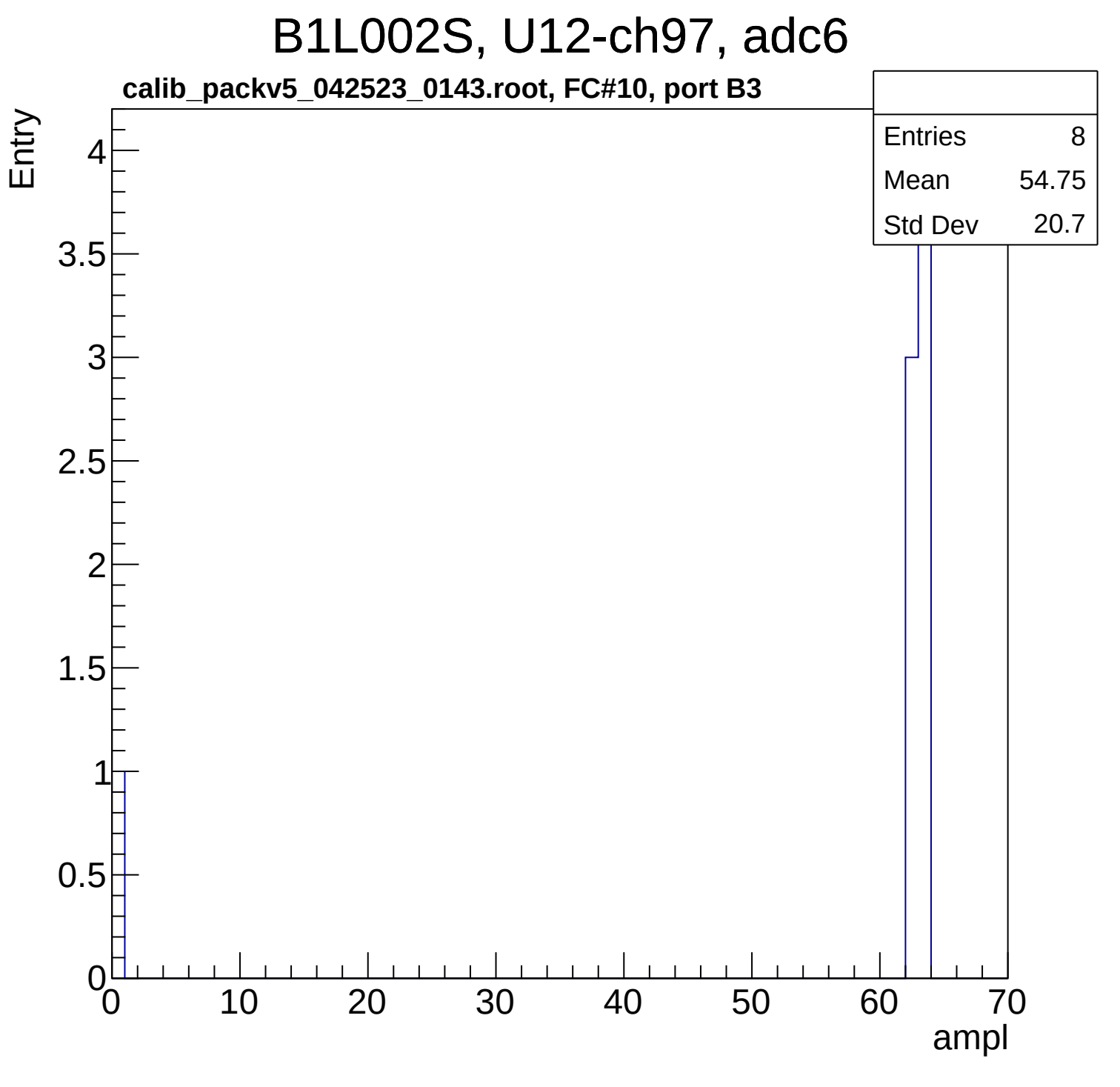
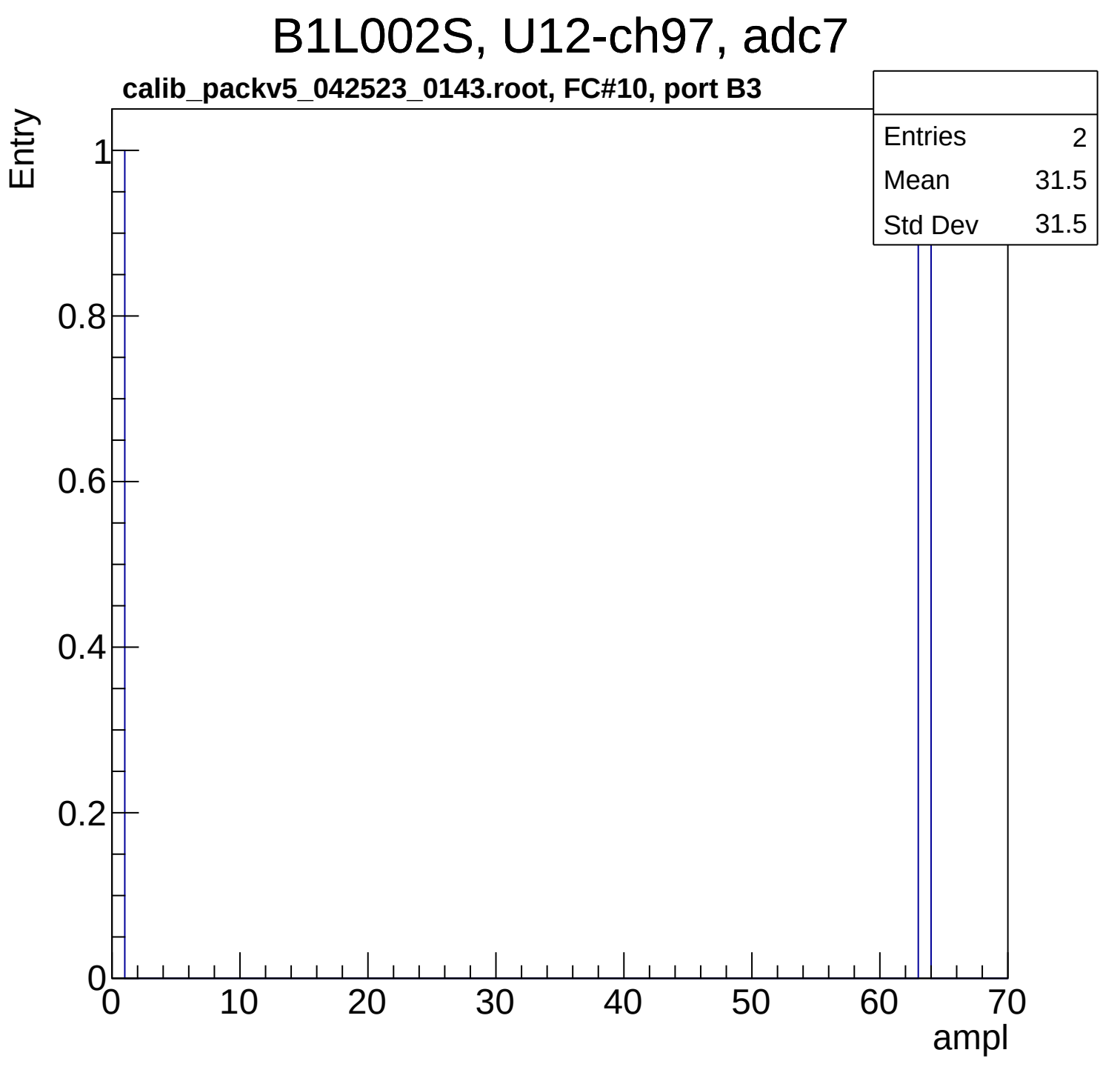

B1L002S, U12-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 54.7
Std Dev 3.216
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 59.25
Std Dev 2.591
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.75
Std Dev 20.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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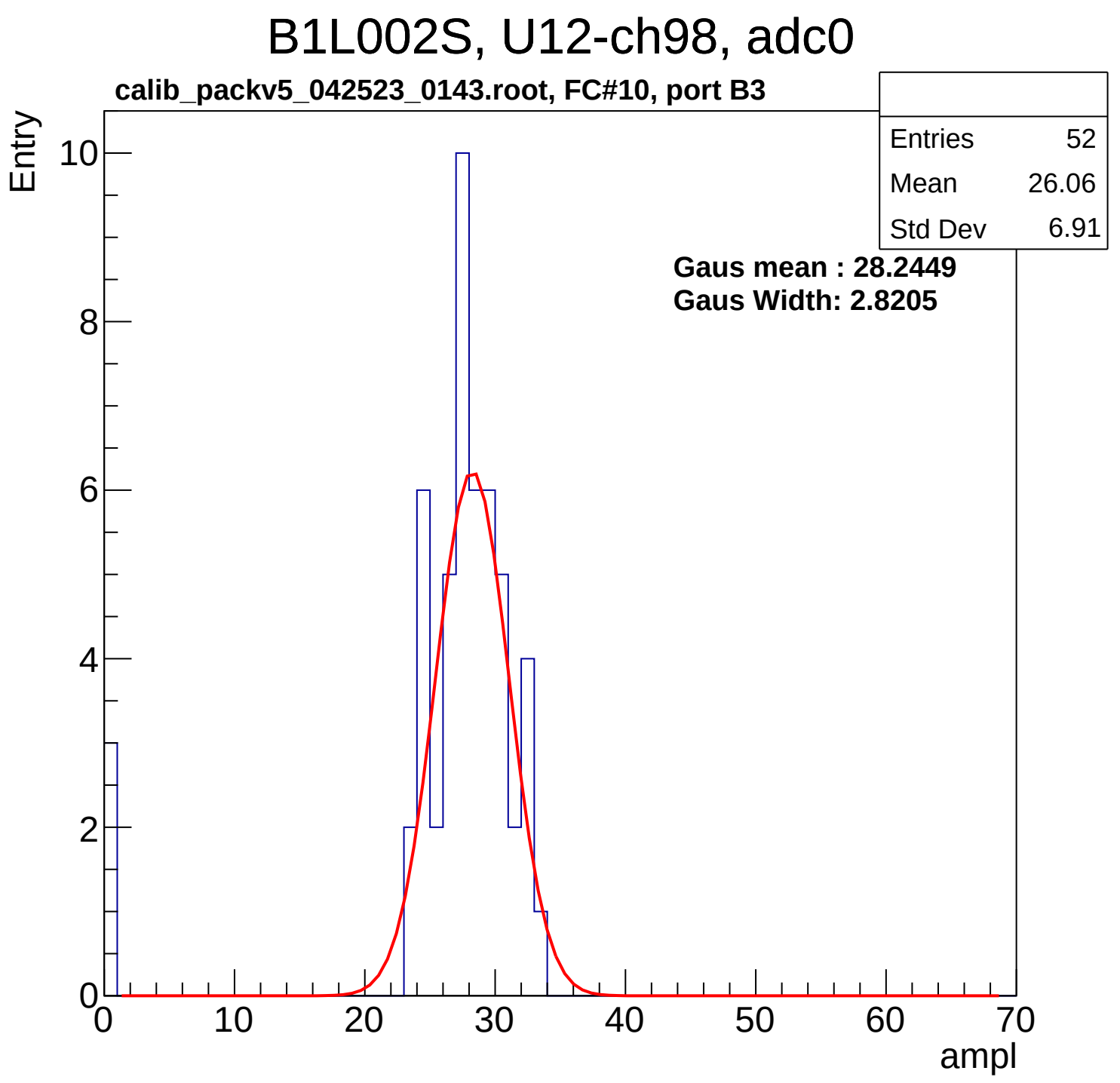
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 26.06
Std Dev 6.91
Gaus mean : 28.2449
Gaus Width: 2.8205
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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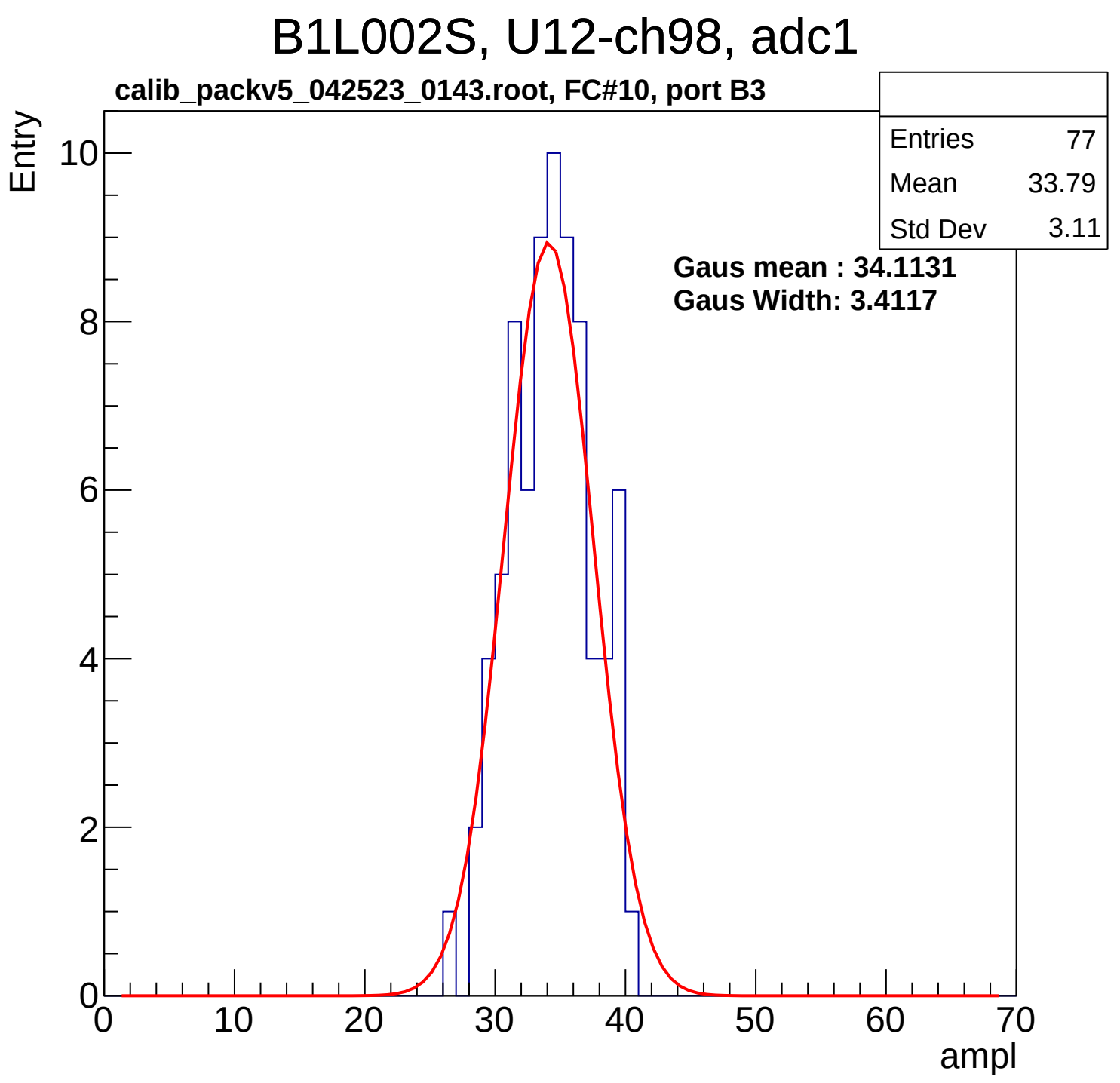
B1L002S, U12-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 33.79
Std Dev 3.11
Gaus mean : 34.1131
Gaus Width: 3.4117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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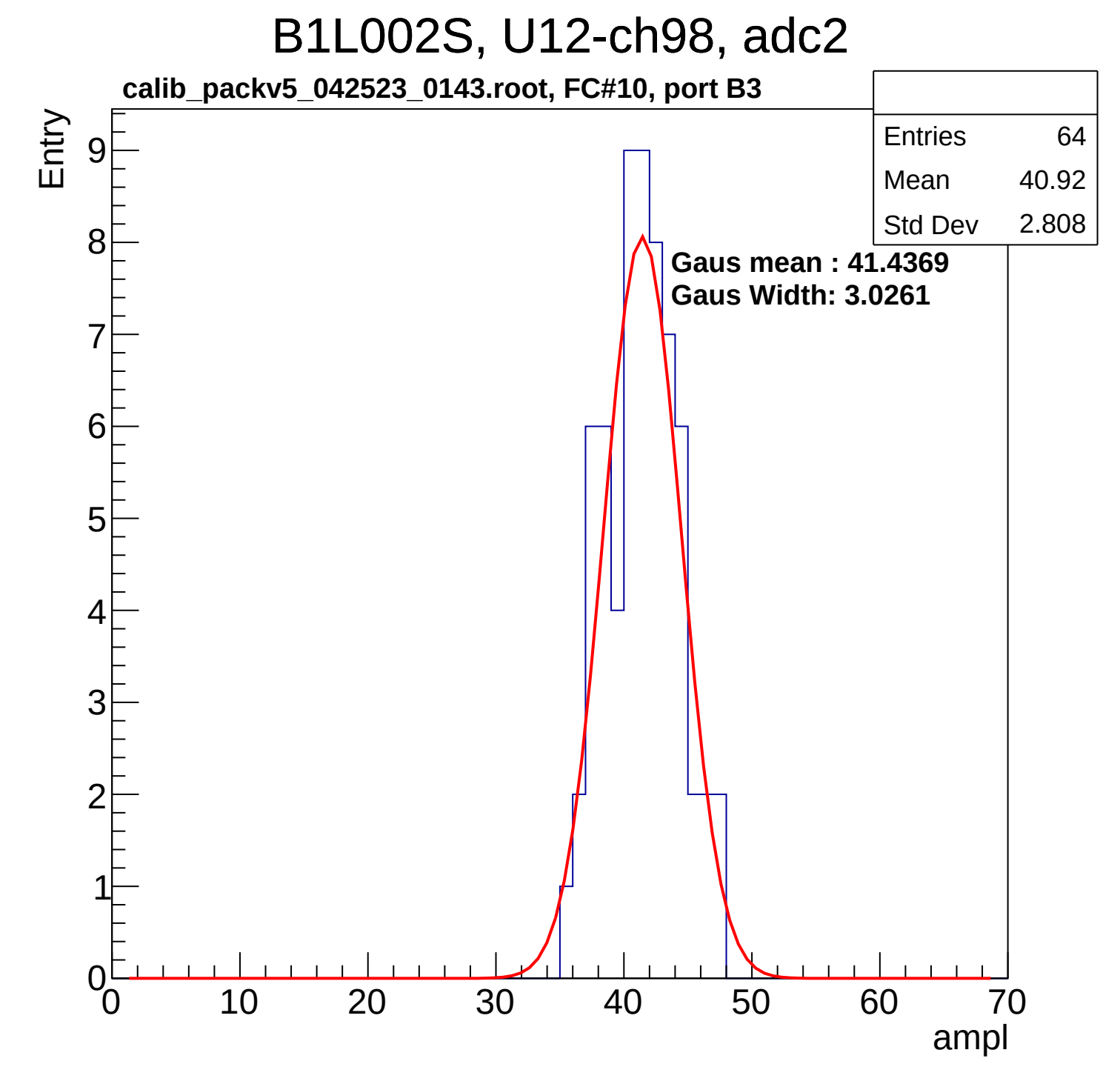
B1L002S, U12-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.92
Std Dev 2.808
Gaus mean : 41.4369
Gaus Width: 3.0261
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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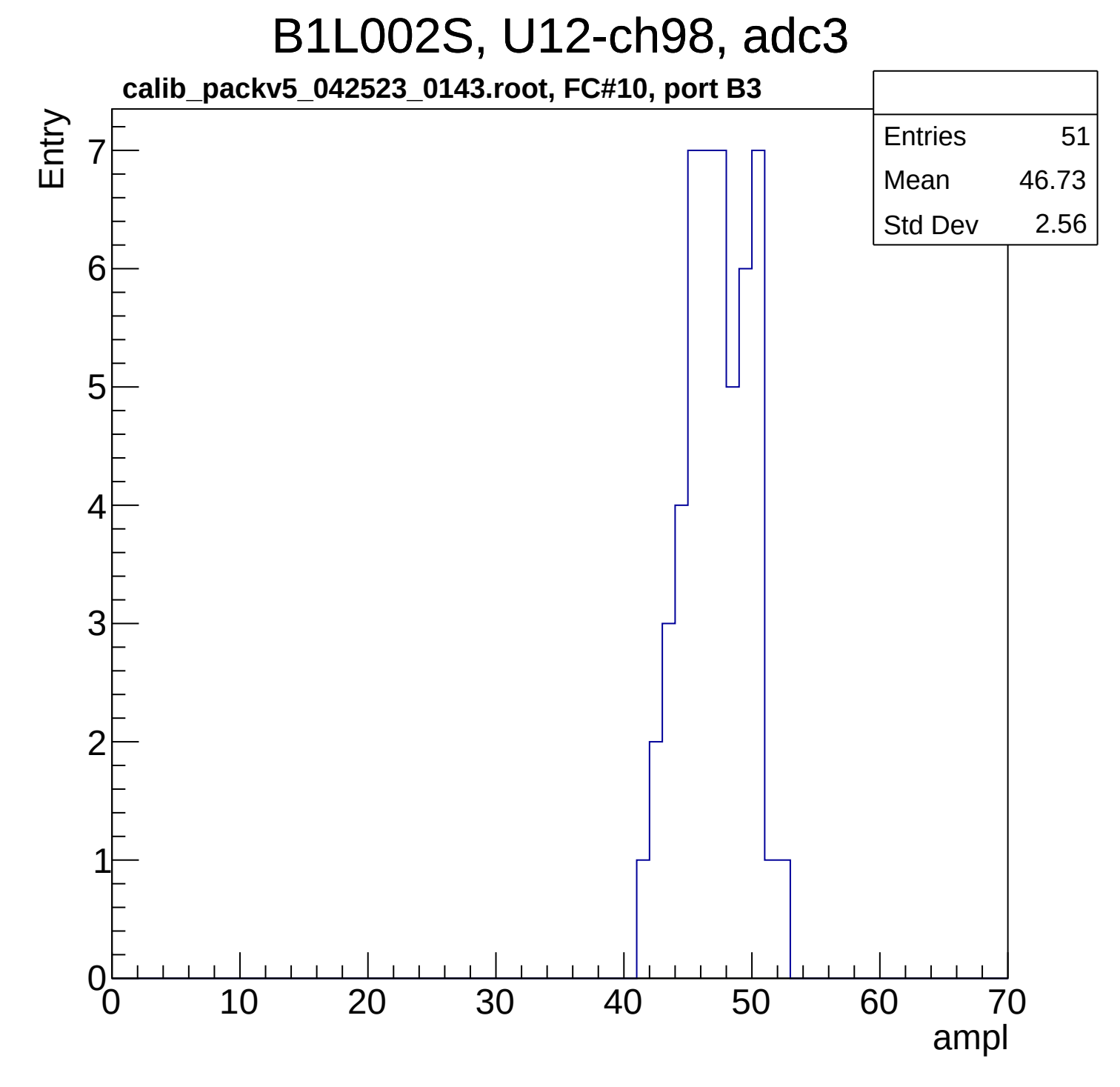
B1L002S, U12-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.73
Std Dev 2.56
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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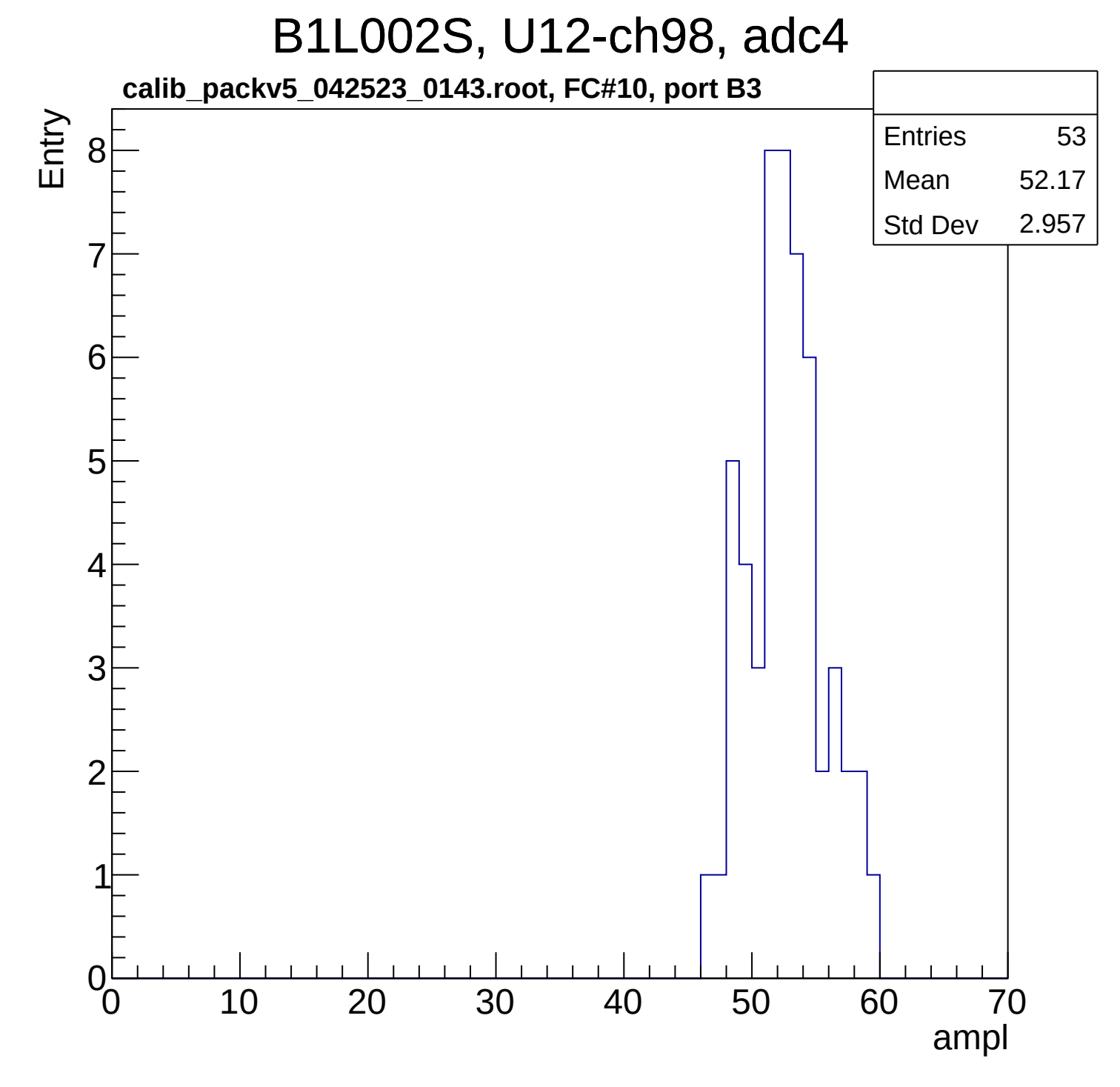
B1L002S, U12-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.17
Std Dev 2.957
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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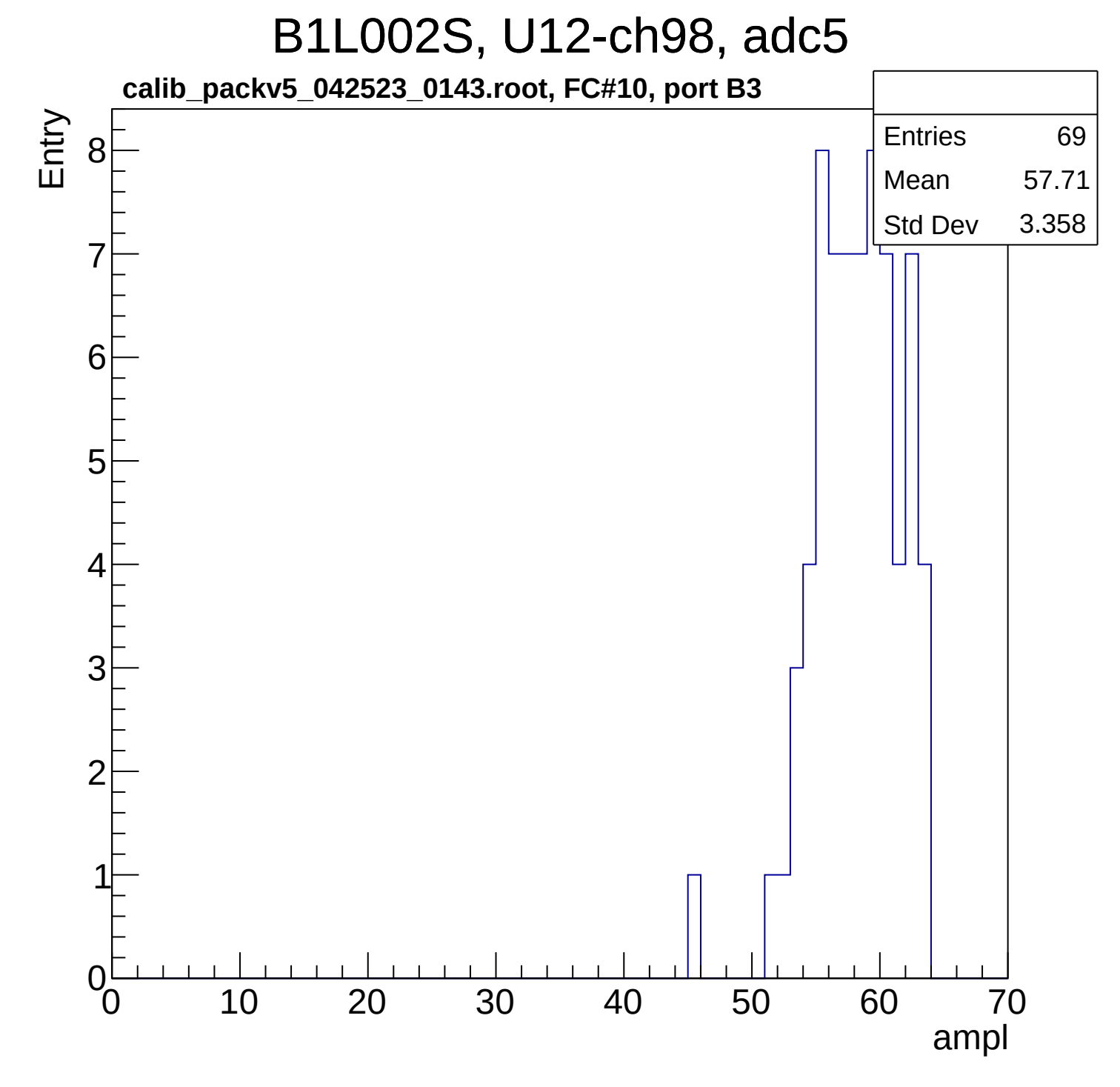
B1L002S, U12-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.71
Std Dev 3.358
Entry
ampl
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2
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4
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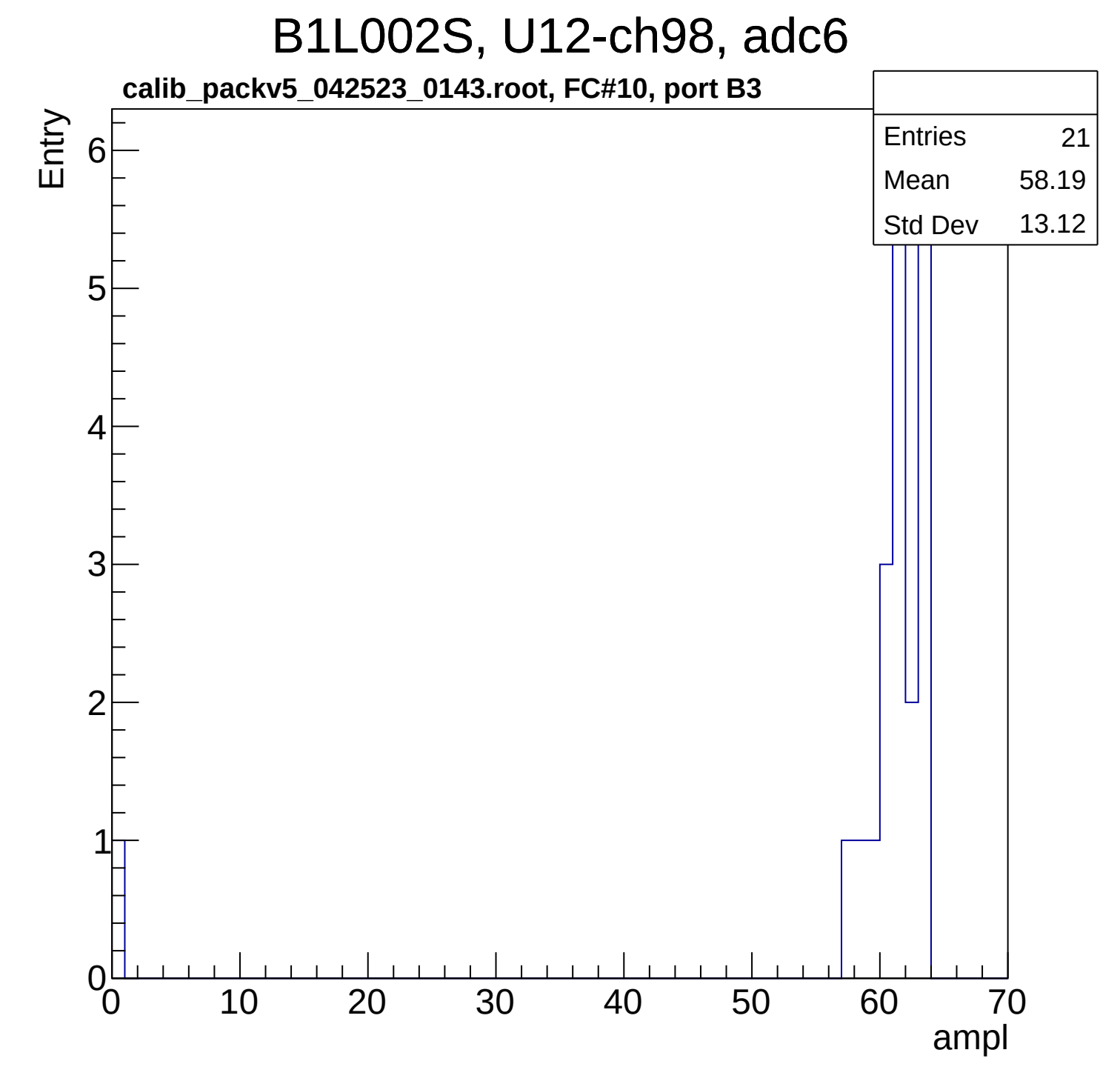
B1L002S, U12-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.19
Std Dev 13.12
Entry
0
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2
3
4
5
6
0
10
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30
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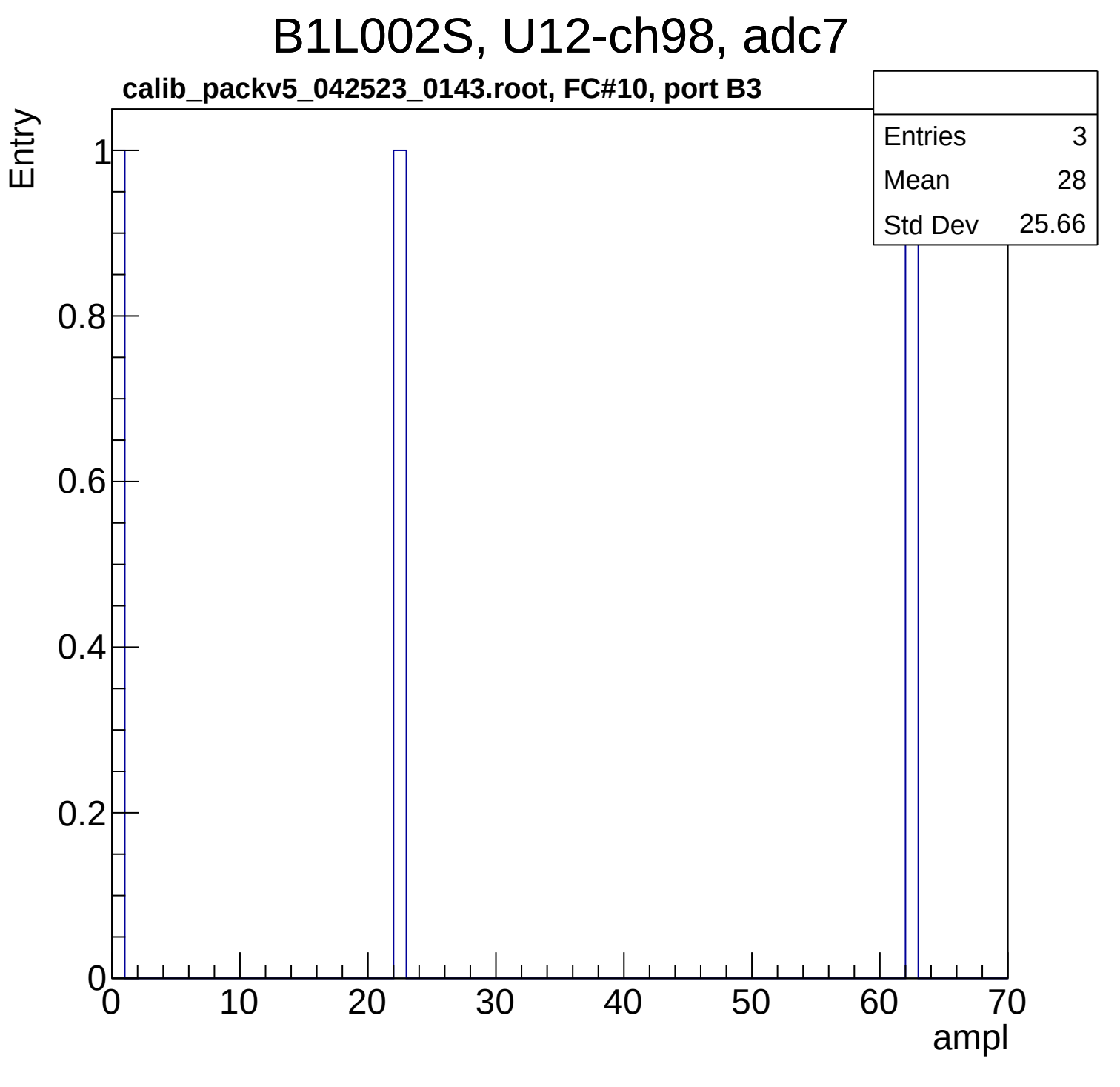
50
60
70
ampl


B1L002S, U12-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
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0.6
0.4
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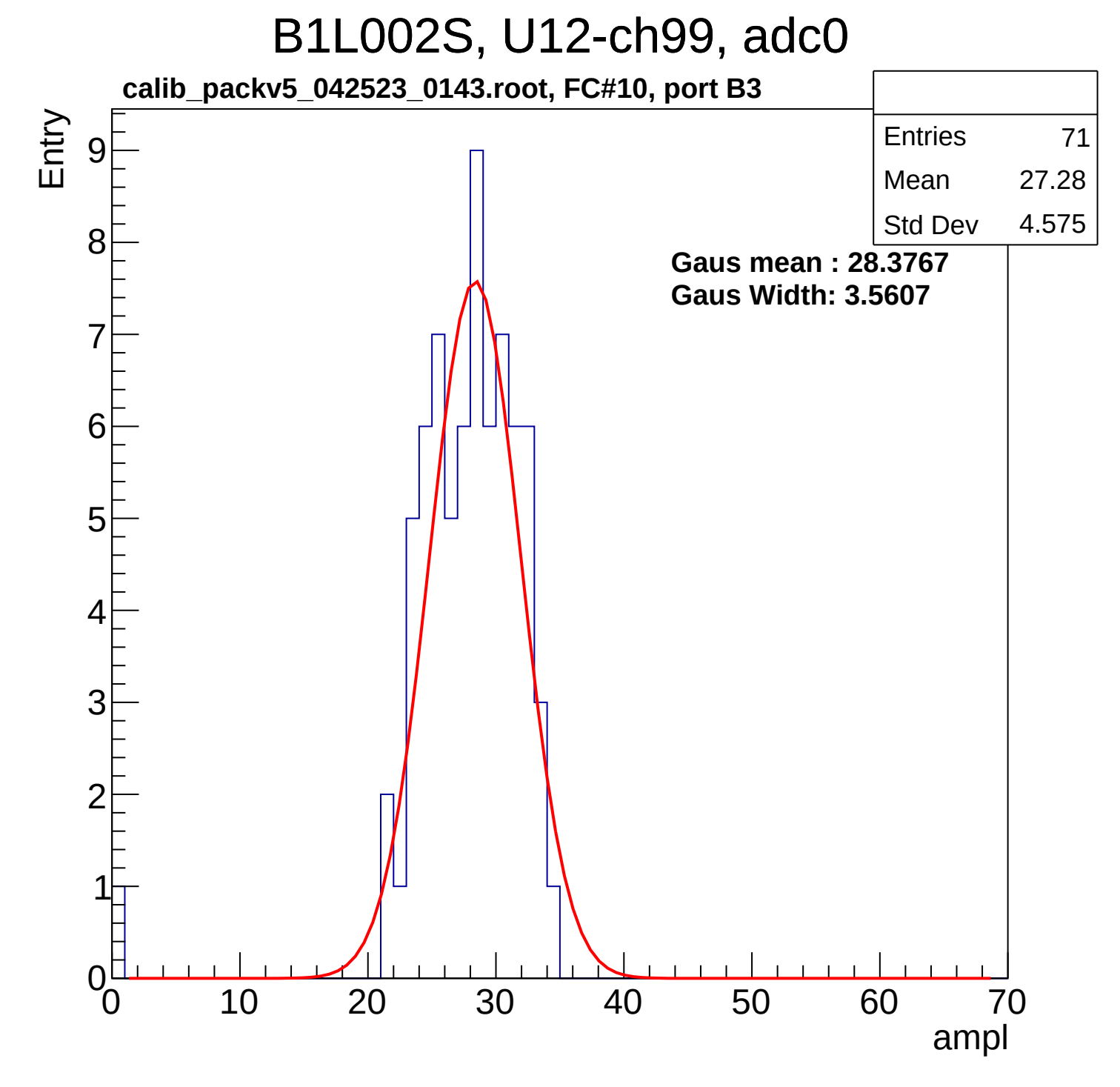
60
70
ampl


B1L002S, U12-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.28
Std Dev 4.575
Gaus mean : 28.3767
Gaus Width: 3.5607
Entry
ampl
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3
4
5
6
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8
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0
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60
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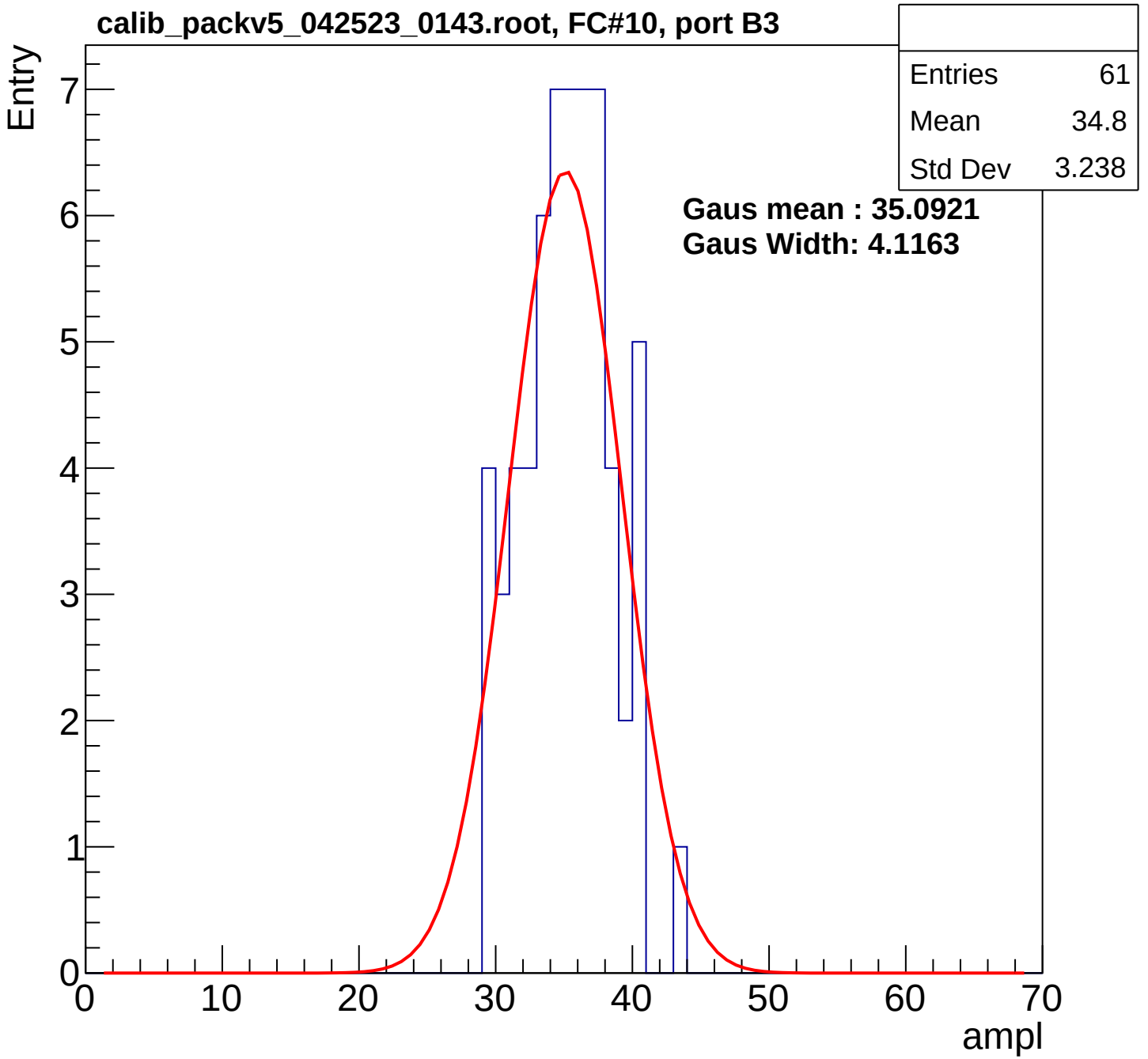
B1L002S, U12-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.8
Std Dev 3.238
Gaus mean : 35.0921
Gaus Width: 4.1163
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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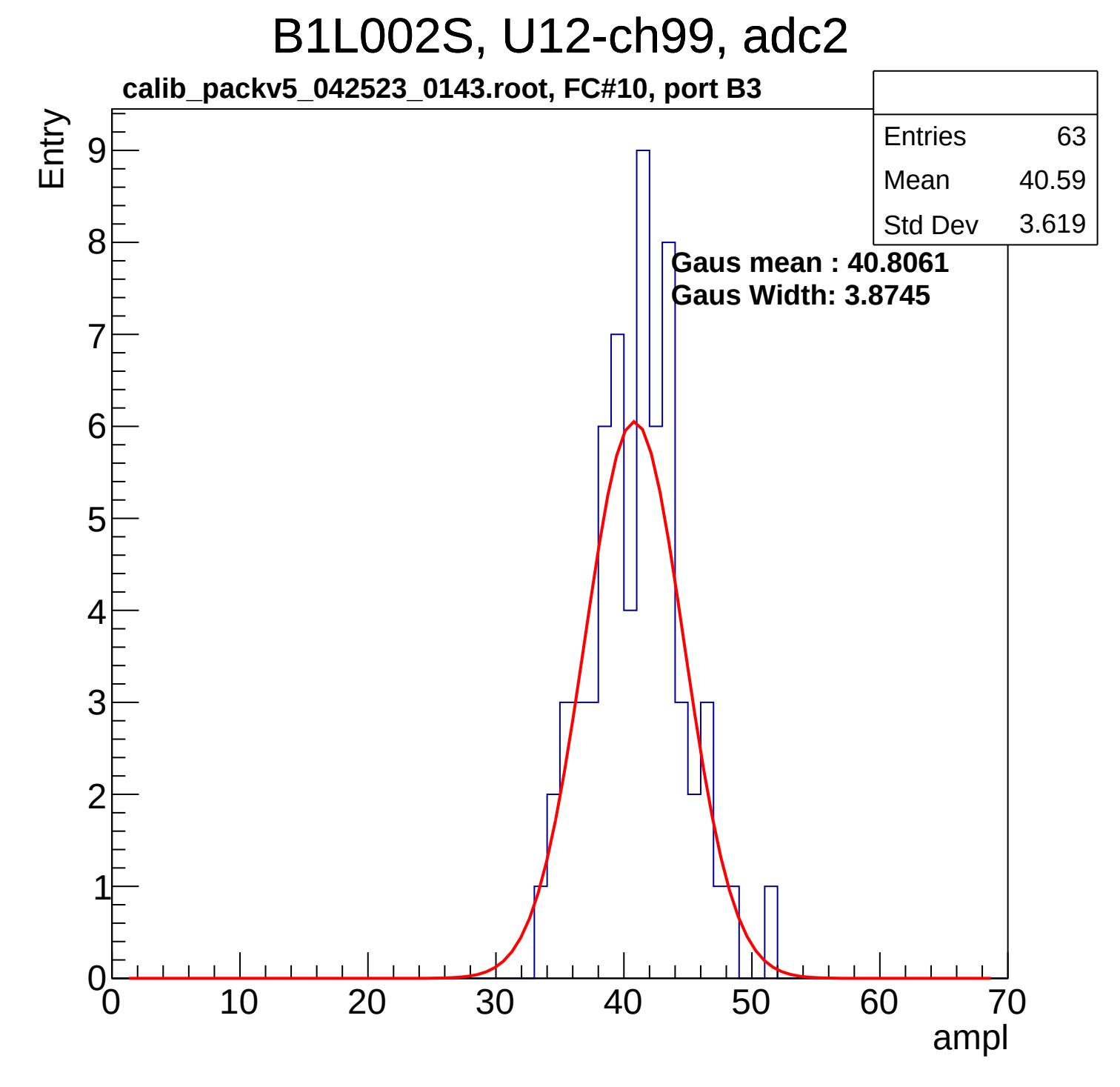
B1L002S, U12-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.59
Std Dev 3.619
Gaus mean : 40.8061
Gaus Width: 3.8745
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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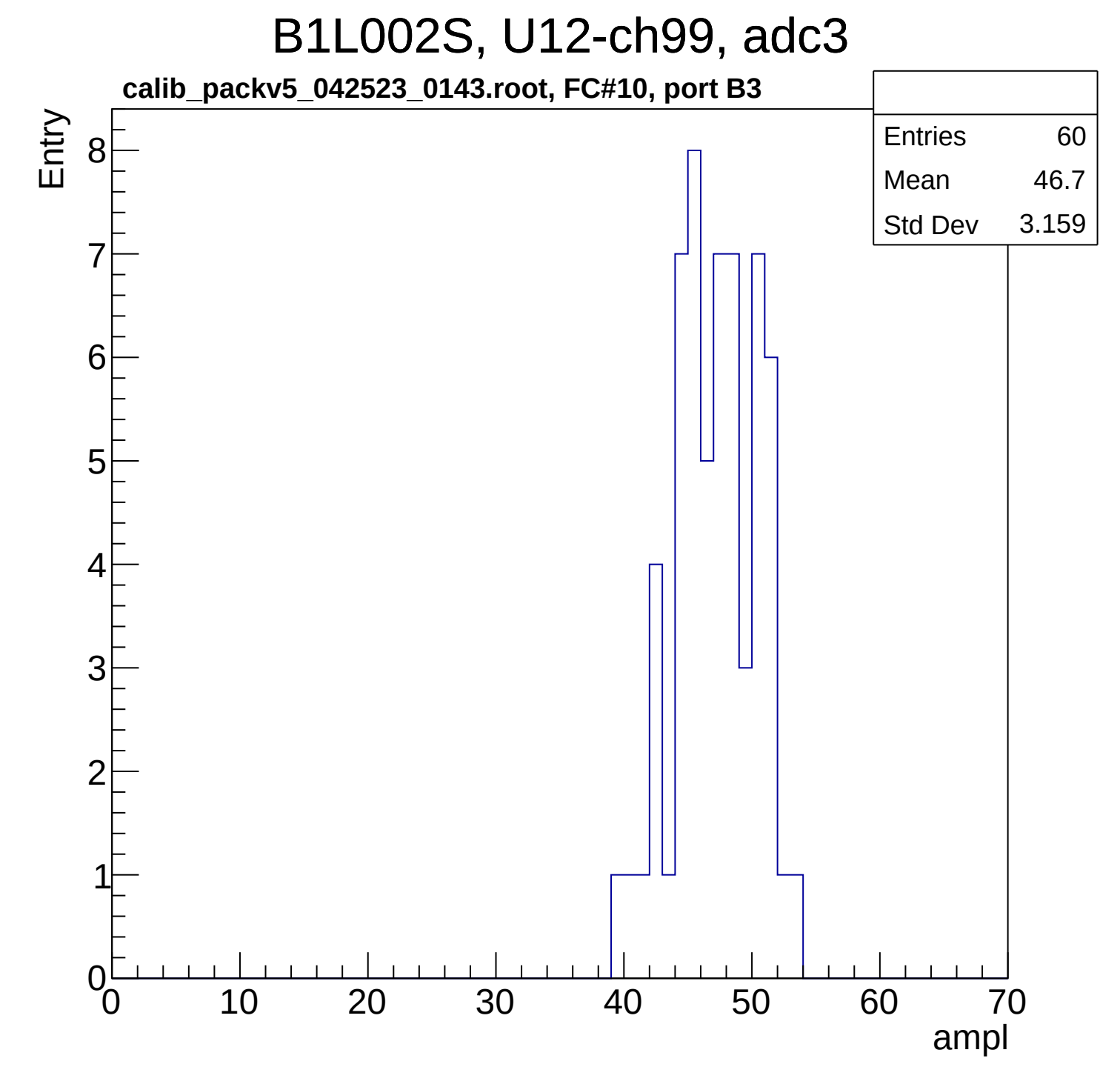
B1L002S, U12-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.7
Std Dev 3.159
Entry
ampl
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1
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3
4
5
6
7
8
0
10
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40
50
60
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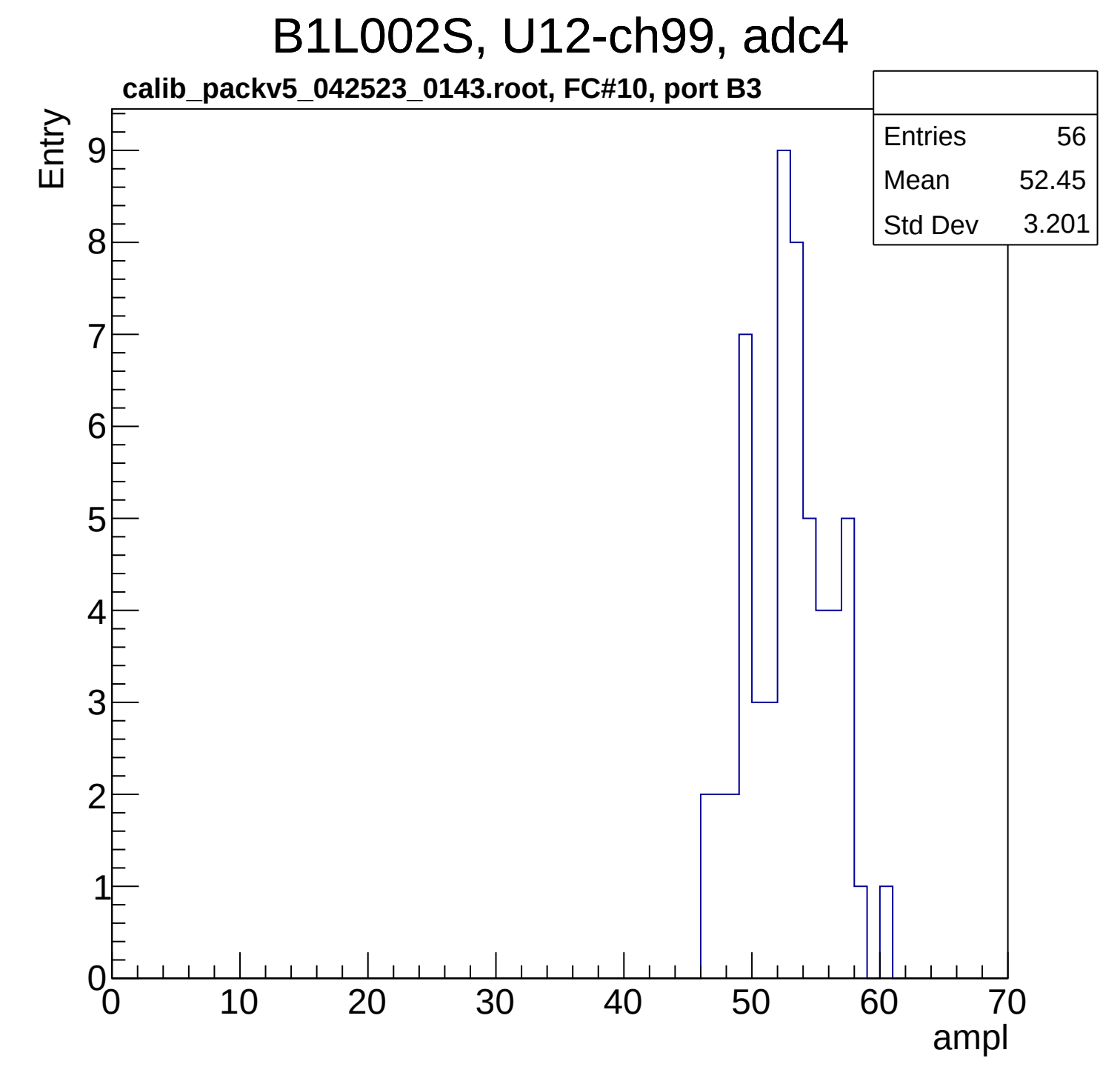
B1L002S, U12-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.45
Std Dev 3.201
Entry
ampl
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4
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0
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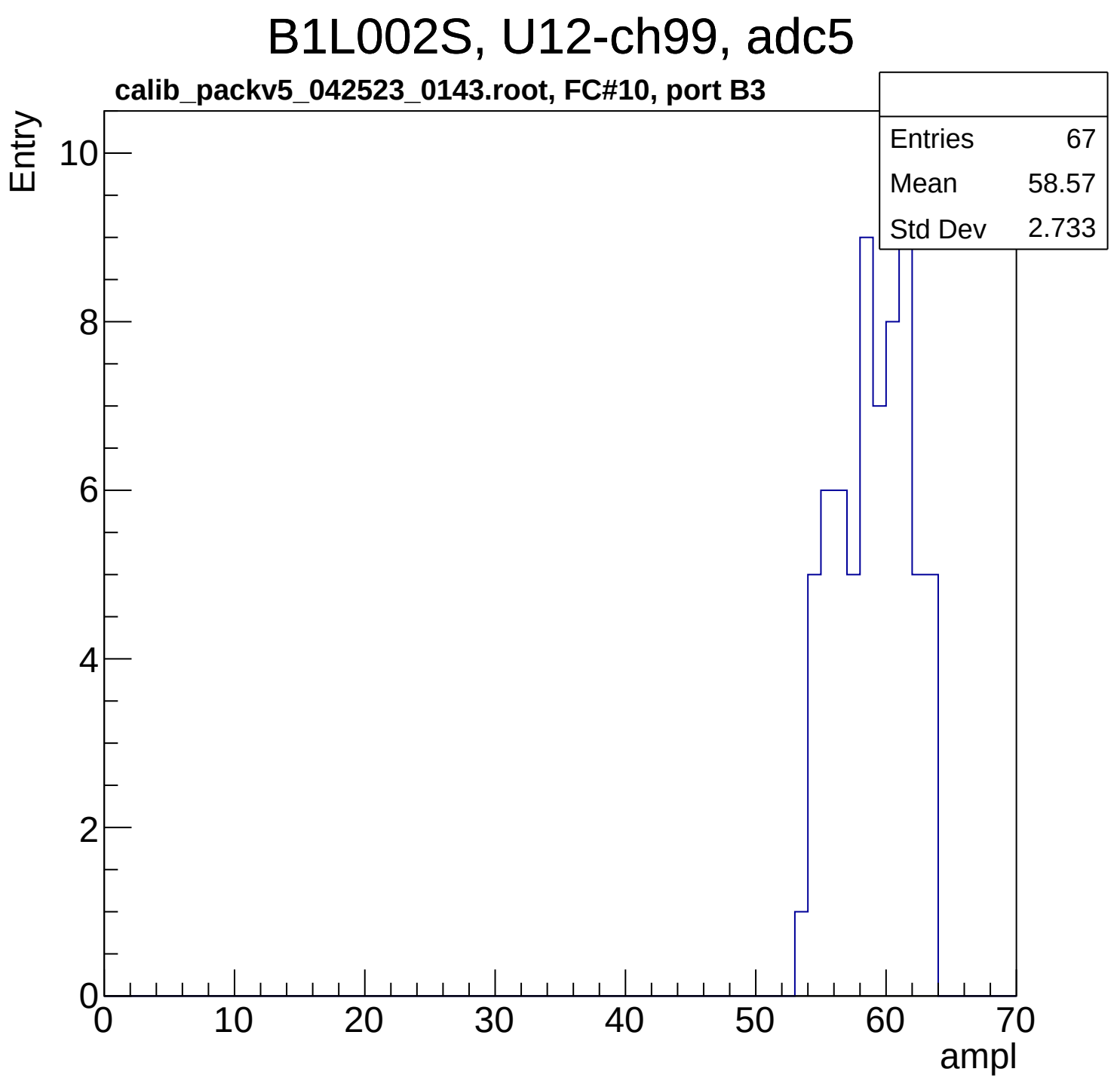
B1L002S, U12-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.57
Std Dev 2.733
Entry
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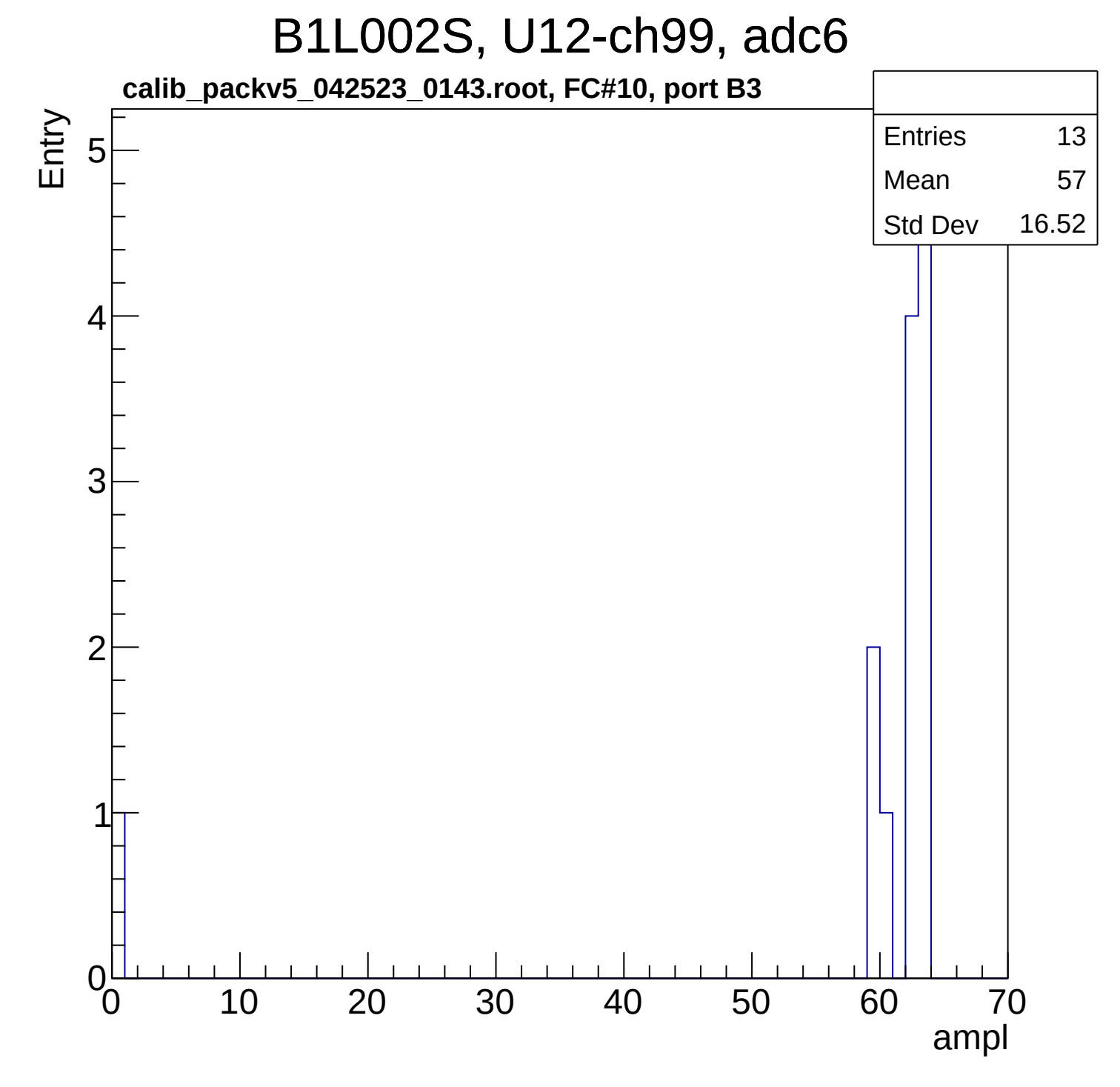
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 57
Std Dev 16.52
Entry
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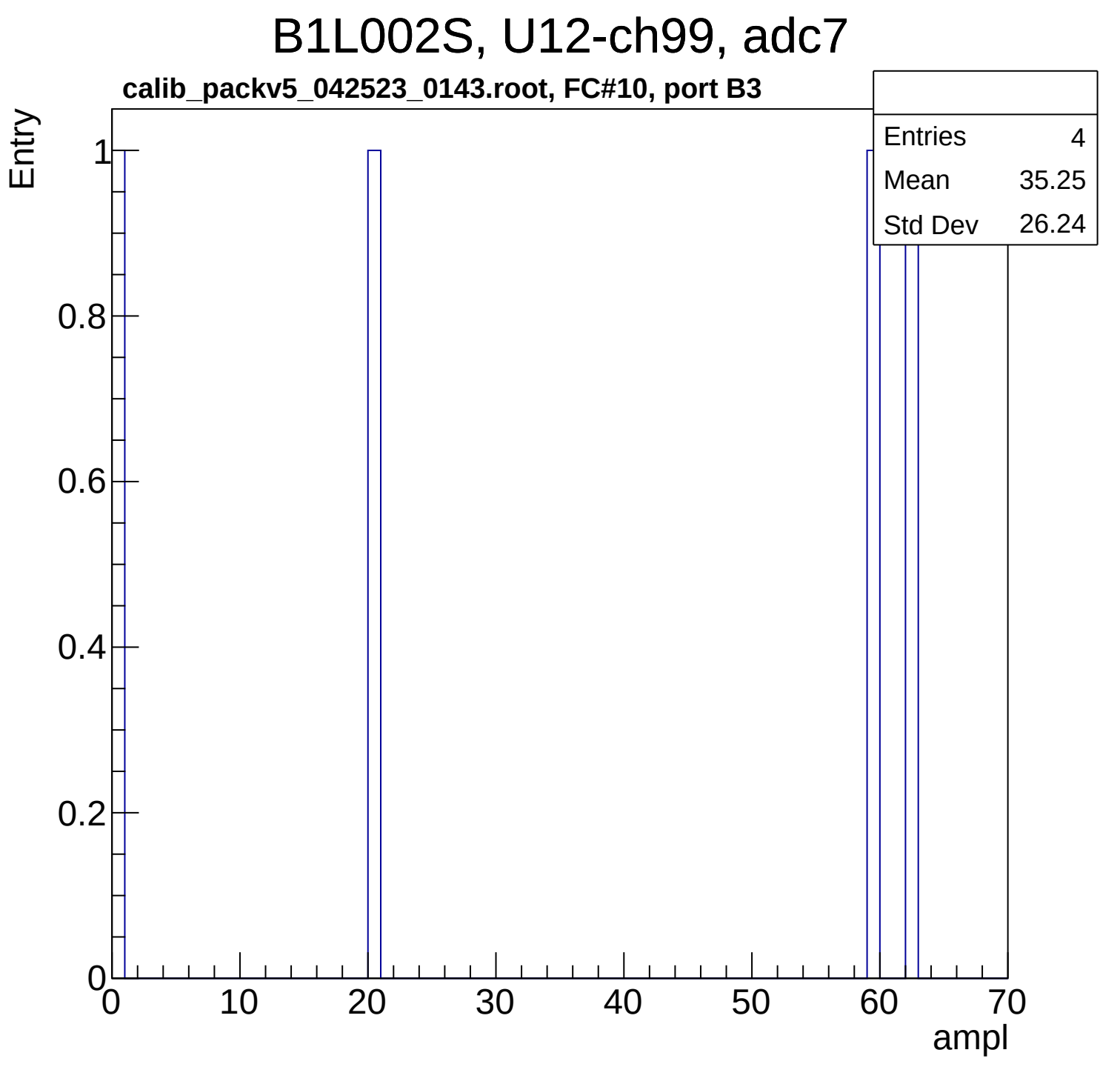
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 35.25
Std Dev 26.24
Entry
ampl
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50
60
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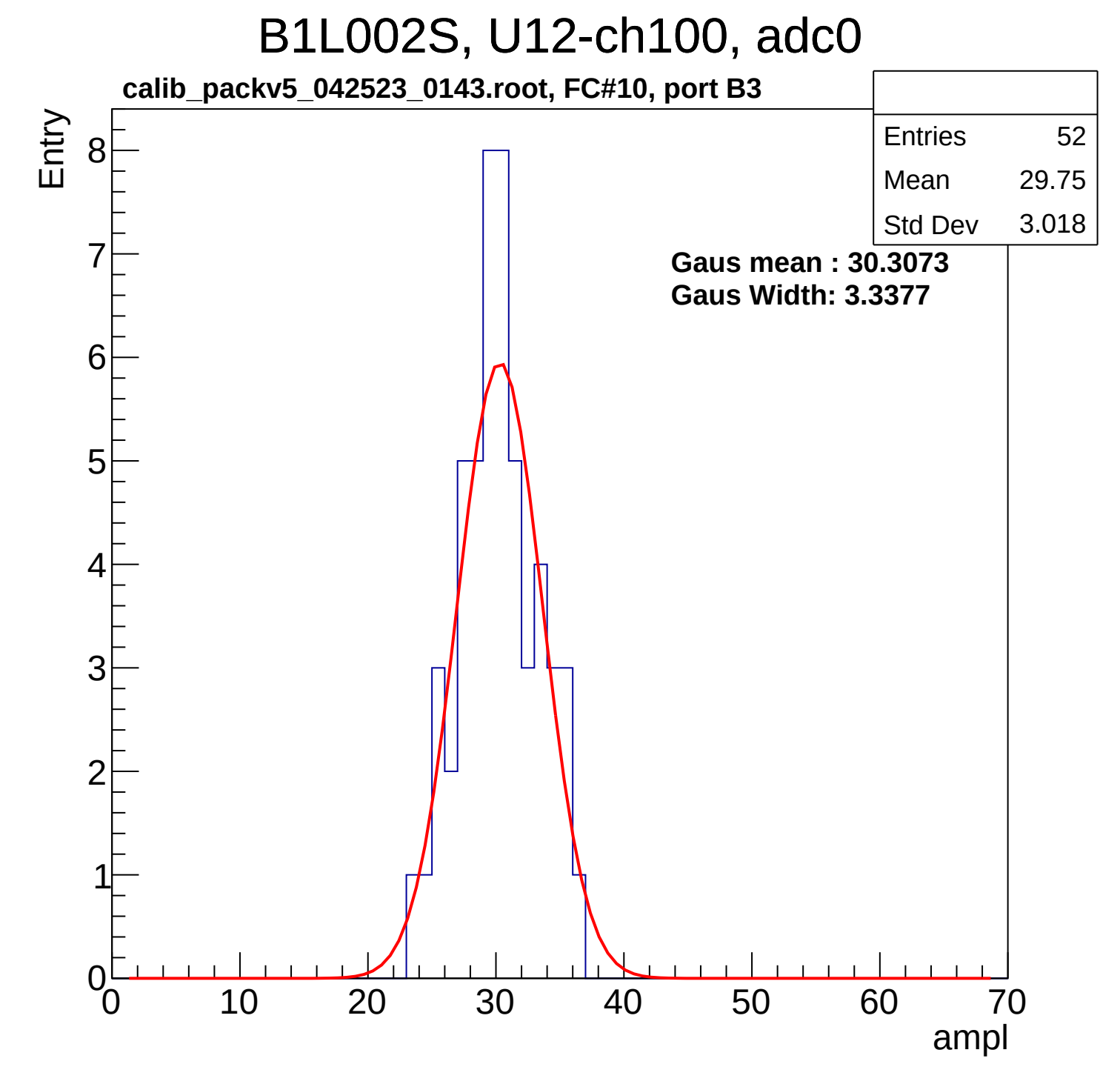
B1L002S, U12-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 29.75
Std Dev 3.018
Gaus mean : 30.3073
Gaus Width: 3.3377
Entry
ampl
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4
5
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0
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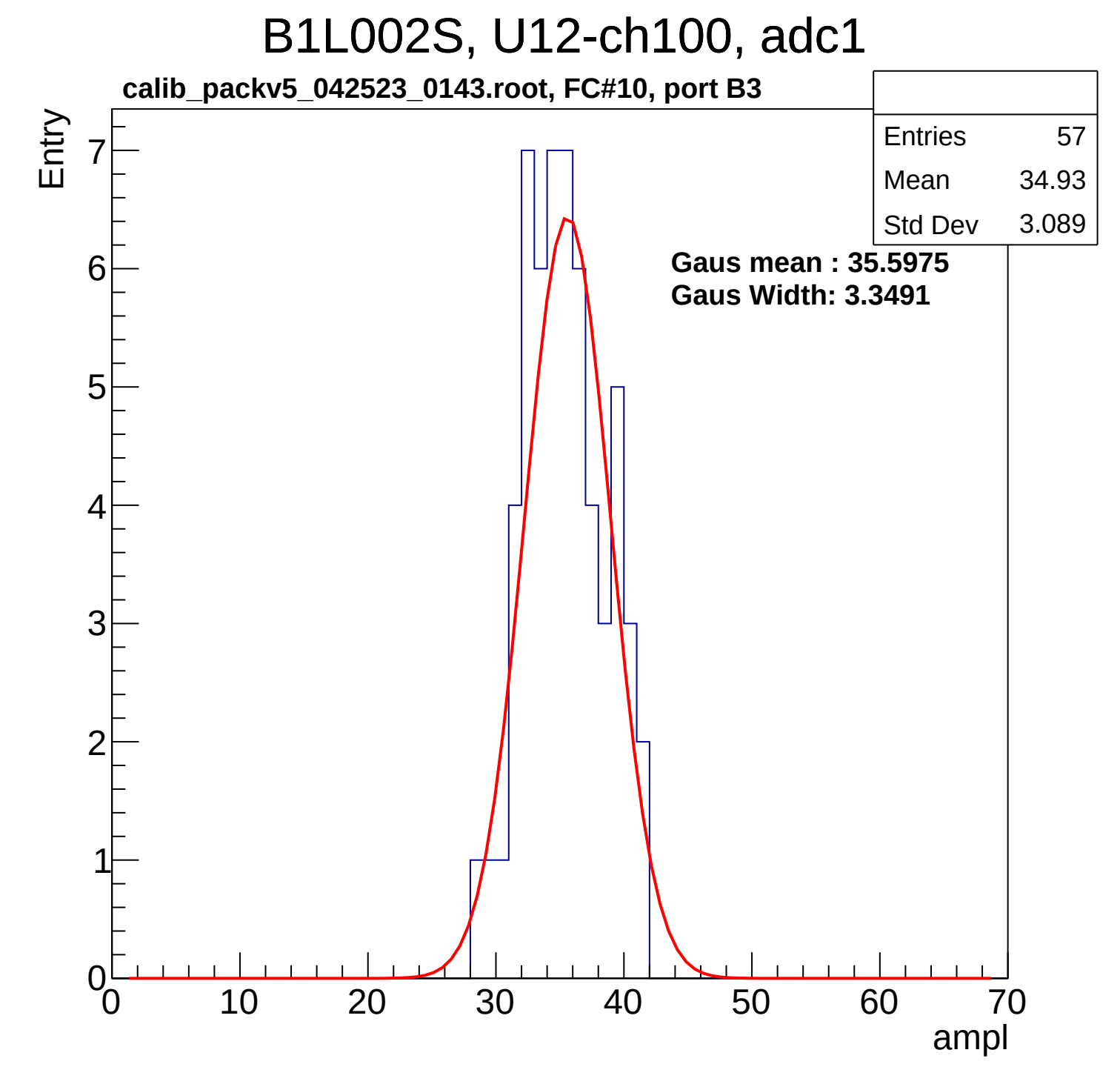
B1L002S, U12-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.93
Std Dev 3.089
Gaus mean : 35.5975
Gaus Width: 3.3491
Entry
ampl
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3
4
5
6
7
0
10
20
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40
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60
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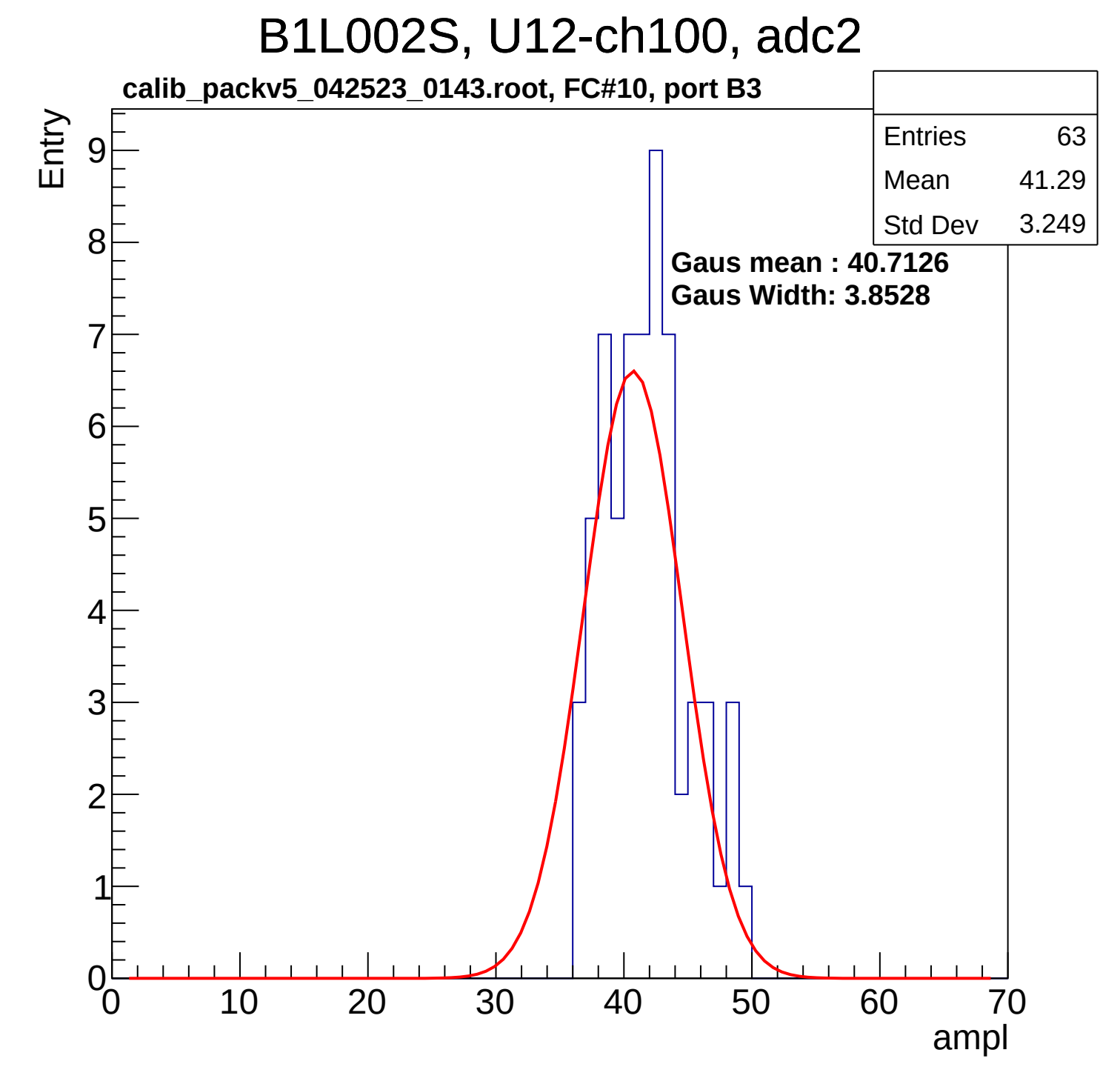
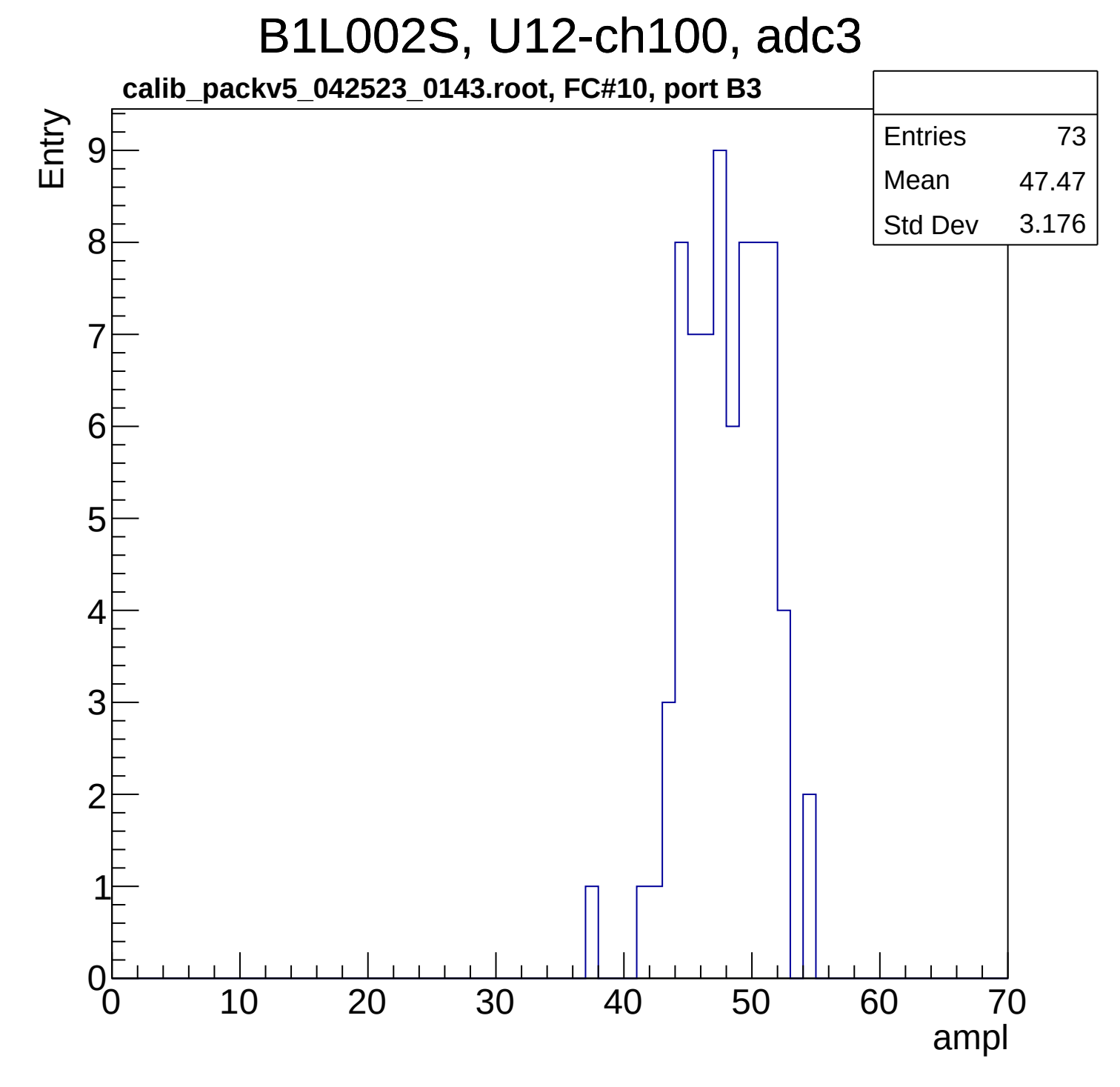

B1L002S, U12-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.29
Std Dev 3.249
Gaus mean : 40.7126
Gaus Width: 3.8528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 47.47
Std Dev 3.176
Entry
ampl
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2
3
4
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6
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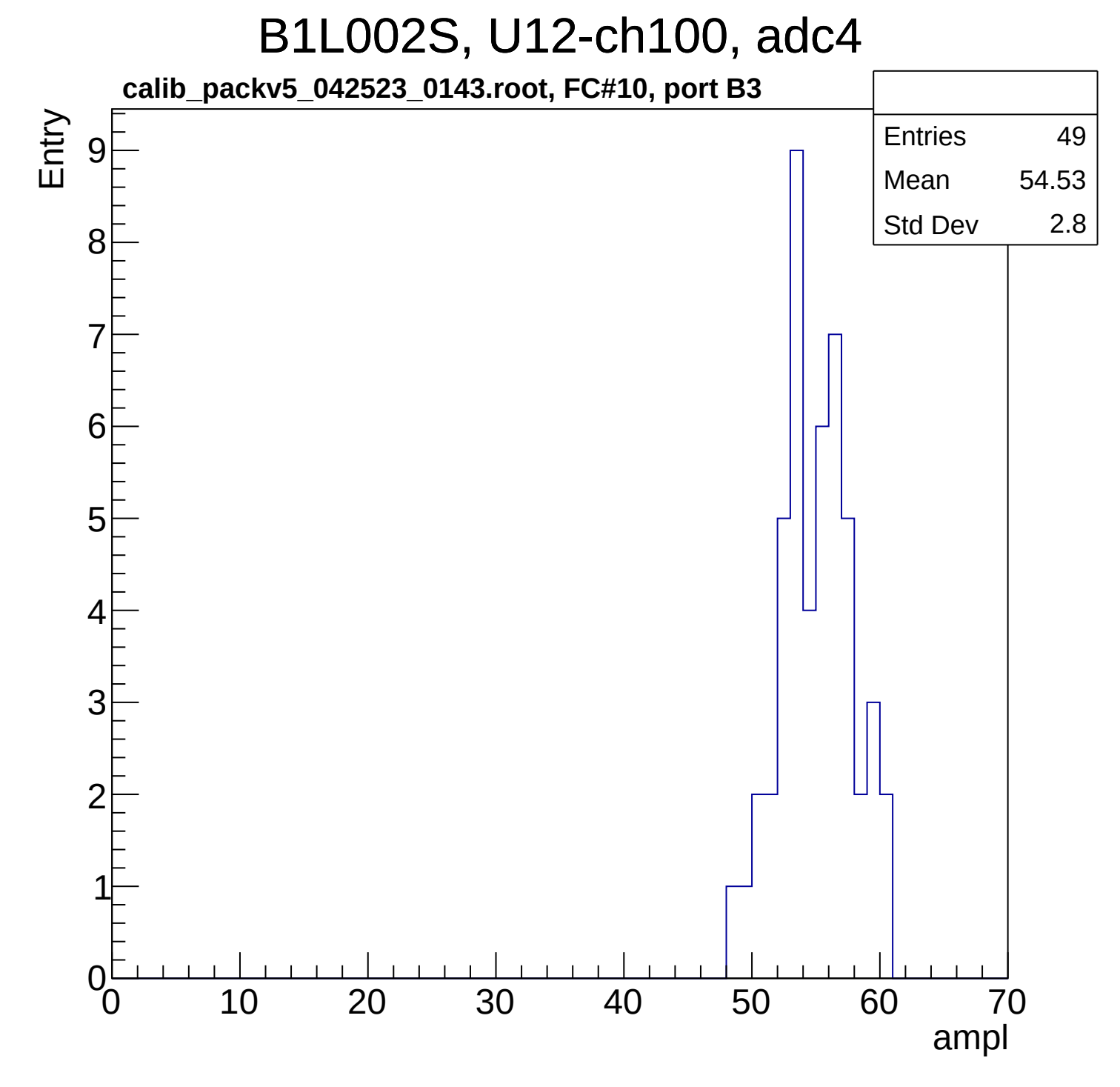
B1L002S, U12-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 54.53
Std Dev 2.8
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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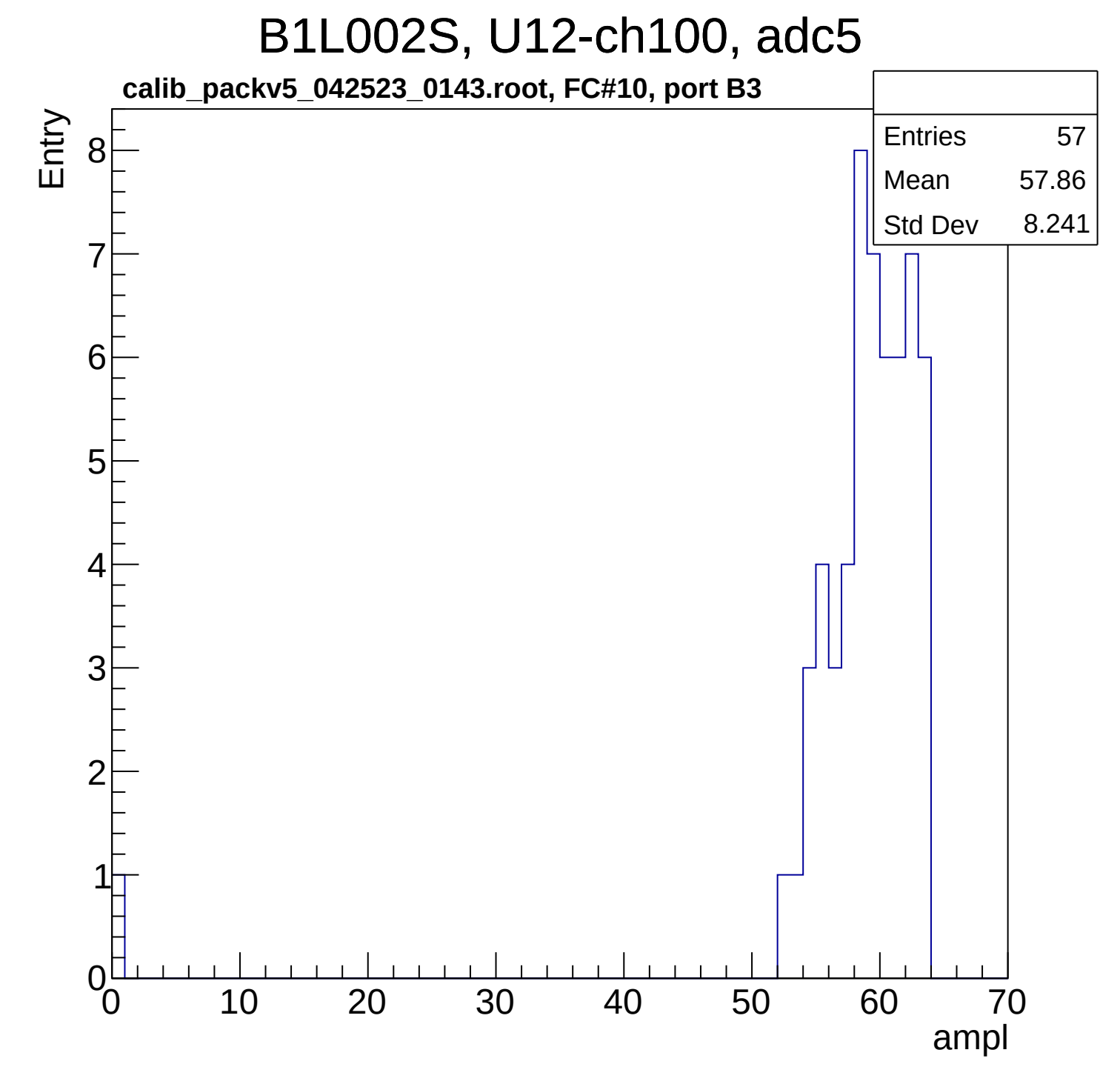
B1L002S, U12-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.86
Std Dev 8.241
Entry
ampl
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2
3
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5
6
7
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0
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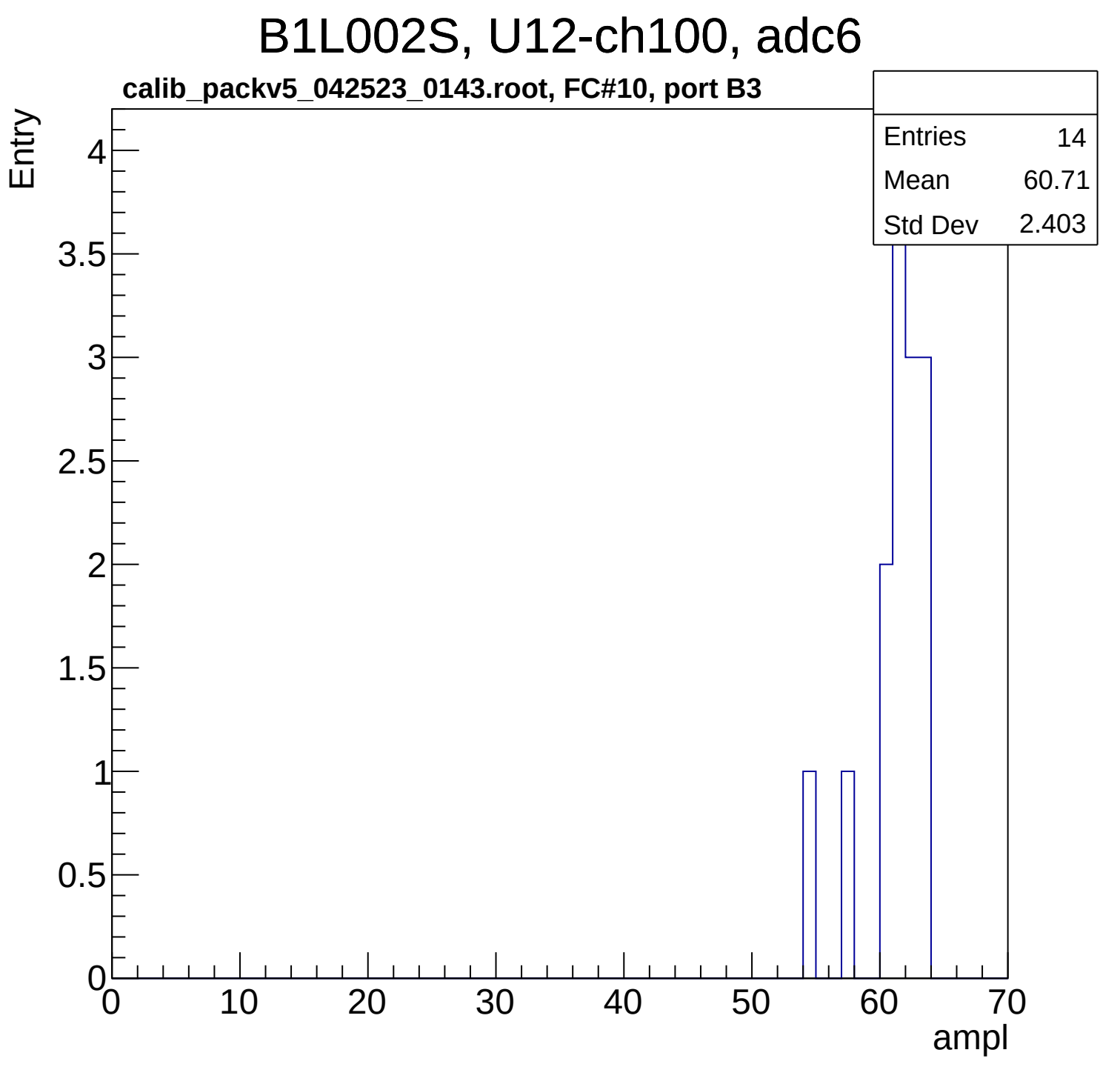
B1L002S, U12-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 60.71
Std Dev 2.403
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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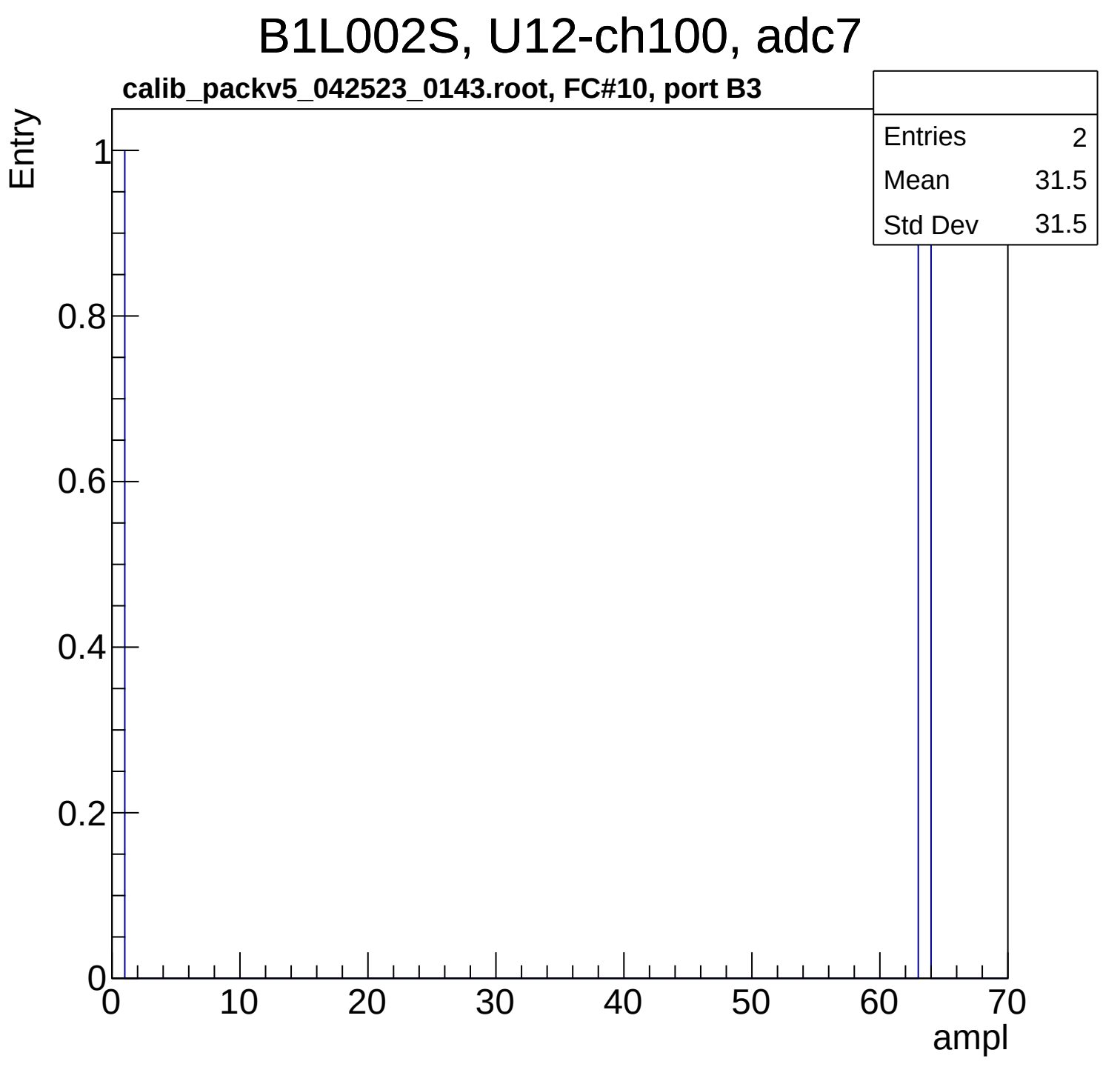
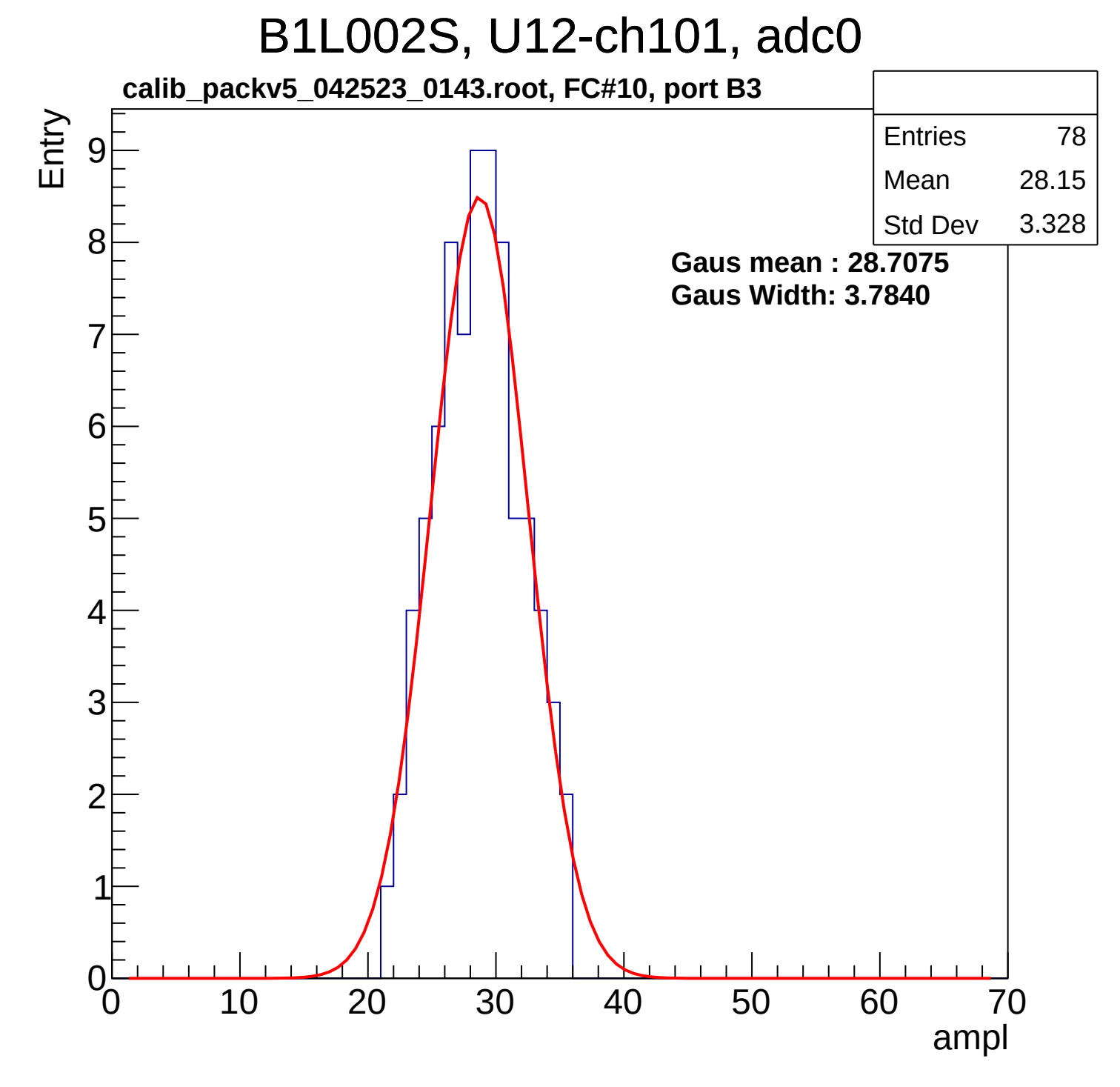

B1L002S, U12-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.15
Std Dev 3.328
Gaus mean : 28.7075
Gaus Width: 3.7840
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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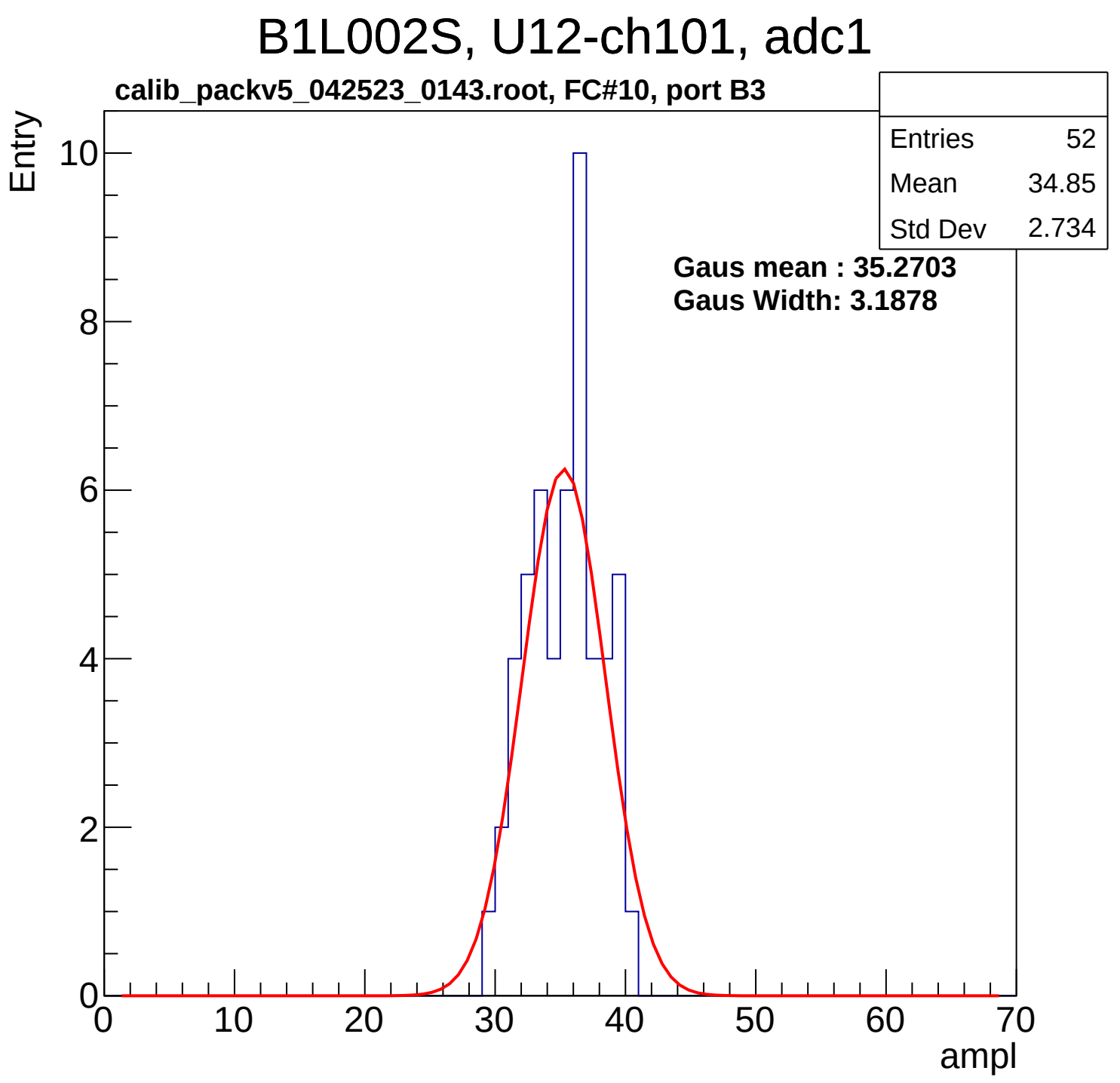
B1L002S, U12-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 34.85
Std Dev 2.734
Gaus mean : 35.2703
Gaus Width: 3.1878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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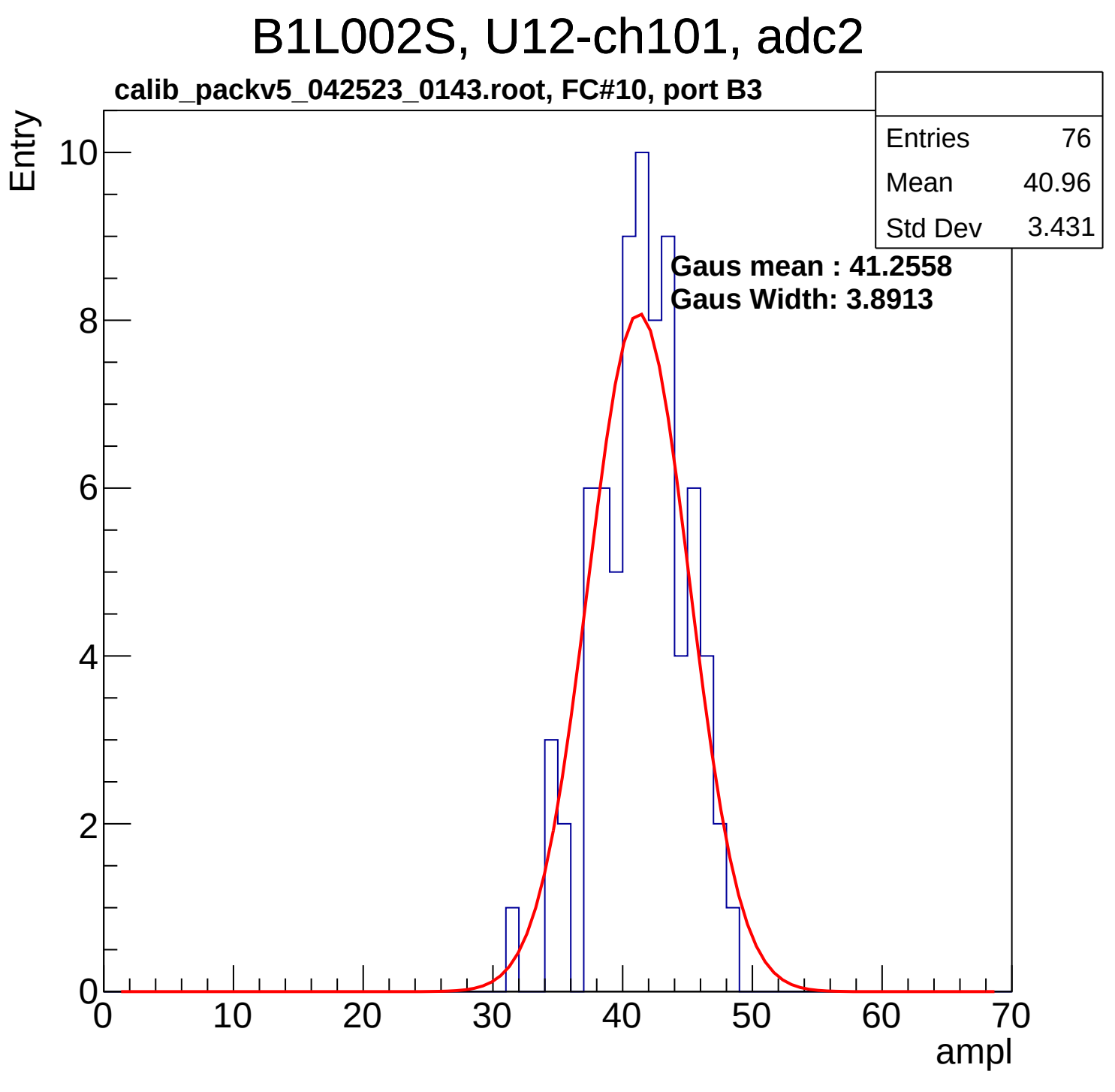
B1L002S, U12-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 40.96
Std Dev 3.431
Gaus mean : 41.2558
Gaus Width: 3.8913
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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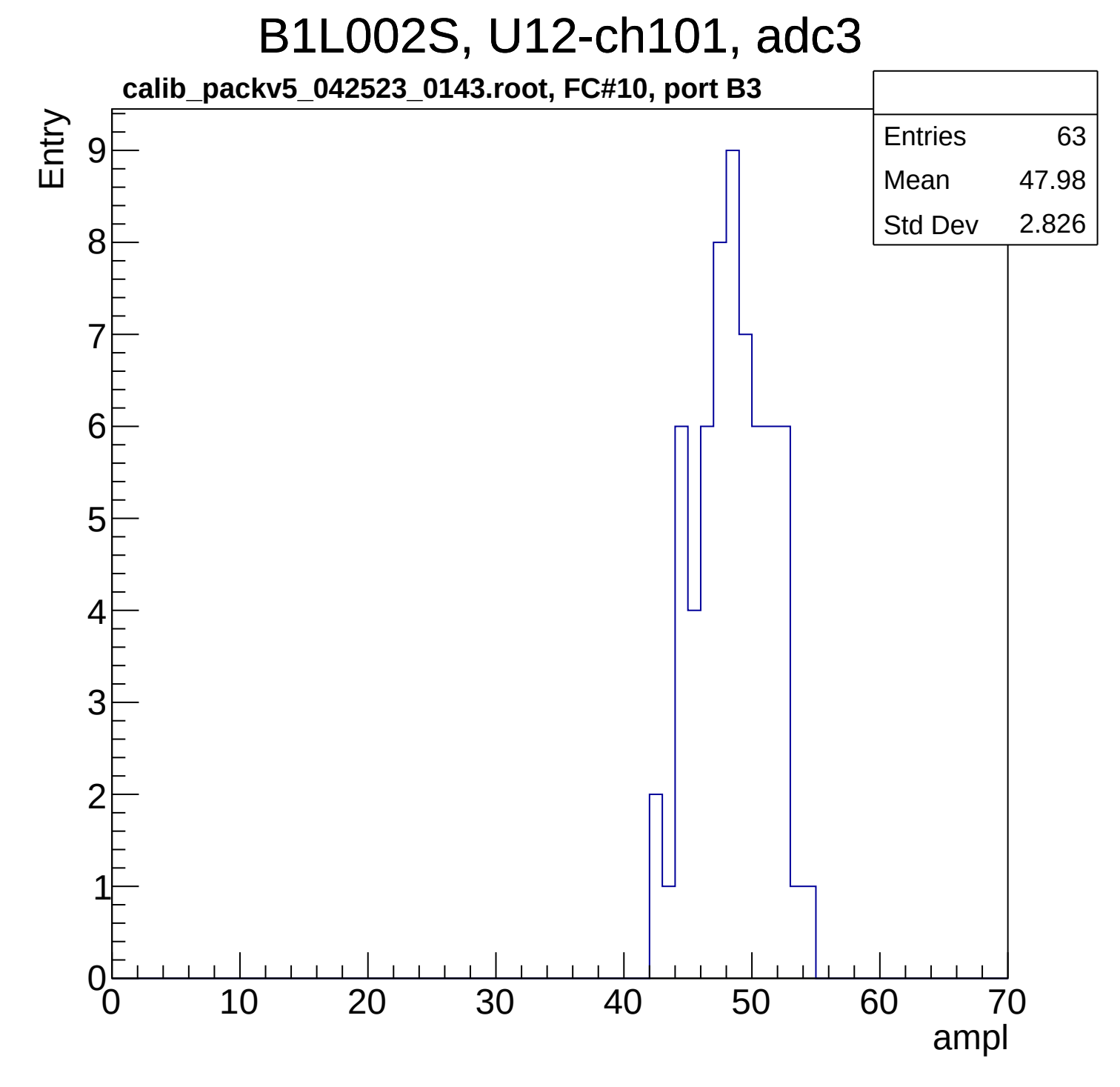
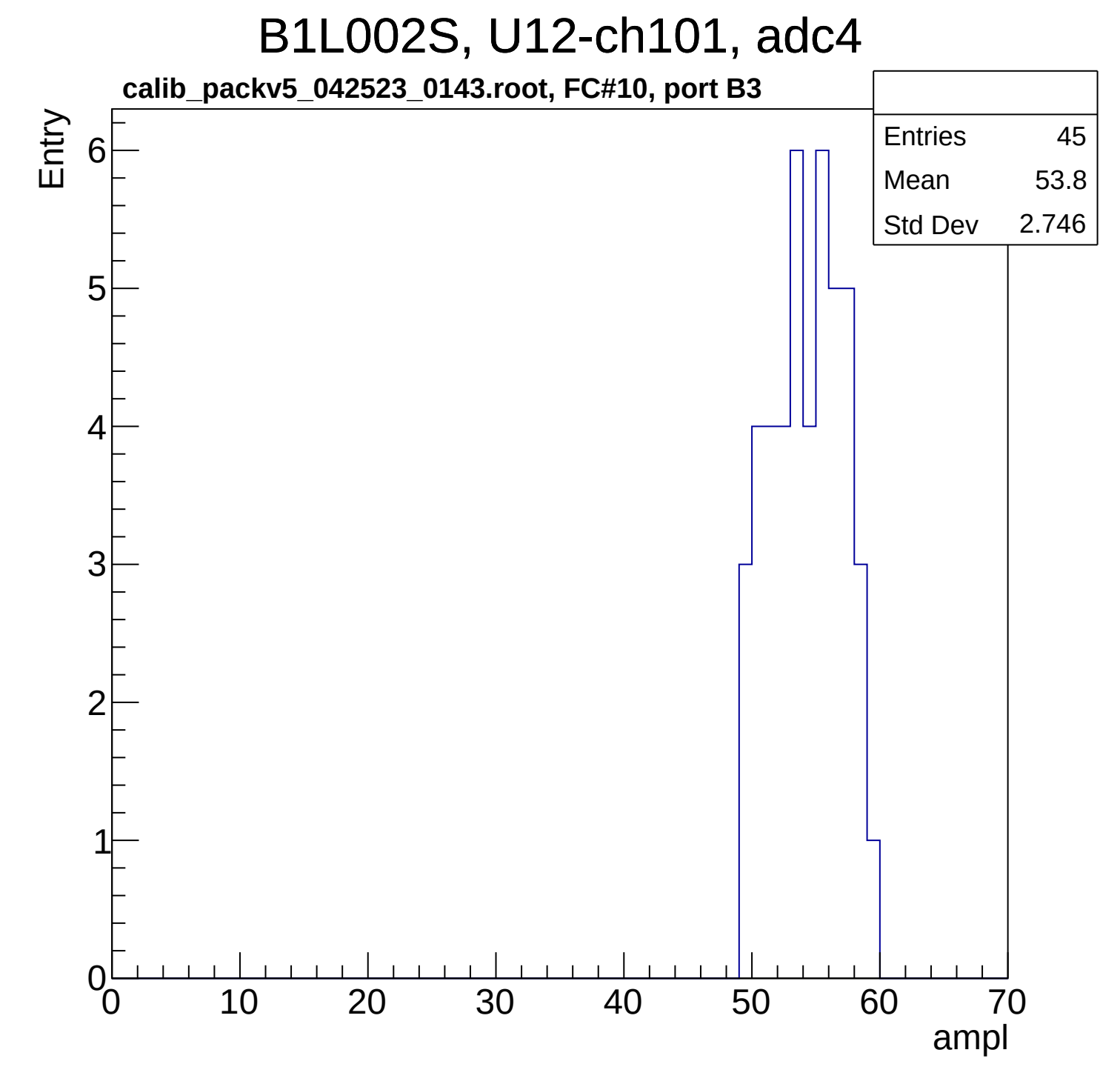
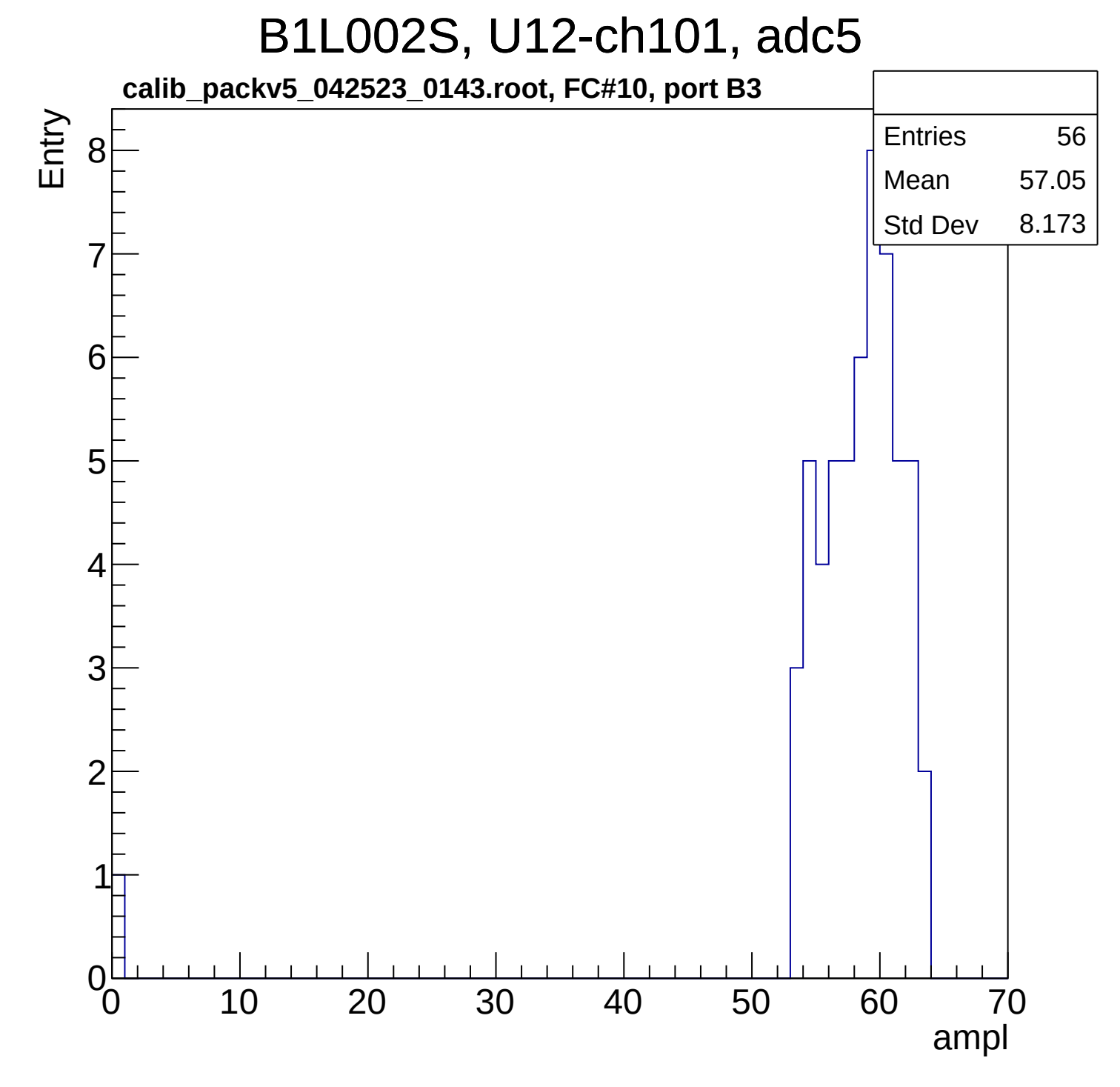

B1L002S, U12-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.98
Std Dev 2.826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53.8
Std Dev 2.746
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.05
Std Dev 8.173
Entry
ampl
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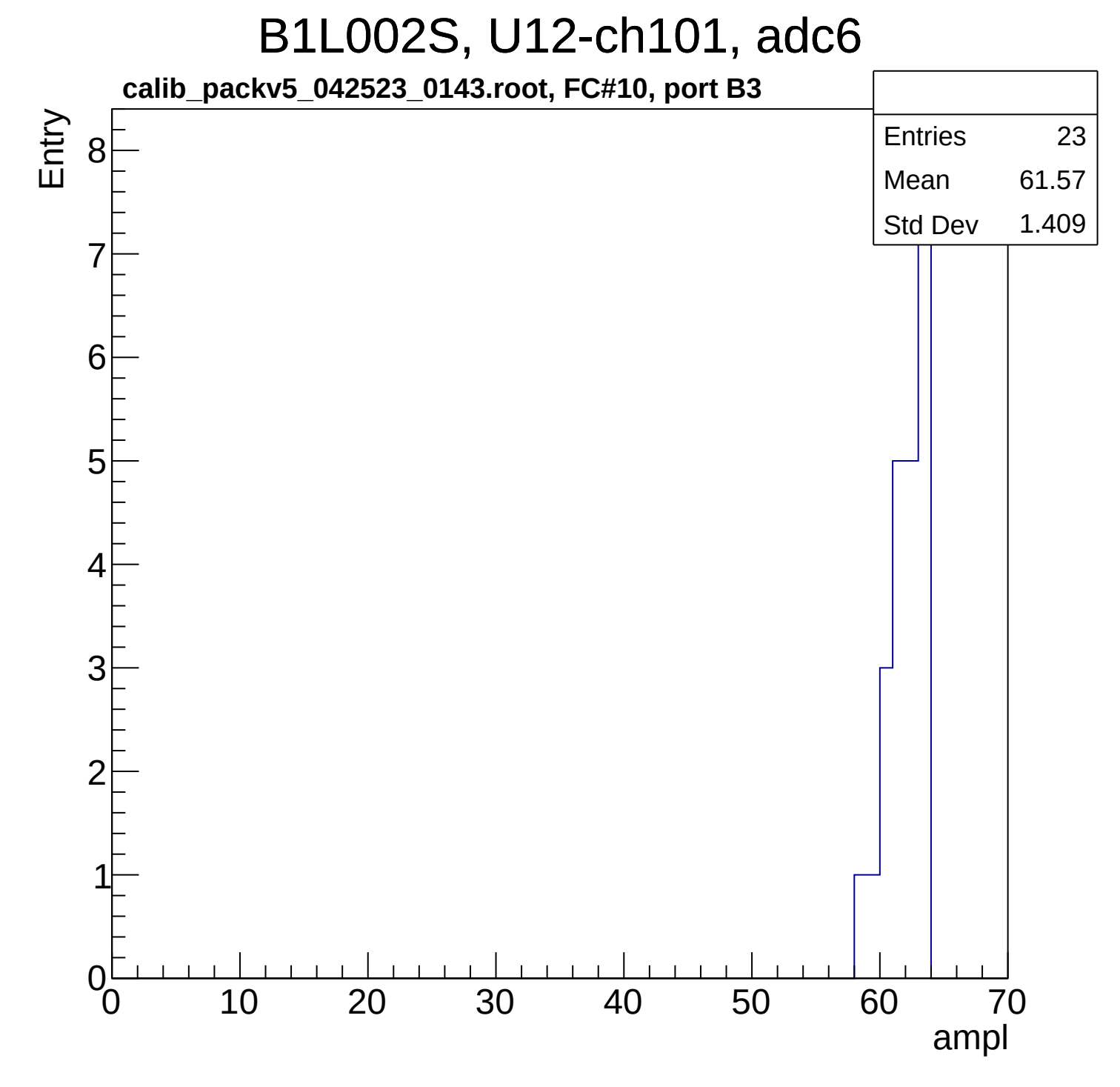
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.57
Std Dev 1.409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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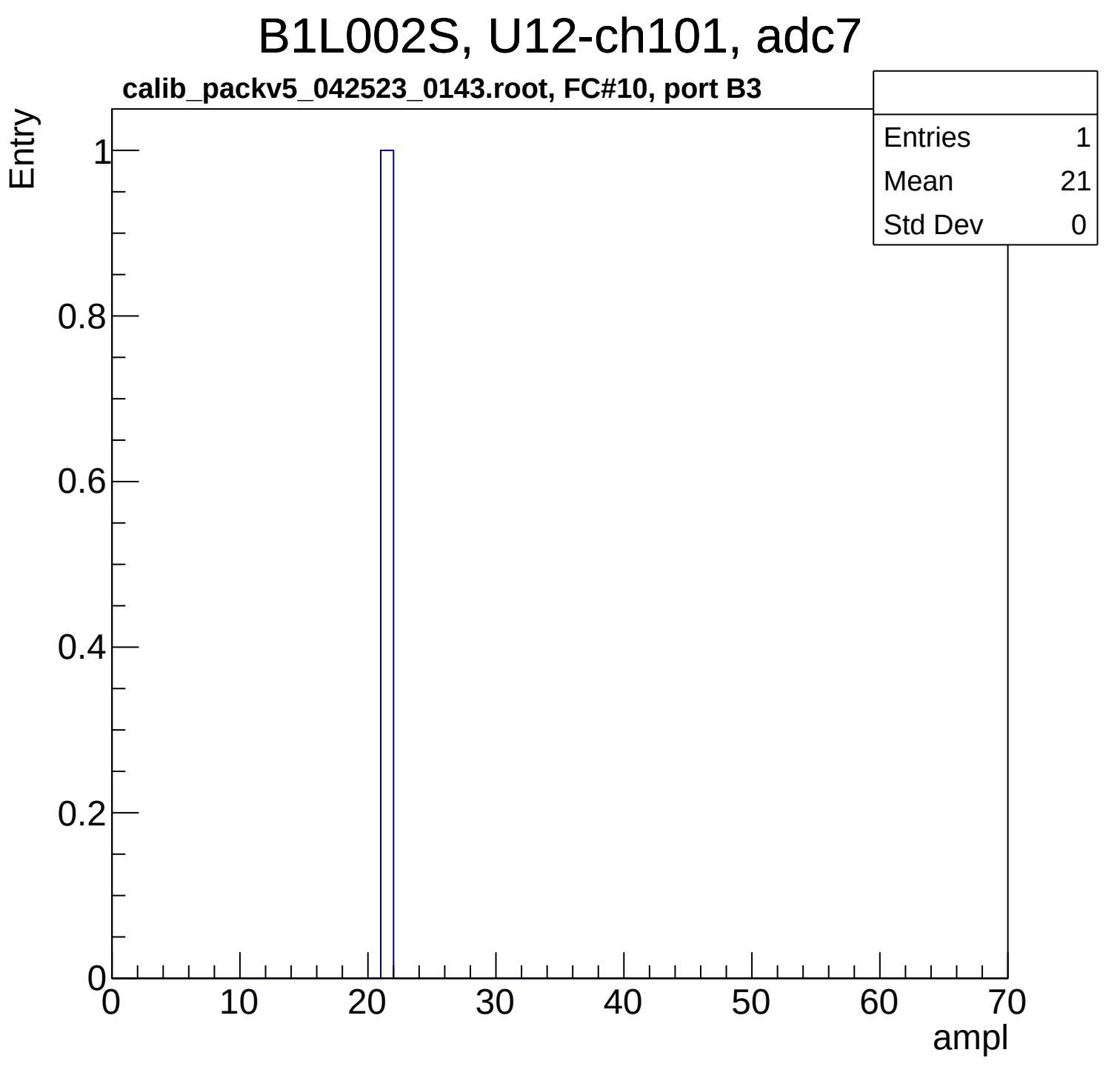
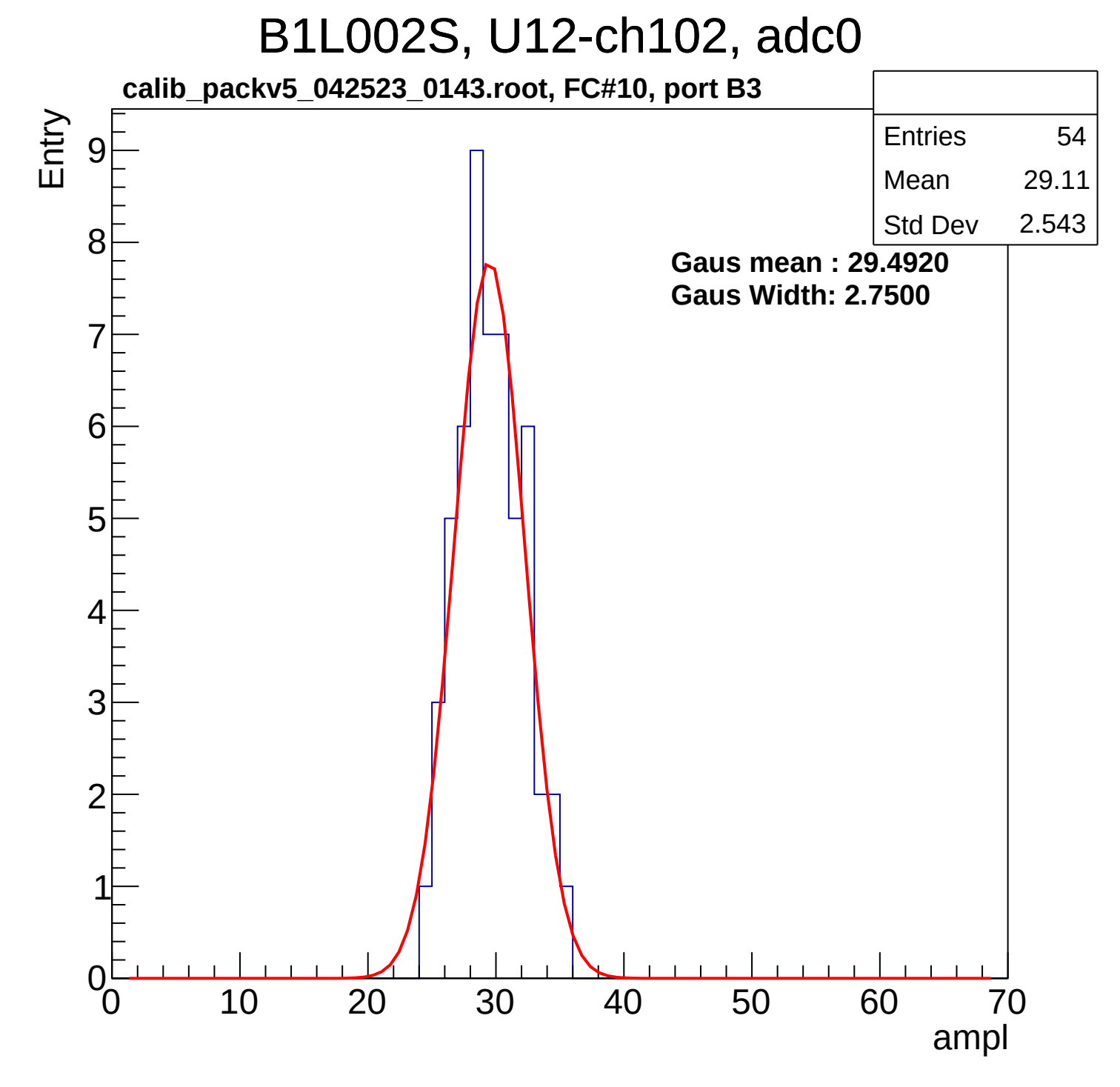
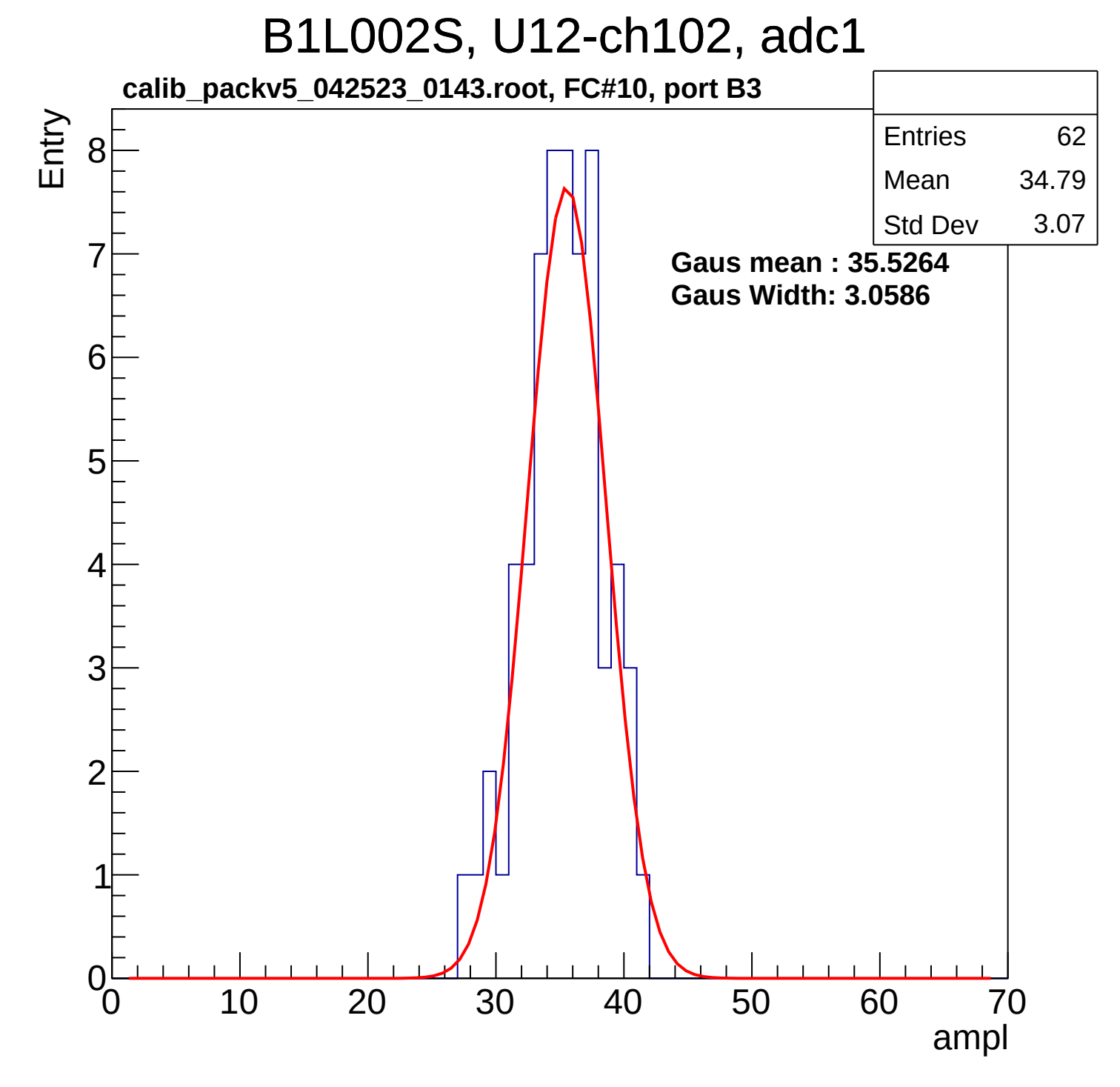
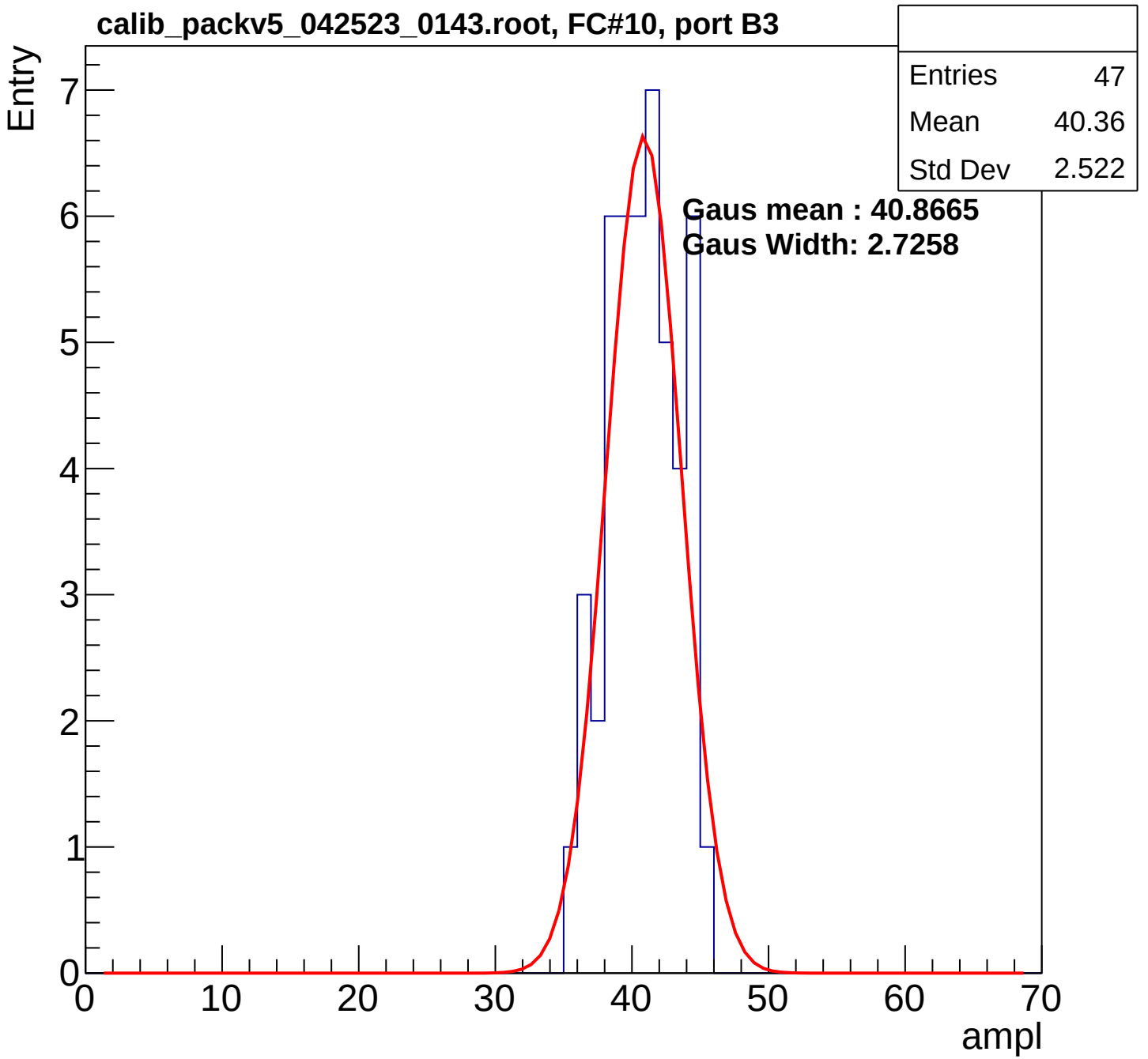
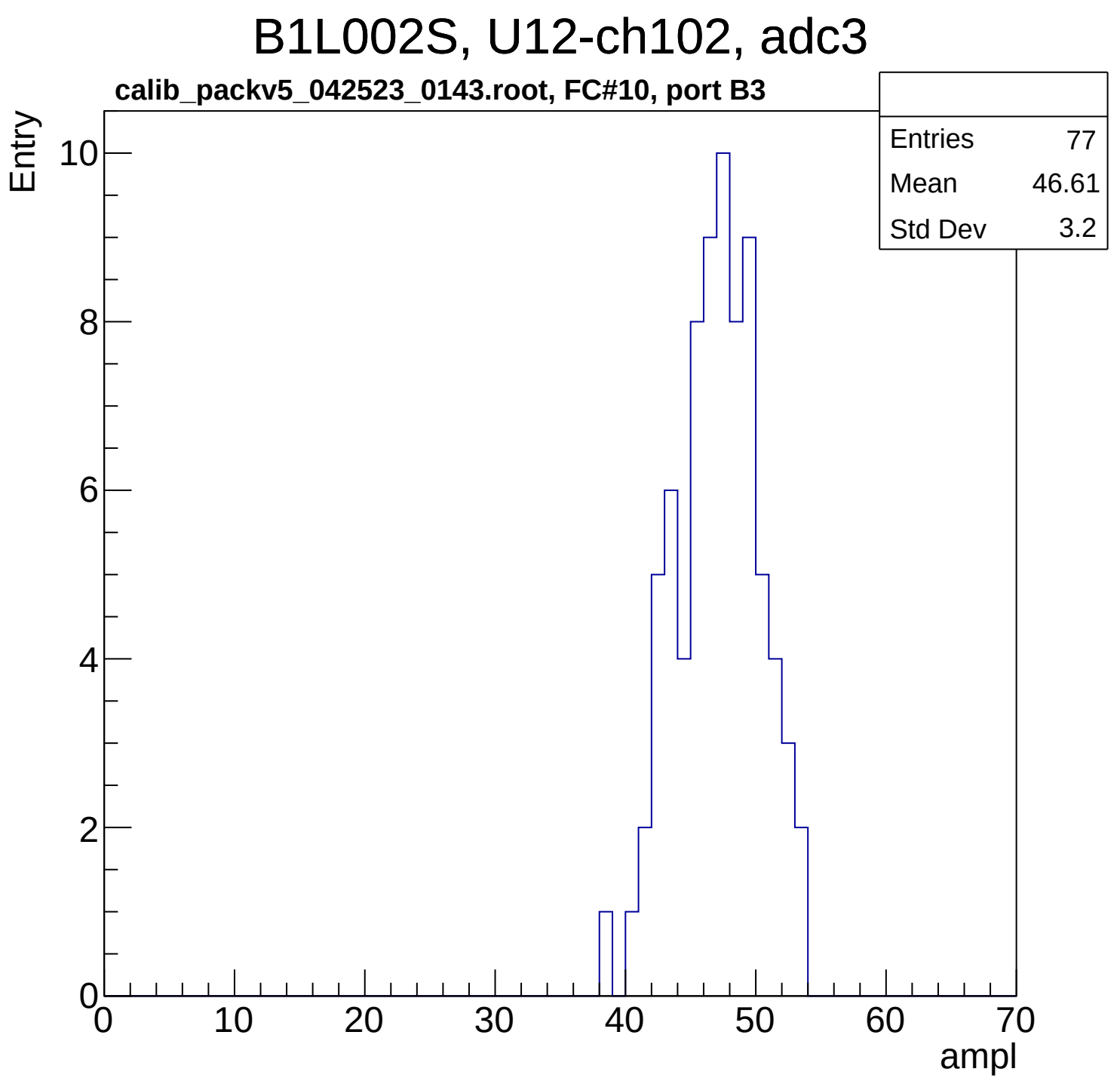
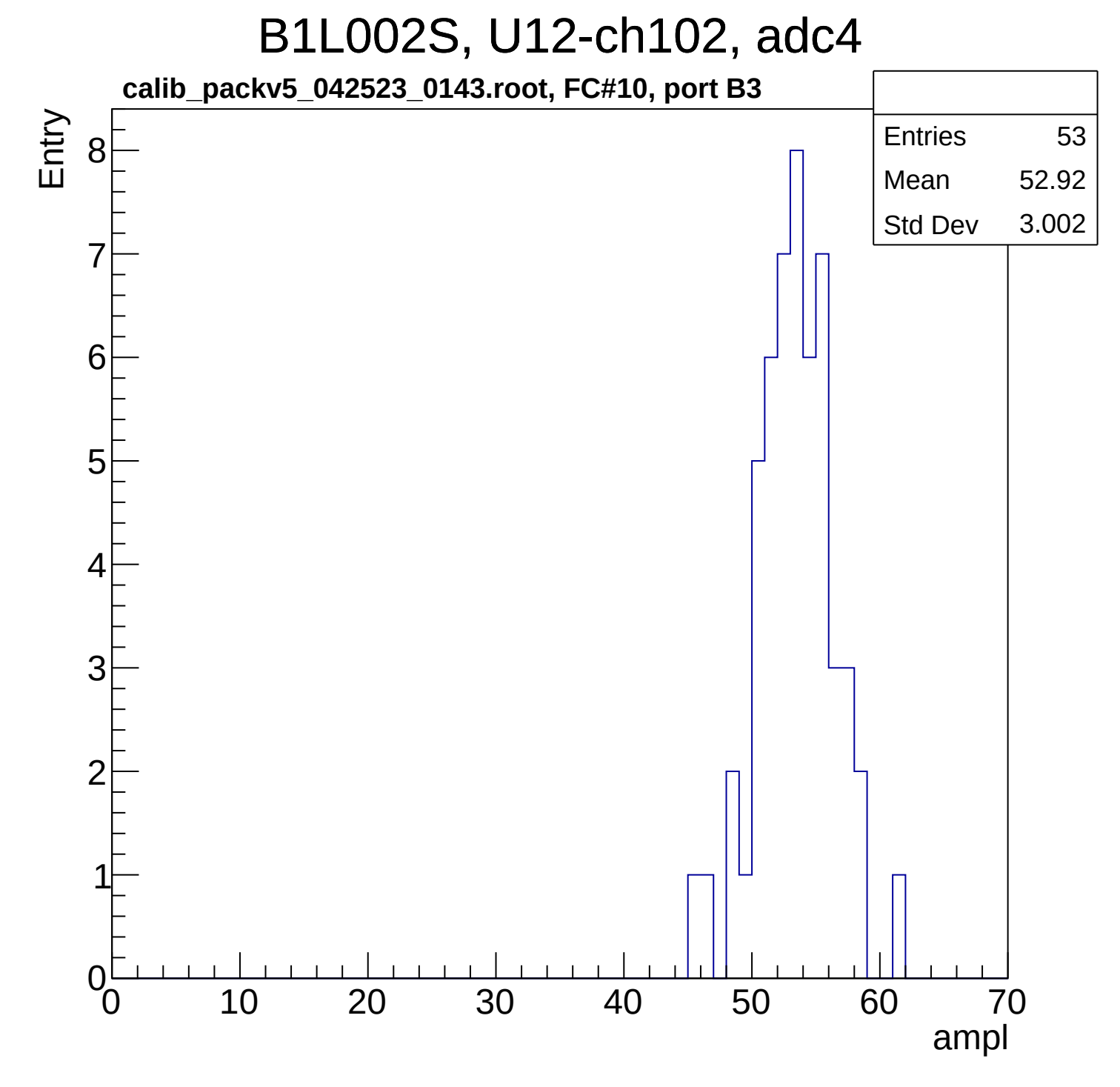

B1L002S, U12-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 29.11
Std Dev 2.543
Gaus mean : 29.4920
Gaus Width: 2.7500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.79
Std Dev 3.07
Gaus mean : 35.5264
Gaus Width: 3.0586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.36
Std Dev 2.522
Gaus mean : 40.8665
Gaus Width: 2.7258
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 46.61
Std Dev 3.2
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.92
Std Dev 3.002
Entry
ampl
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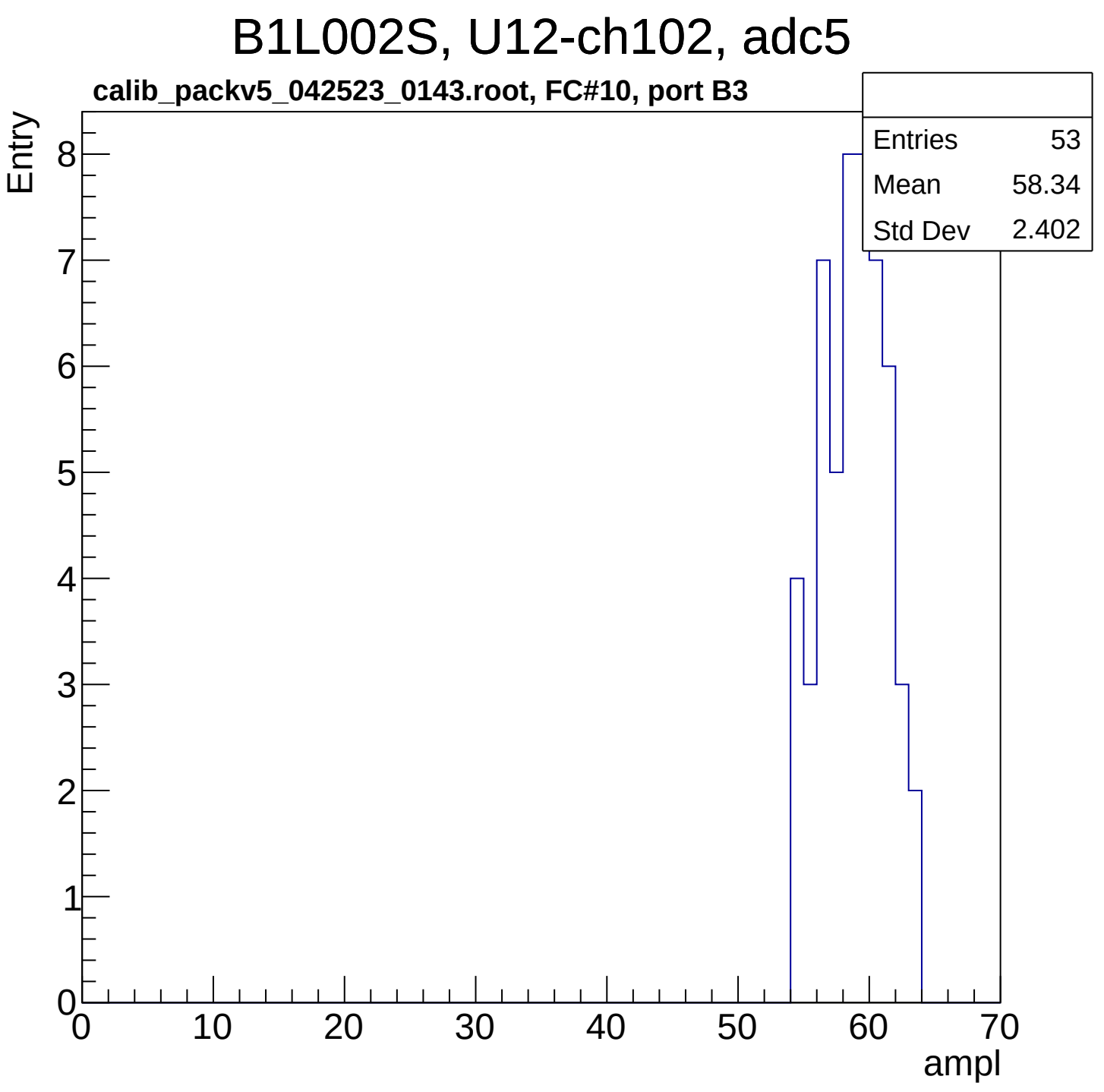
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.34
Std Dev 2.402
Entry
ampl
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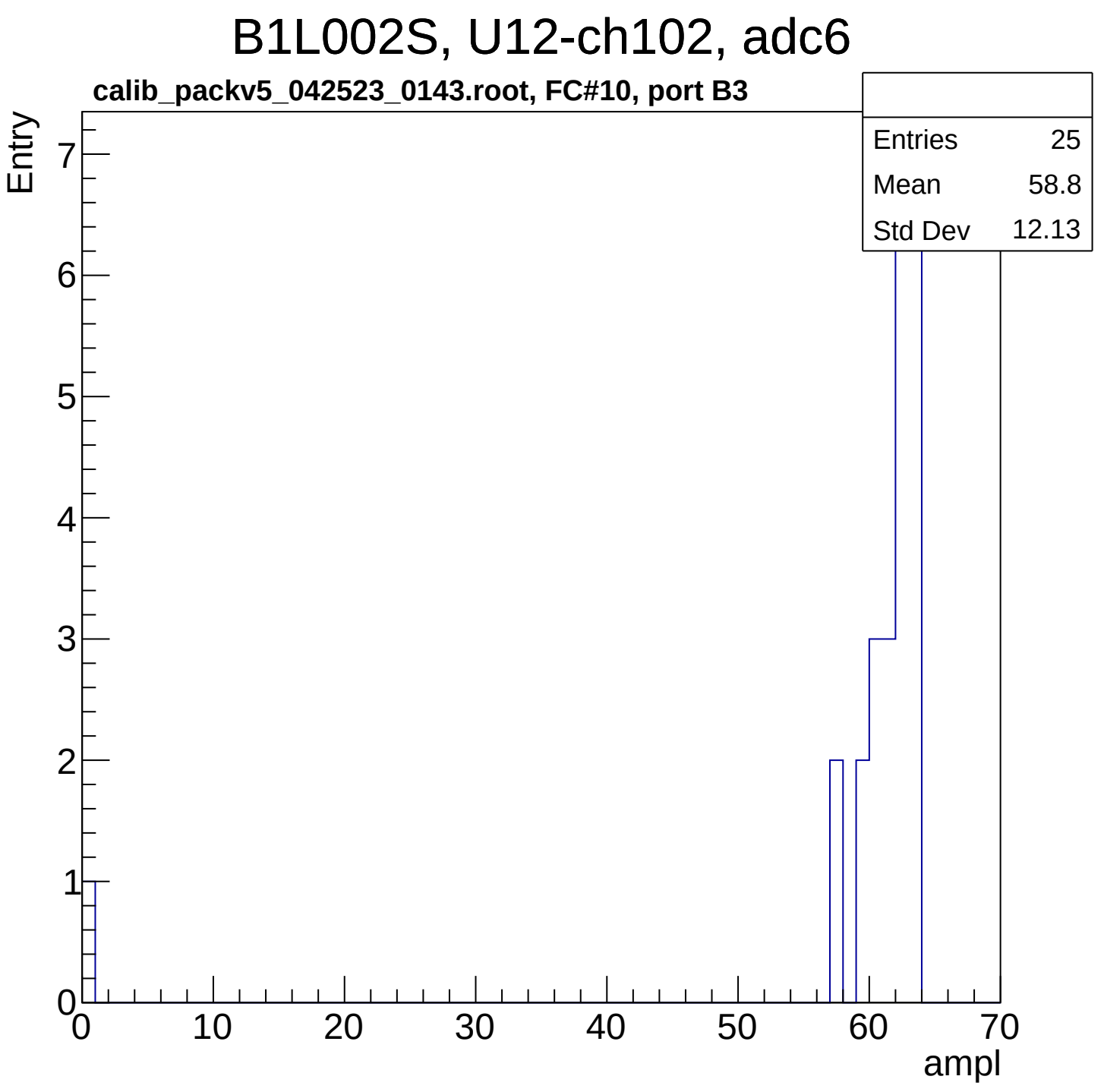
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.8
Std Dev 12.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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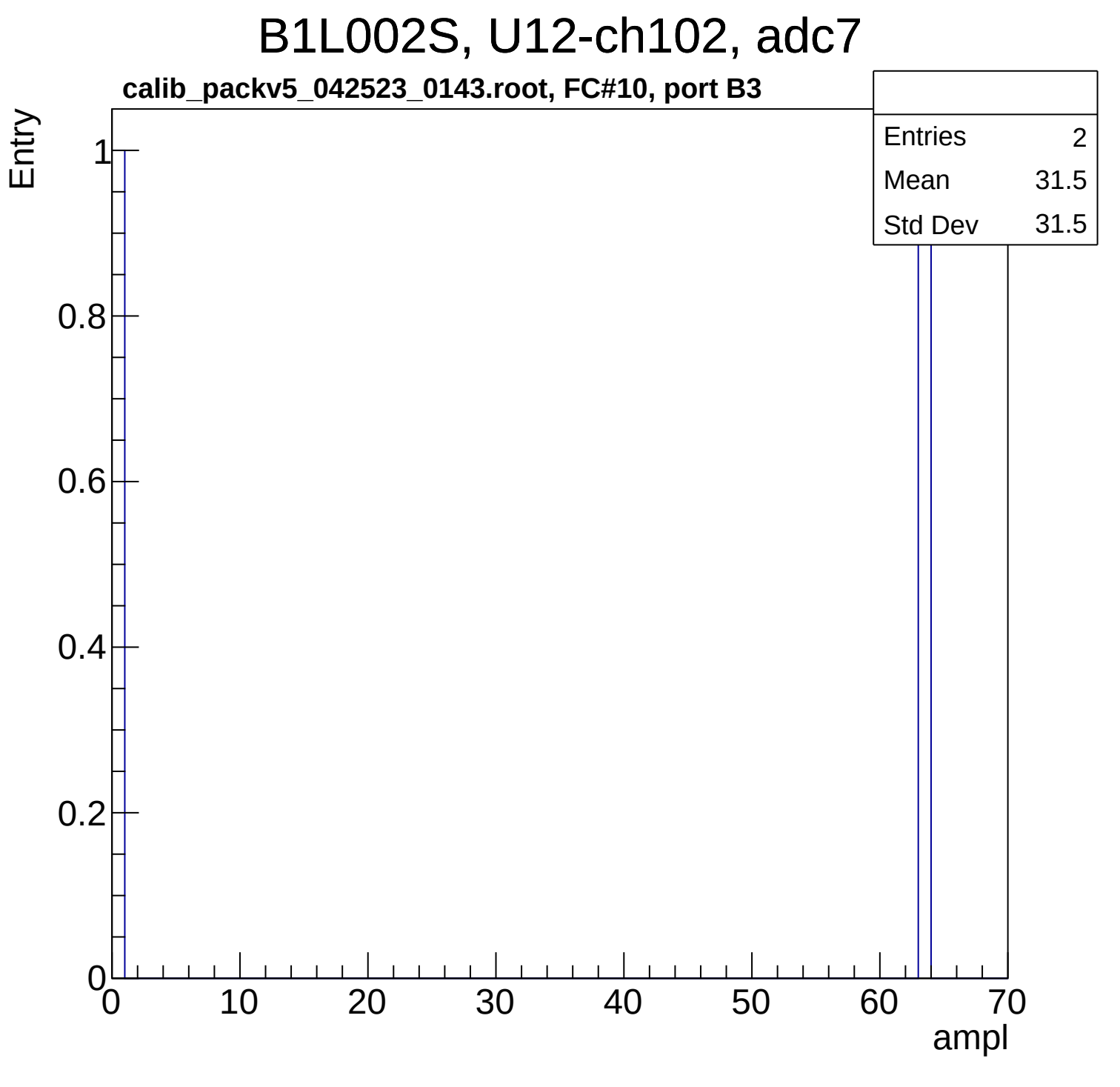
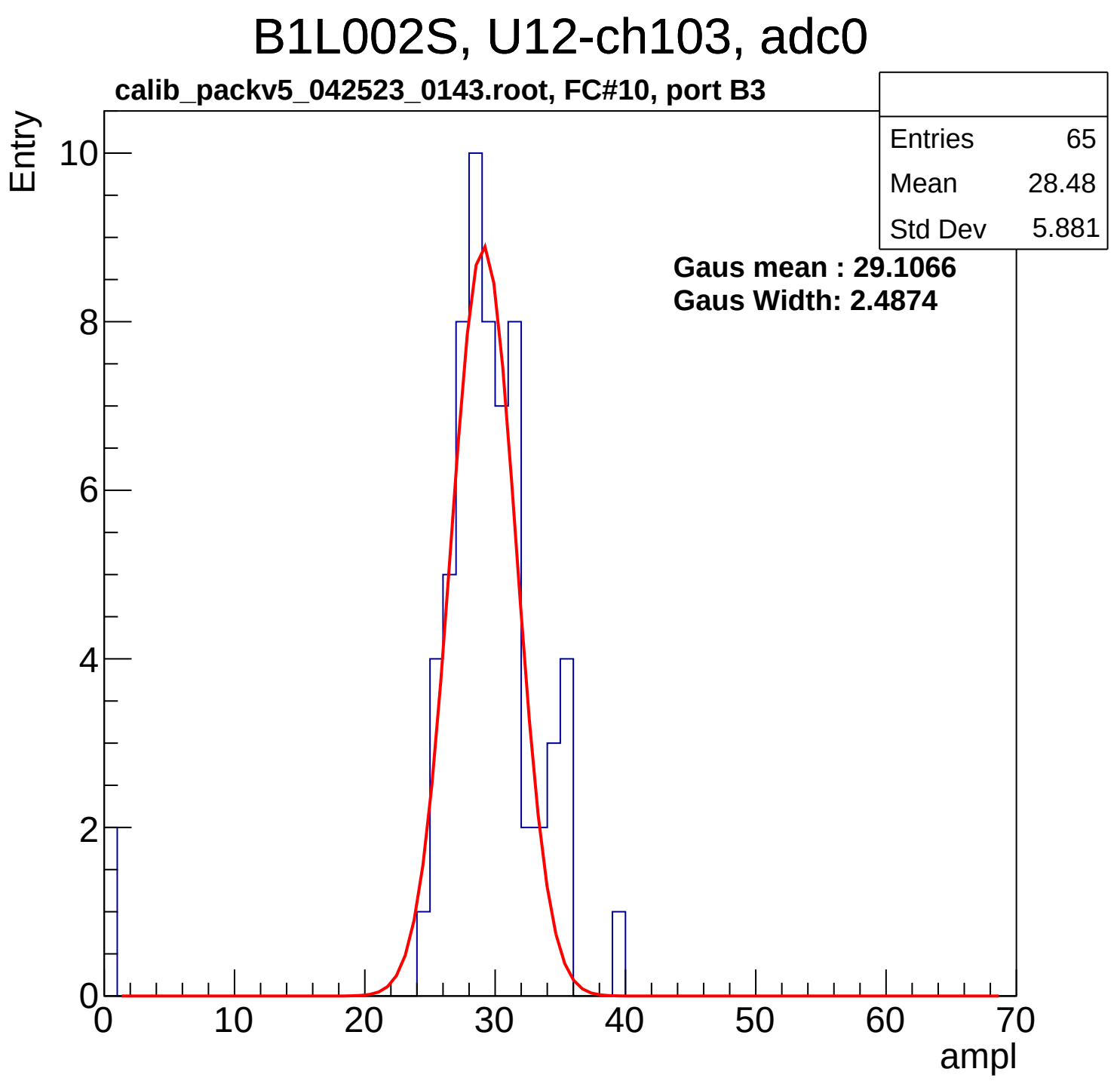
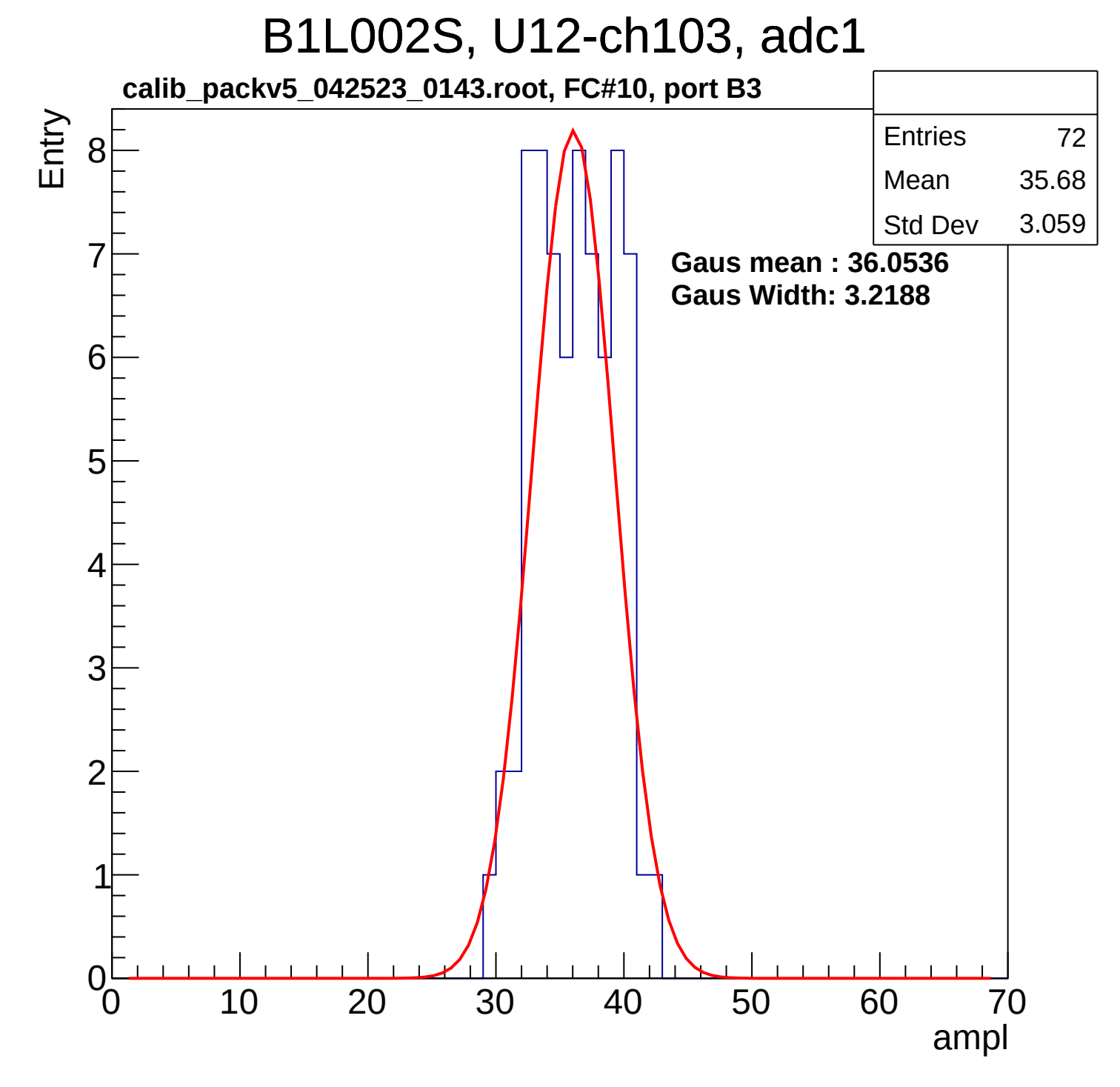

B1L002S, U12-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.48
Std Dev 5.881
Gaus mean : 29.1066
Gaus Width: 2.4874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.68
Std Dev 3.059
Gaus mean : 36.0536
Gaus Width: 3.2188
Entry
ampl
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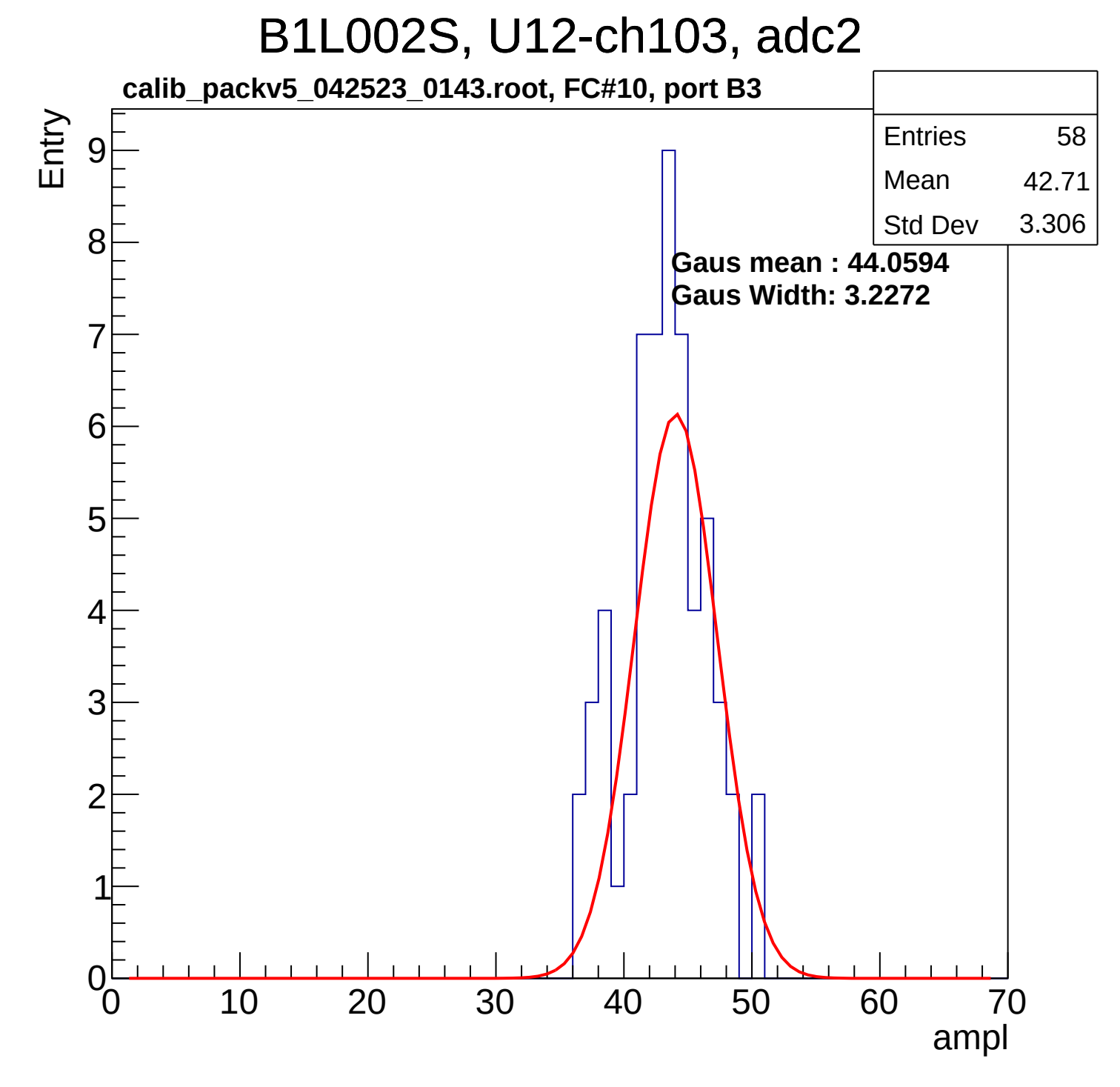
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 42.71
Std Dev 3.306
Gaus mean : 44.0594
Gaus Width: 3.2272
Entry
ampl
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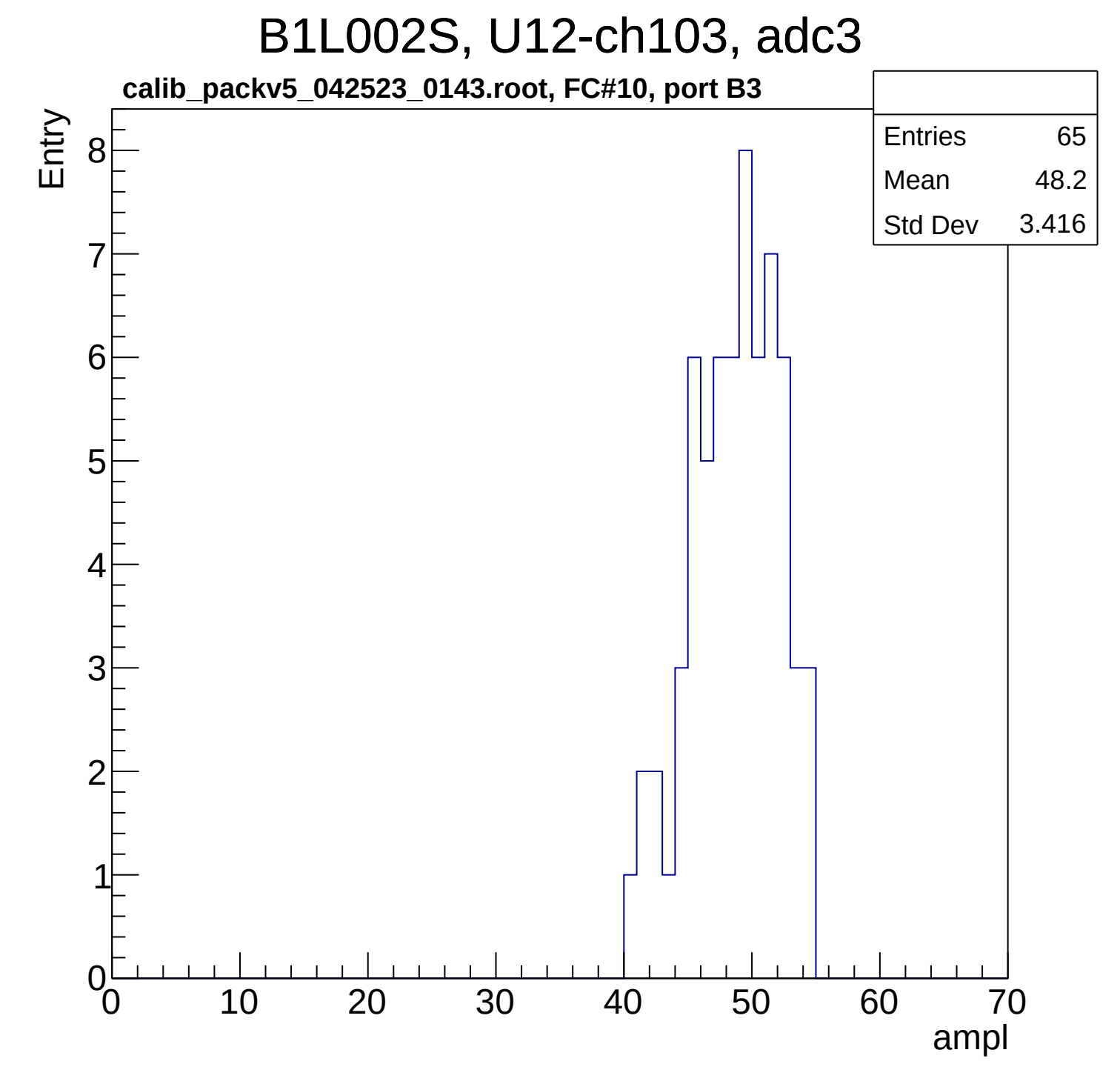
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 48.2
Std Dev 3.416
Entry
ampl
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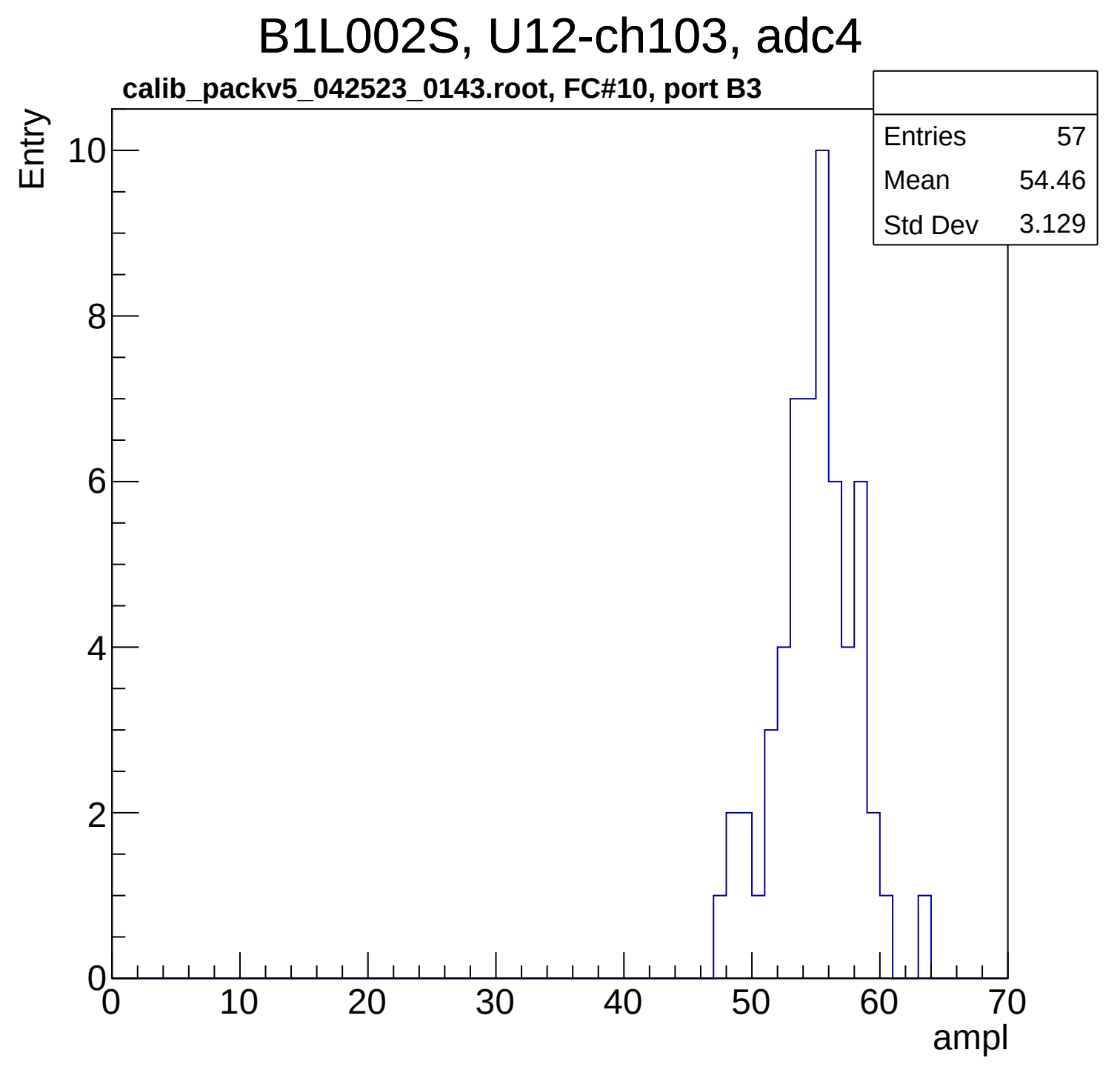
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.46
Std Dev 3.129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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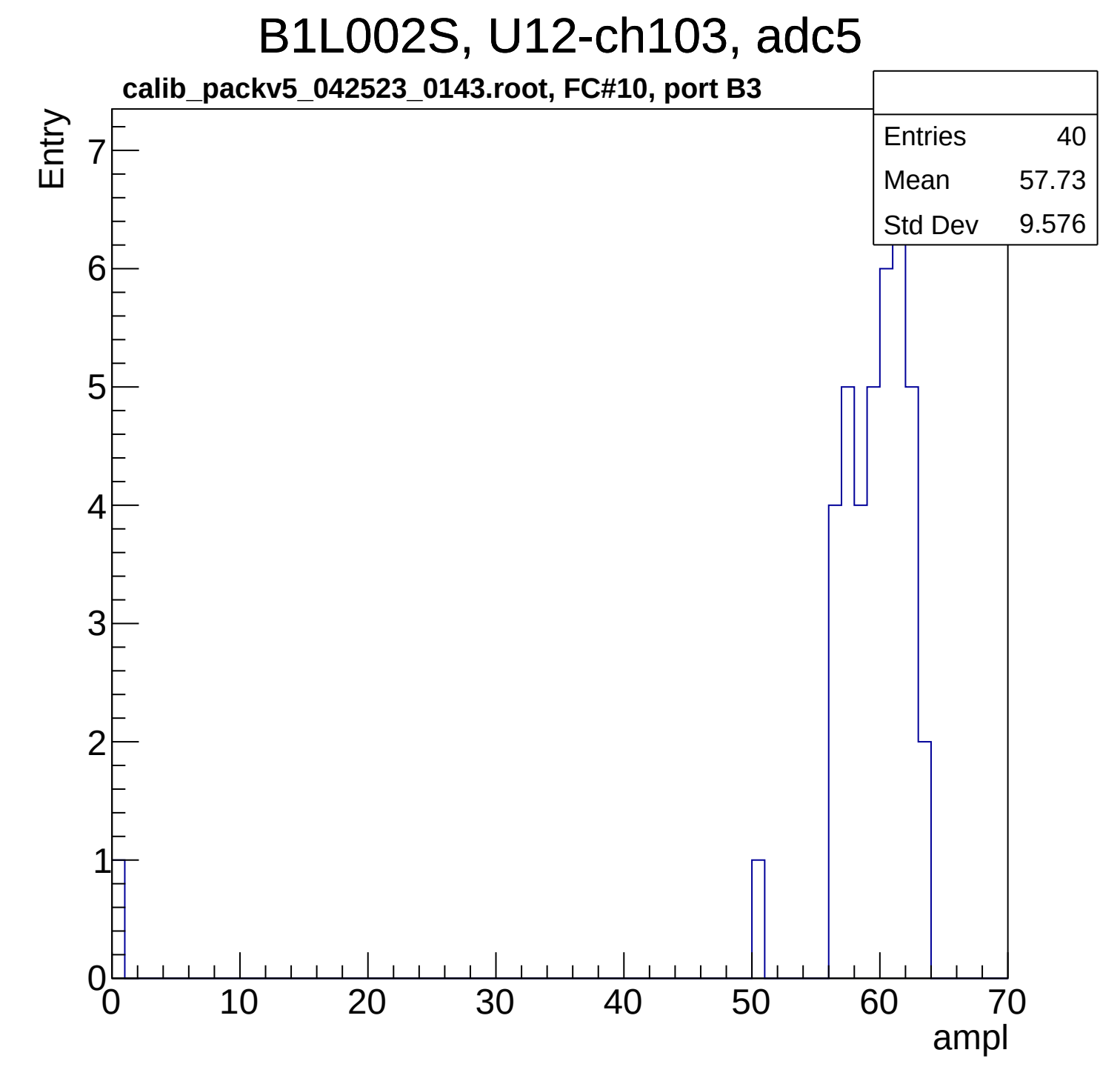
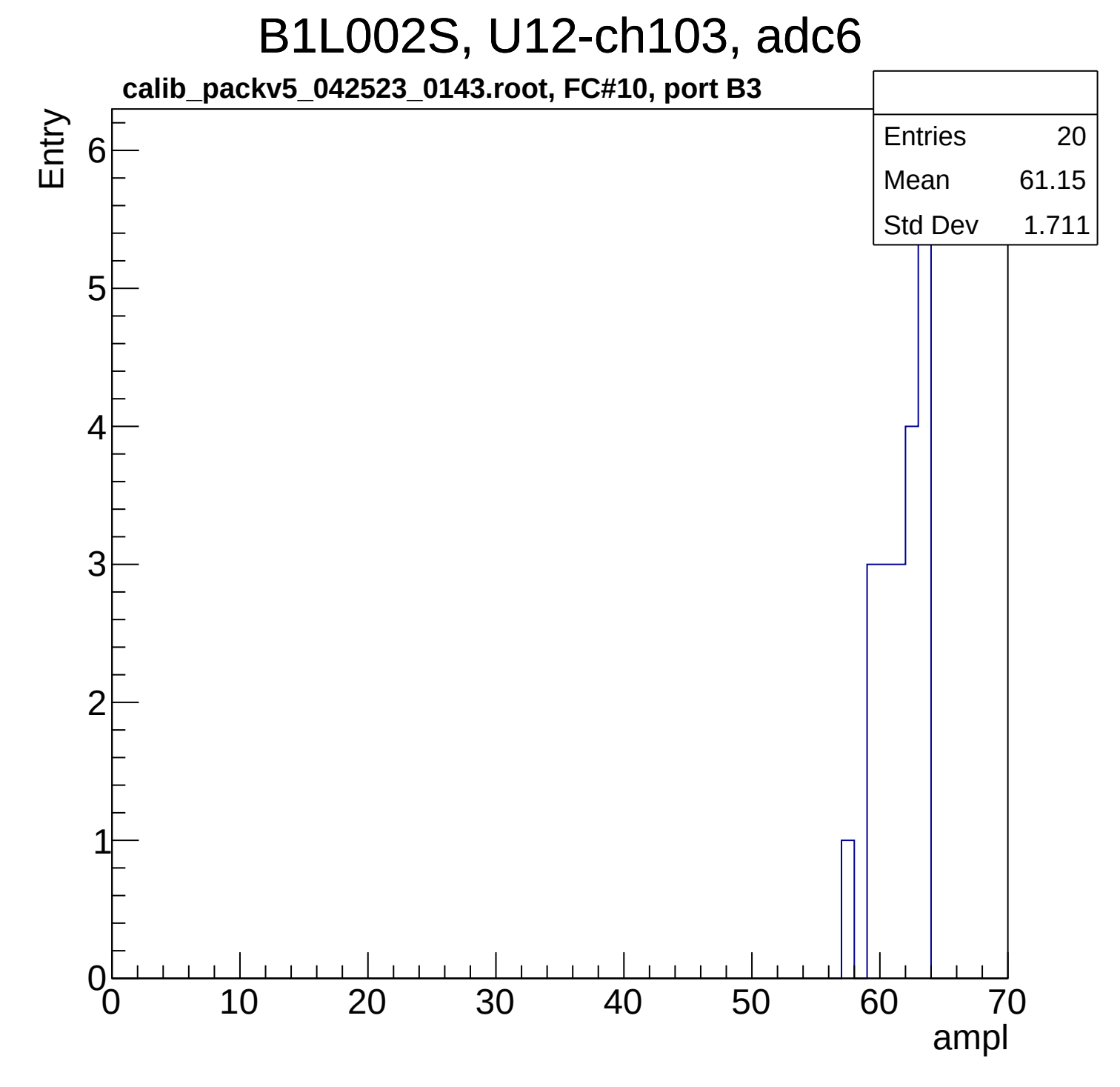
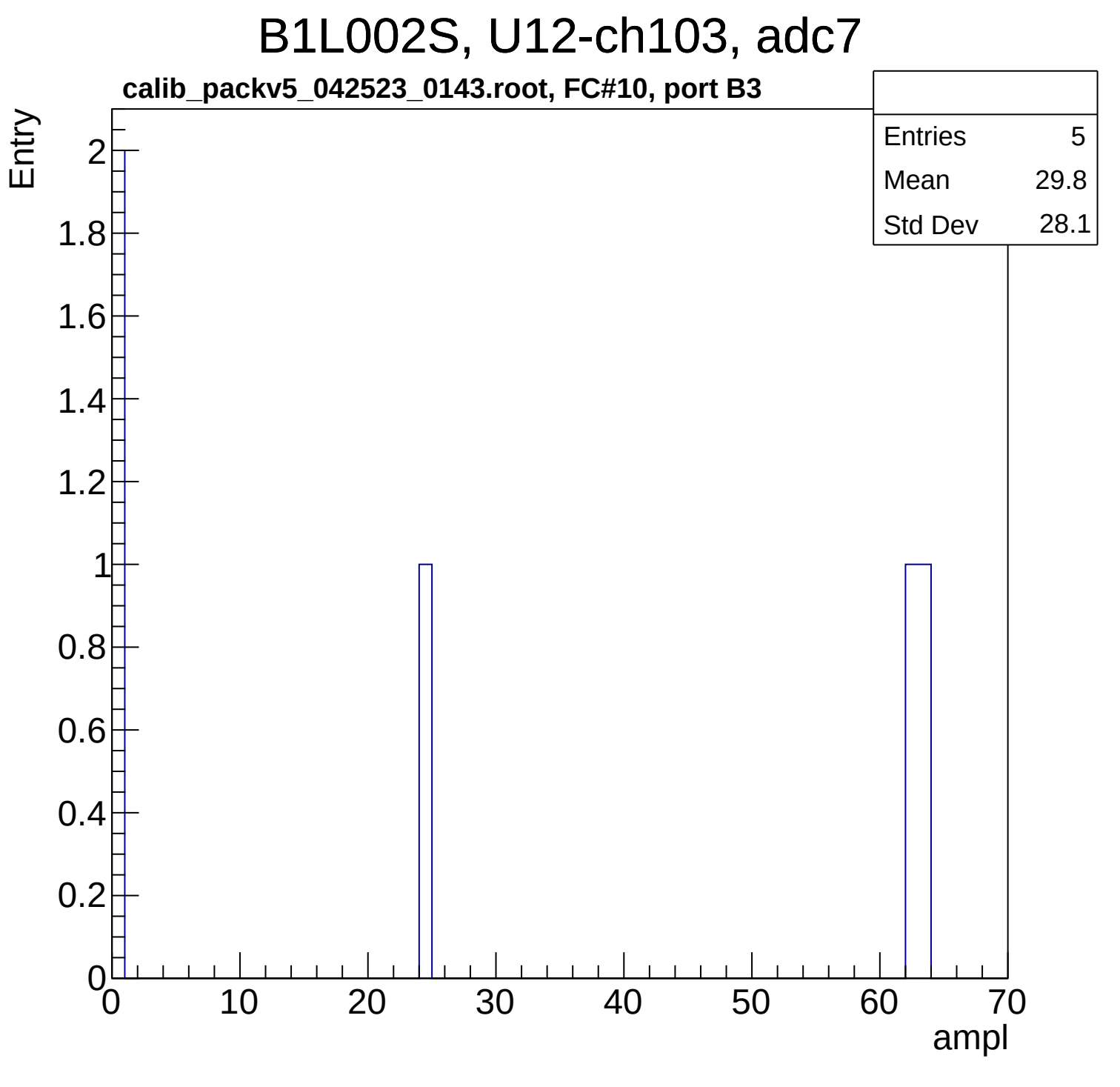

B1L002S, U12-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 57.73
Std Dev 9.576
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.15
Std Dev 1.711
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 29.8
Std Dev 28.1
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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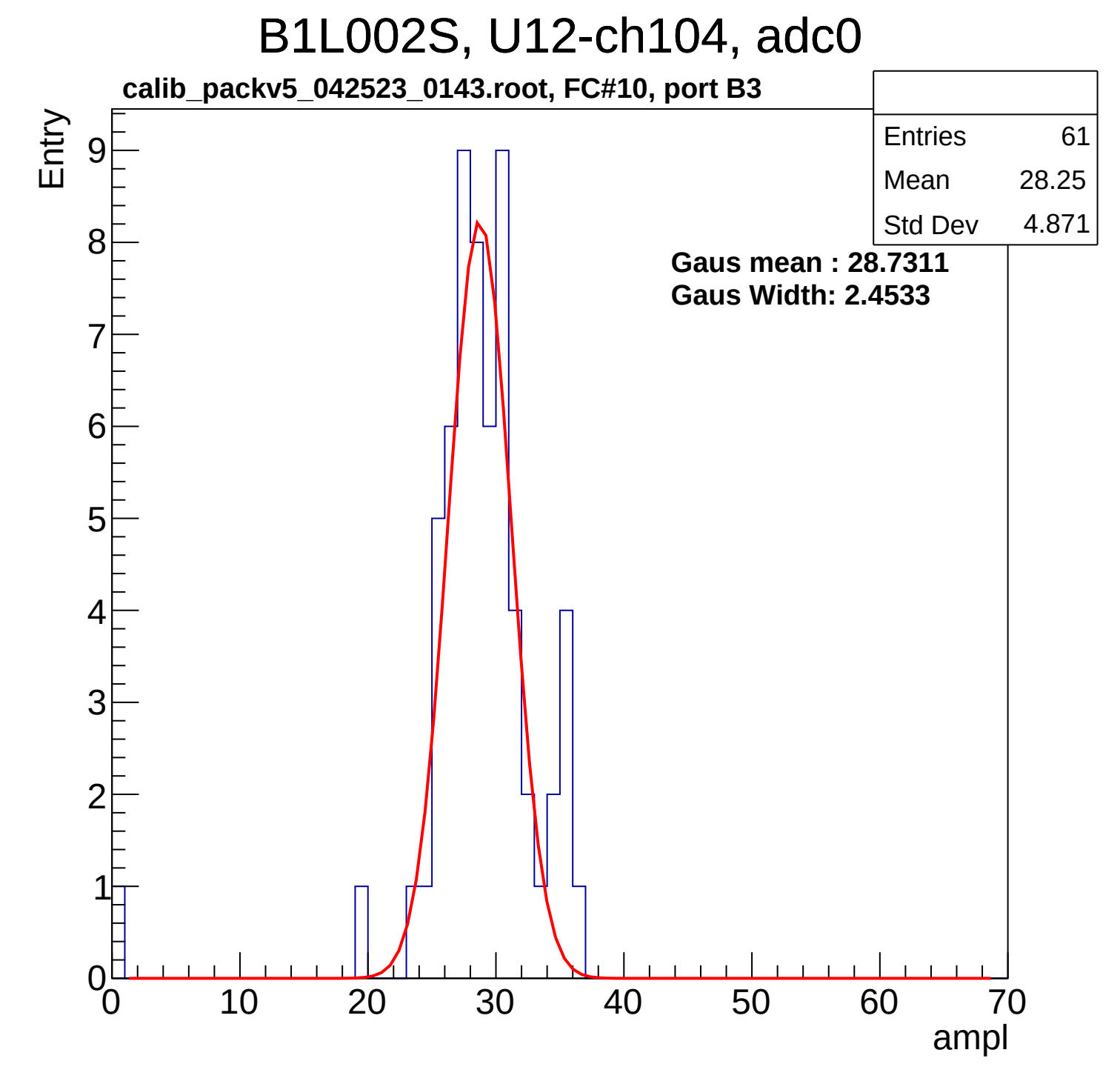
B1L002S, U12-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.25
Std Dev 4.871
Gaus mean : 28.7311
Gaus Width: 2.4533
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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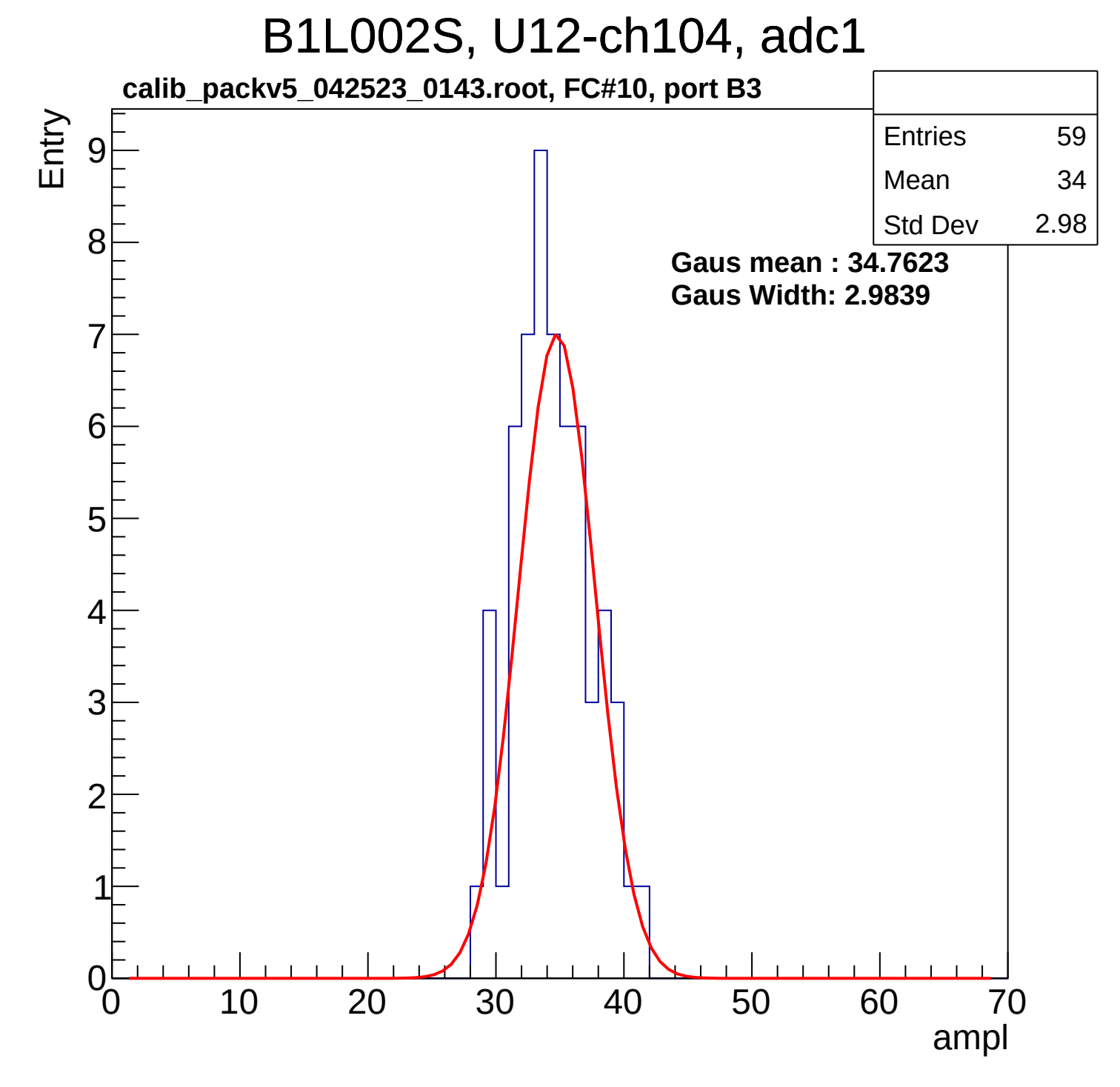
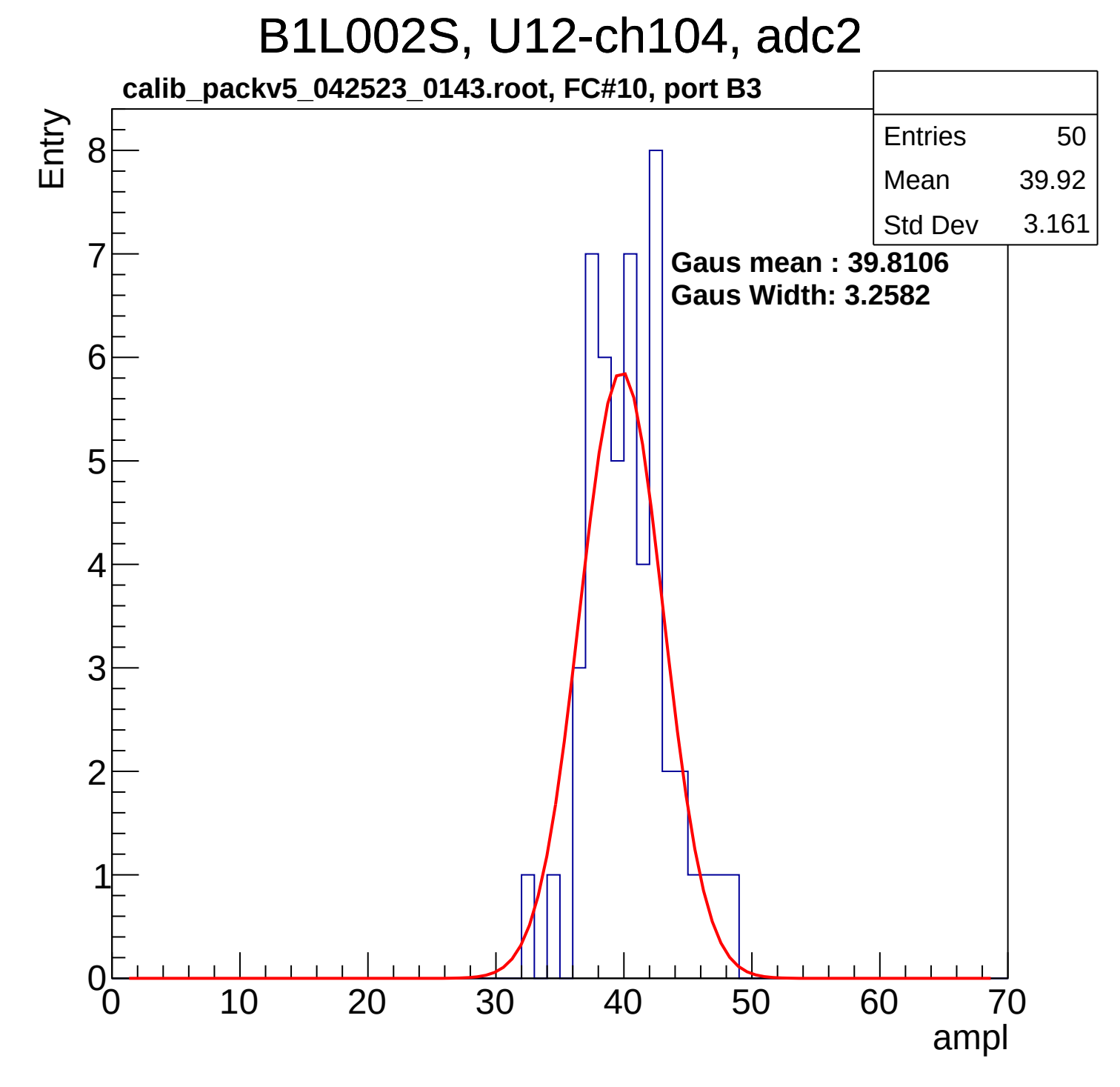

B1L002S, U12-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34
Std Dev 2.98
Gaus mean : 34.7623
Gaus Width: 2.9839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 39.92
Std Dev 3.161
Gaus mean : 39.8106
Gaus Width: 3.2582
Entry
ampl
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1
2
3
4
5
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7
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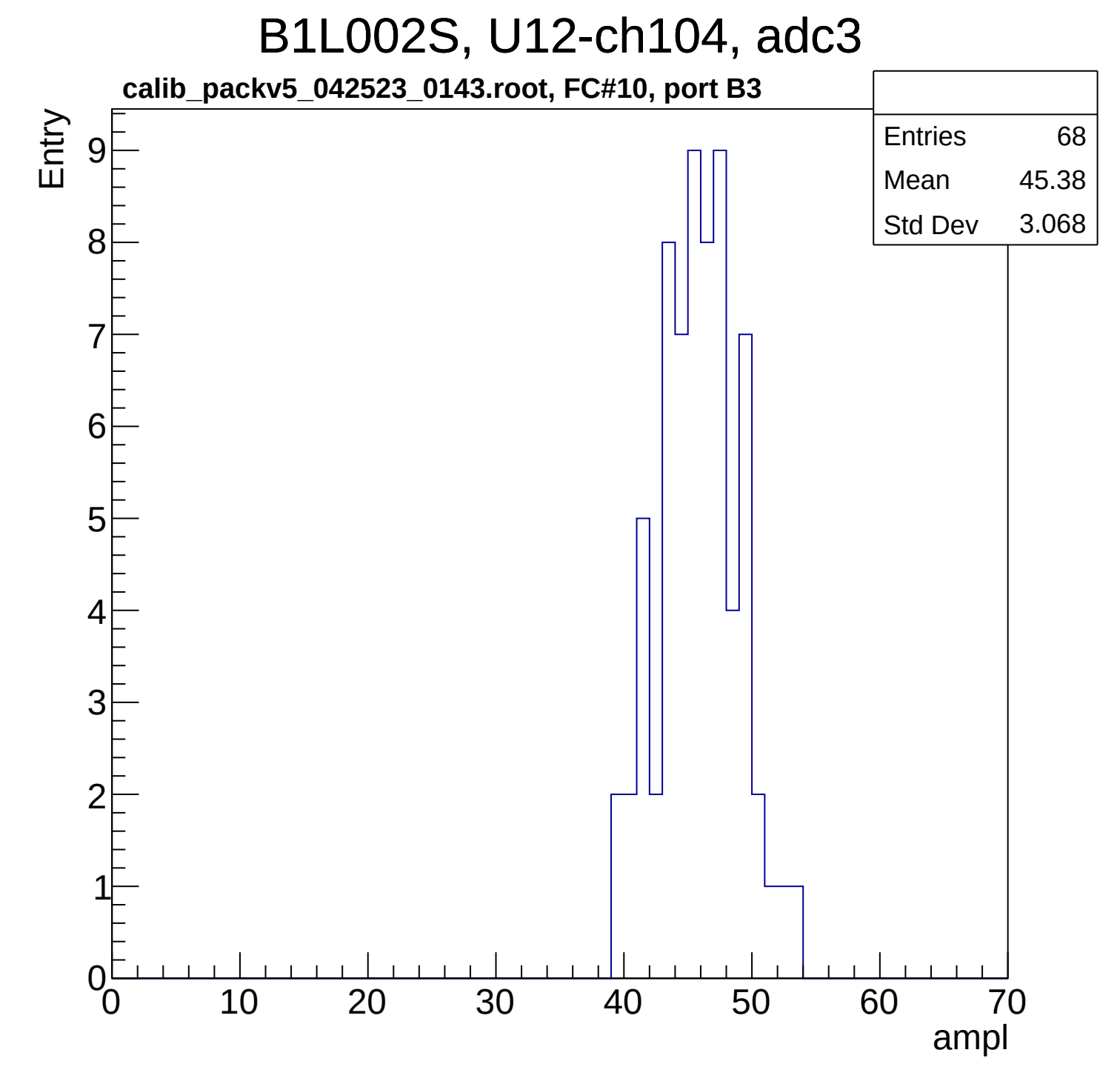
B1L002S, U12-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.38
Std Dev 3.068
Entry
ampl
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5
6
7
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0
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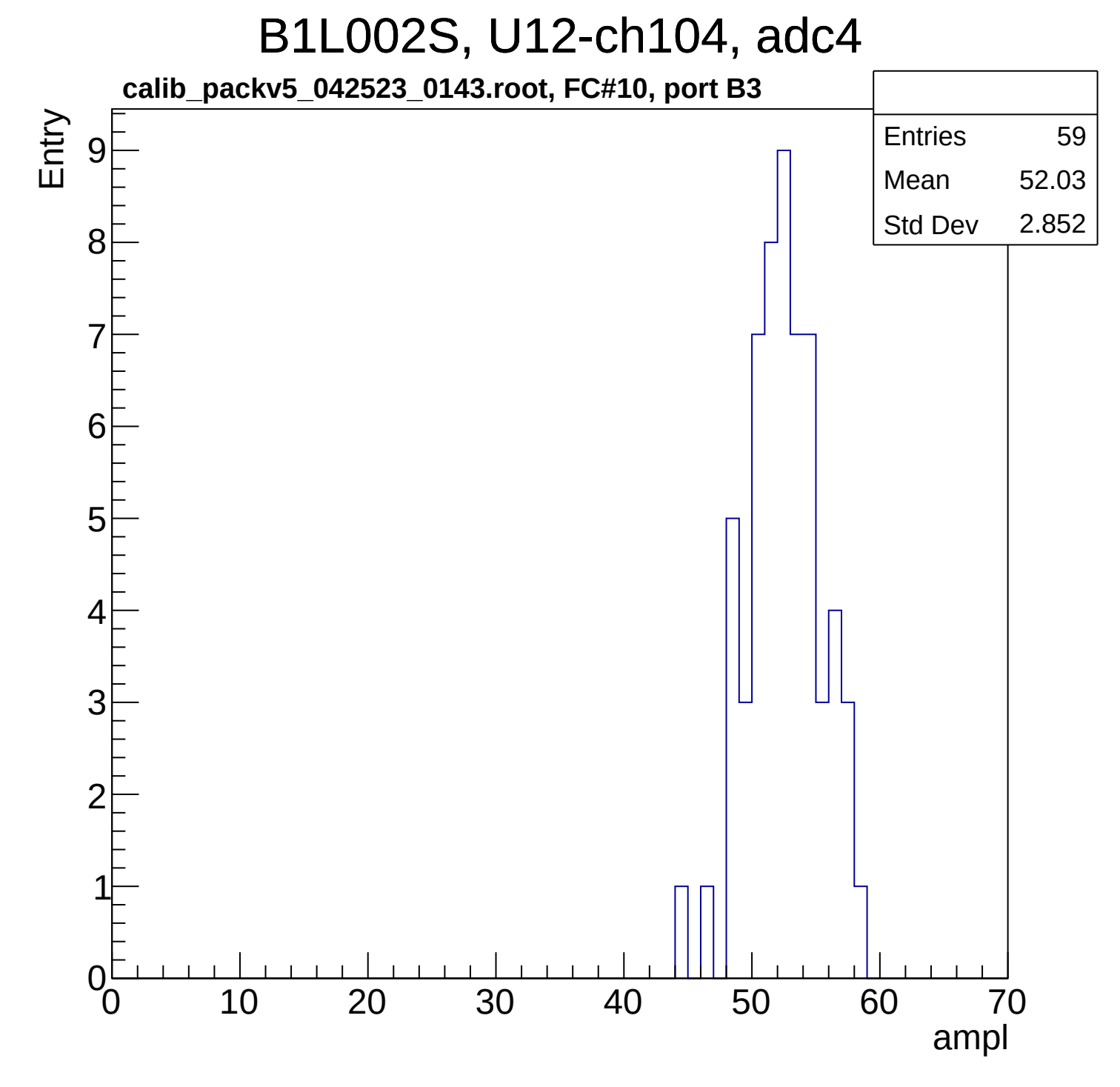
B1L002S, U12-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.03
Std Dev 2.852
Entry
ampl
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2
3
4
5
6
7
8
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0
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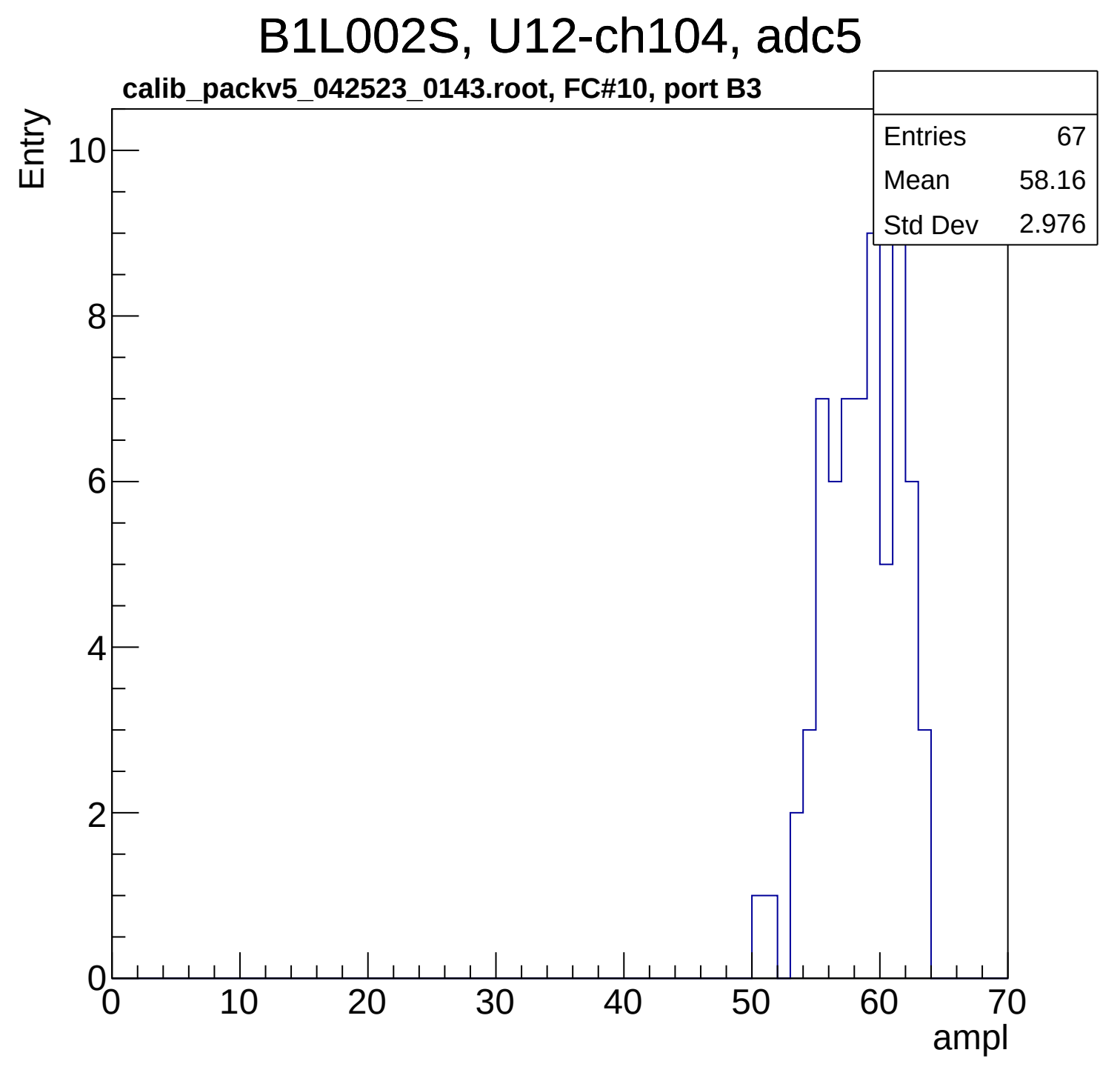
B1L002S, U12-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.16
Std Dev 2.976
Entry
ampl
0
2
4
6
8
10
0
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20
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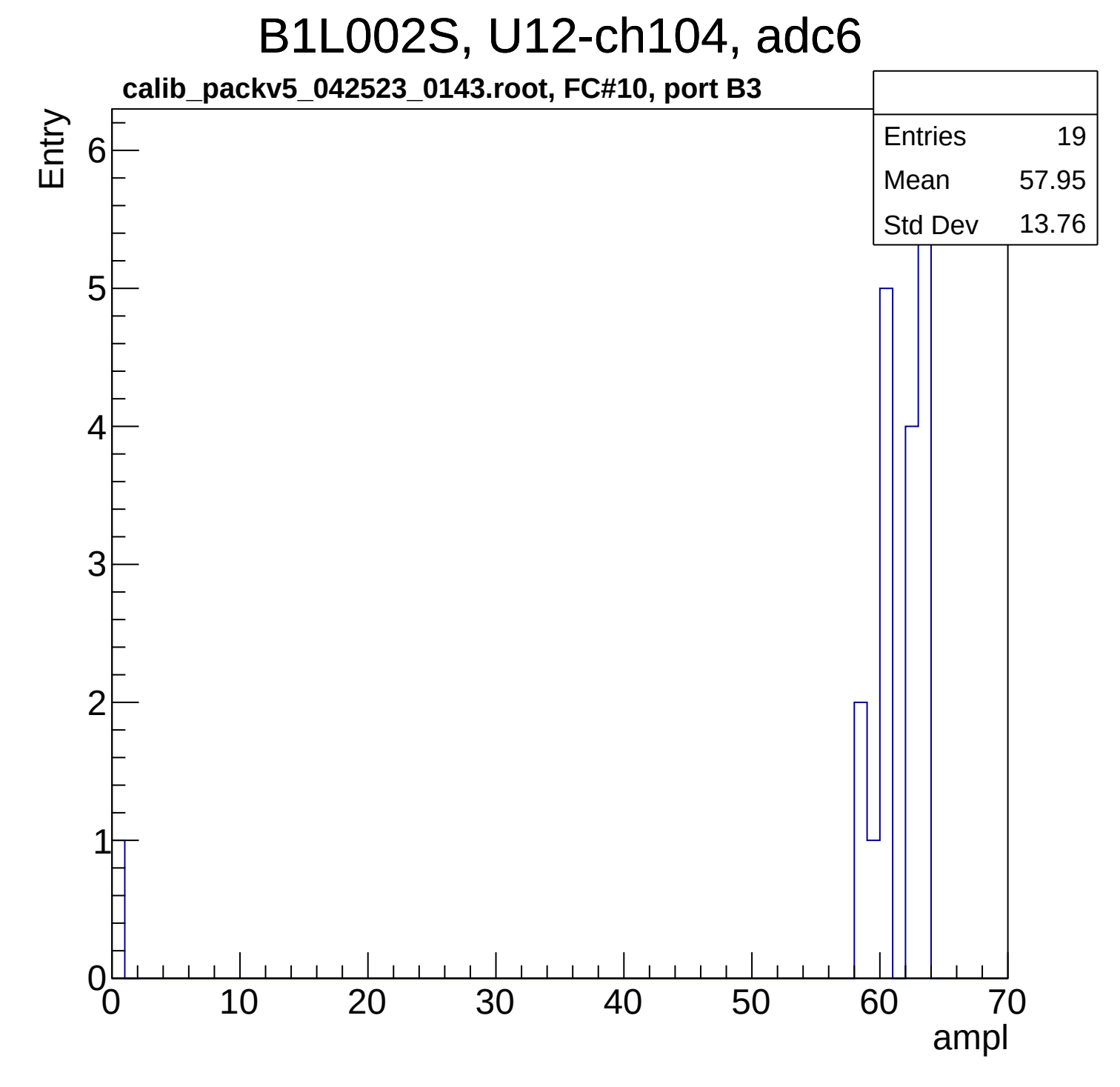
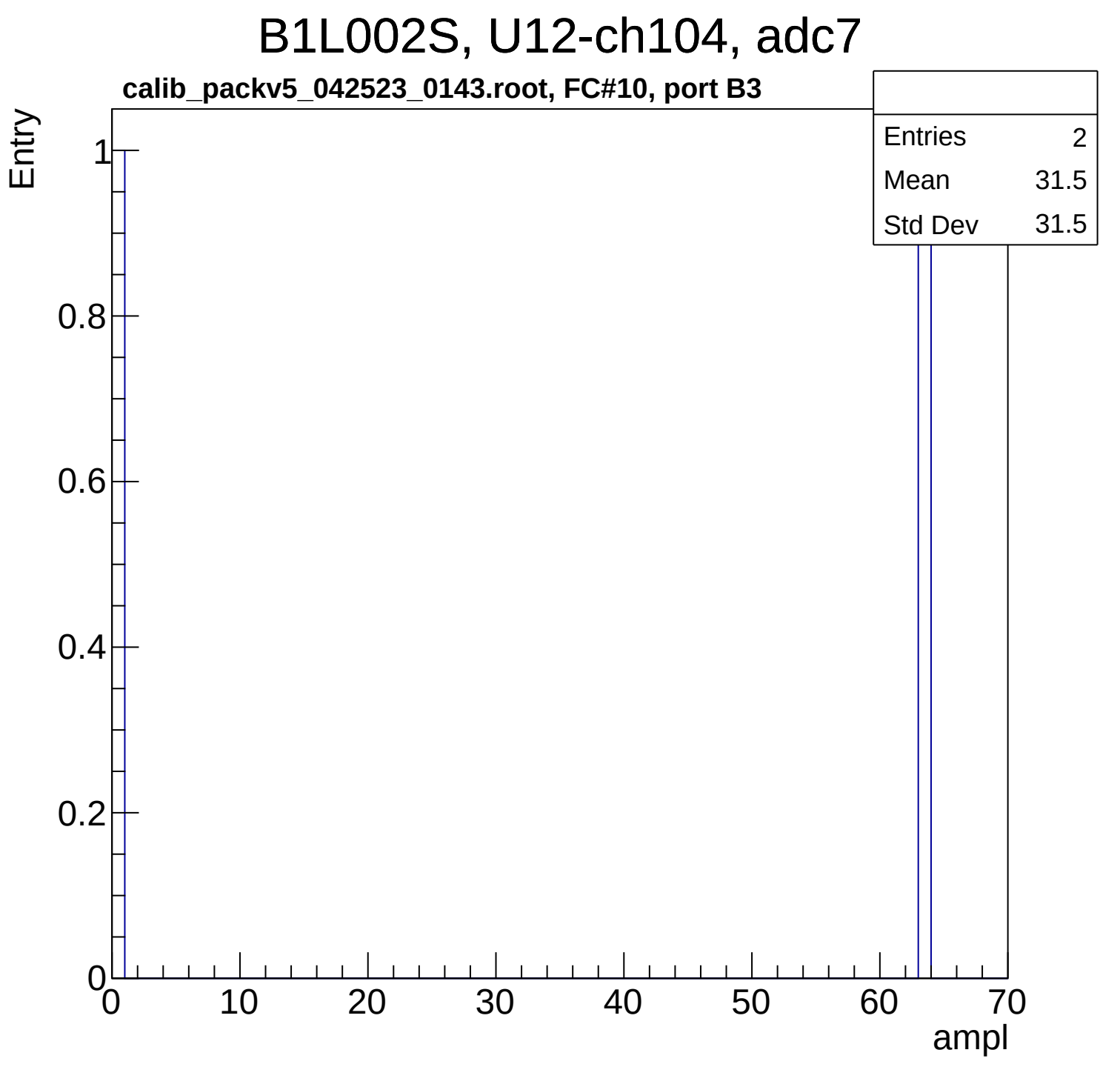
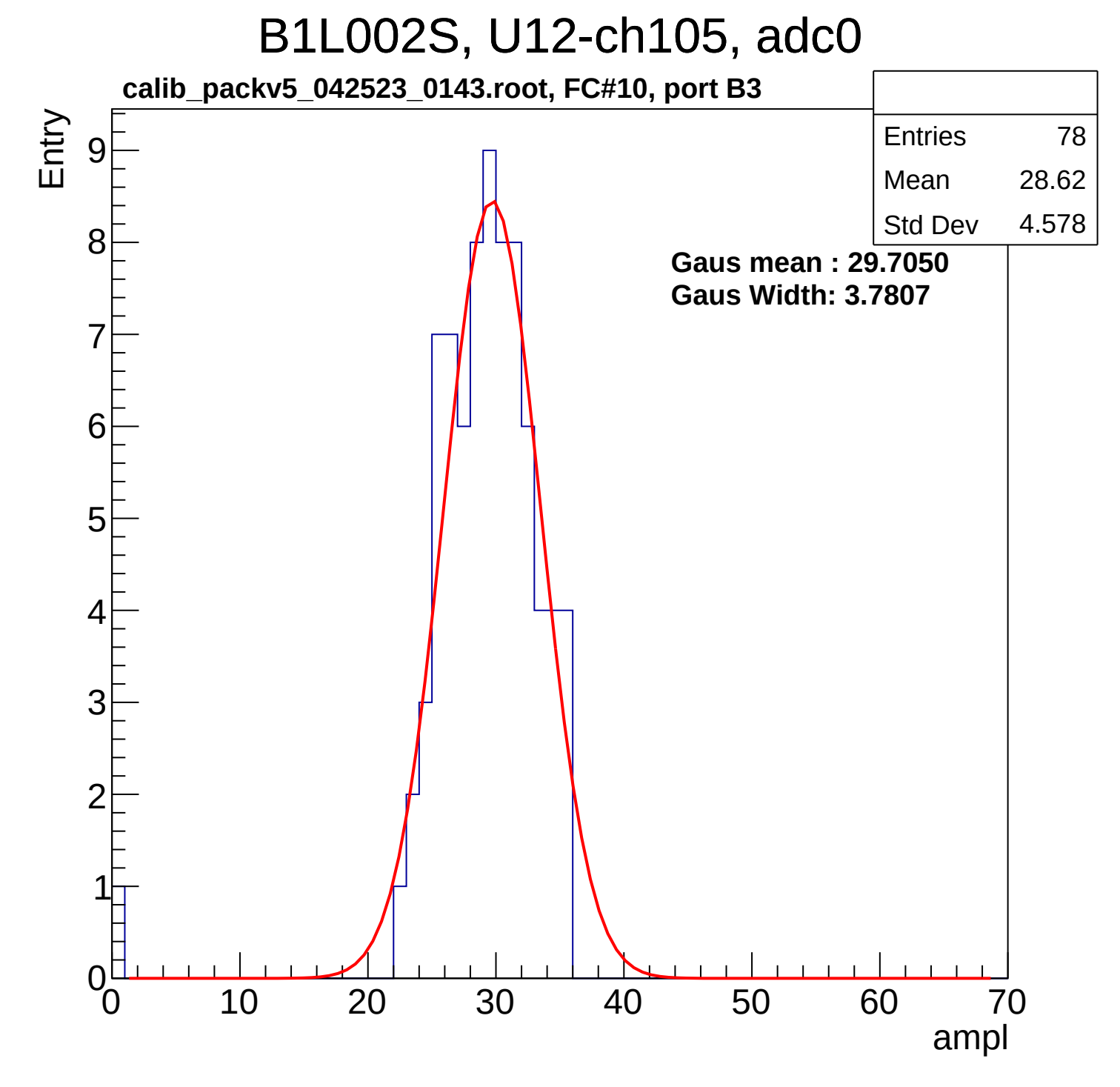

B1L002S, U12-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.95
Std Dev 13.76
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.62
Std Dev 4.578
Gaus mean : 29.7050
Gaus Width: 3.7807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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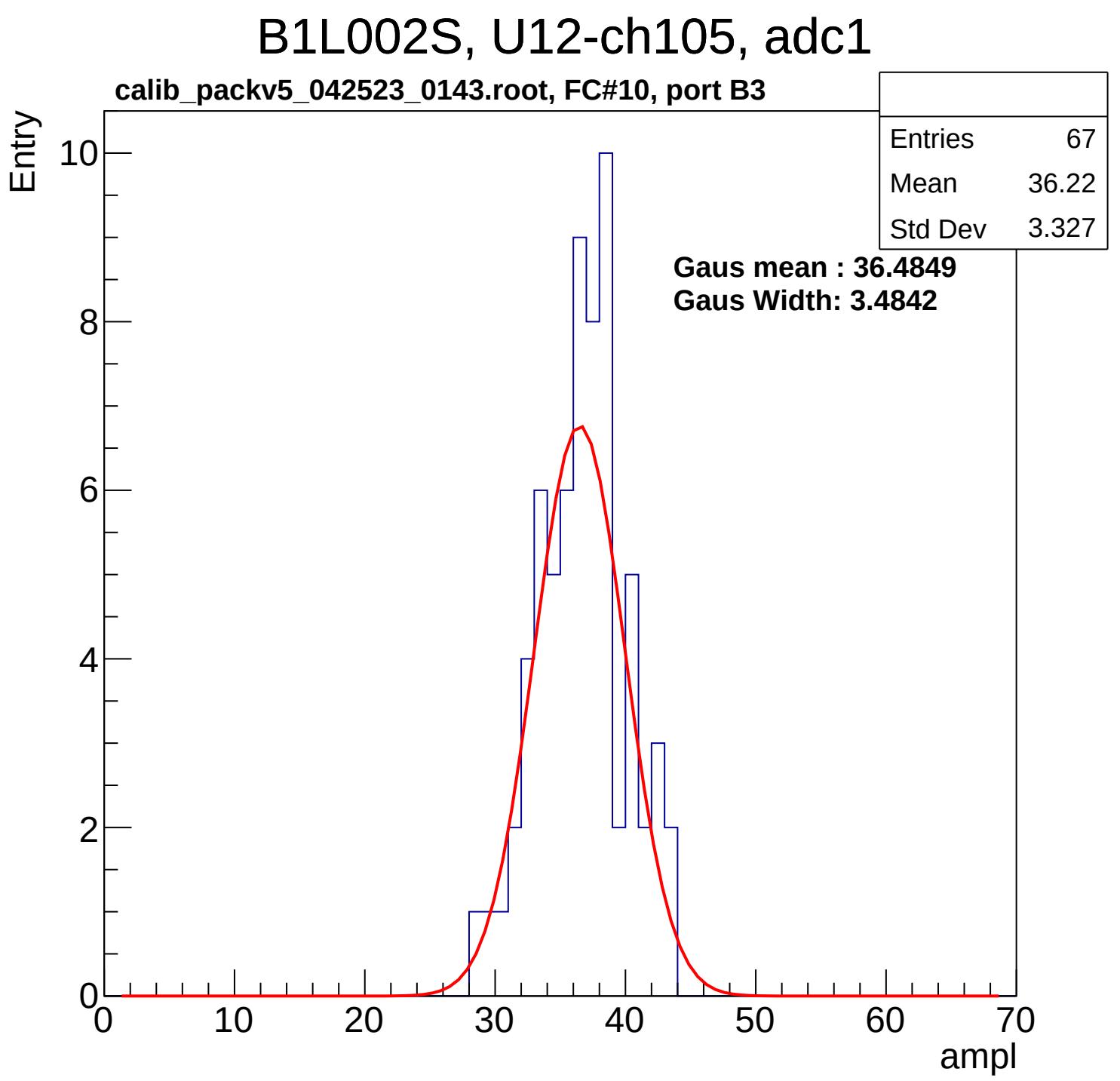
B1L002S, U12-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.22
Std Dev 3.327
Gaus mean : 36.4849
Gaus Width: 3.4842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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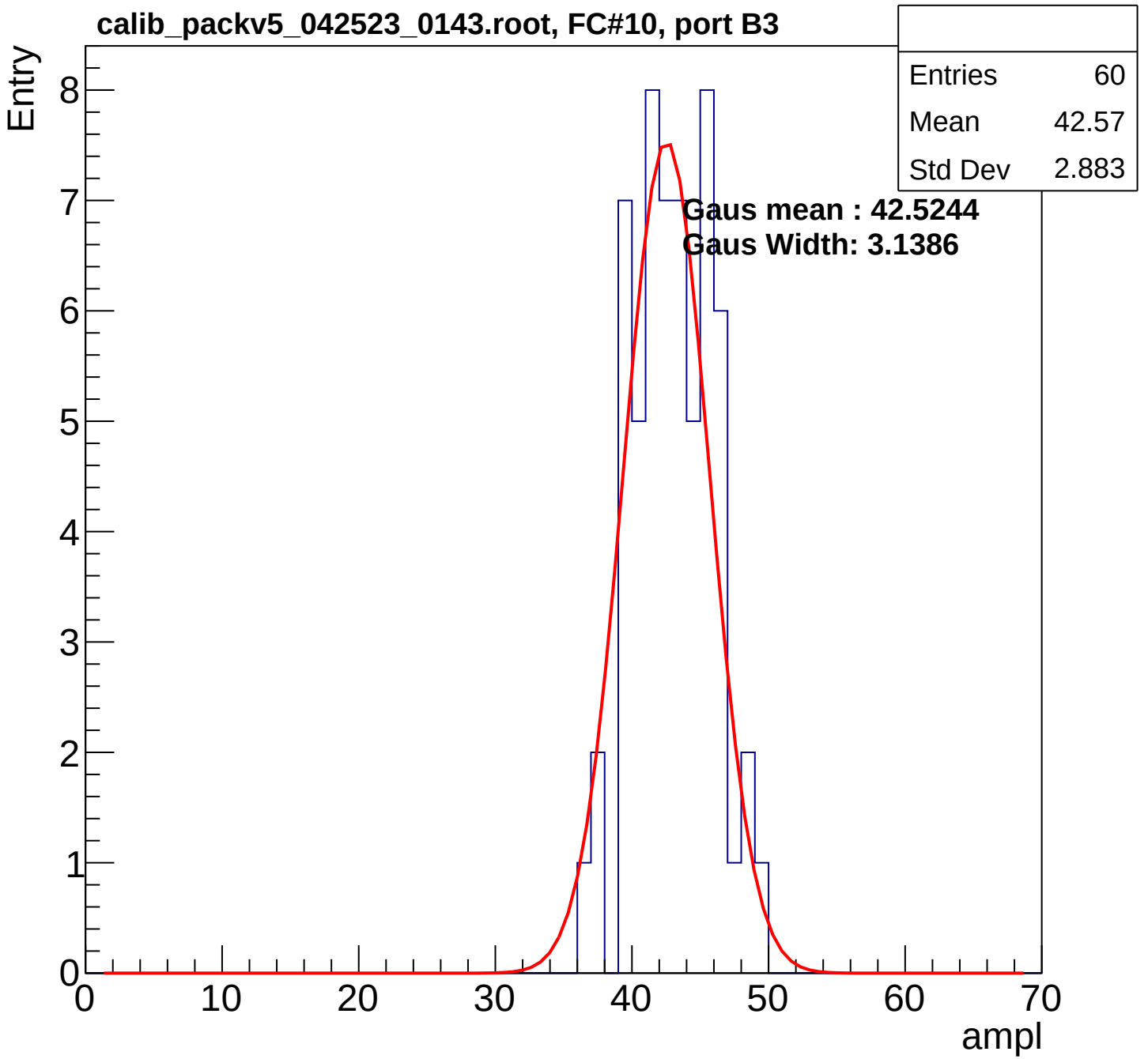
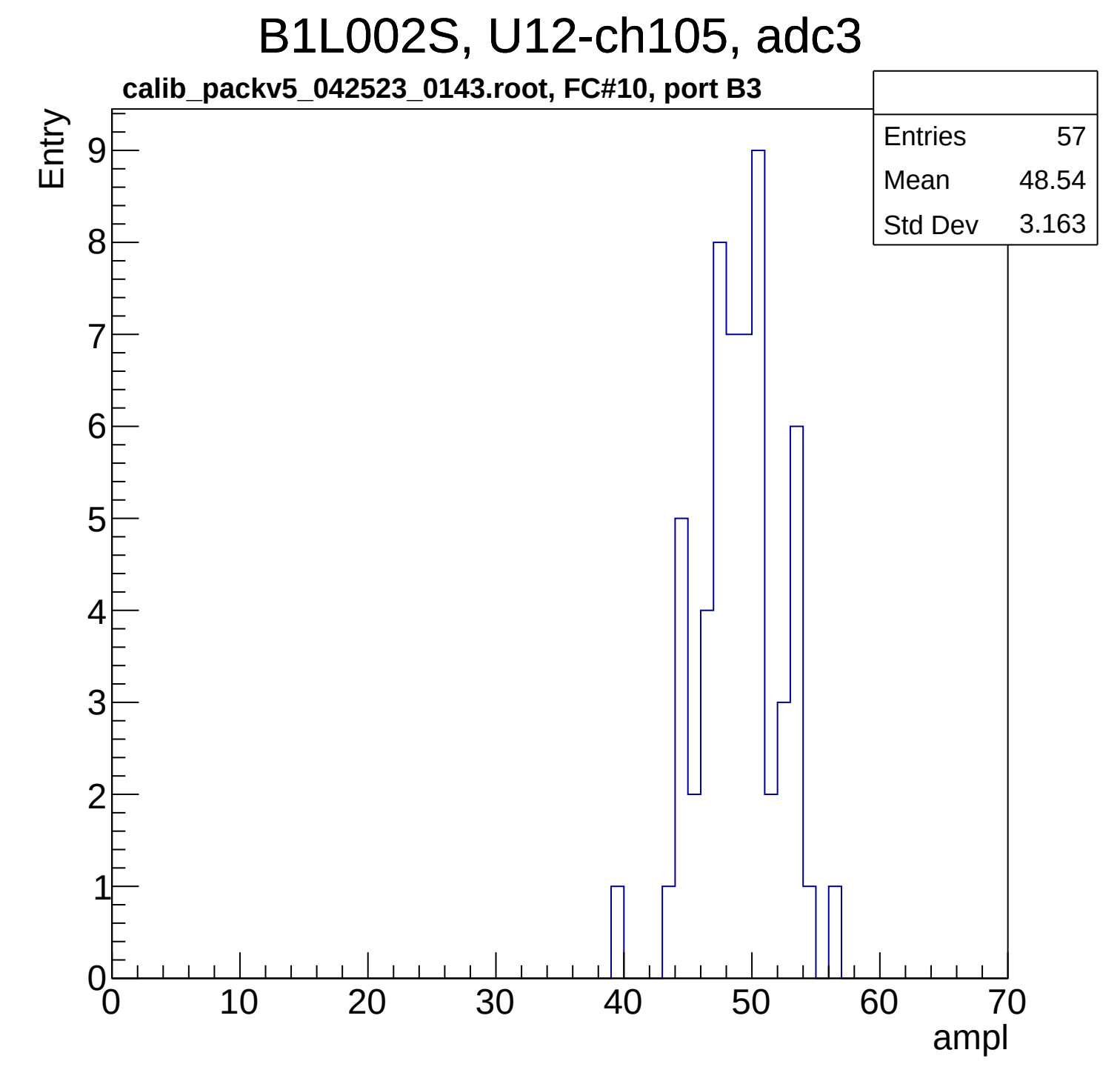

B1L002S, U12-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 42.57
Std Dev 2.883
Gaus mean : 42.5244
Gaus Width: 3.1386
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.54
Std Dev 3.163
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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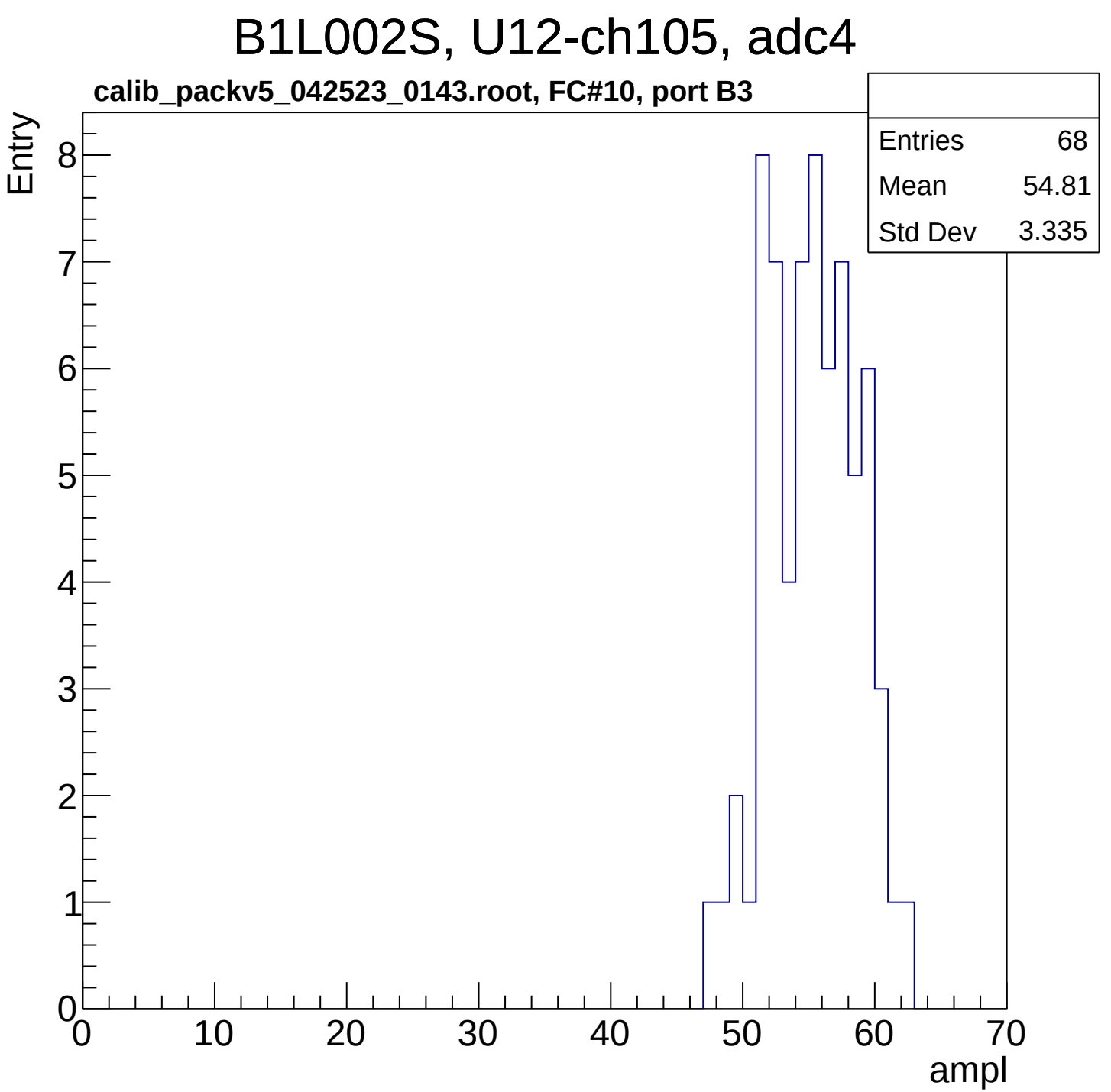
B1L002S, U12-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 54.81
Std Dev 3.335
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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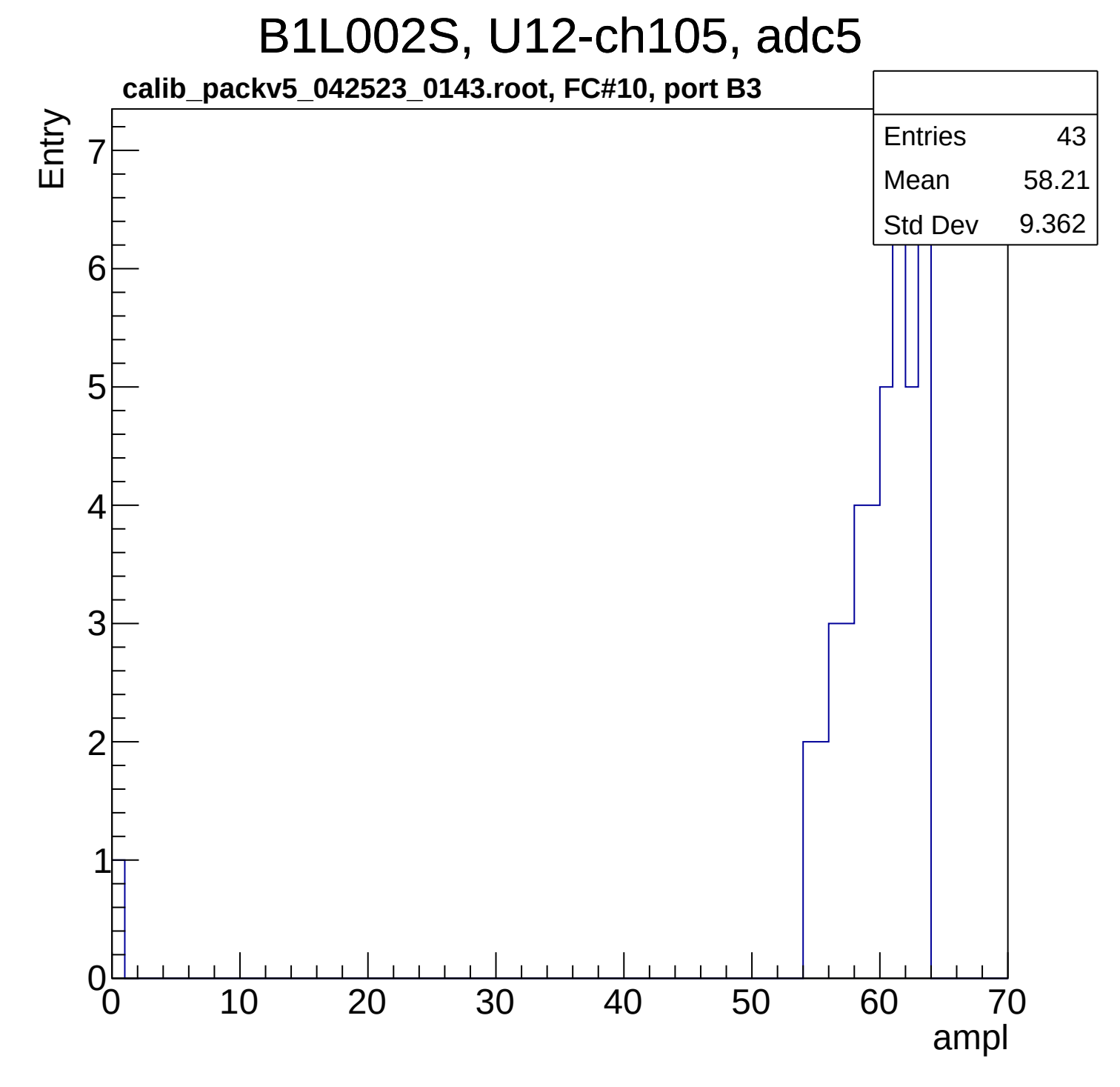
40
50
60
70


B1L002S, U12-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.21
Std Dev 9.362
Entry
ampl
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1
2
3
4
5
6
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0
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40
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60
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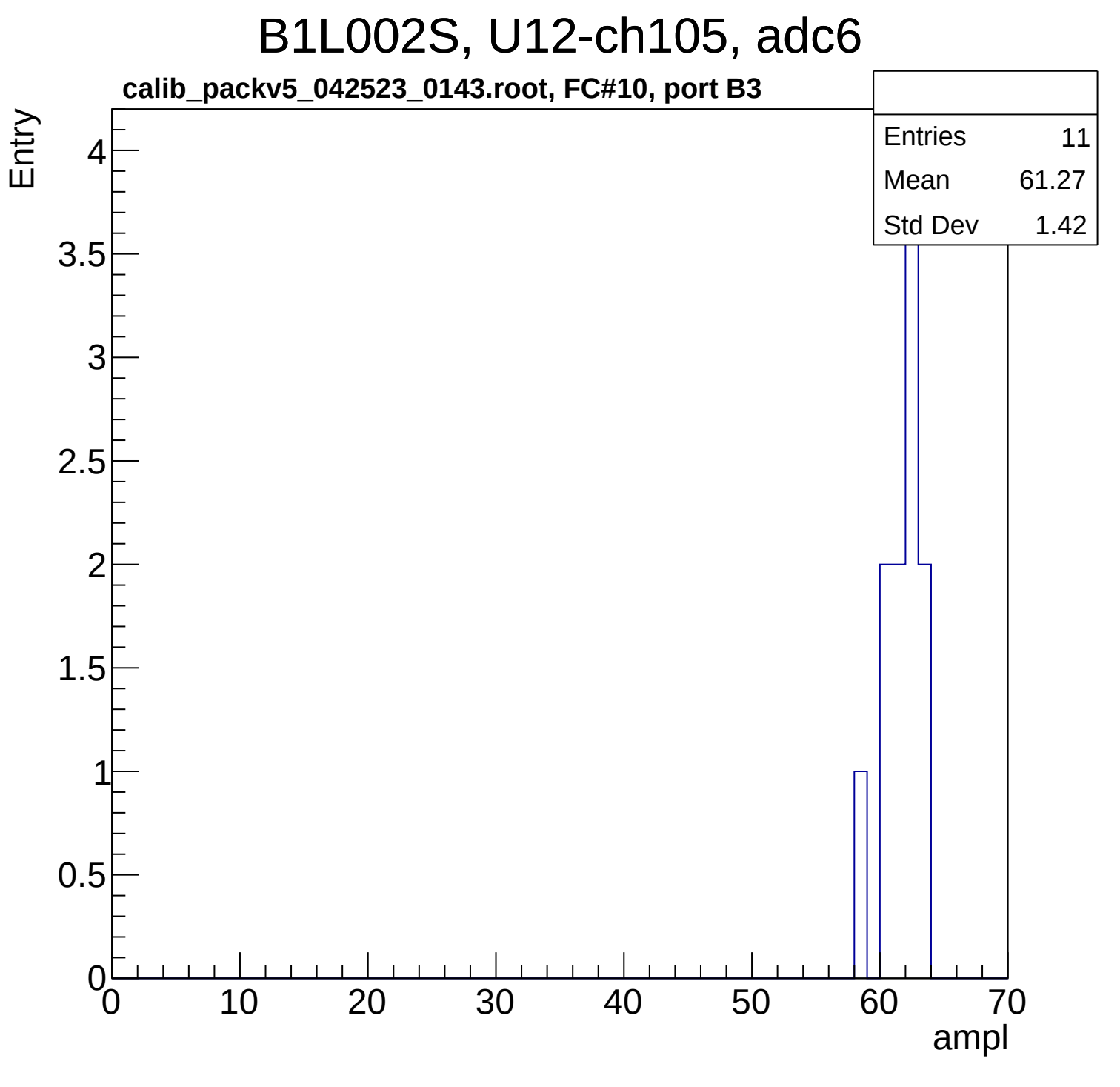
B1L002S, U12-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.27
Std Dev 1.42
Entry
ampl
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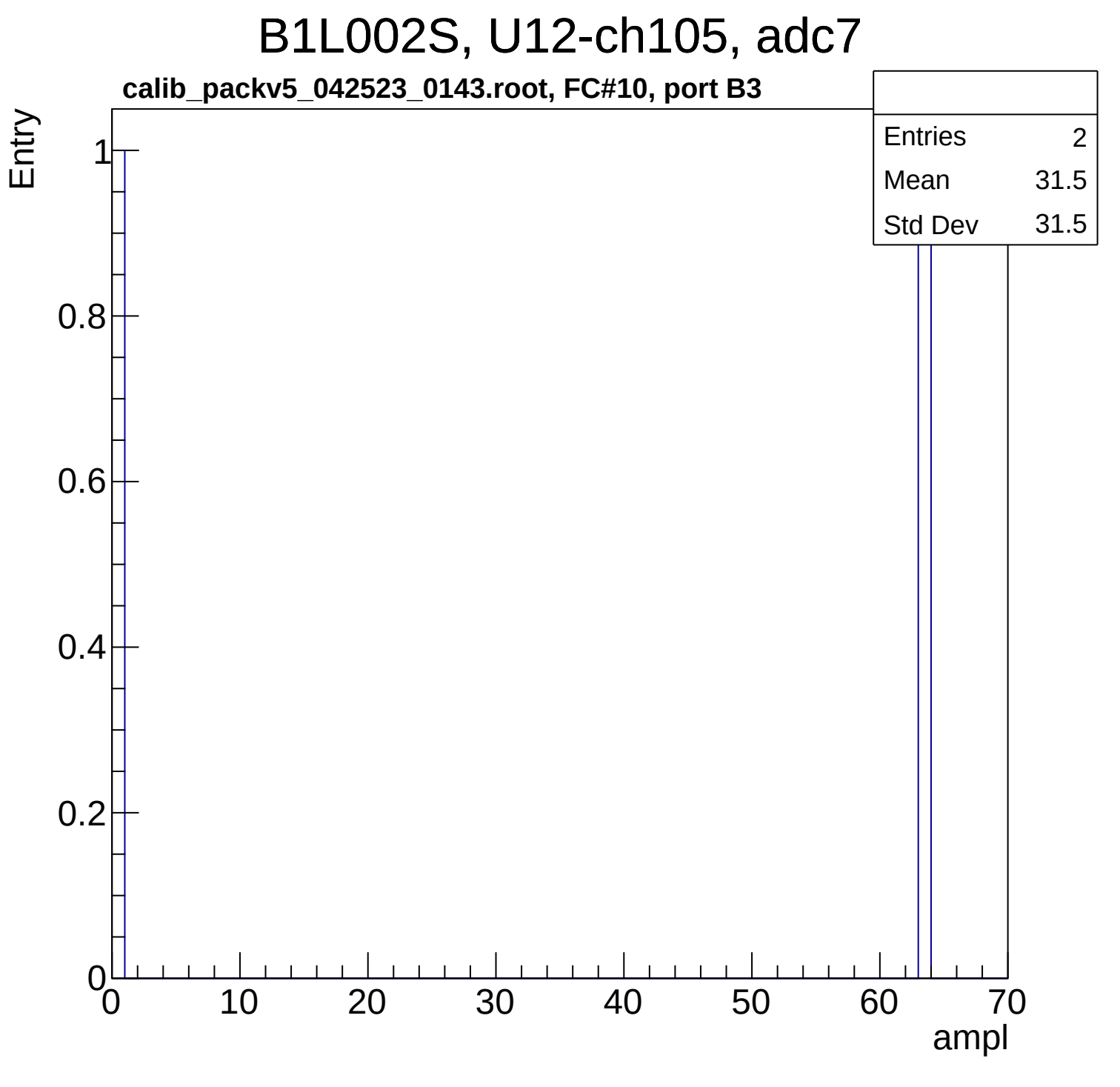
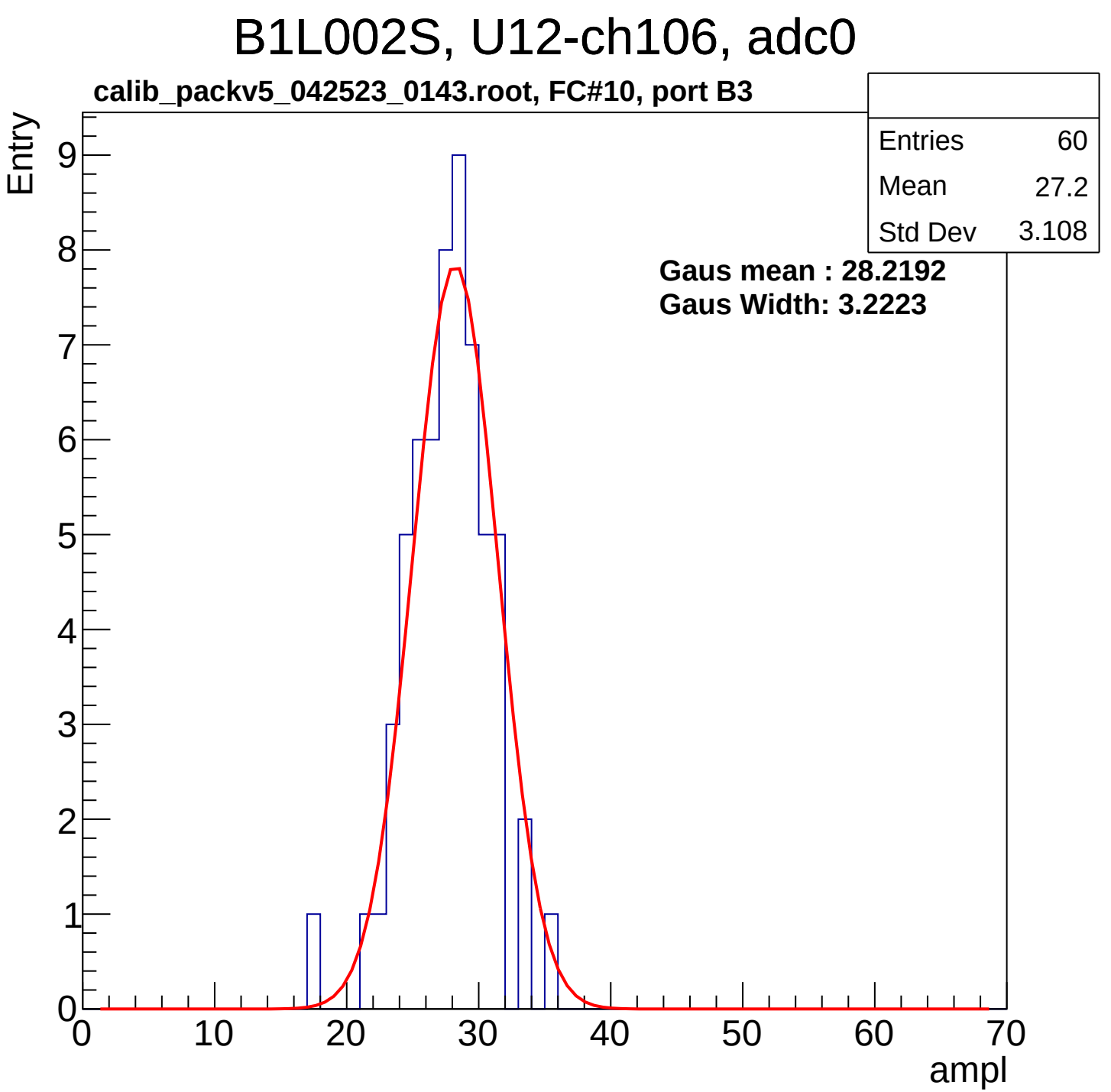
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.2
Std Dev 3.108
Gaus mean : 28.2192
Gaus Width: 3.2223
Entry
ampl
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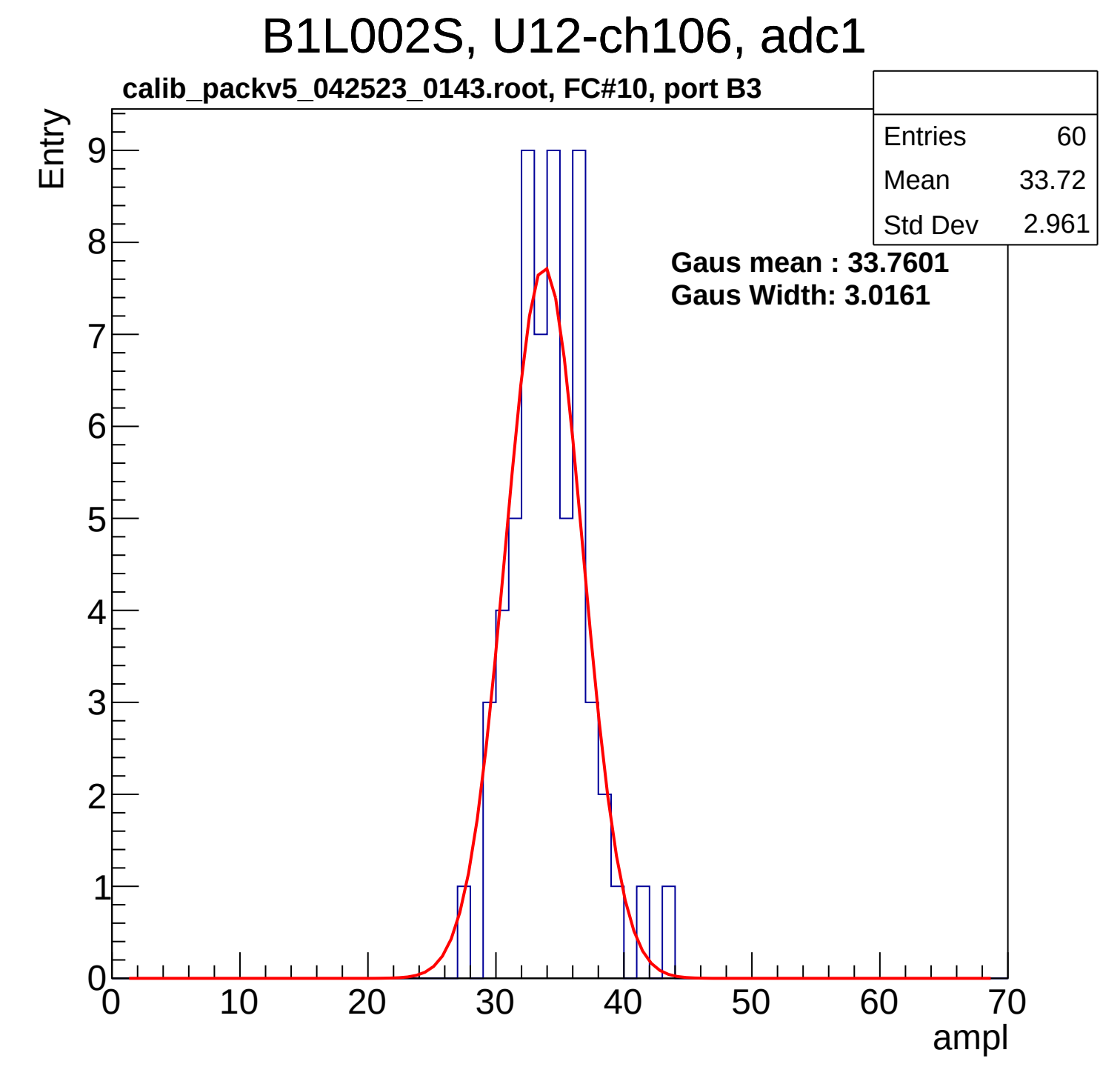
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 33.72
Std Dev 2.961
Gaus mean : 33.7601
Gaus Width: 3.0161
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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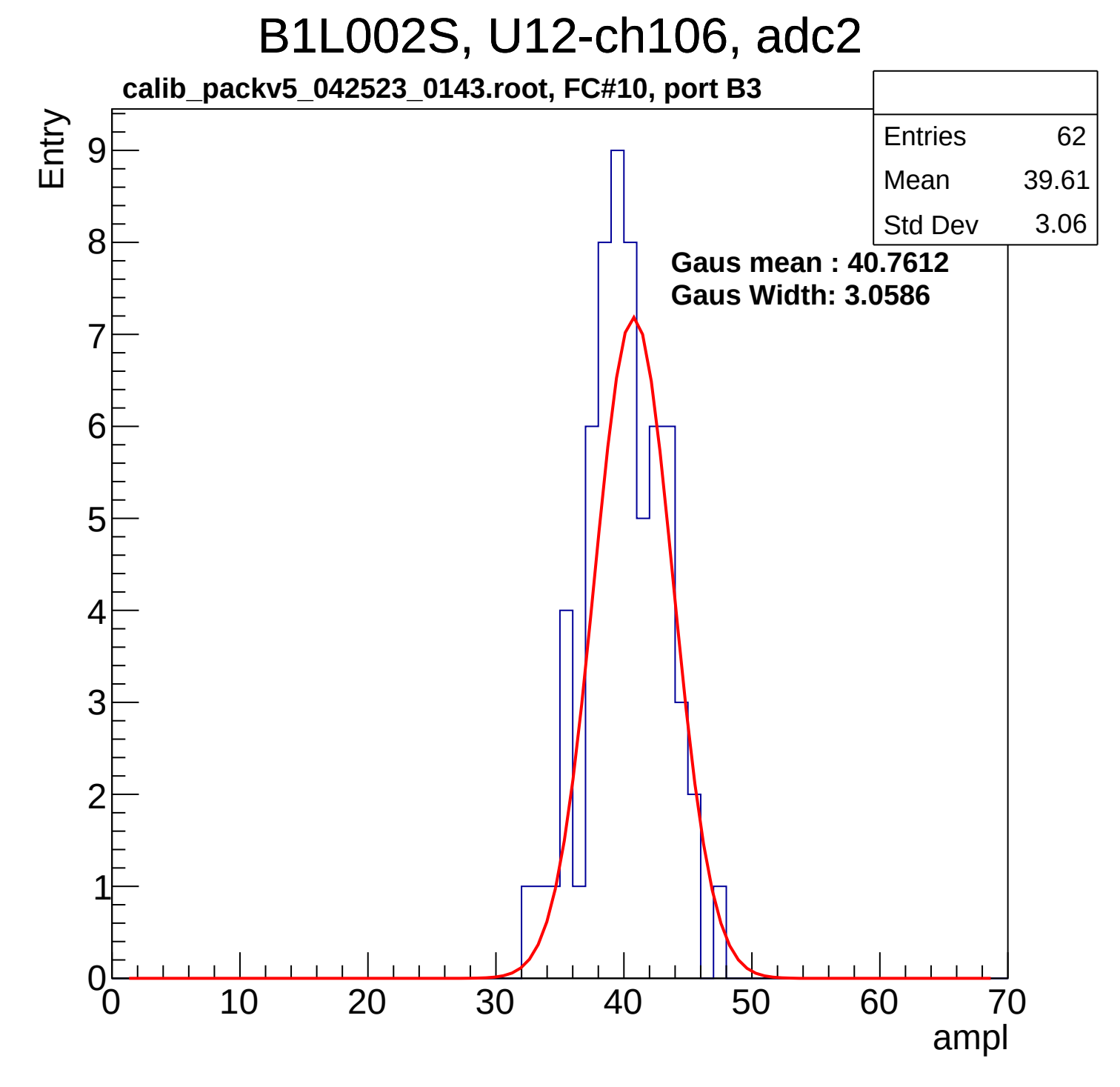
B1L002S, U12-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 39.61
Std Dev 3.06
Gaus mean : 40.7612
Gaus Width: 3.0586
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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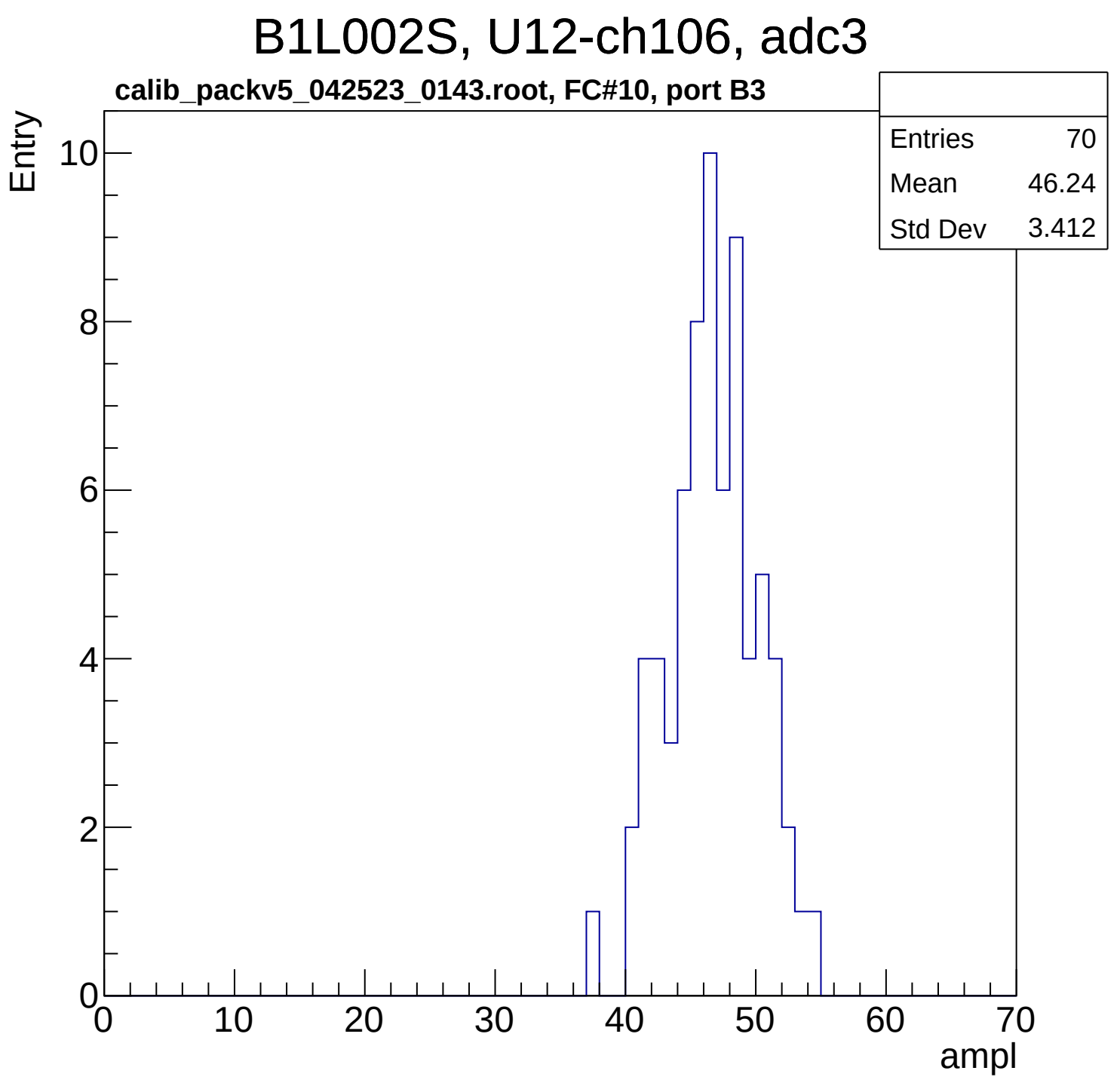
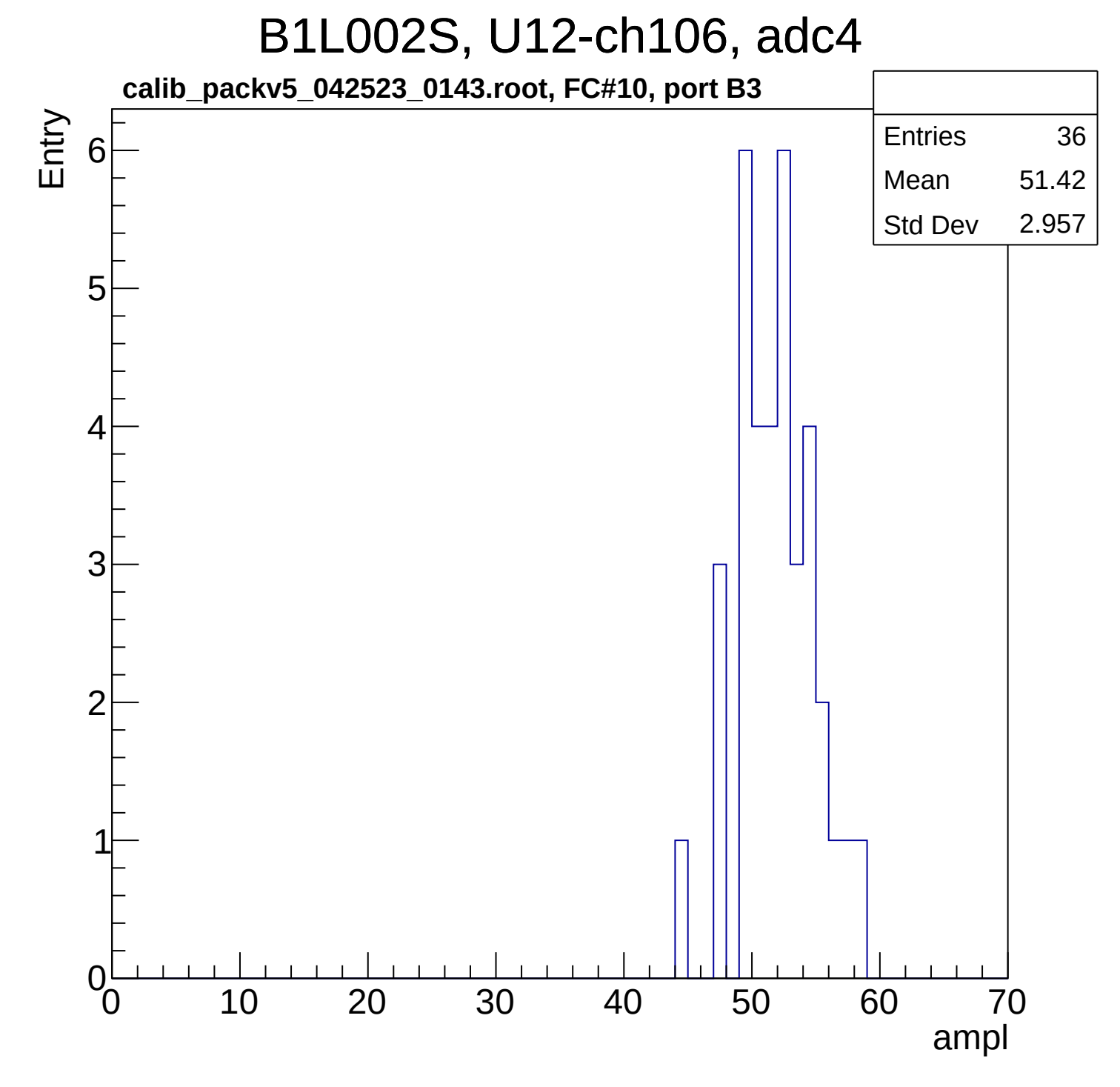
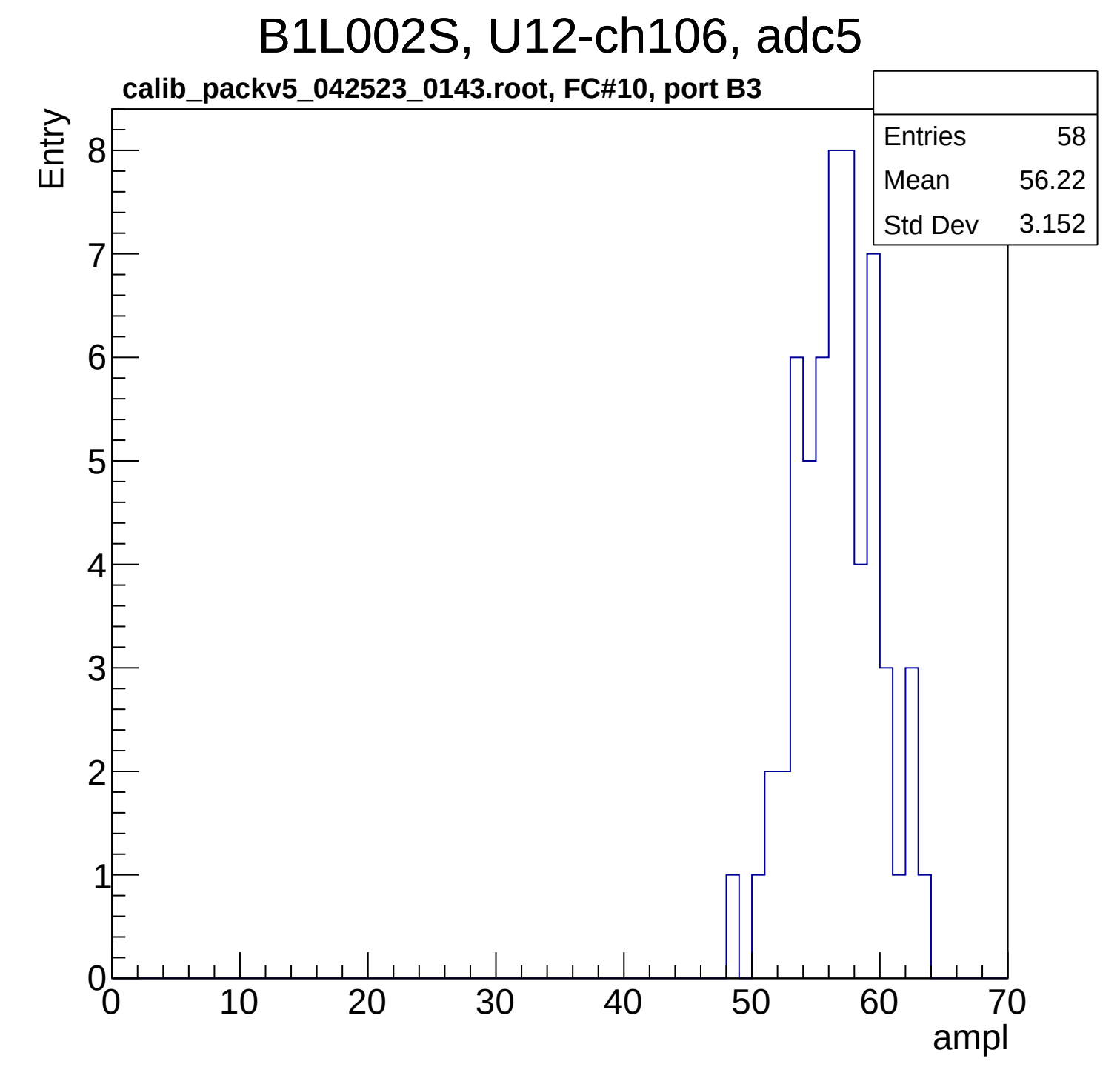
60
70


B1L002S, U12-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.24
Std Dev 3.412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 51.42
Std Dev 2.957
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 56.22
Std Dev 3.152
Entry
ampl
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1
2
3
4
5
6
7
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0
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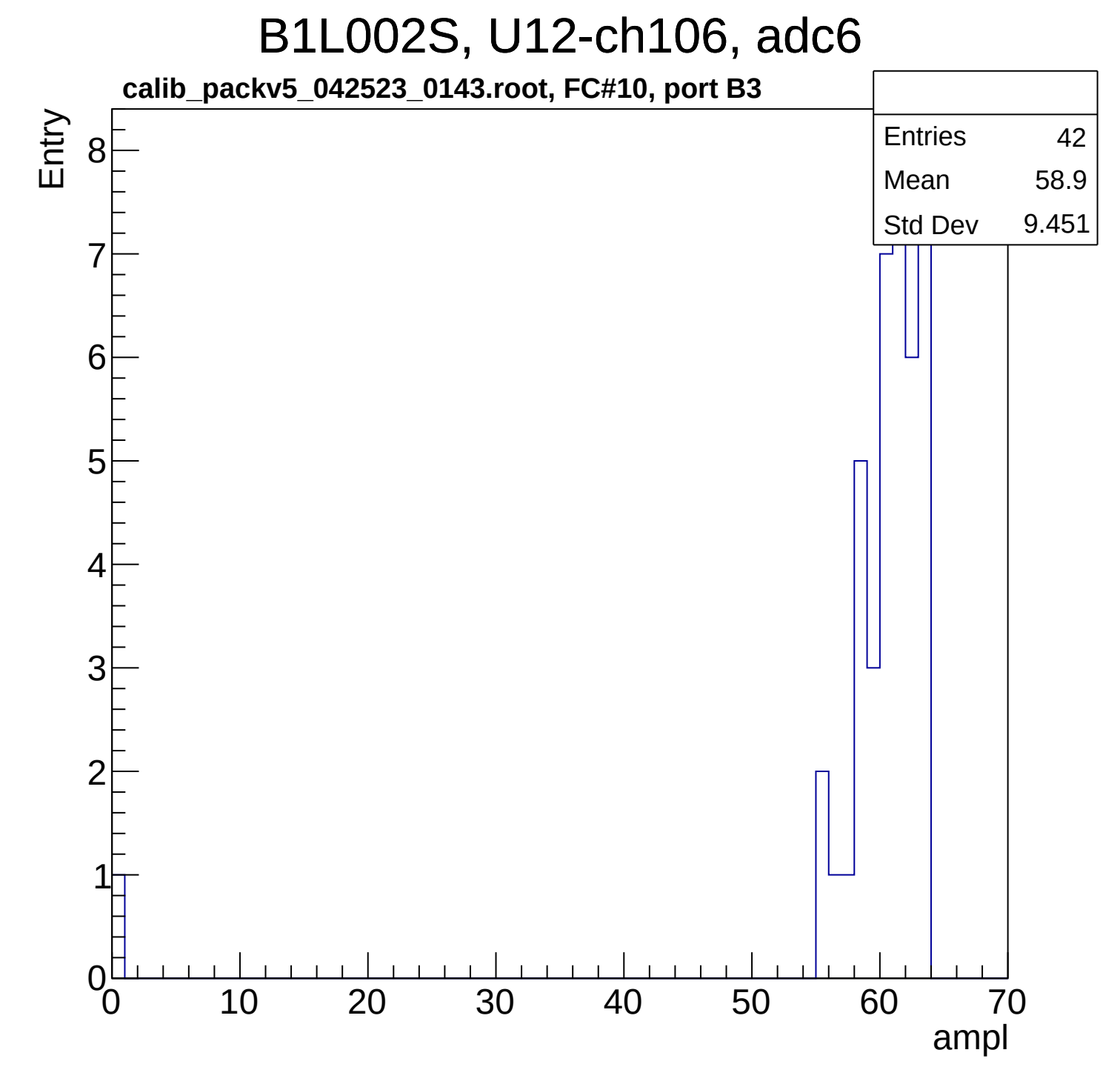
60
70


B1L002S, U12-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.9
Std Dev 9.451
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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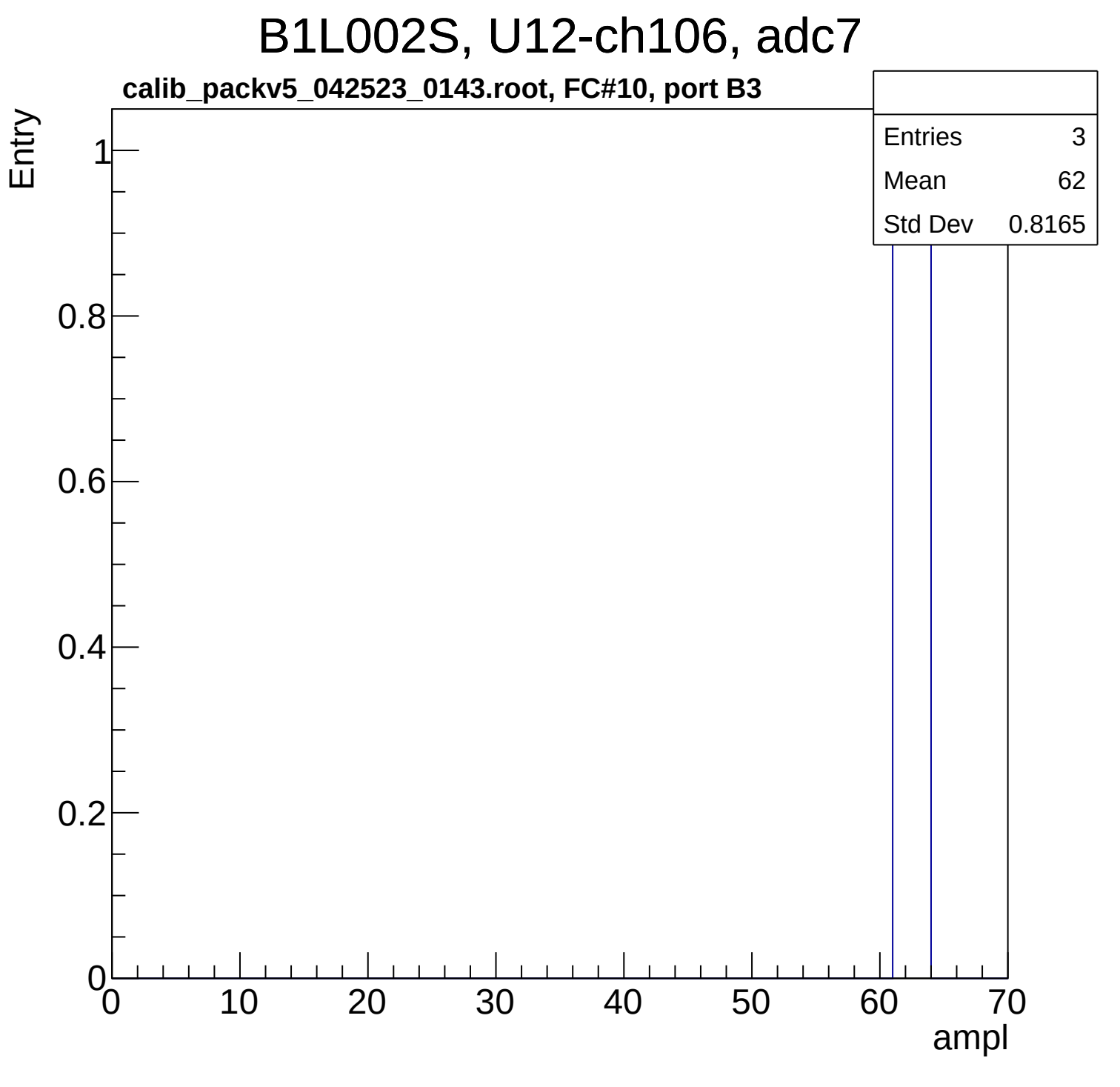
60
70


B1L002S, U12-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
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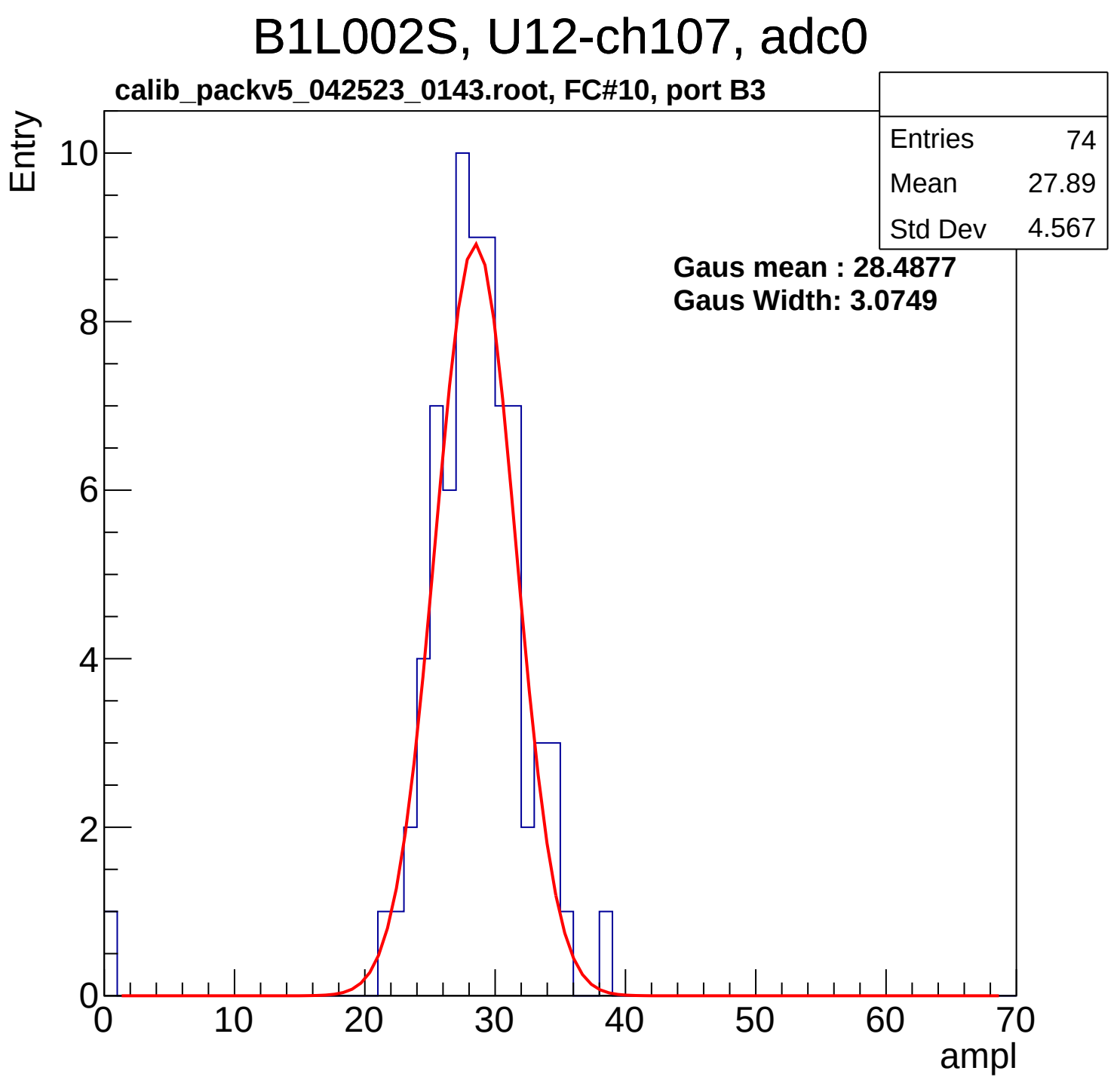
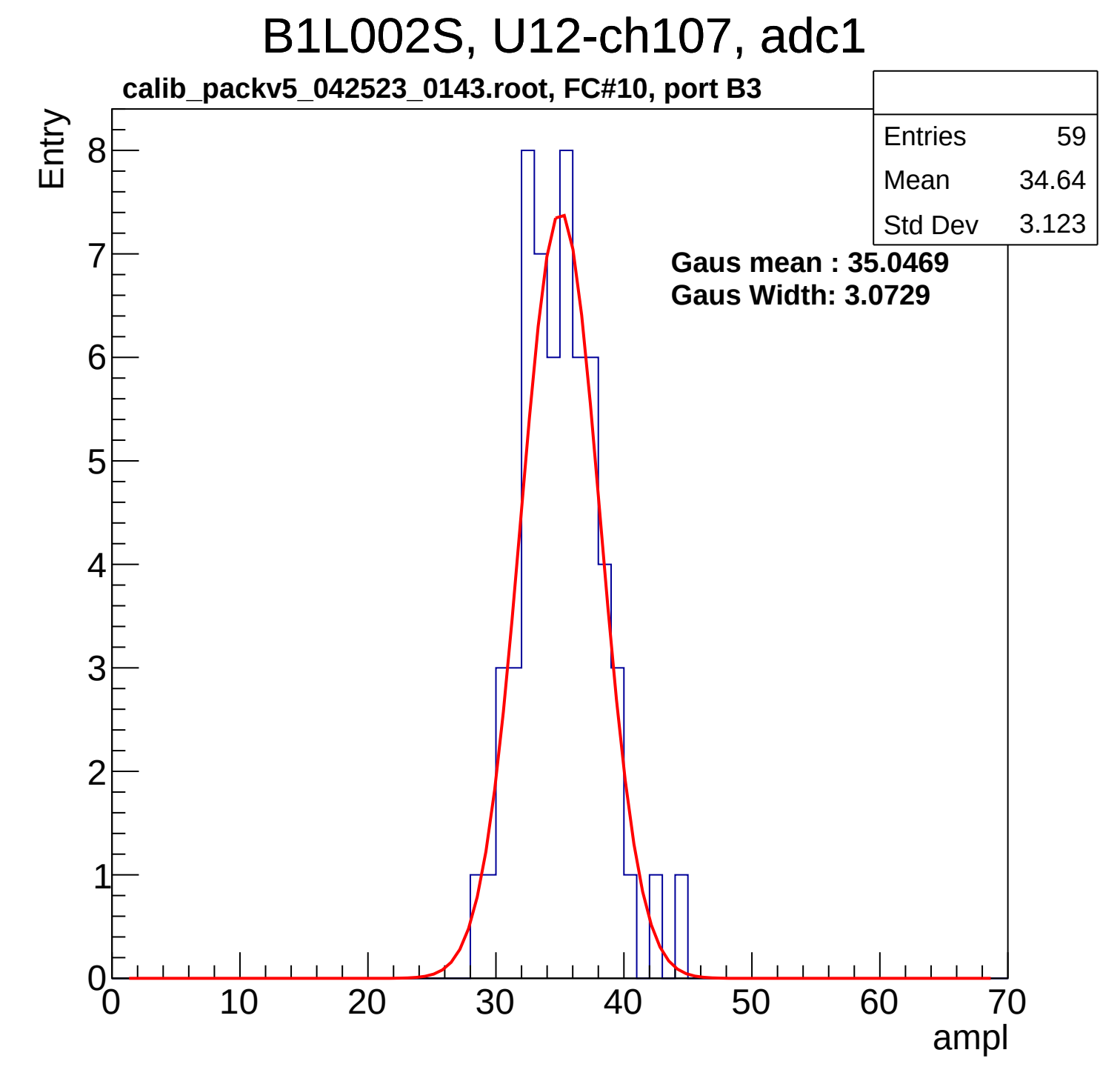
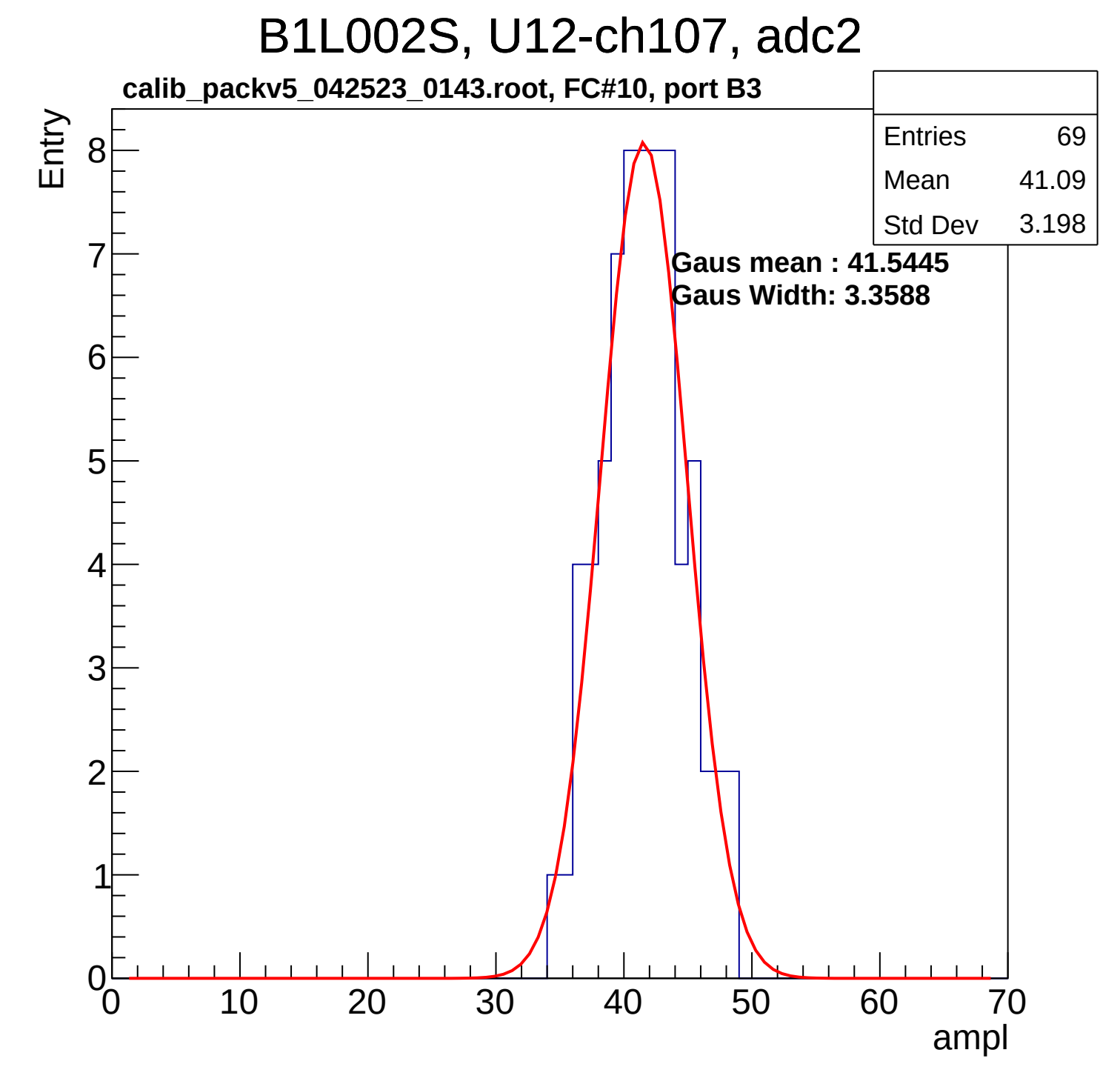
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.89
Std Dev 4.567
Gaus mean : 28.4877
Gaus Width: 3.0749
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.64
Std Dev 3.123
Gaus mean : 35.0469
Gaus Width: 3.0729
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.09
Std Dev 3.198
Gaus mean : 41.5445
Gaus Width: 3.3588
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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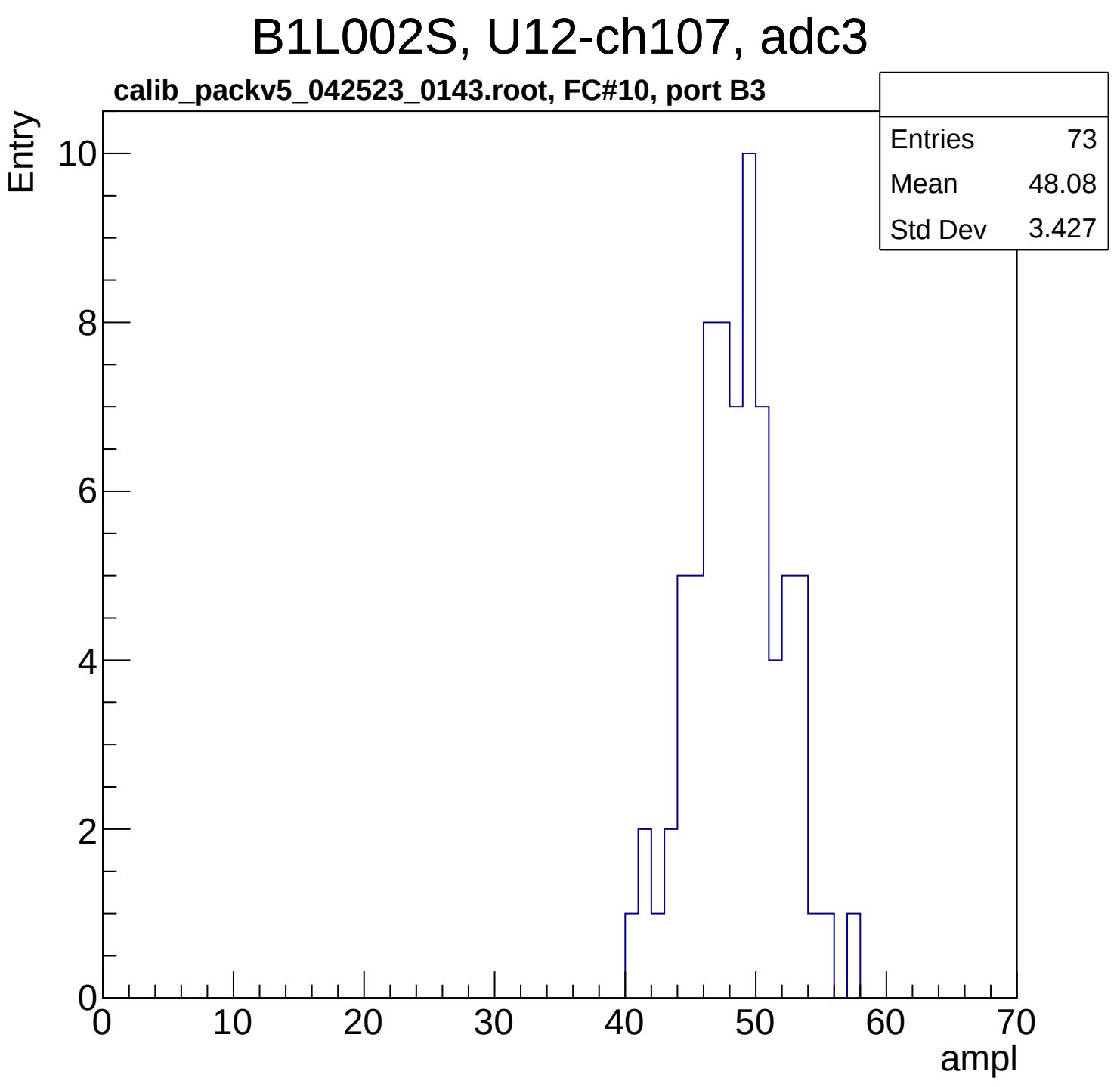
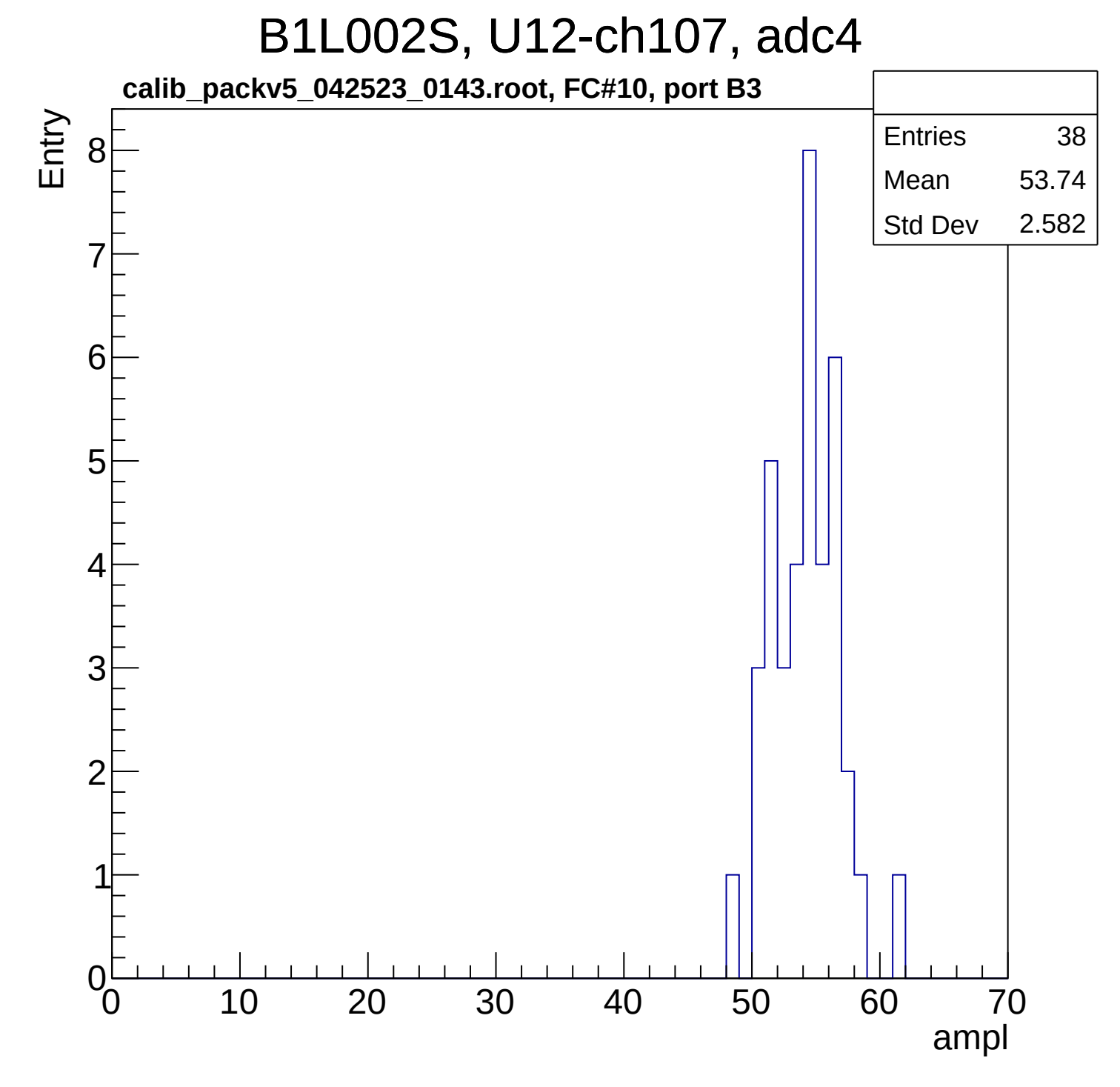

B1L002S, U12-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 48.08
Std Dev 3.427
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 53.74
Std Dev 2.582
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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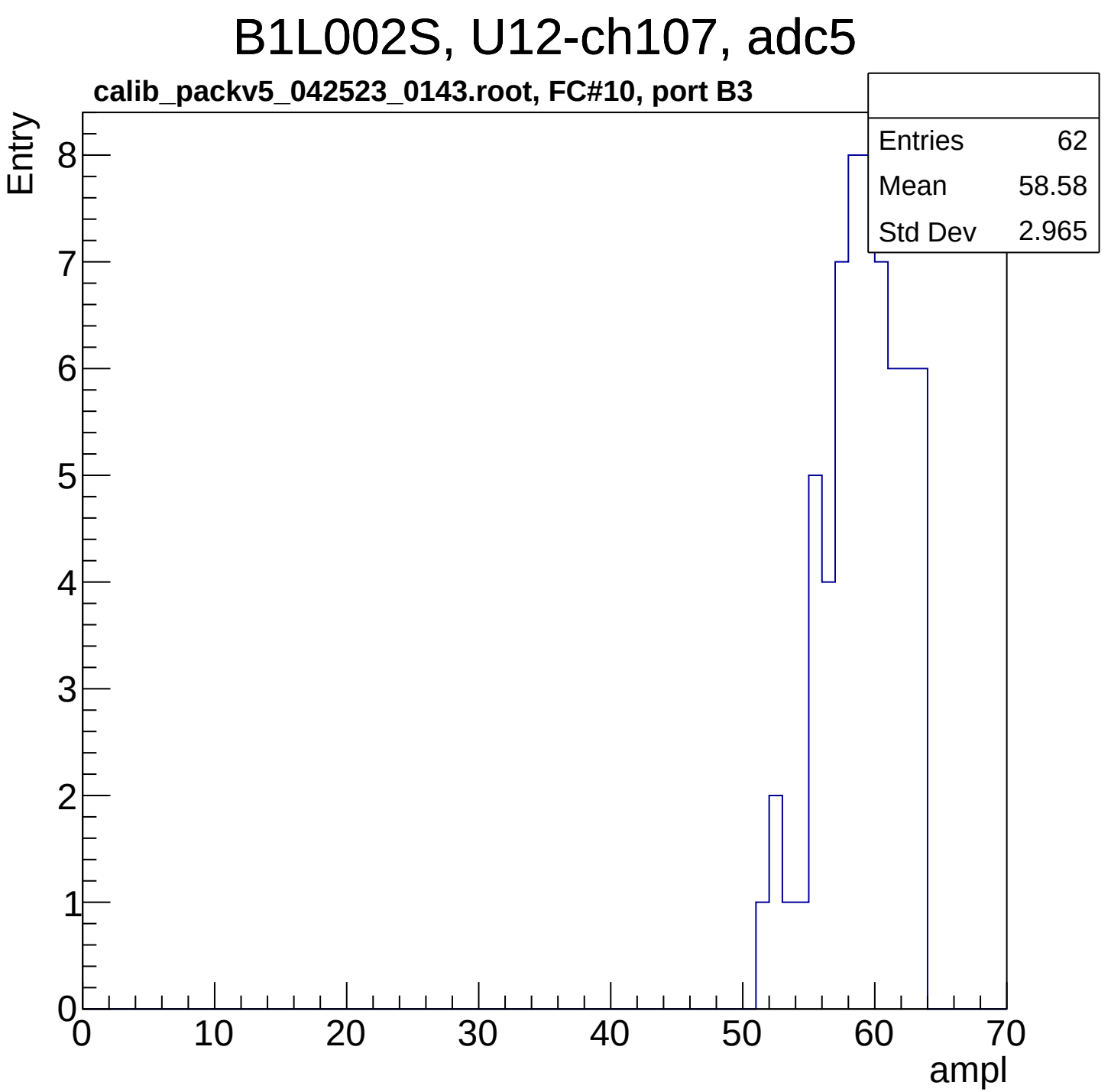
B1L002S, U12-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.58
Std Dev 2.965
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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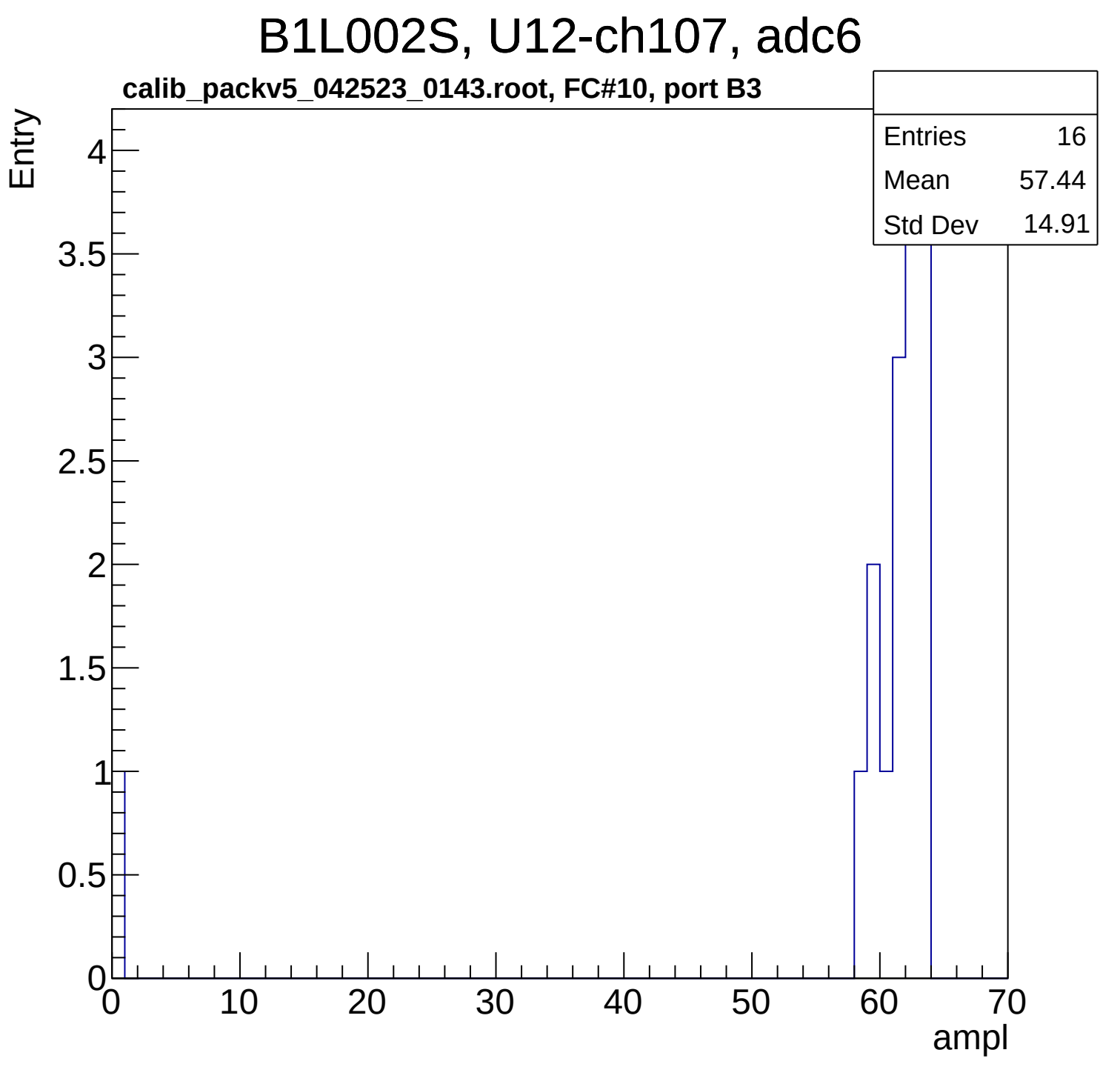
B1L002S, U12-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.44
Std Dev 14.91
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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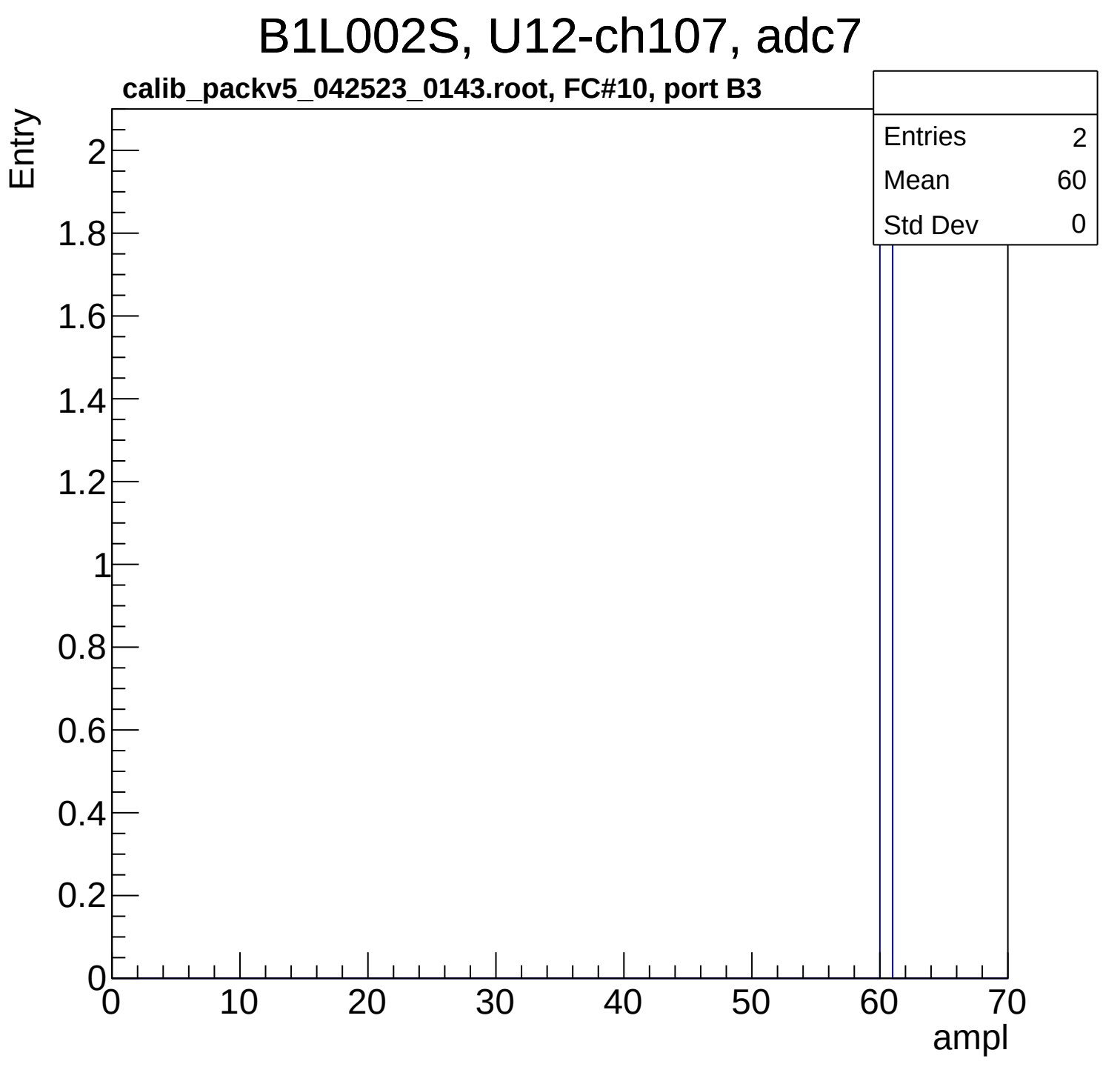
B1L002S, U12-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 60
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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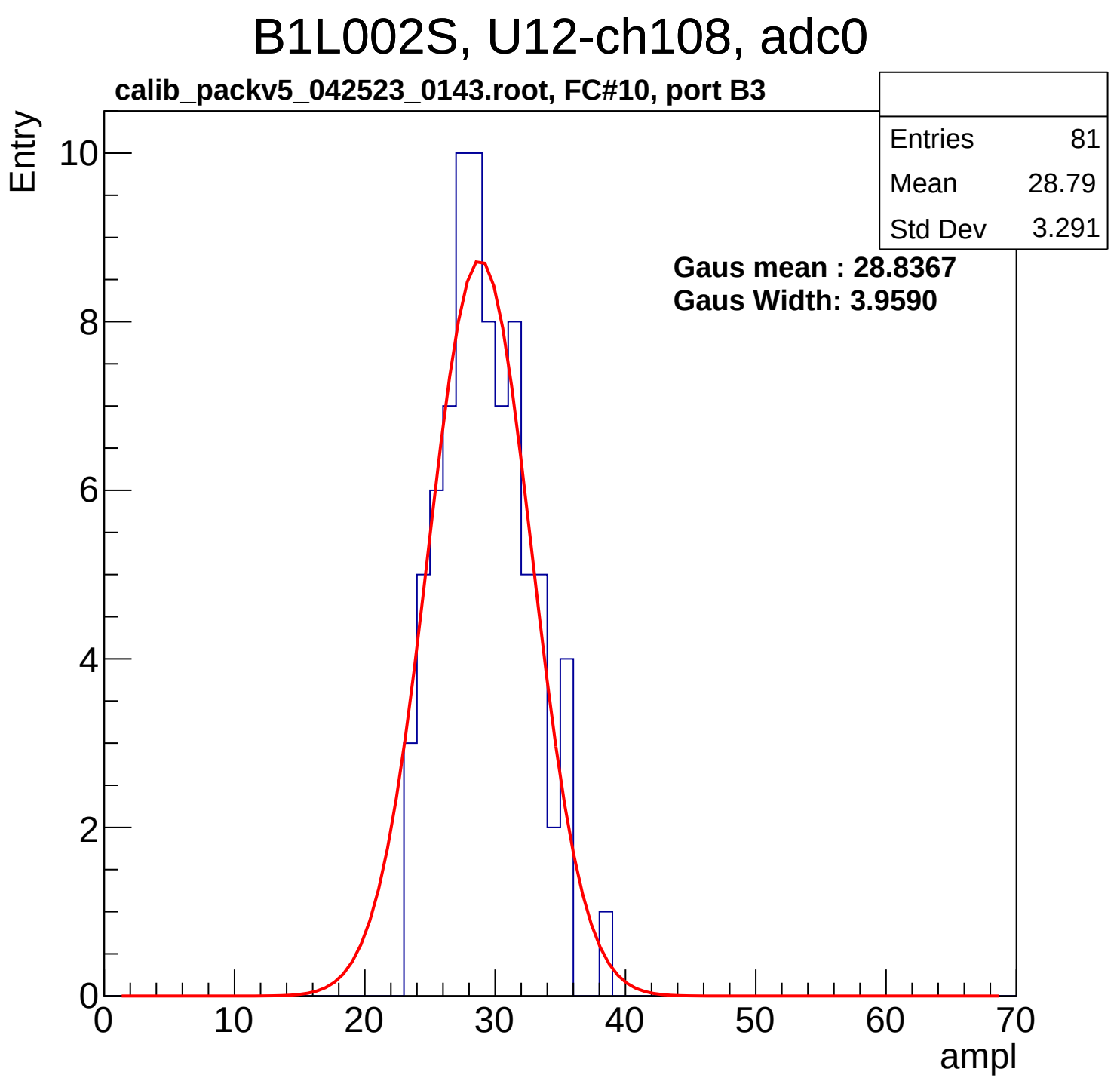
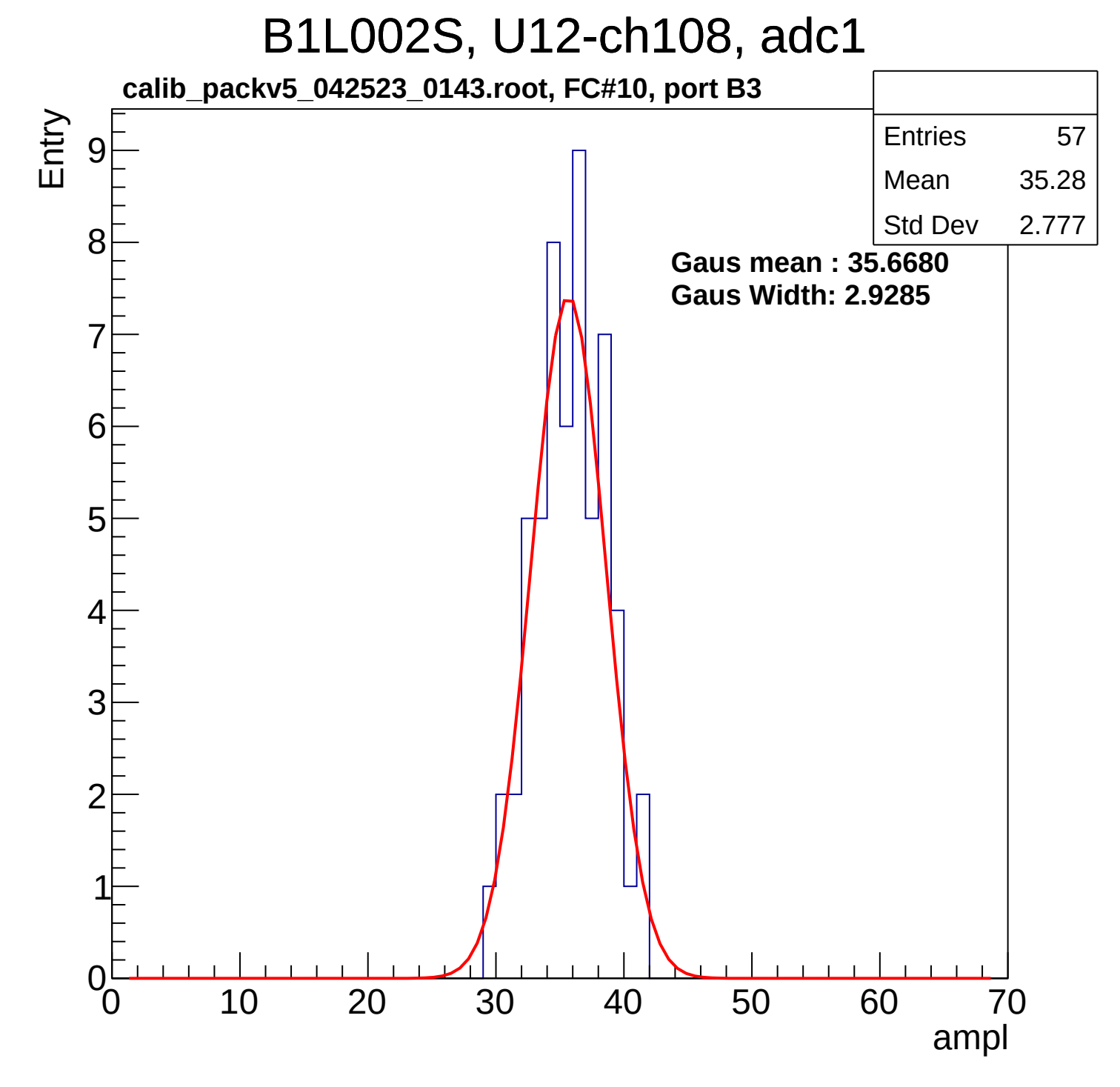

B1L002S, U12-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 28.79
Std Dev 3.291
Gaus mean : 28.8367
Gaus Width: 3.9590
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.28
Std Dev 2.777
Gaus mean : 35.6680
Gaus Width: 2.9285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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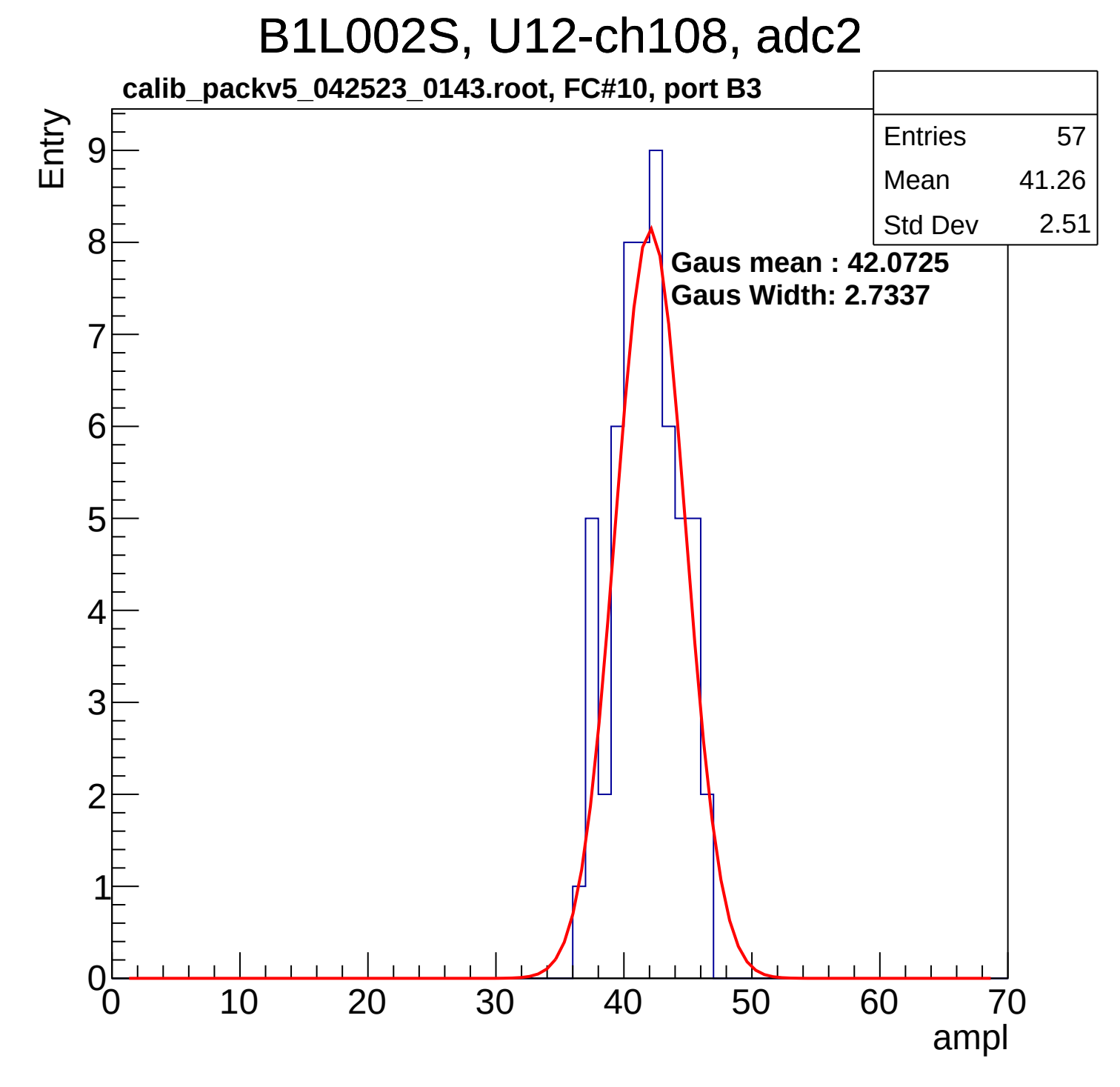
B1L002S, U12-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.26
Std Dev 2.51
Gaus mean : 42.0725
Gaus Width: 2.7337
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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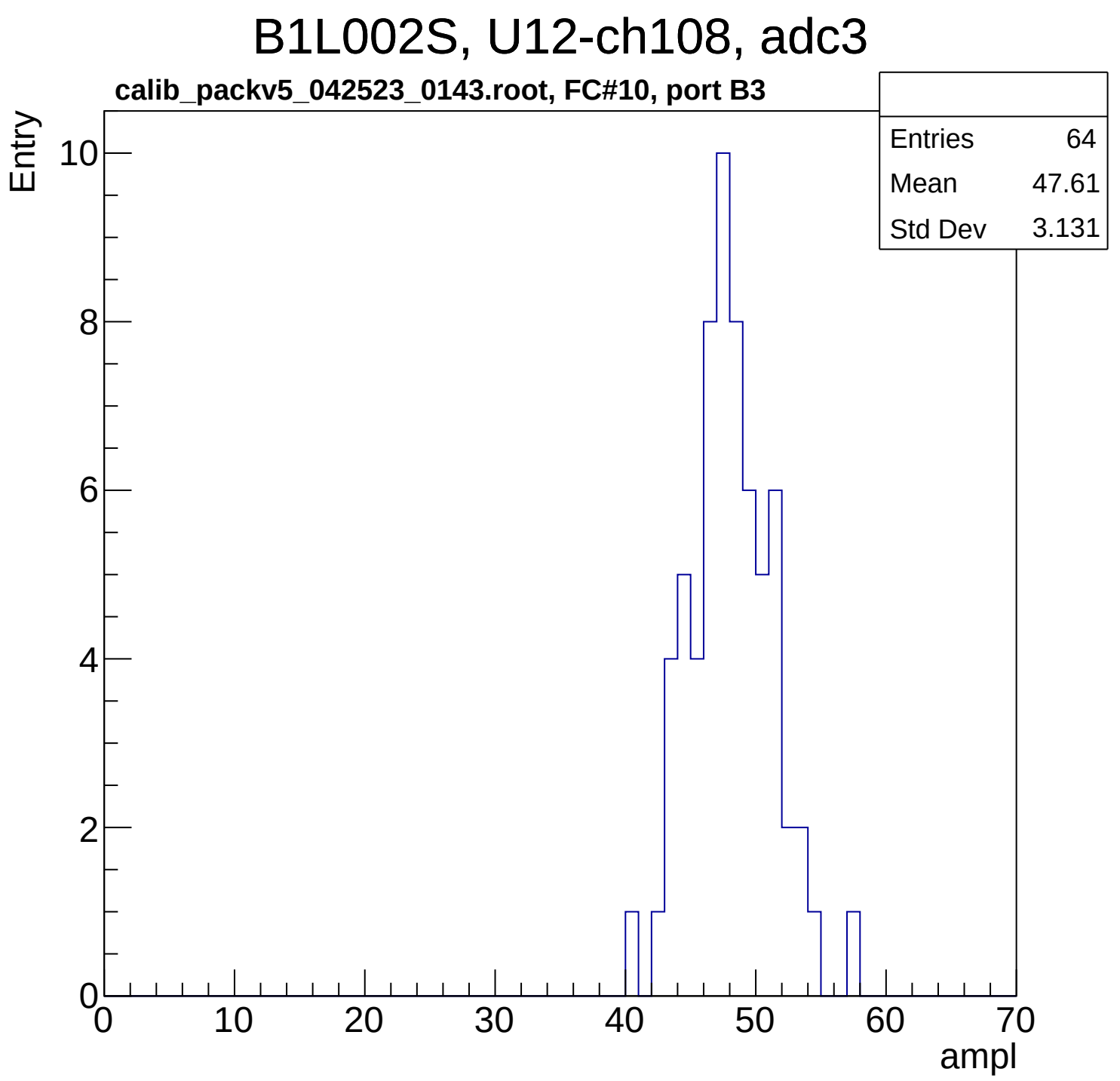
B1L002S, U12-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.61
Std Dev 3.131
Entry
ampl
0
2
4
6
8
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0
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40
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60
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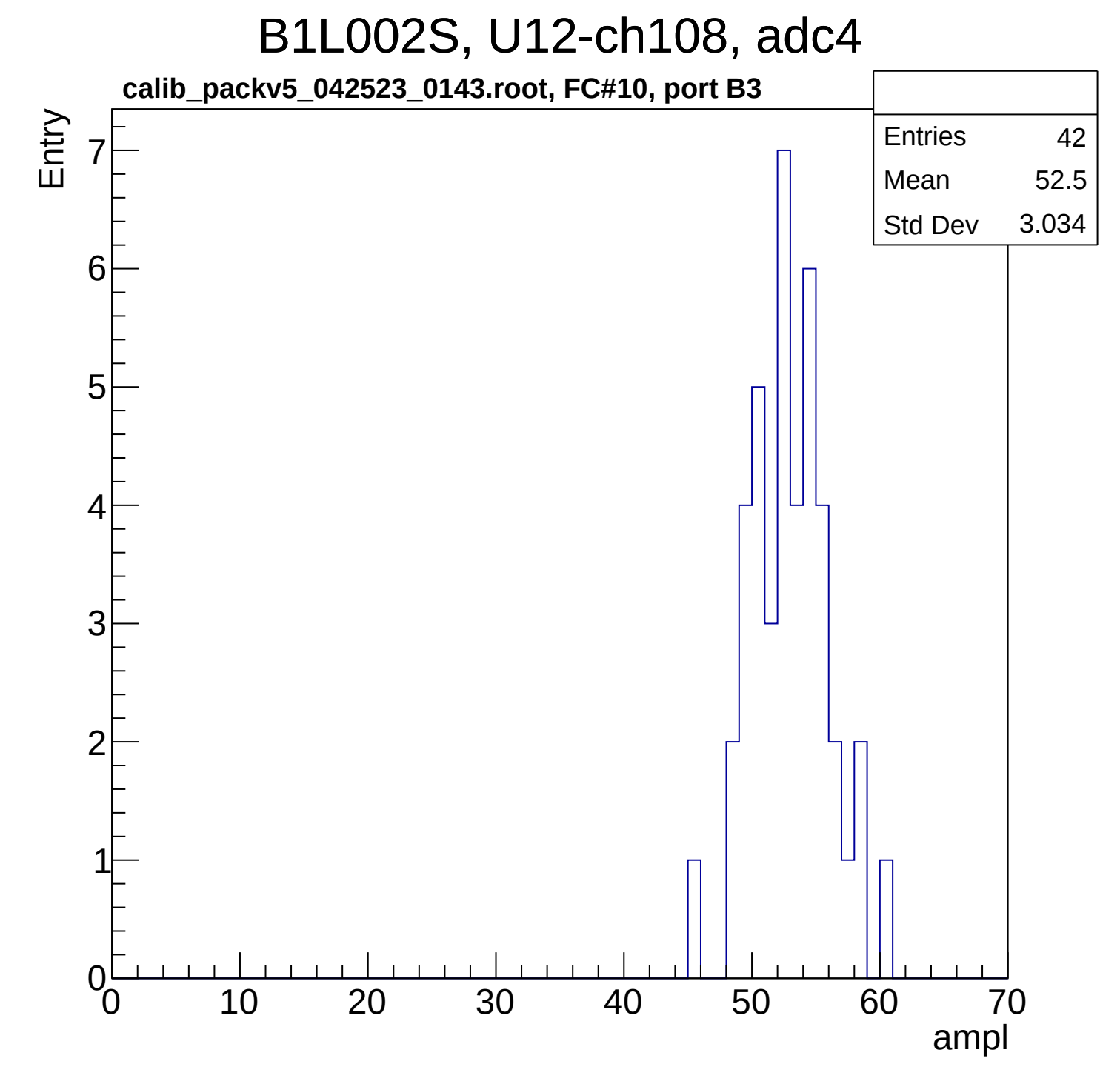
B1L002S, U12-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 52.5
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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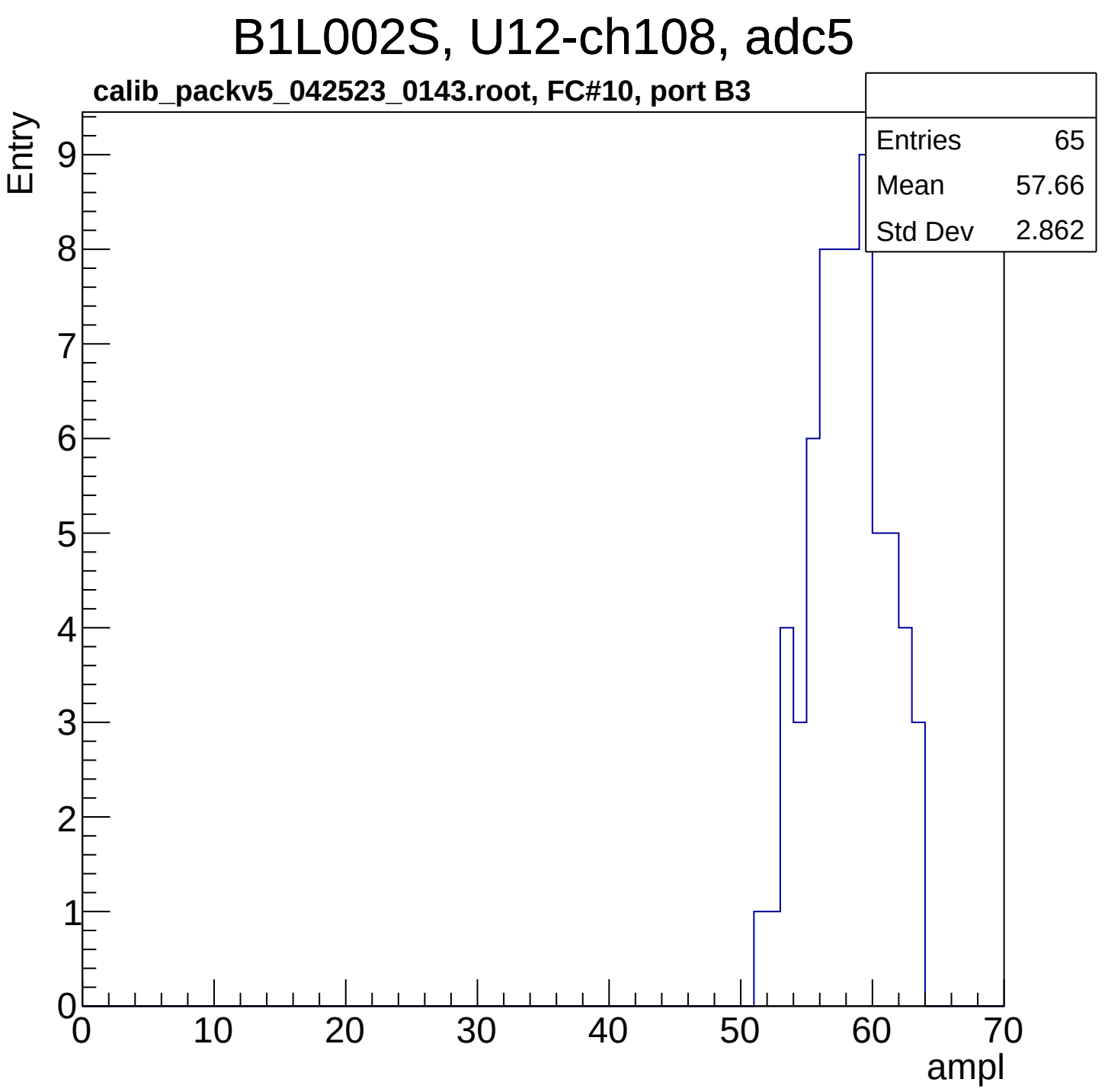
40
50
60
70


B1L002S, U12-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.66
Std Dev 2.862
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
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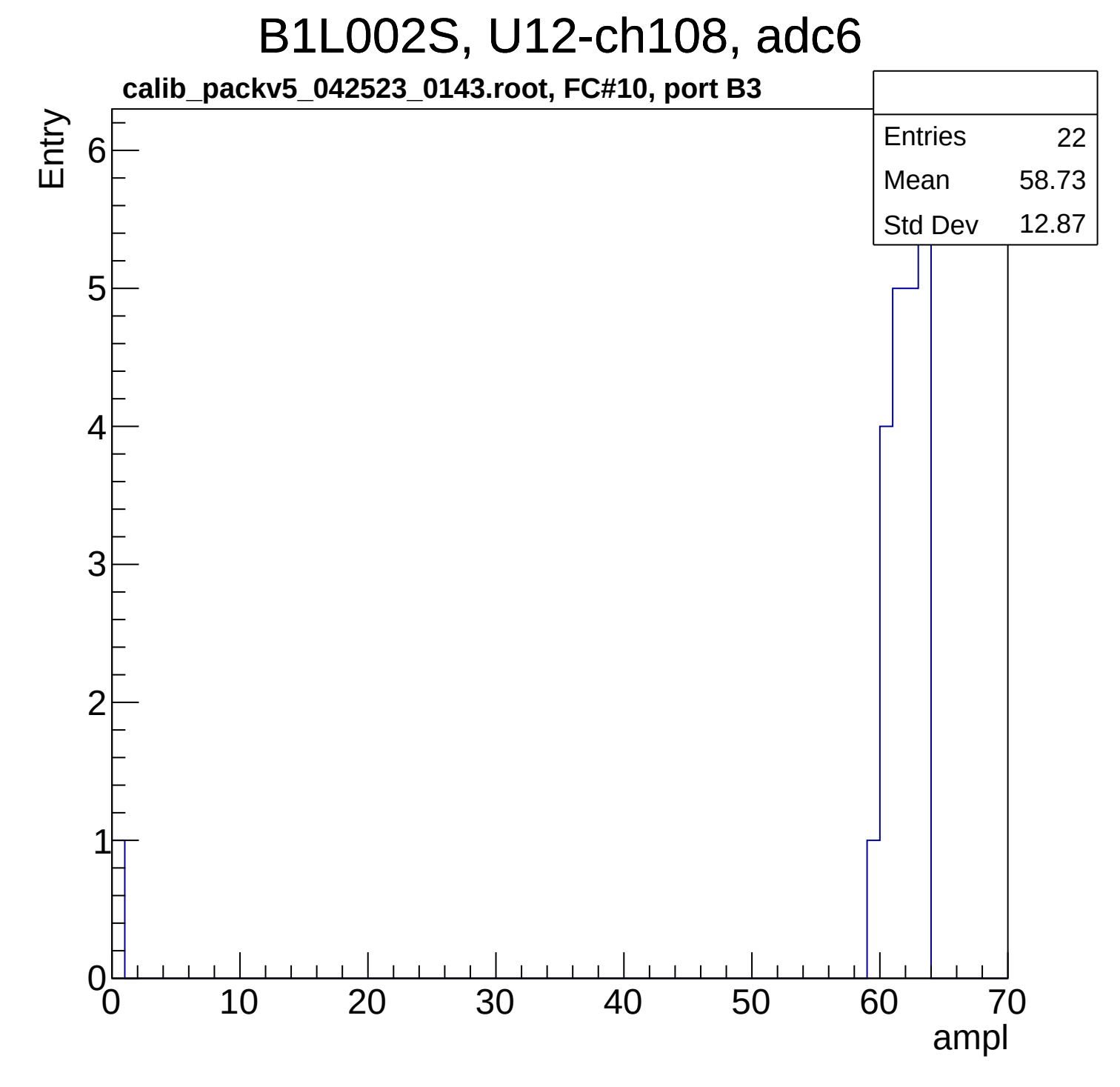
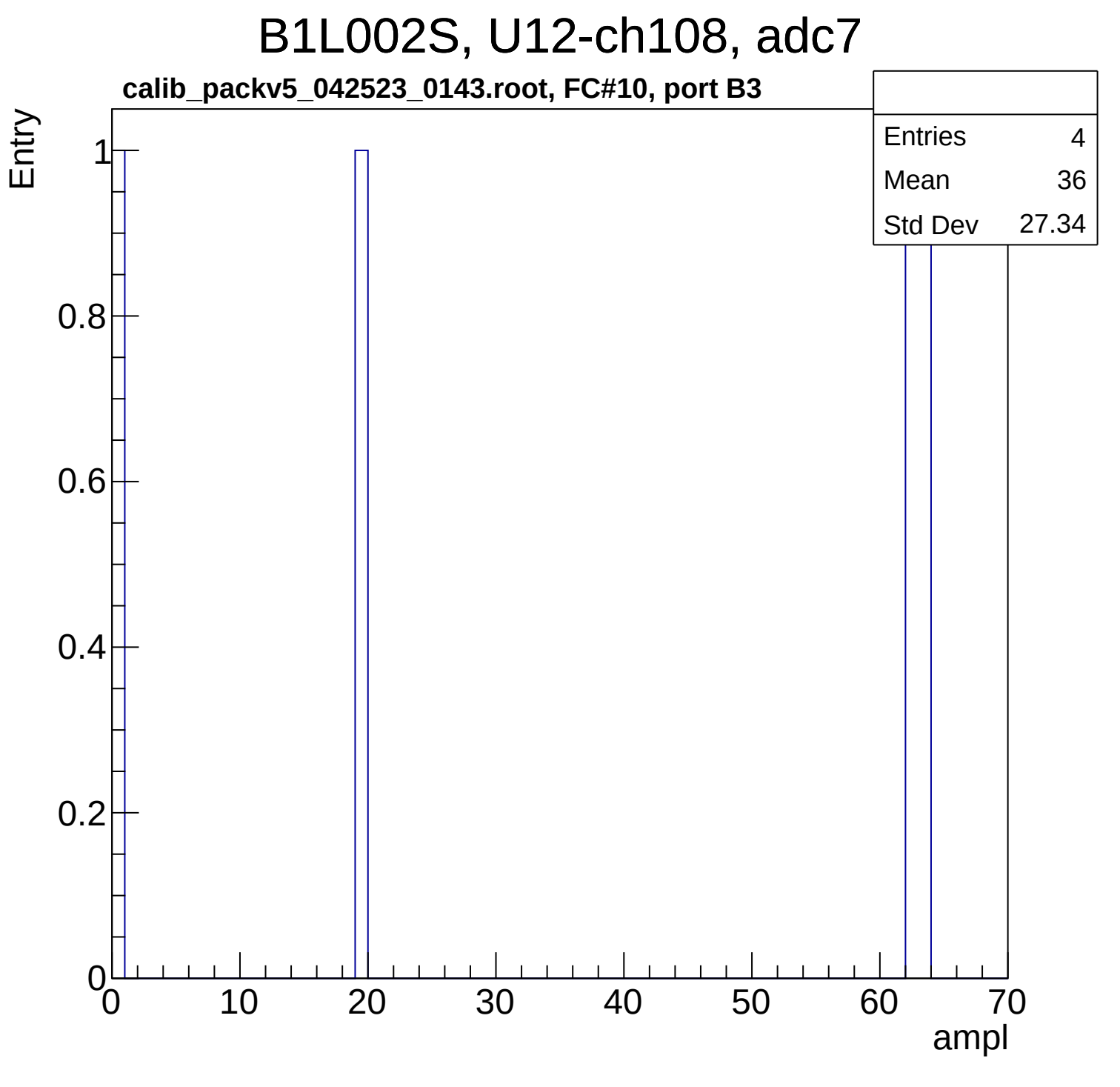
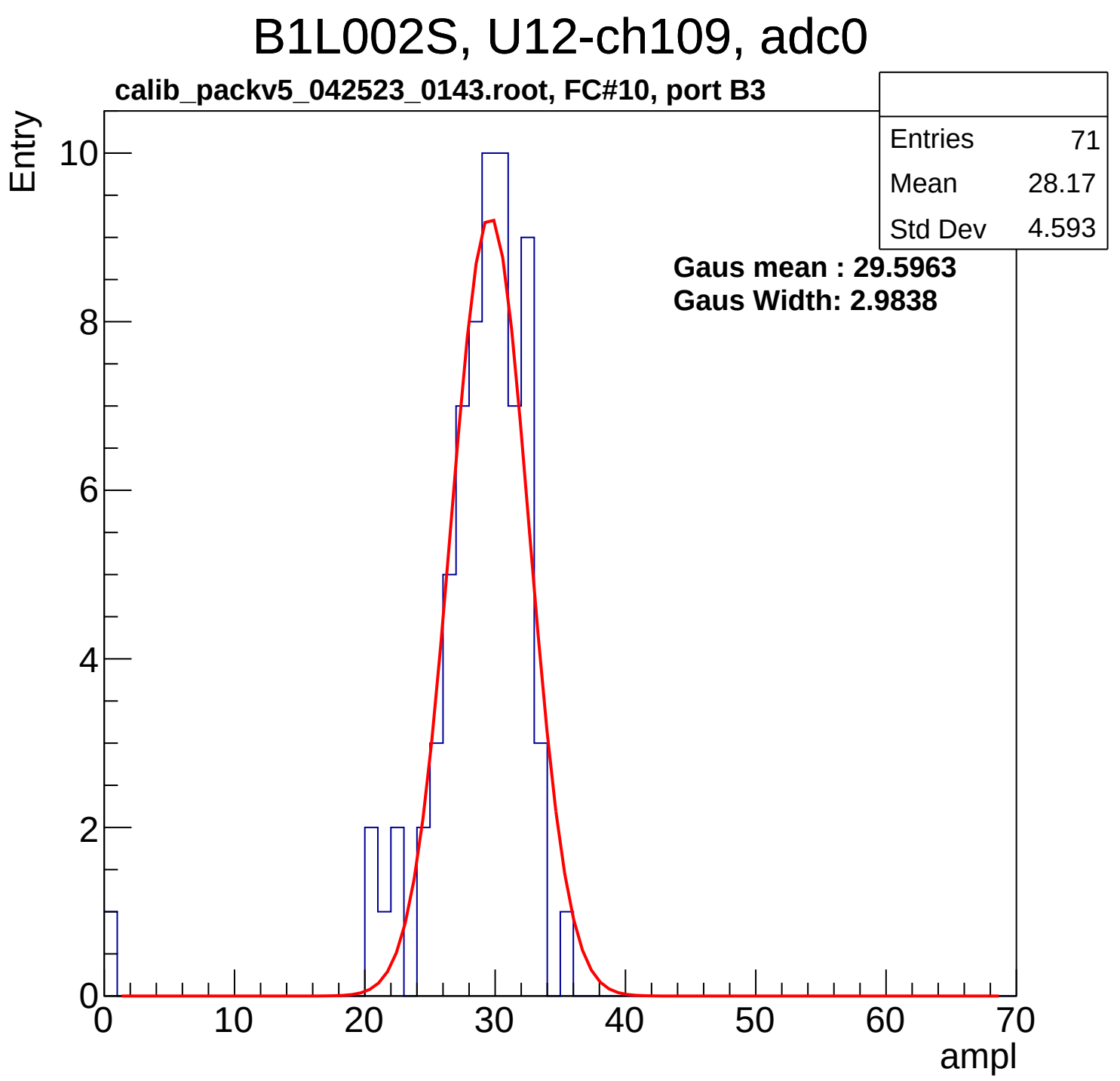
40
50
60
70
ampl


B1L002S, U12-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.73
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36
Std Dev 27.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U12-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.17
Std Dev 4.593
Gaus mean : 29.5963
Gaus Width: 2.9838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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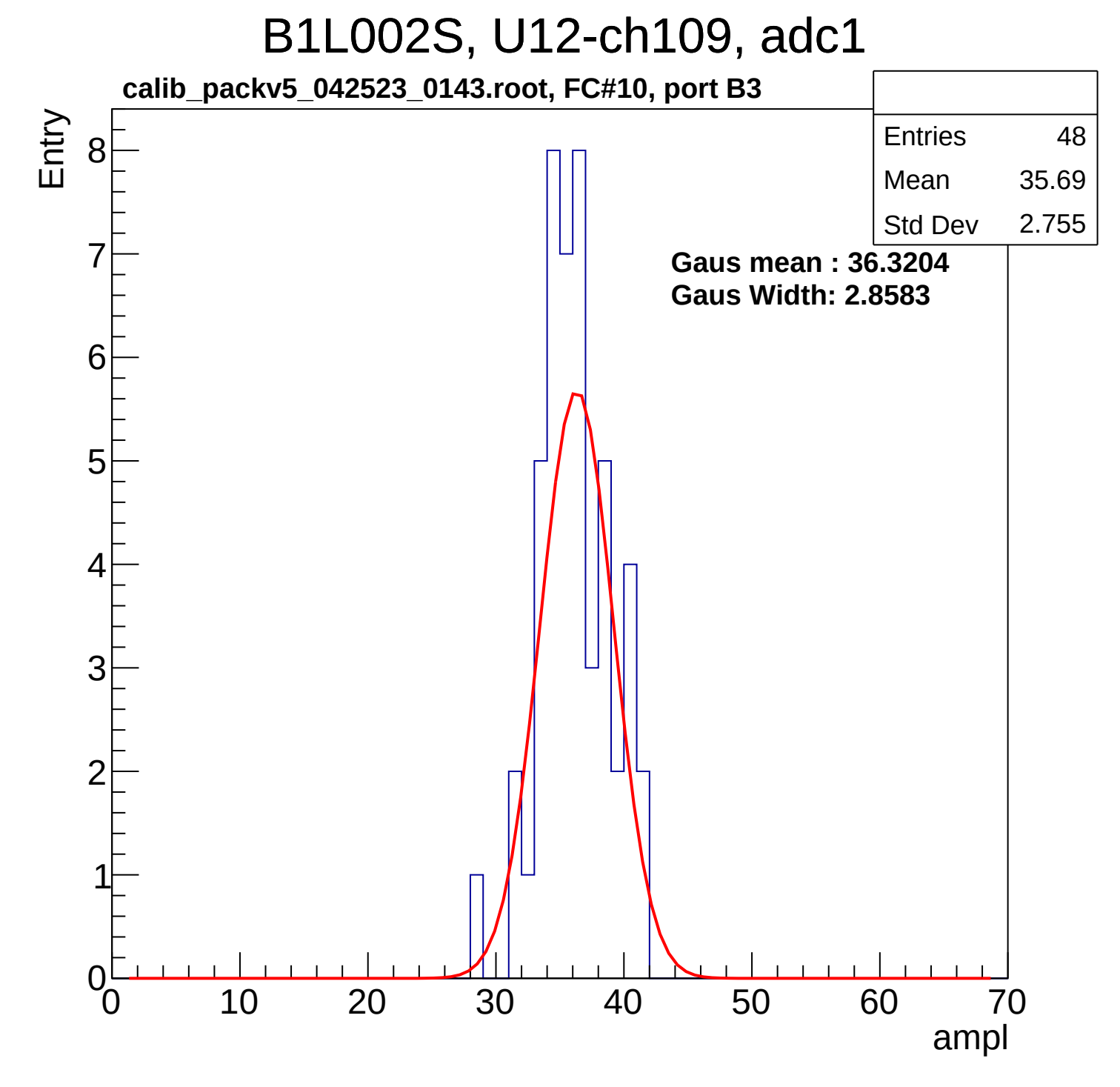
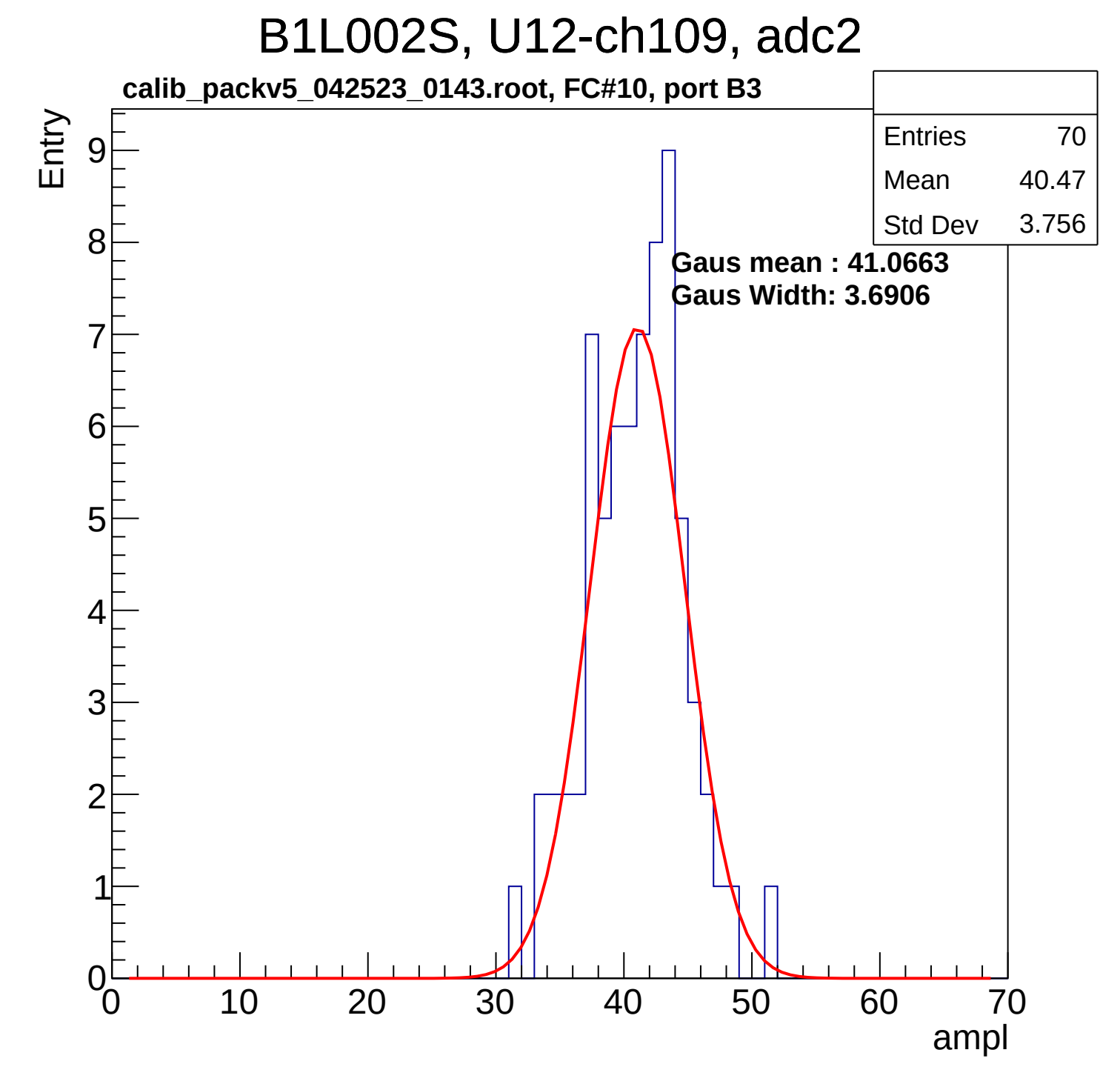
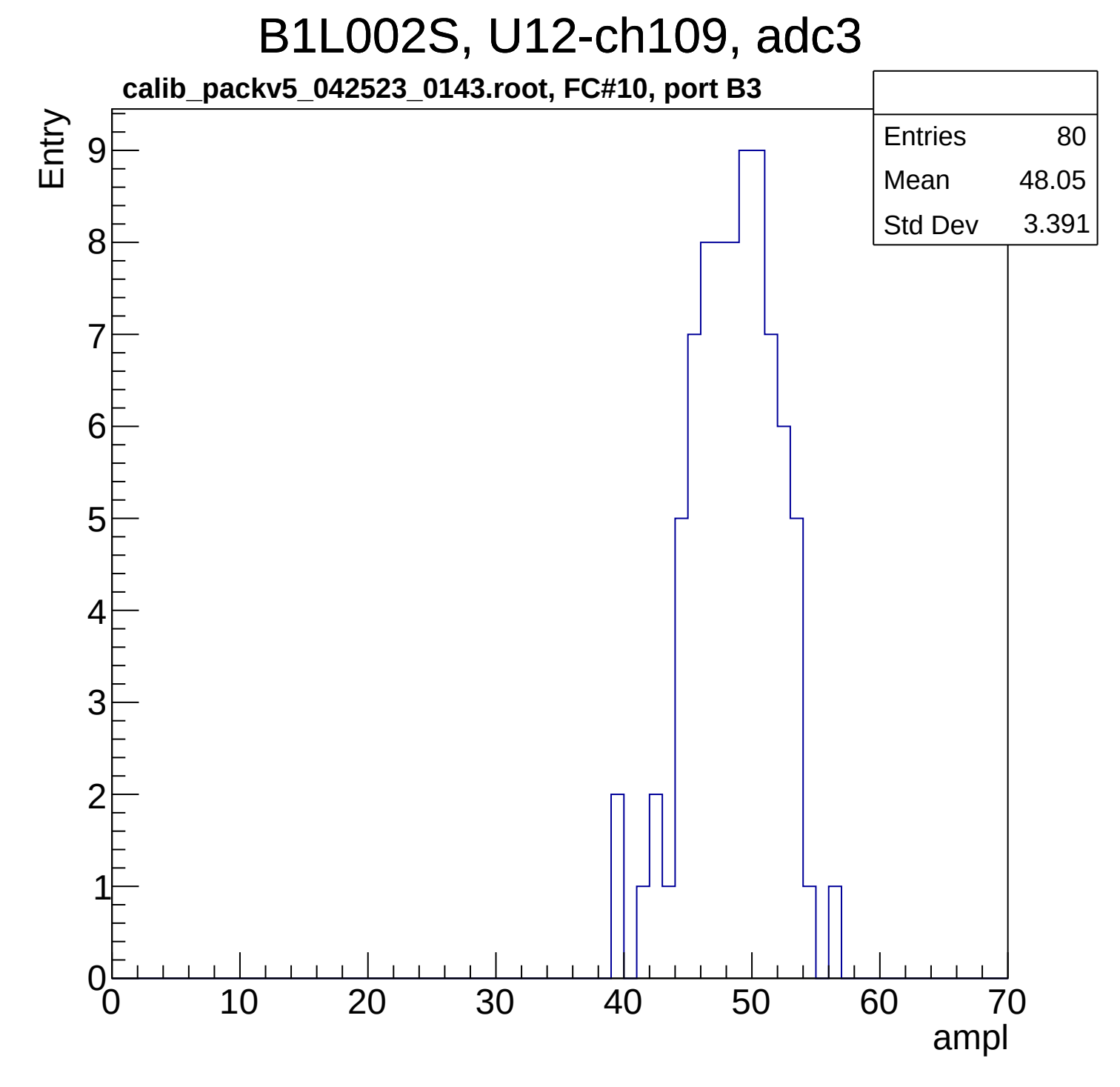
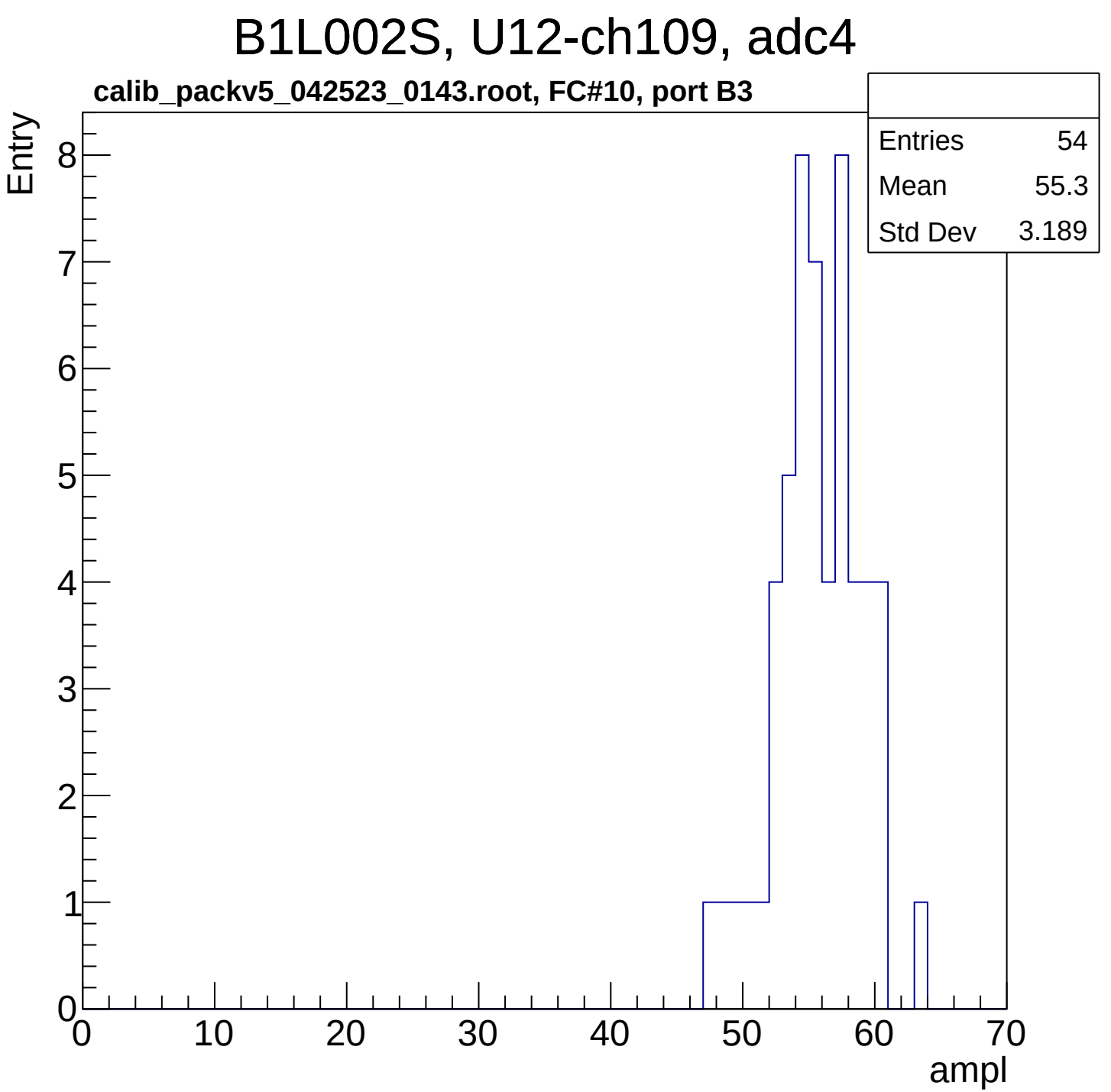
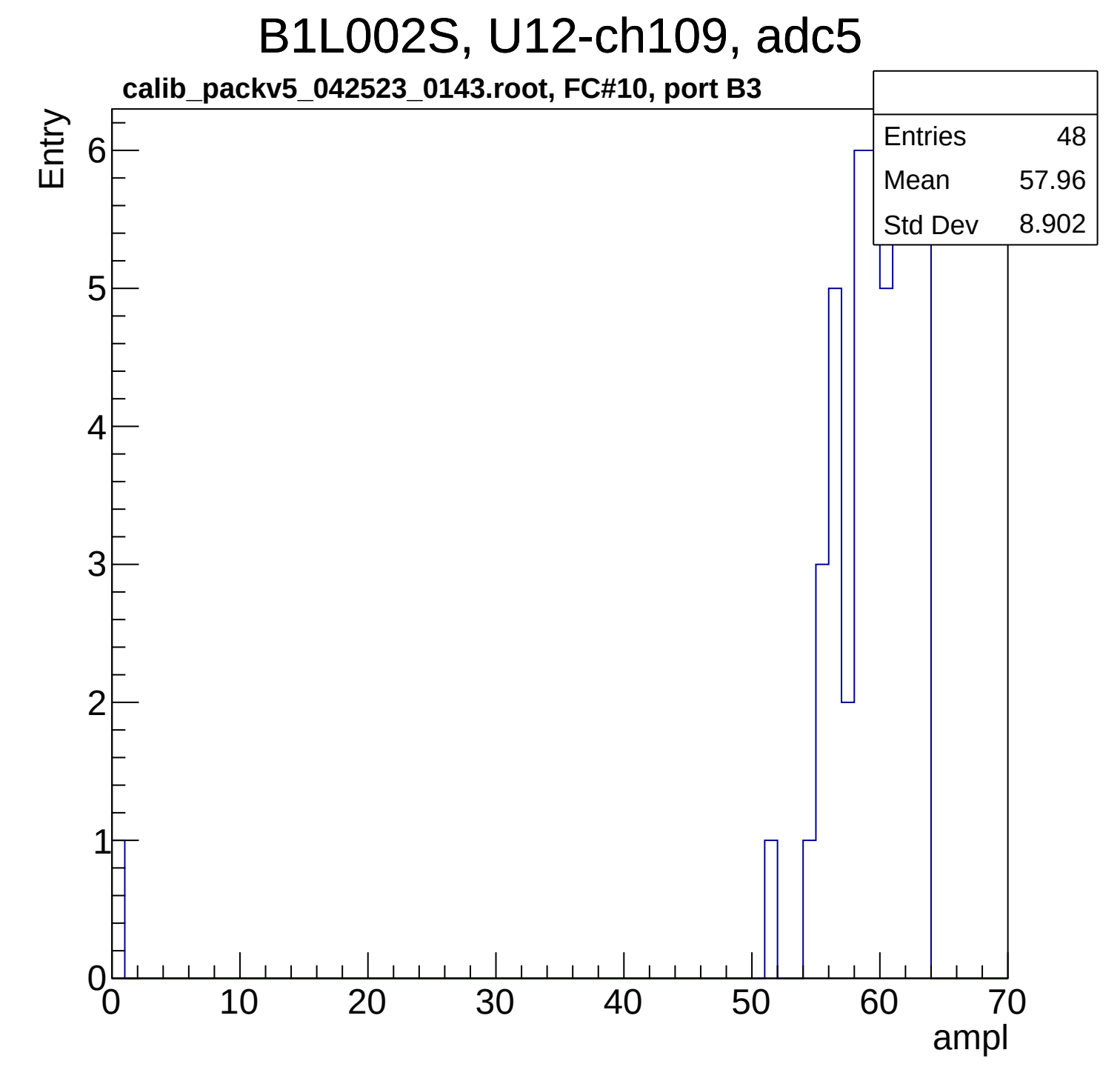
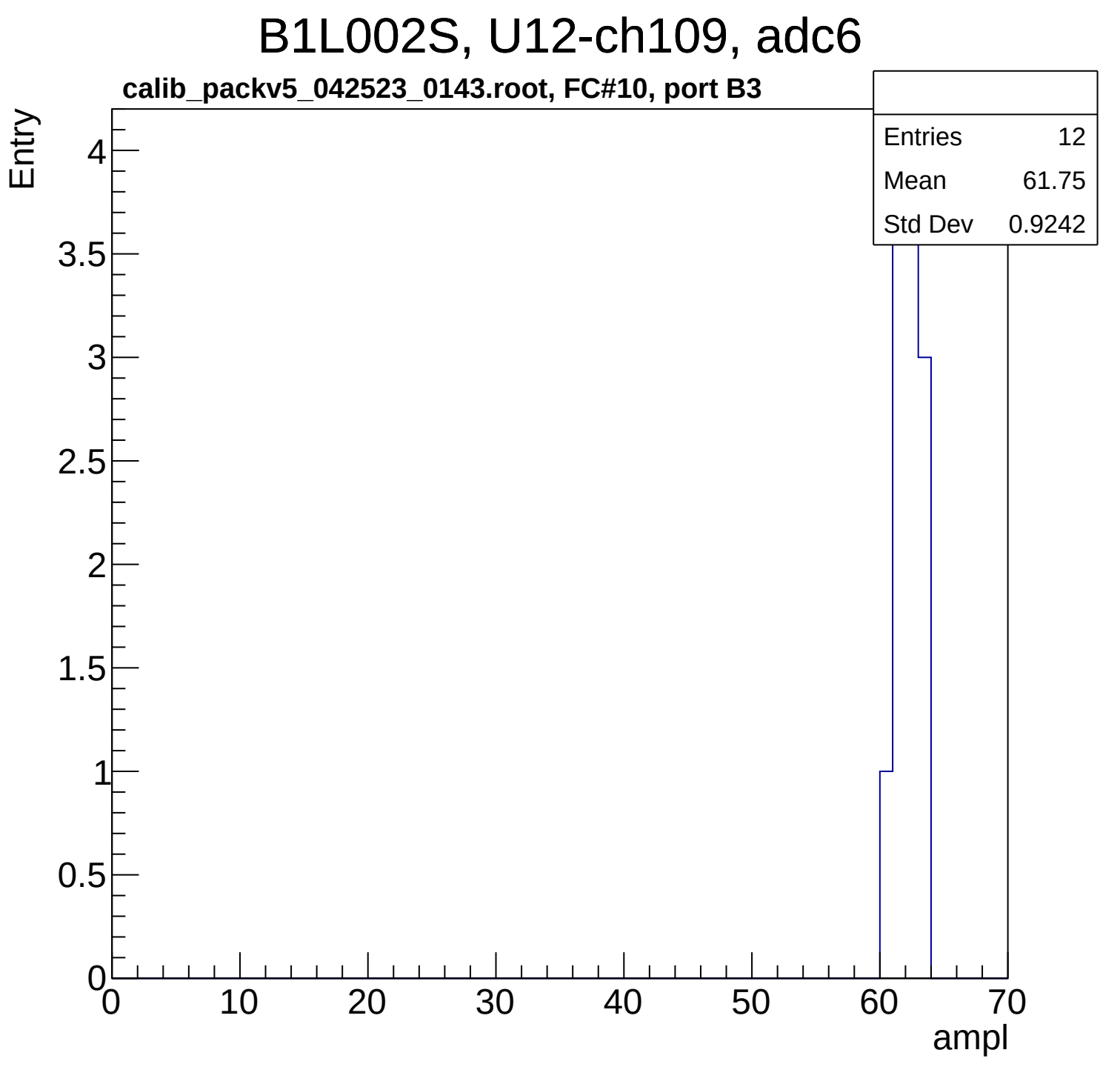

B1L002S, U12-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 35.69
Std Dev 2.755
Gaus mean : 36.3204
Gaus Width: 2.8583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

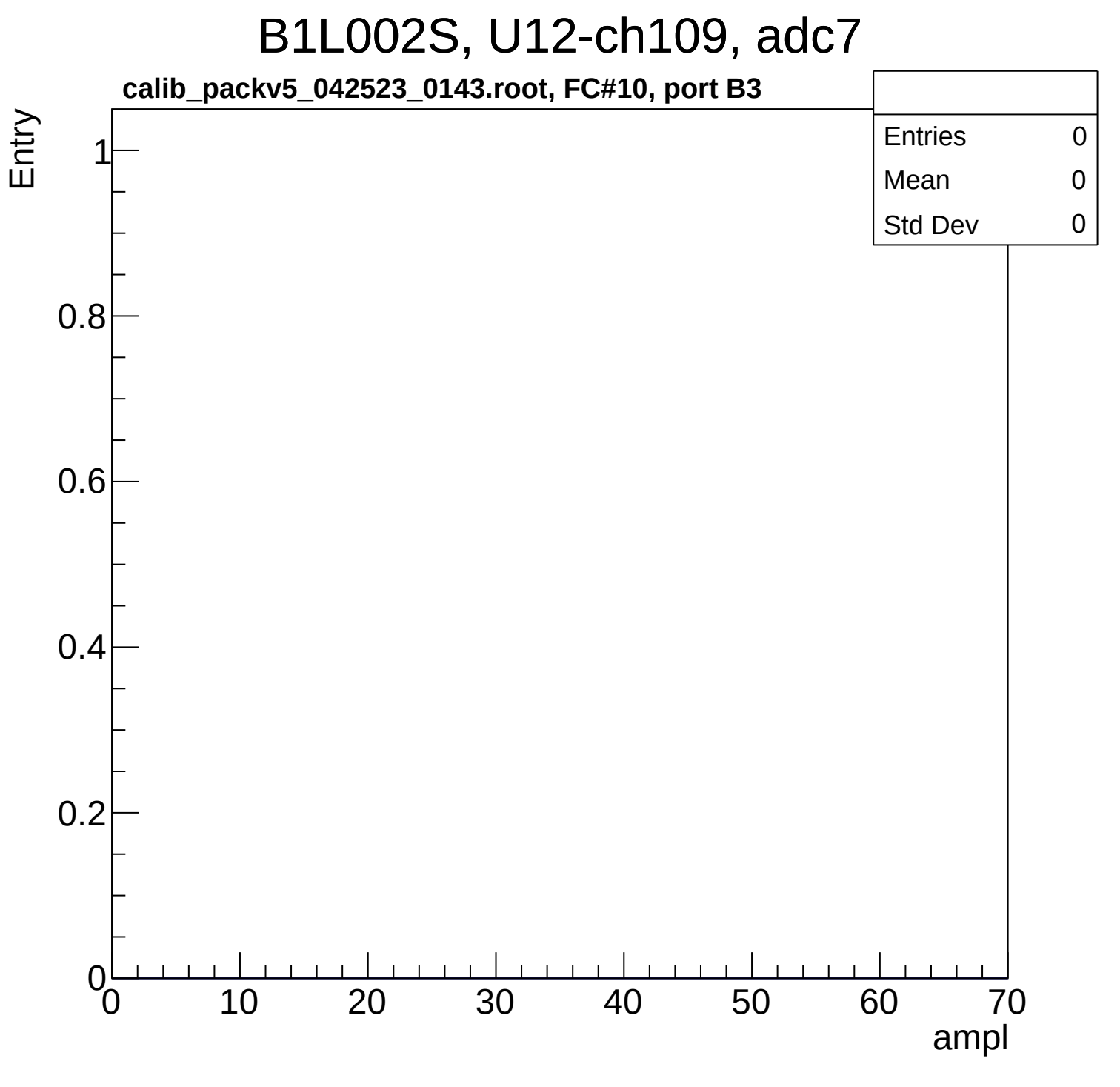

B1L002S, U12-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 40.47
Std Dev 3.756
Gaus mean : 41.0663
Gaus Width: 3.6906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

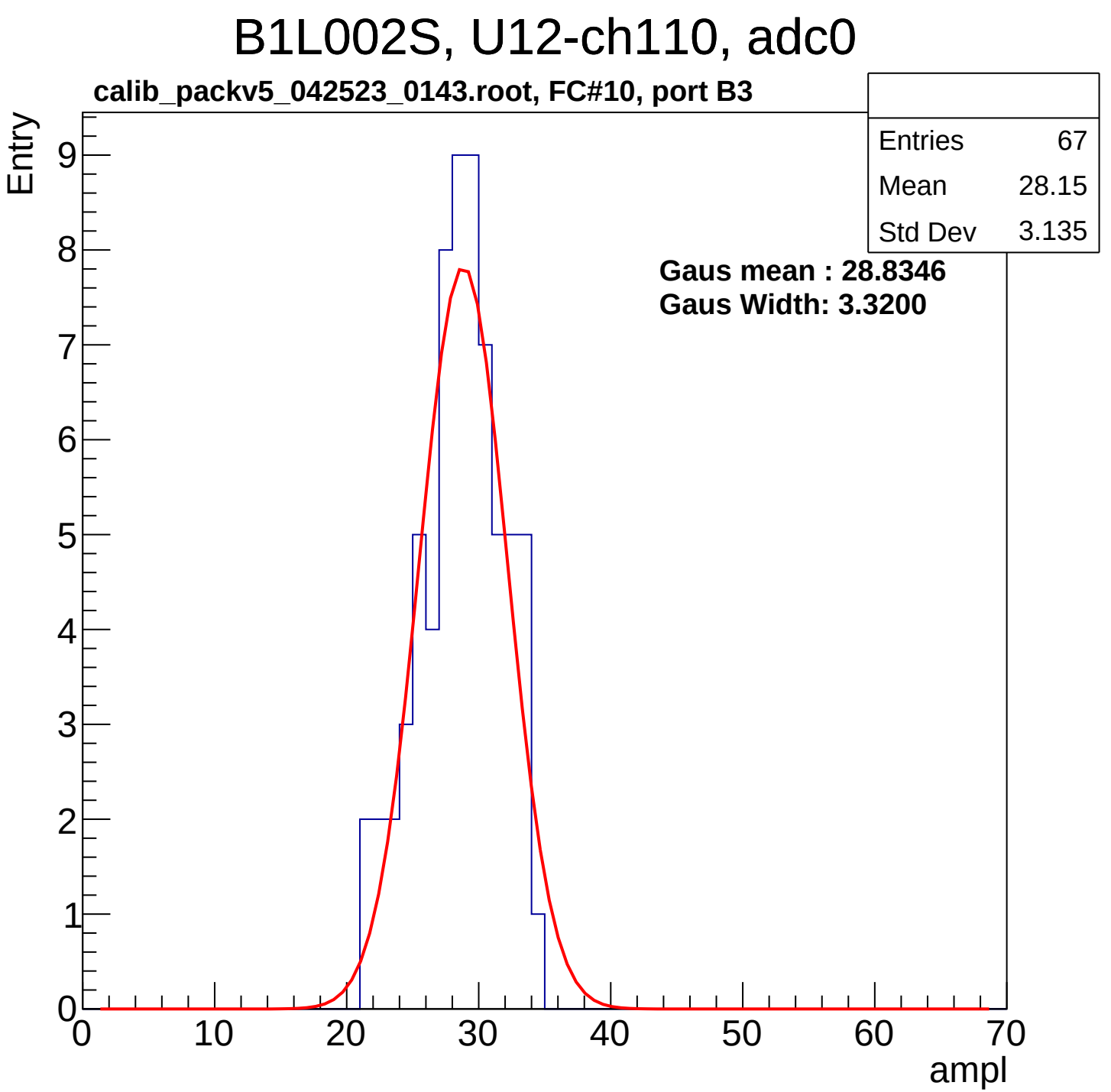

B1L002S, U12-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 48.05
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

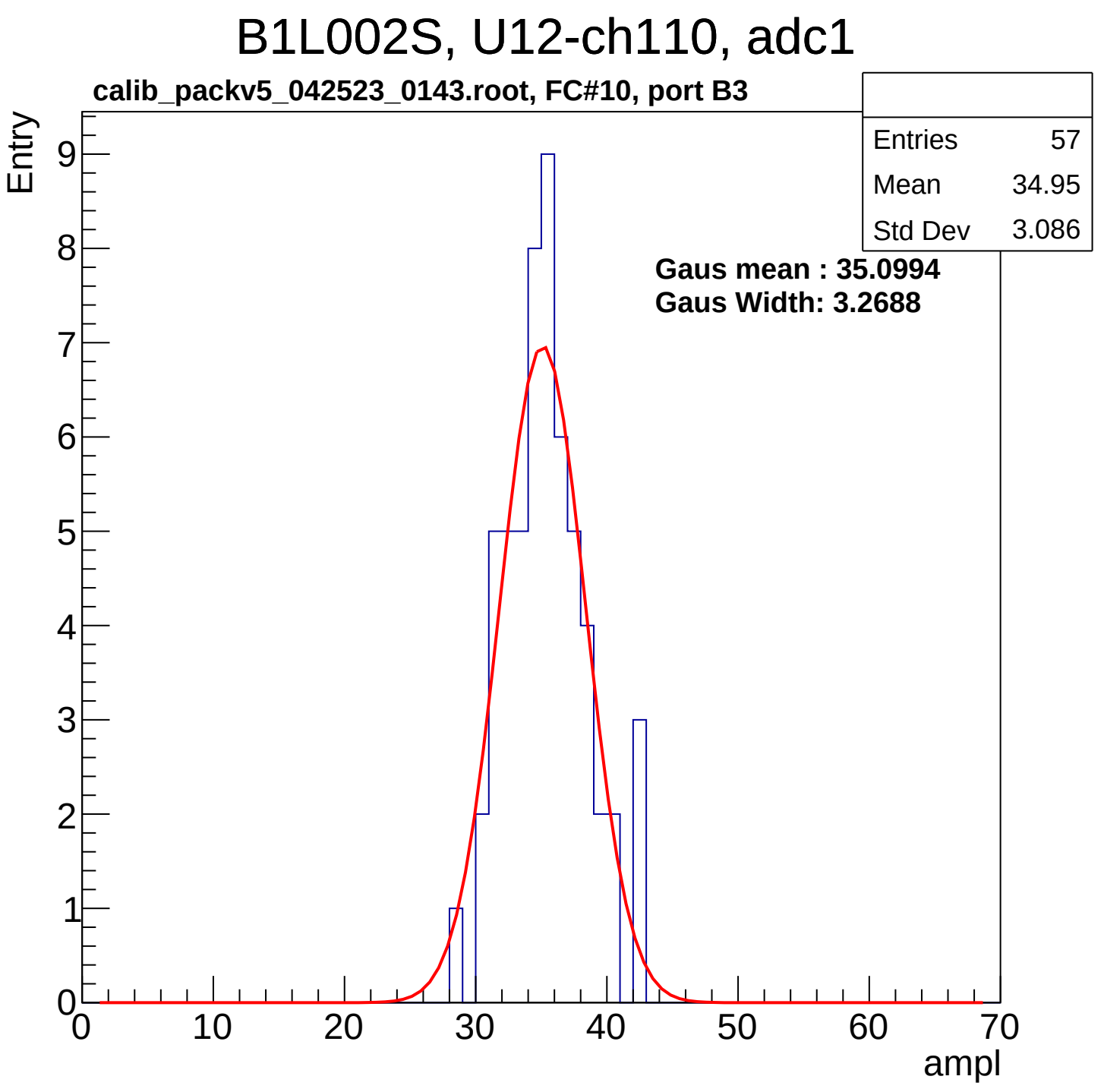

B1L002S, U12-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 55.3
Std Dev 3.189
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

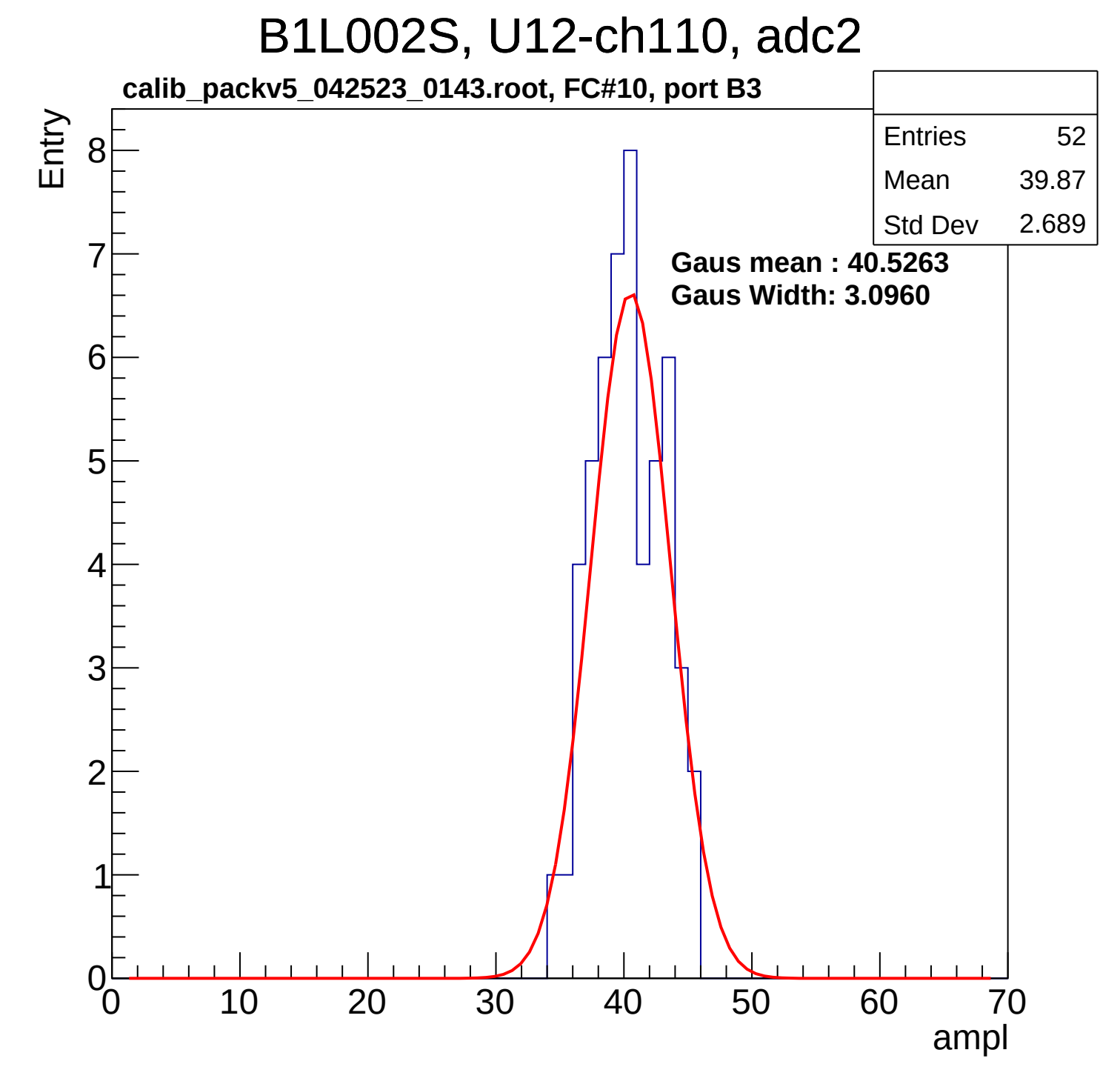

B1L002S, U12-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.96
Std Dev 8.902
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

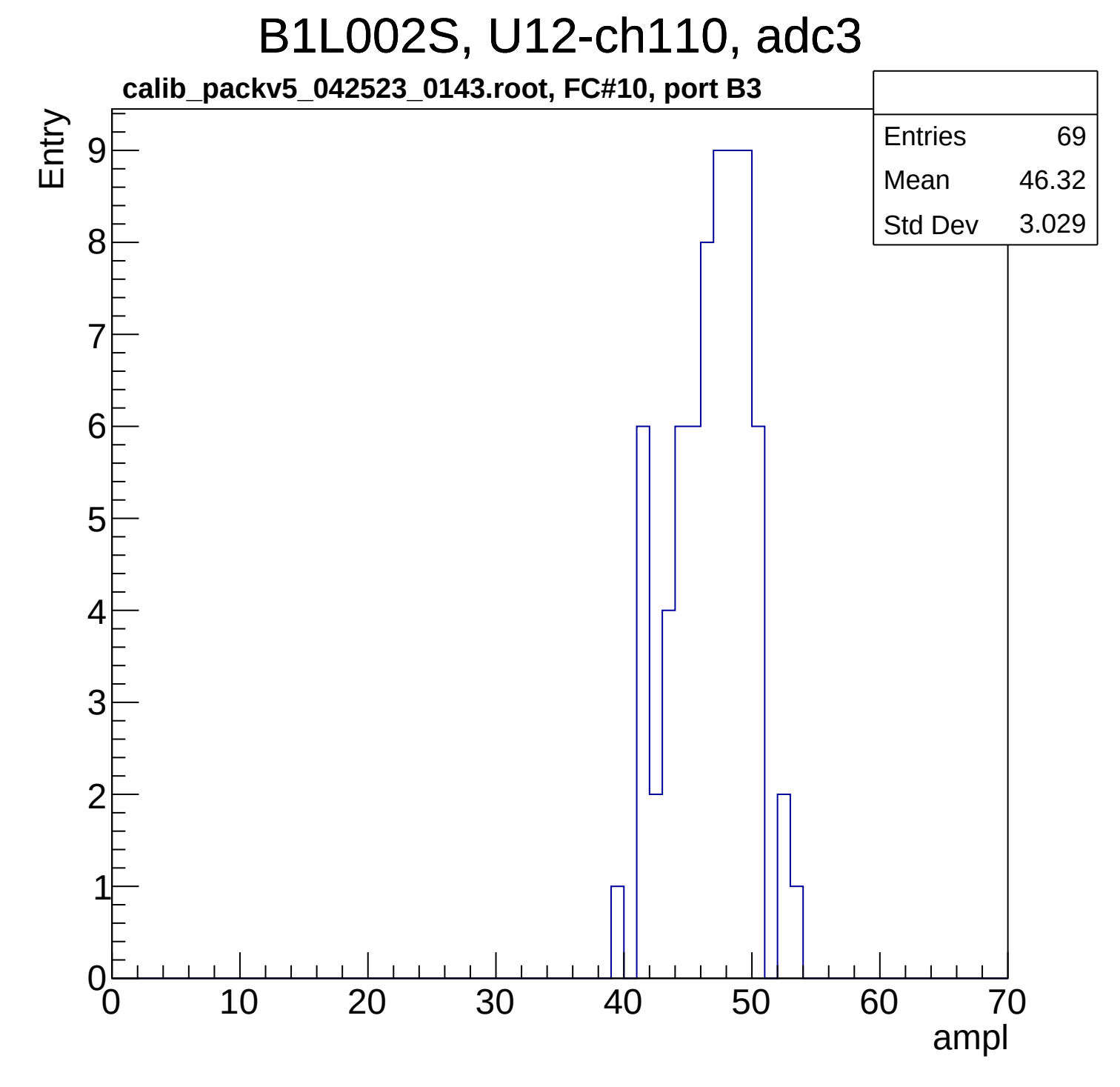

B1L002S, U12-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.75
Std Dev 0.9242
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U12-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 28.15
Std Dev 3.135
Gaus mean : 28.8346
Gaus Width: 3.3200
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.95
Std Dev 3.086
Gaus mean : 35.0994
Gaus Width: 3.2688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 39.87
Std Dev 2.689
Gaus mean : 40.5263
Gaus Width: 3.0960
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.32
Std Dev 3.029
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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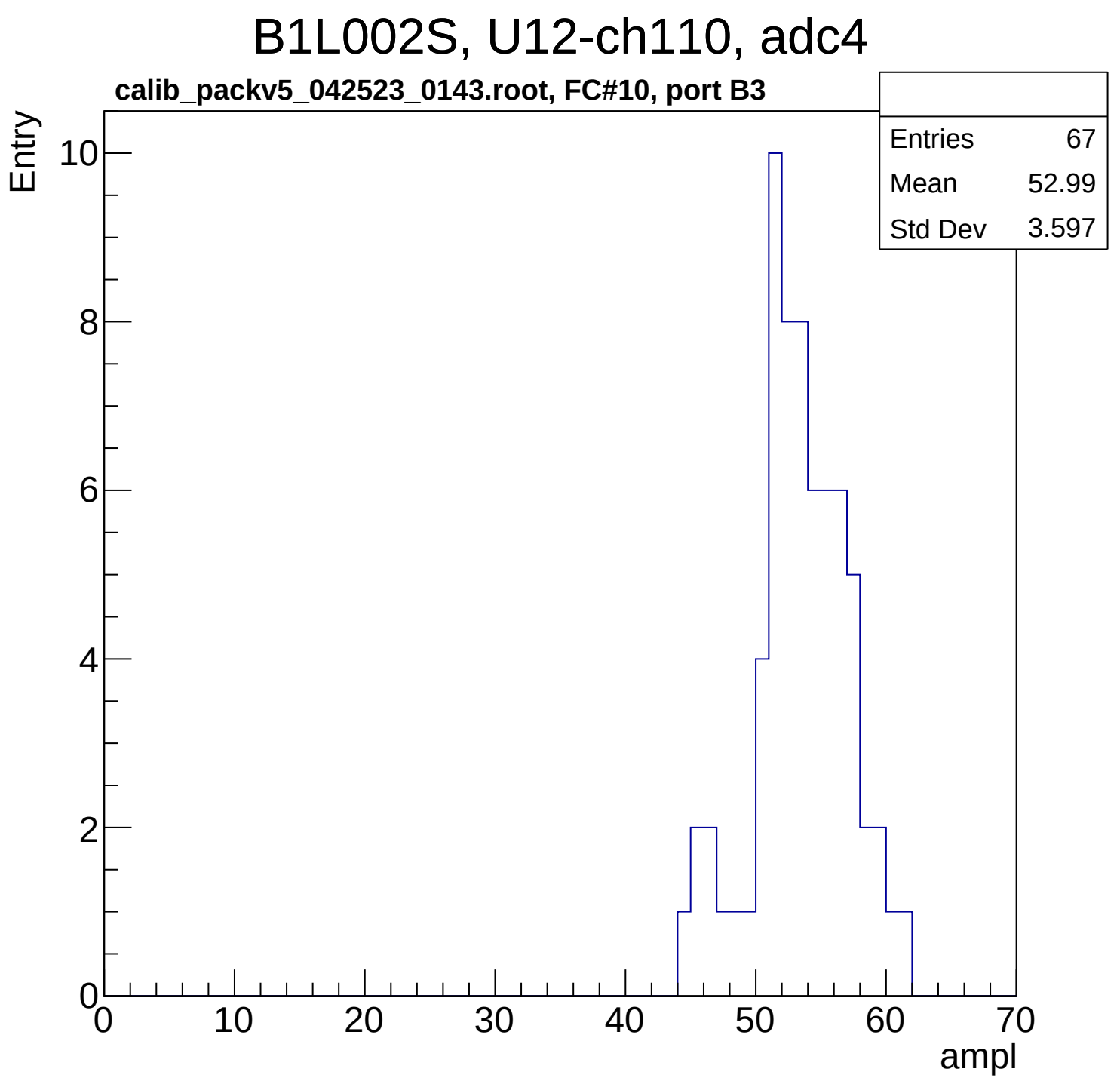
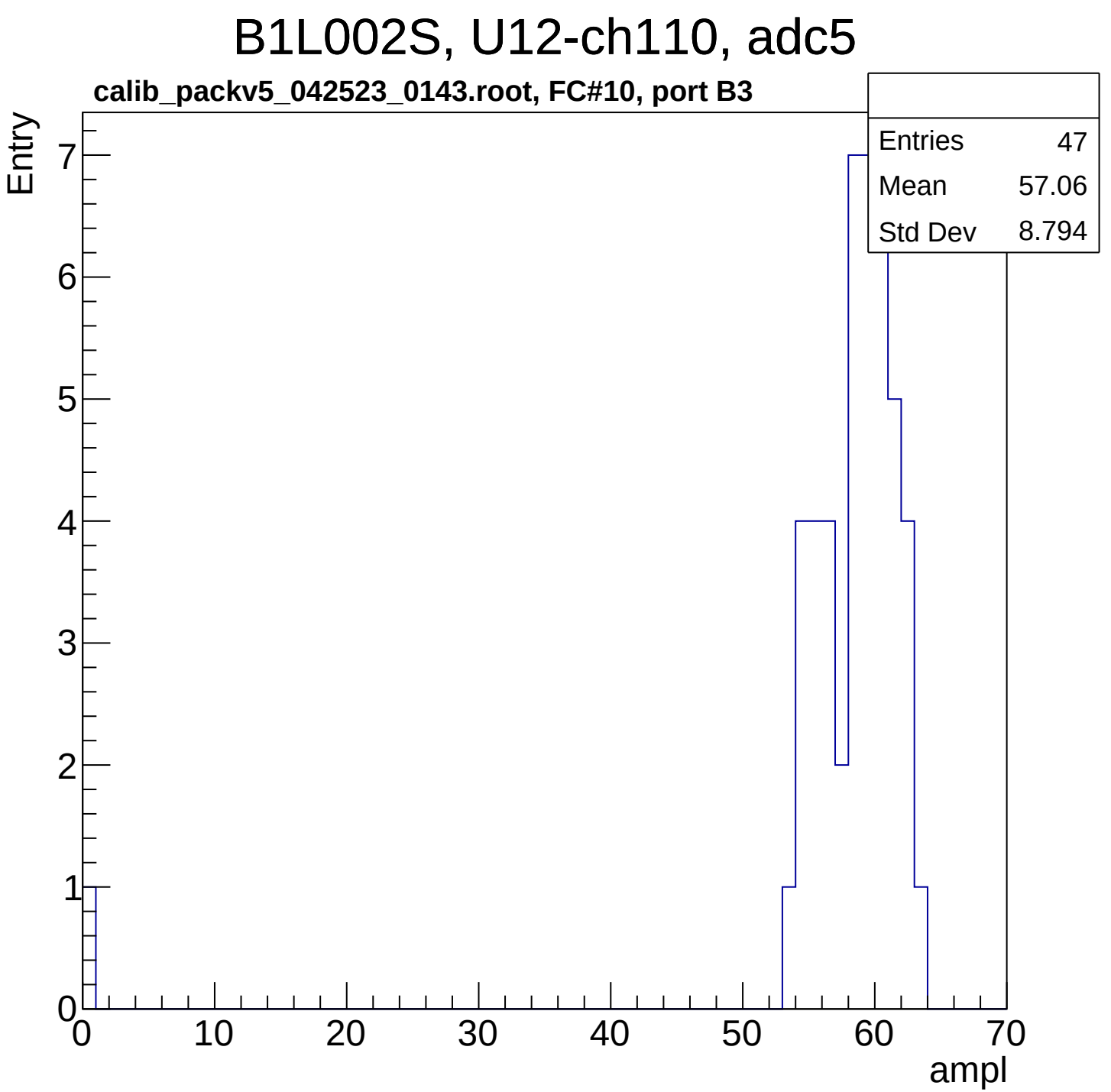

B1L002S, U12-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 52.99
Std Dev 3.597
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 57.06
Std Dev 8.794
Entry
ampl
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0
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60
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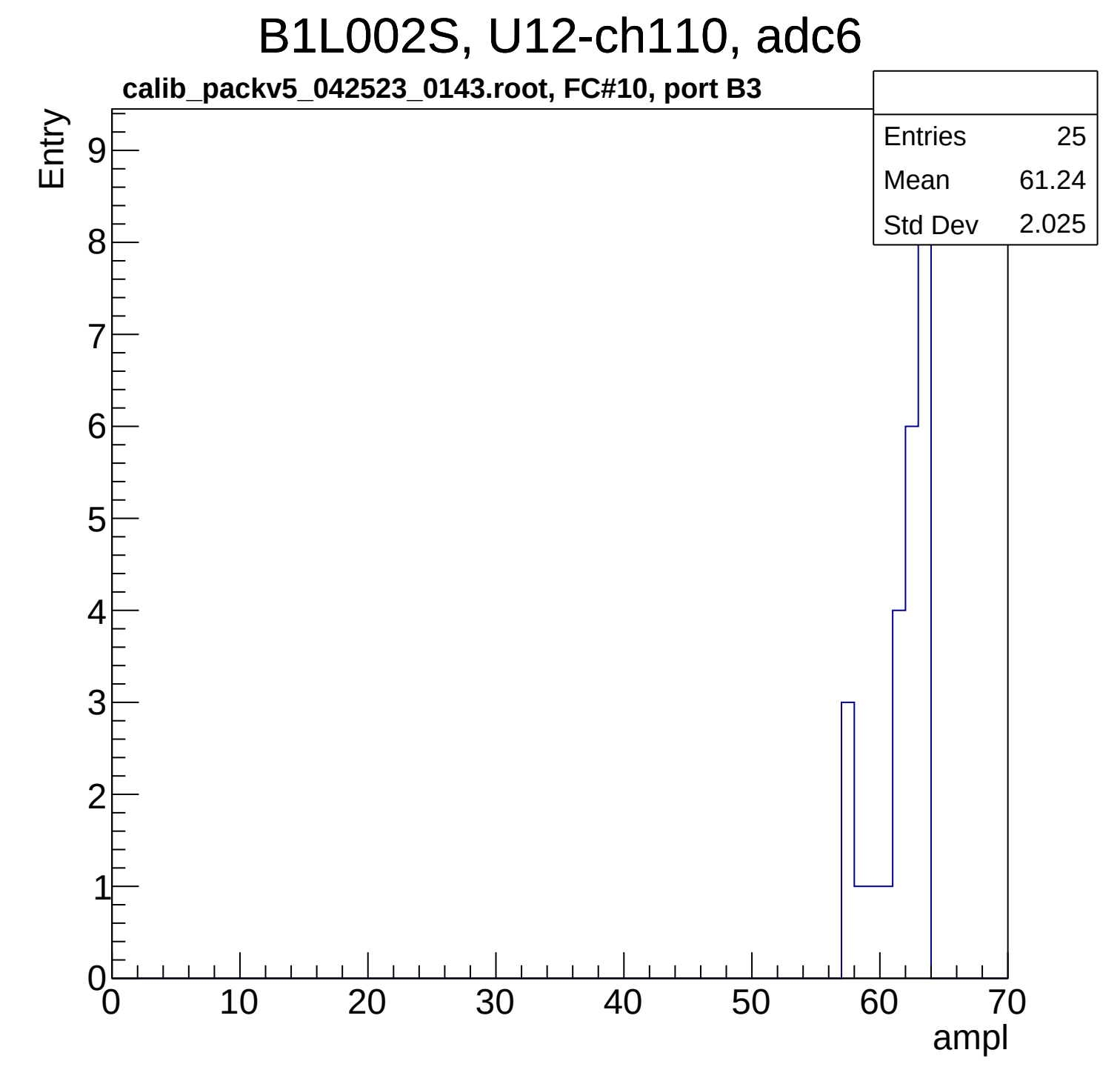
B1L002S, U12-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.24
Std Dev 2.025
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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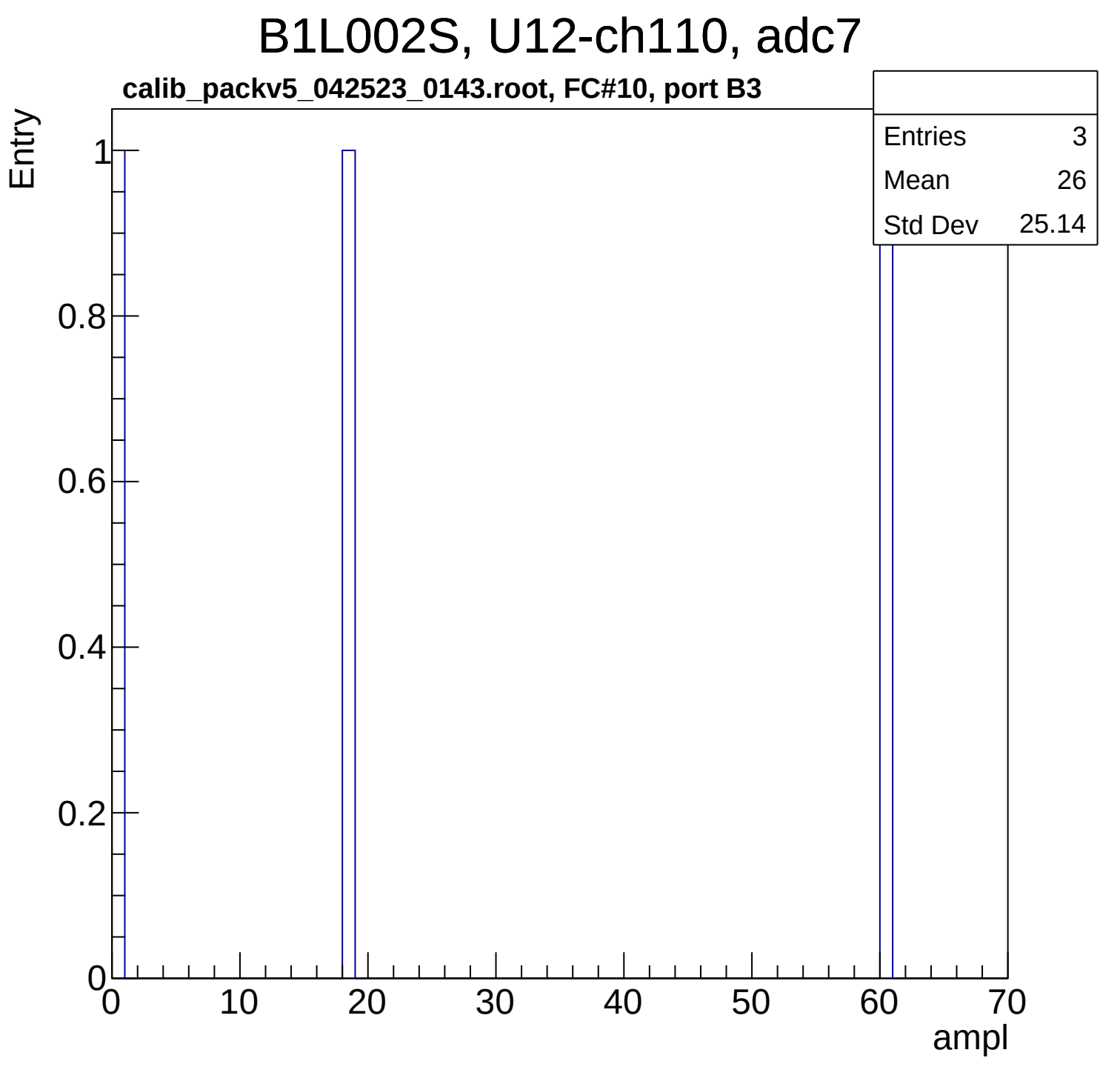
B1L002S, U12-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 26
Std Dev 25.14
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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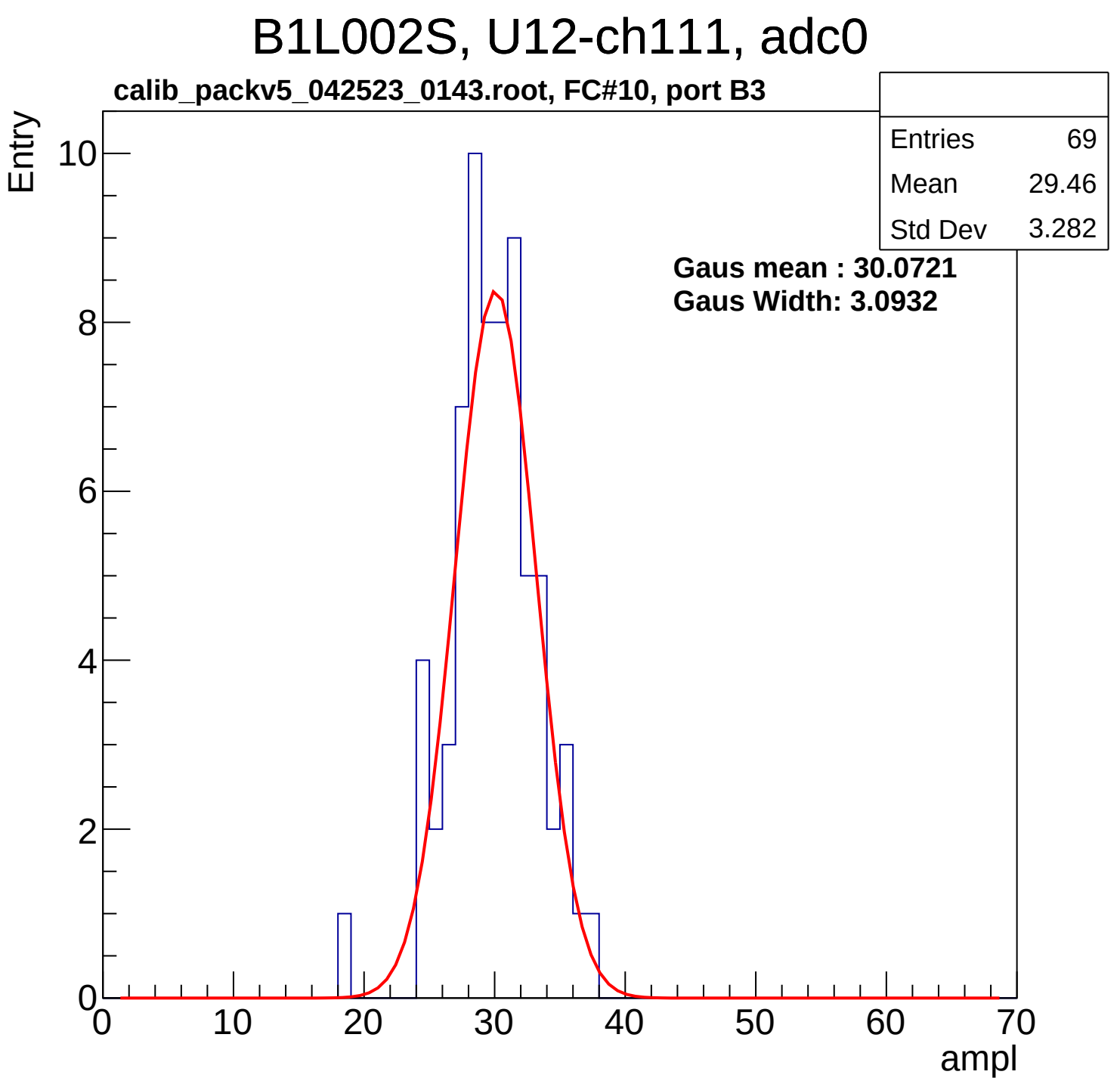
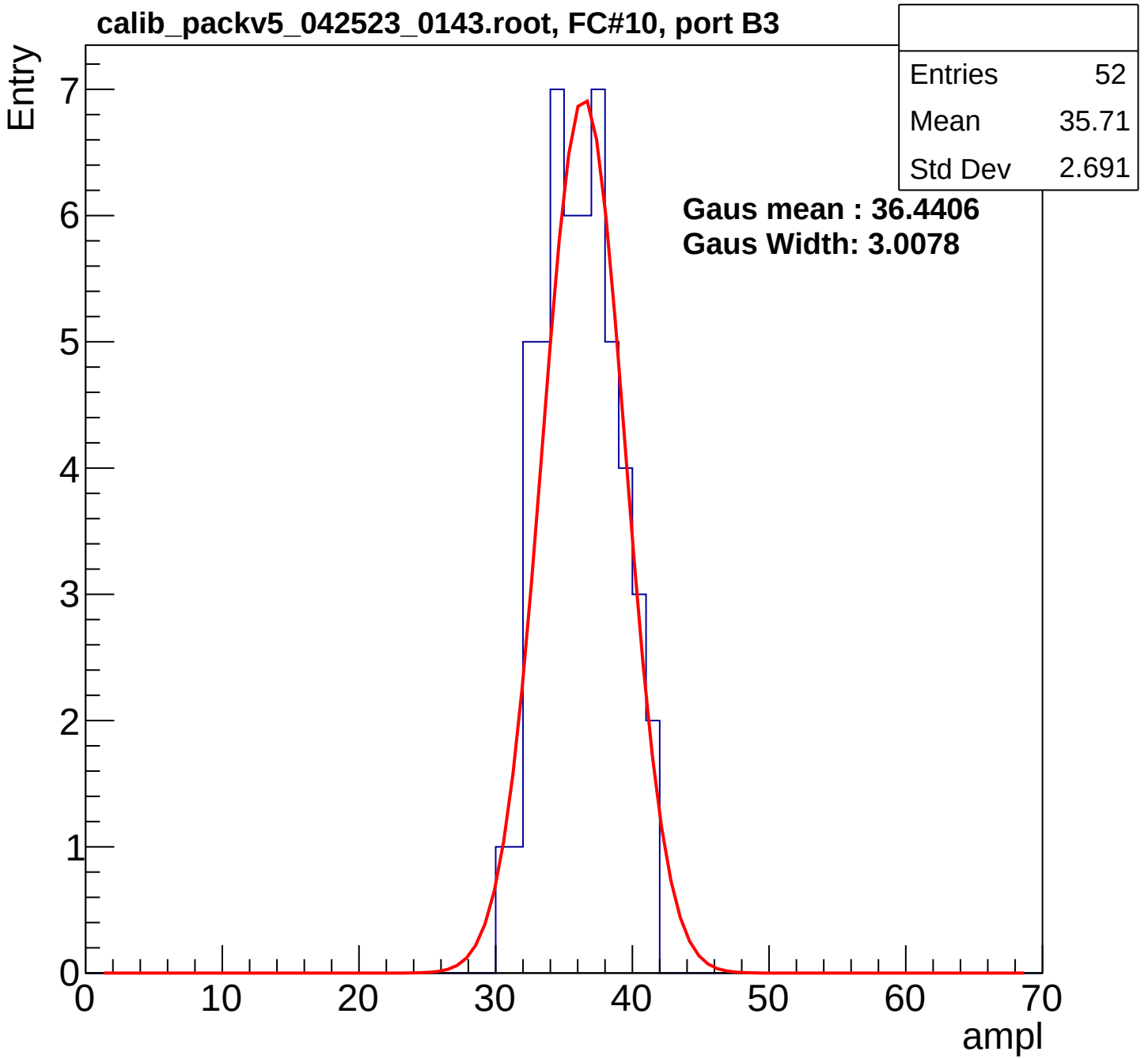
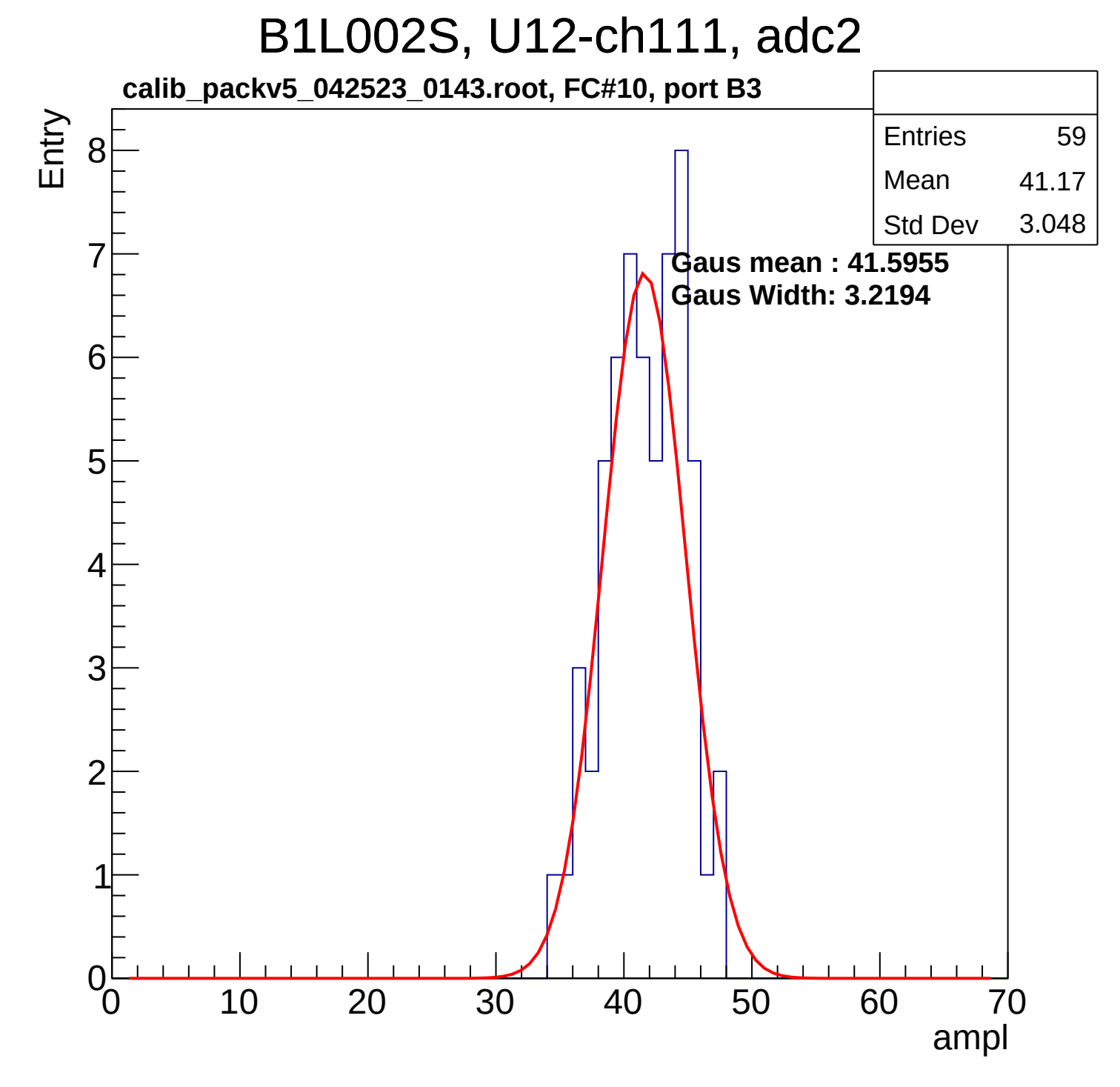
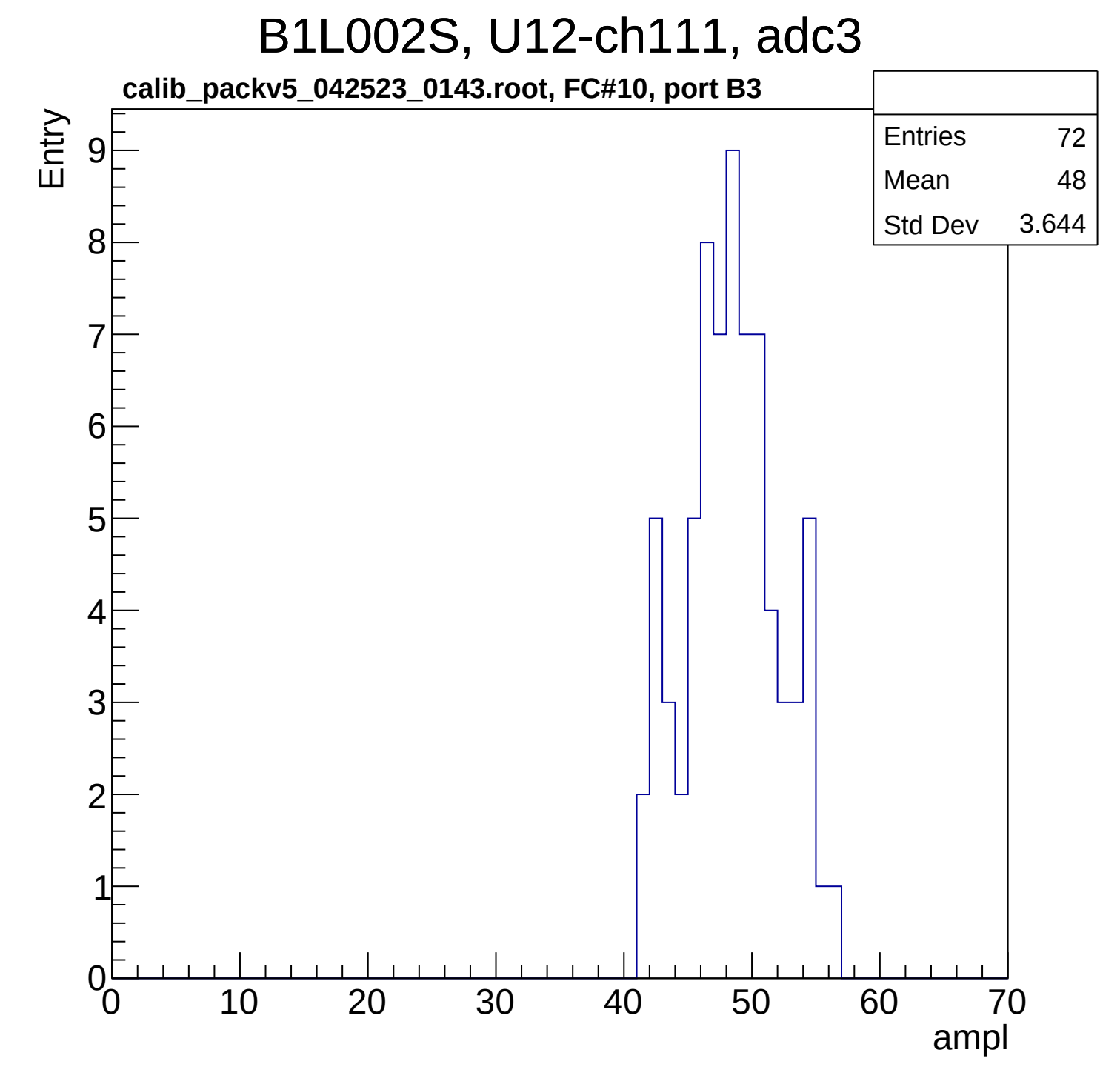

B1L002S, U12-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 29.46
Std Dev 3.282
Gaus mean : 30.0721
Gaus Width: 3.0932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.71
Std Dev 2.691
Gaus mean : 36.4406
Gaus Width: 3.0078
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.17
Std Dev 3.048
Gaus mean : 41.5955
Gaus Width: 3.2194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 48
Std Dev 3.644
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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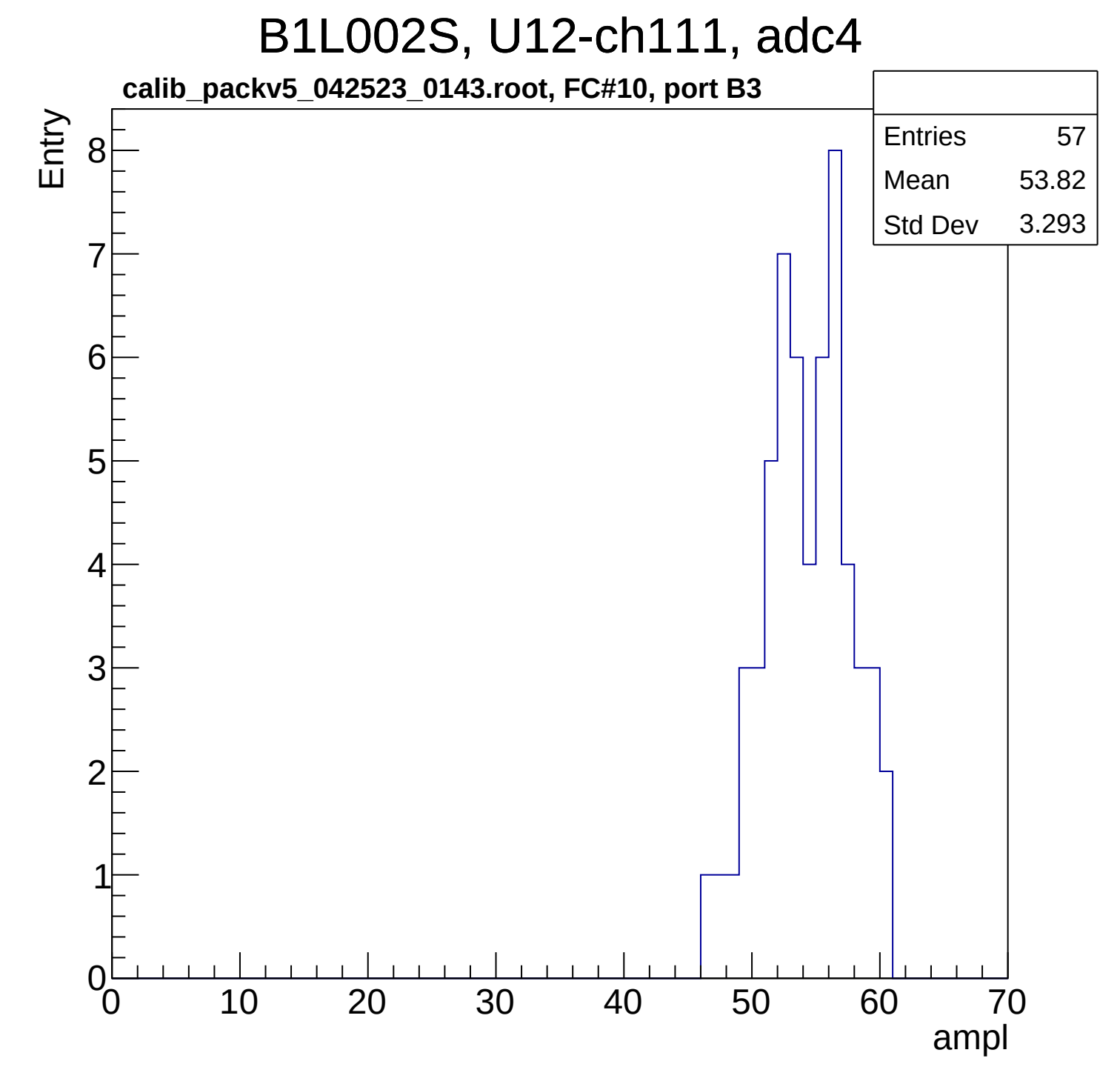
B1L002S, U12-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.82
Std Dev 3.293
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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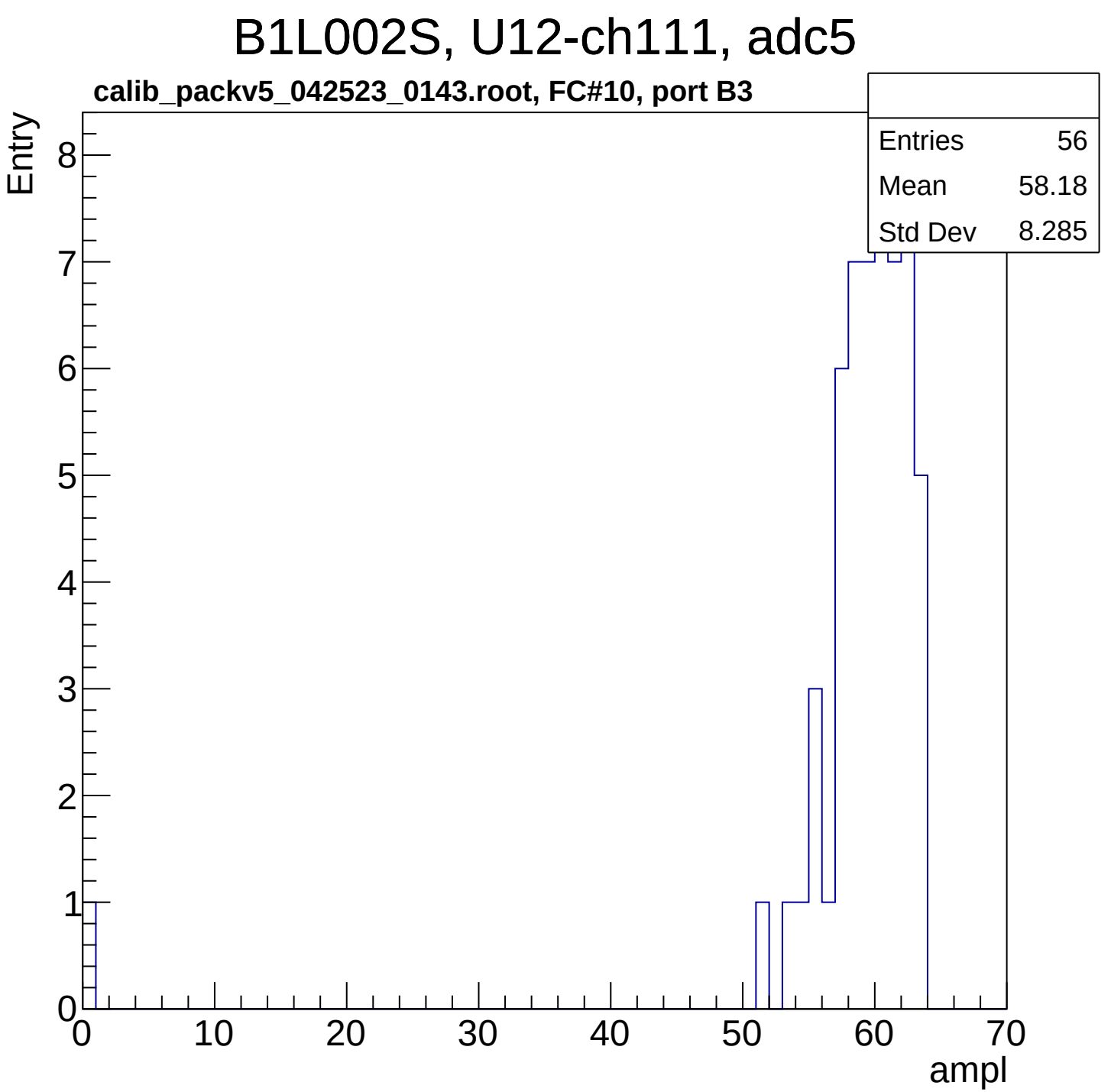
B1L002S, U12-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.18
Std Dev 8.285
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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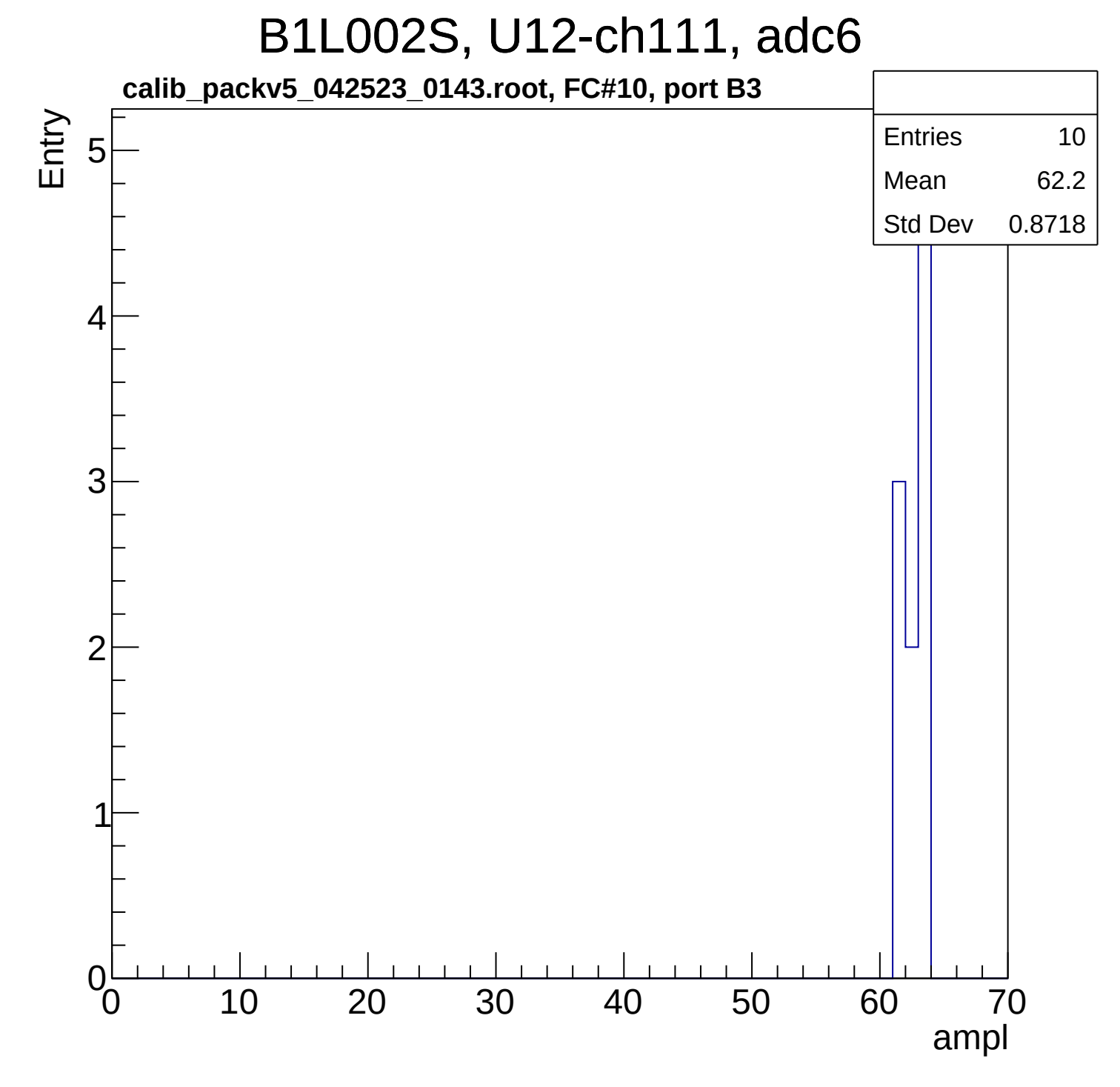
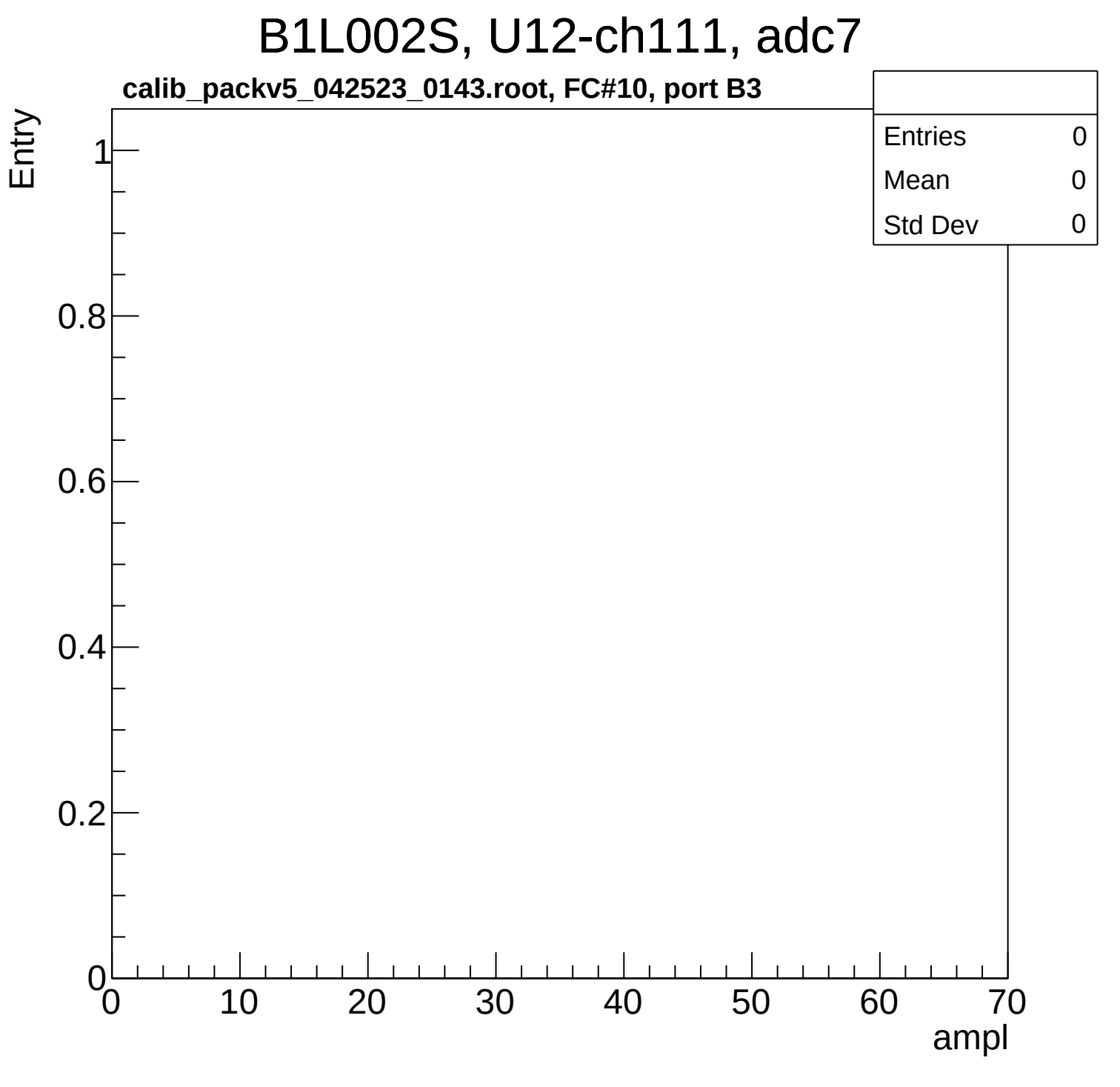
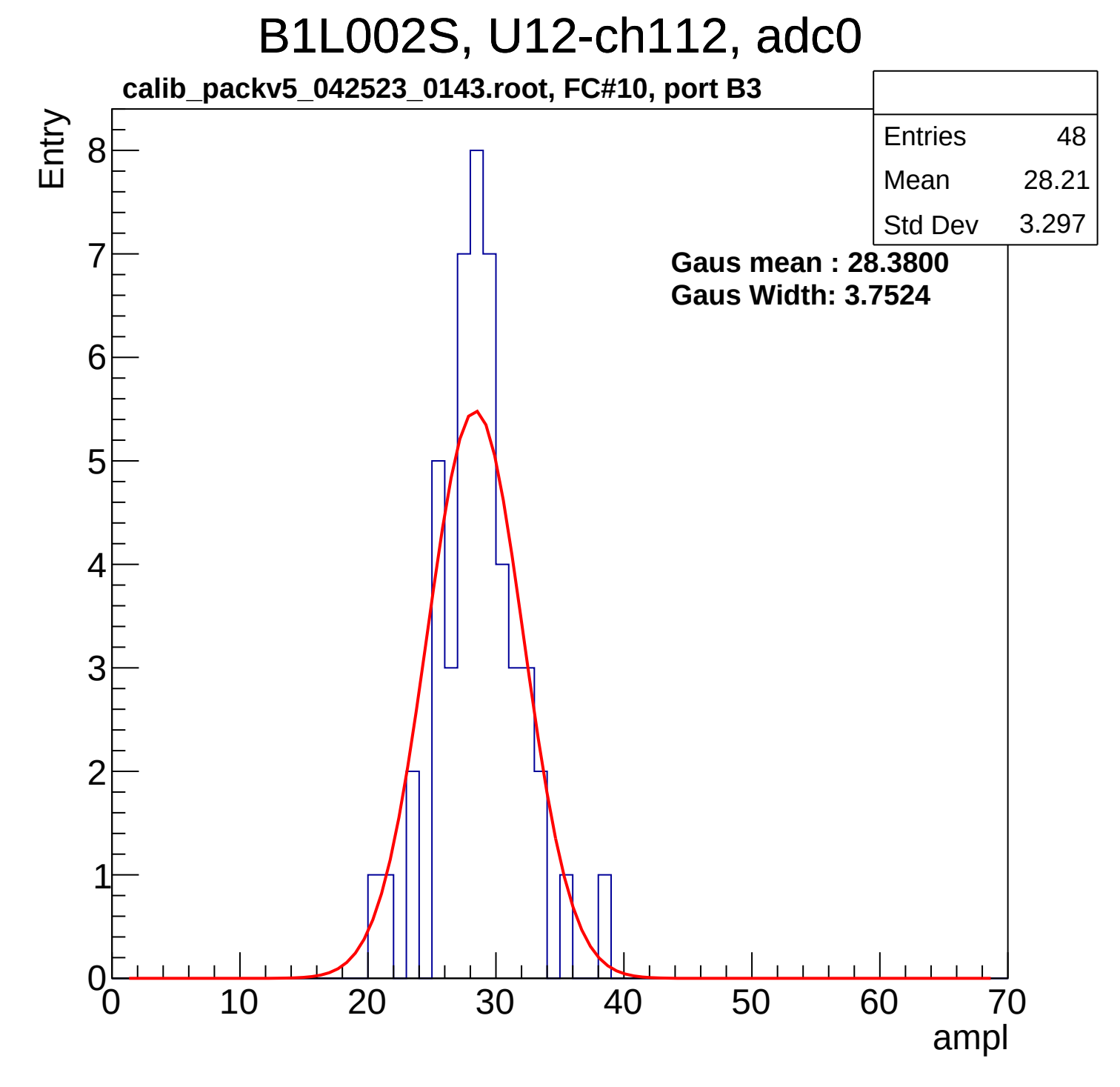

B1L002S, U12-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 62.2
Std Dev 0.8718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.21
Std Dev 3.297
Gaus mean : 28.3800
Gaus Width: 3.7524
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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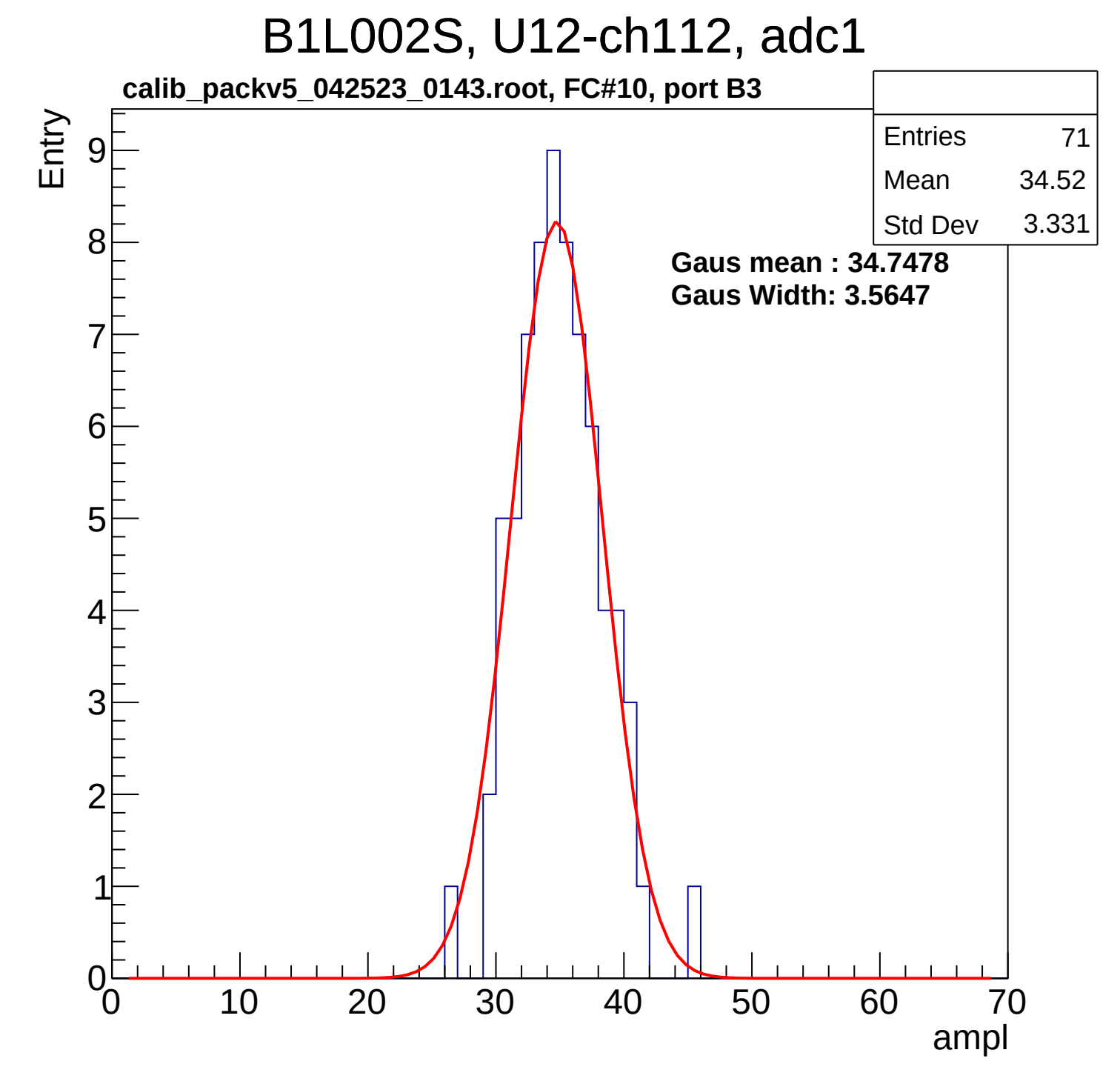
B1L002S, U12-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.52
Std Dev 3.331
Gaus mean : 34.7478
Gaus Width: 3.5647
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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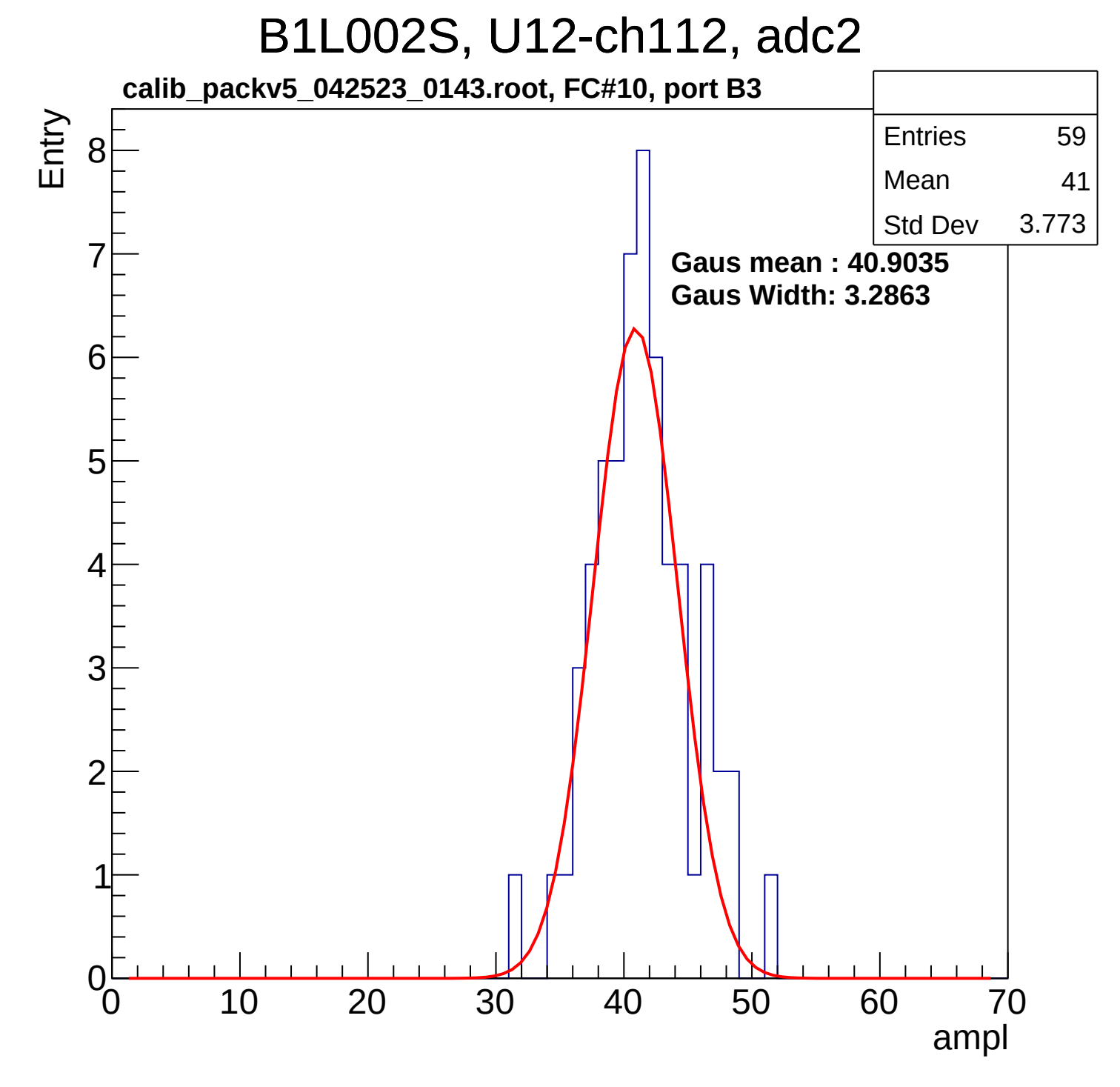
B1L002S, U12-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41
Std Dev 3.773
Gaus mean : 40.9035
Gaus Width: 3.2863
Entry
ampl
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1
2
3
4
5
6
7
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0
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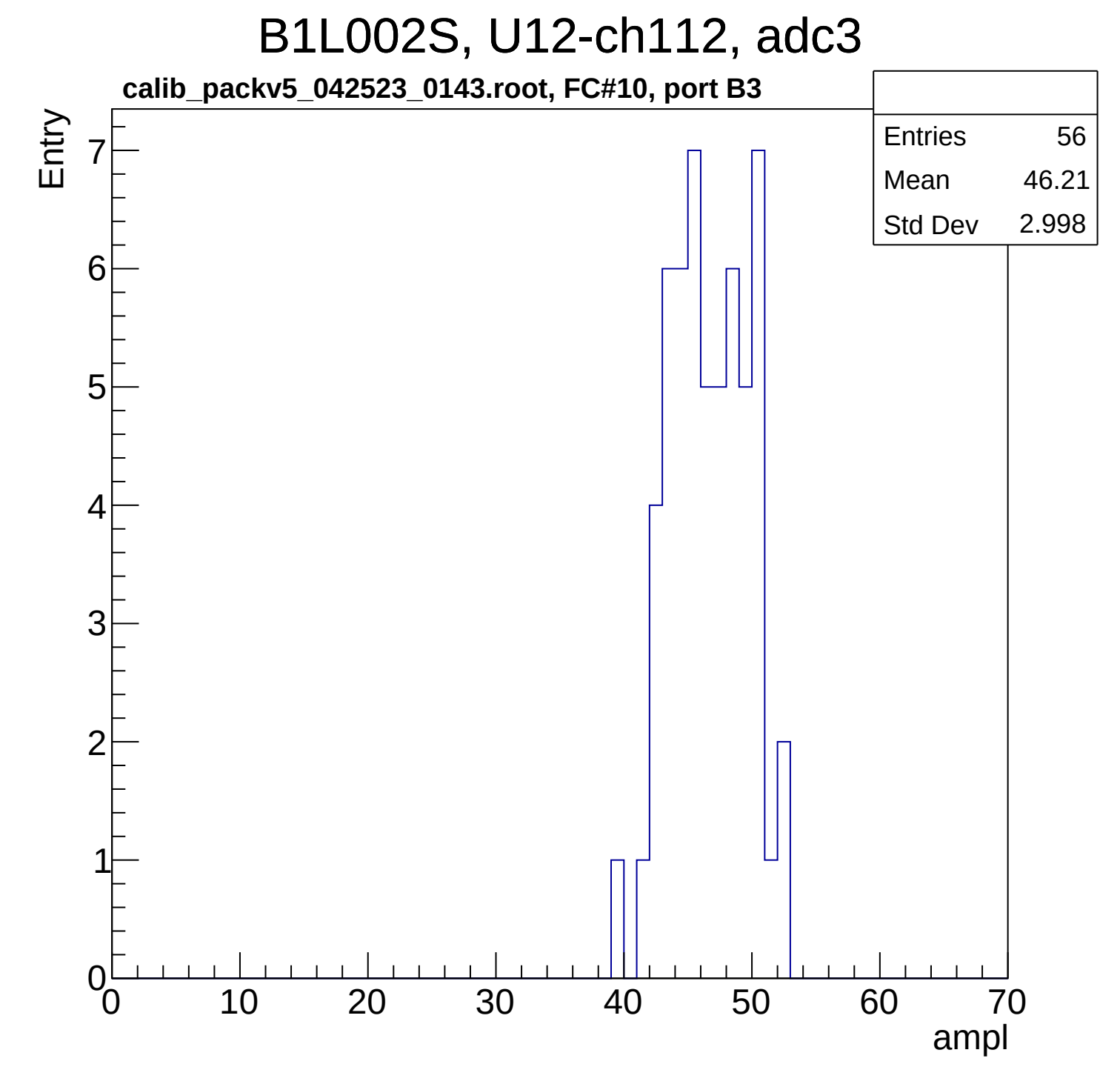
B1L002S, U12-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.21
Std Dev 2.998
Entry
ampl
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2
3
4
5
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0
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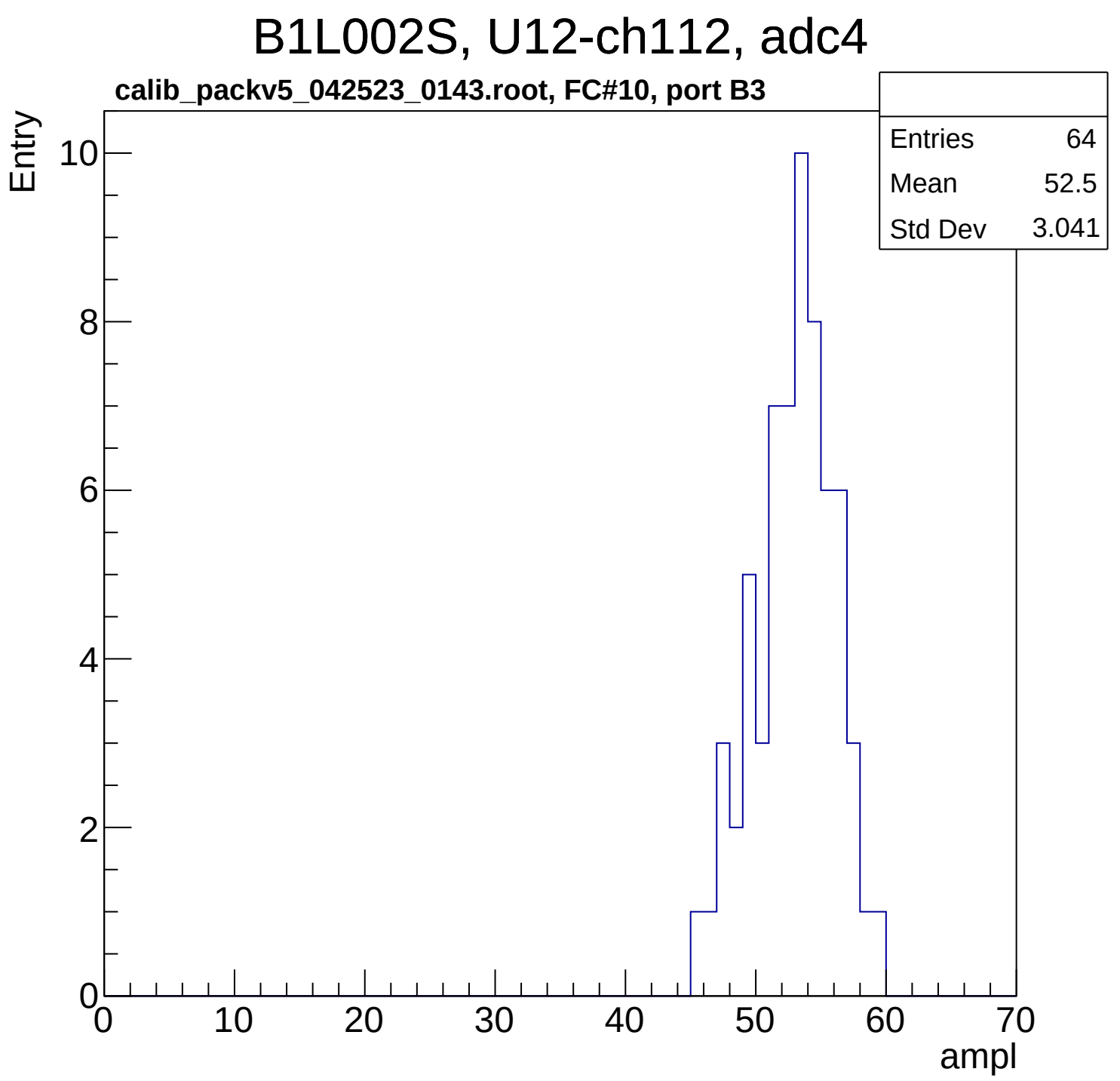
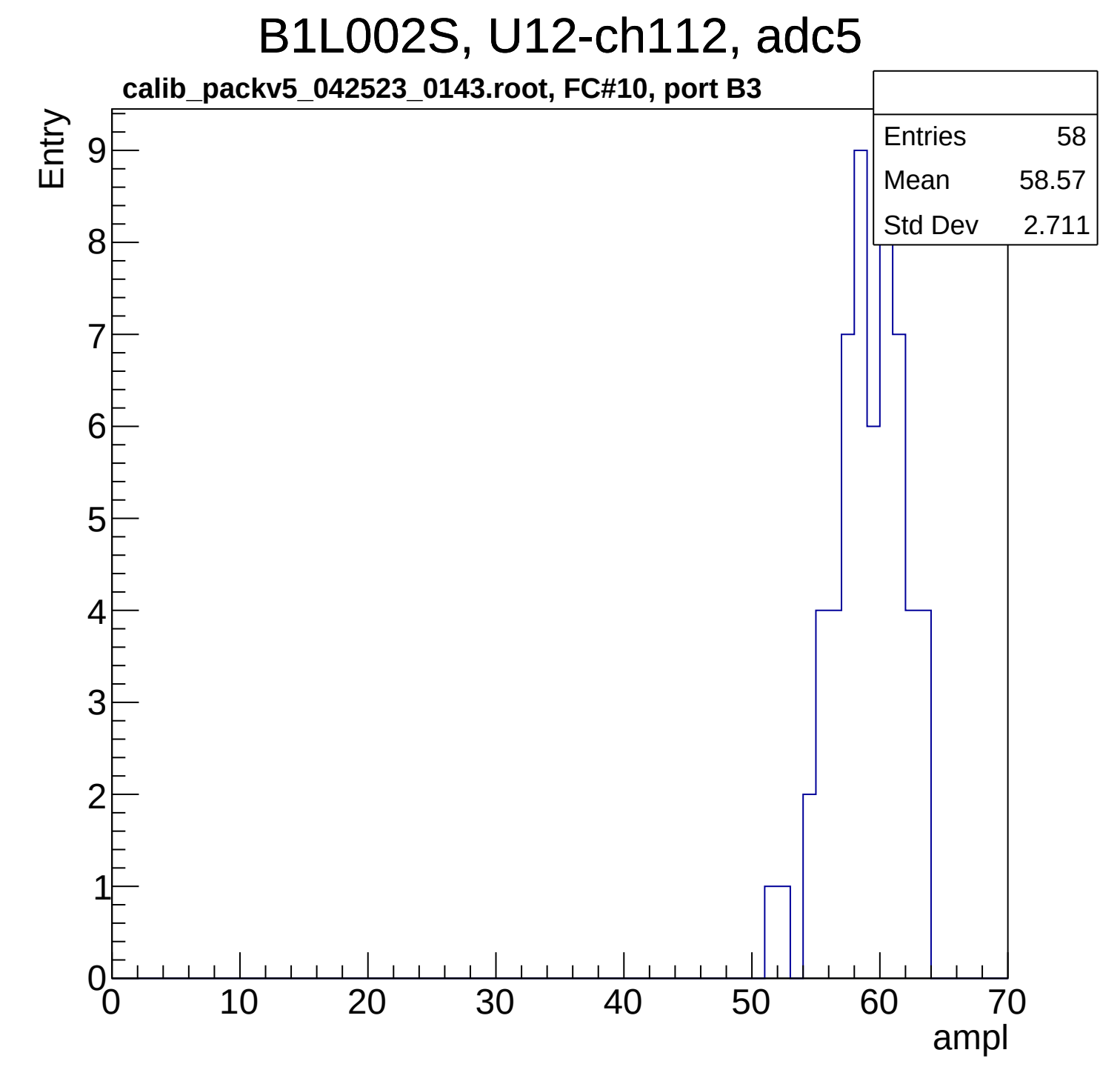

B1L002S, U12-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.5
Std Dev 3.041
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.57
Std Dev 2.711
Entry
ampl
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3
4
5
6
7
8
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0
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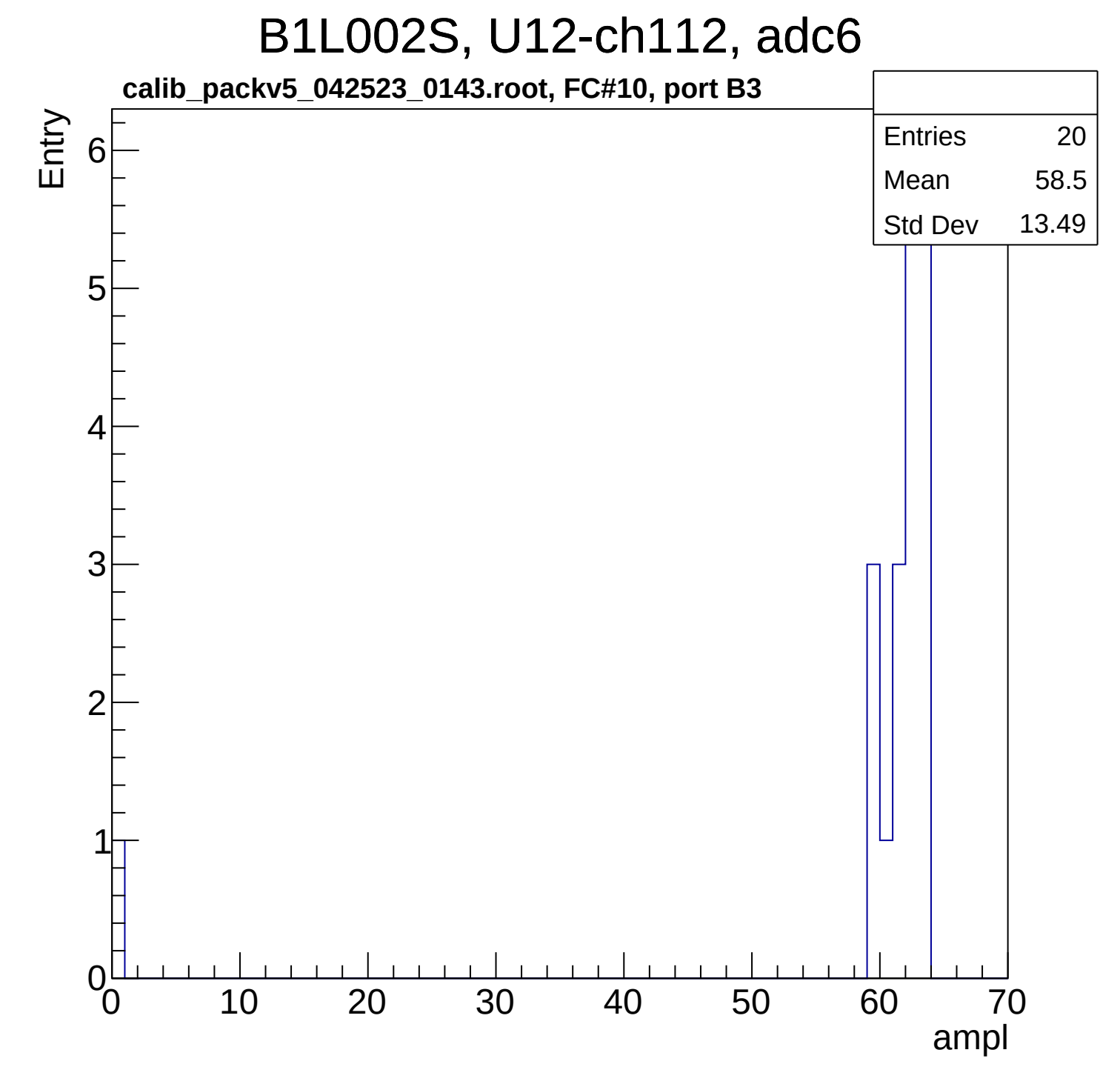
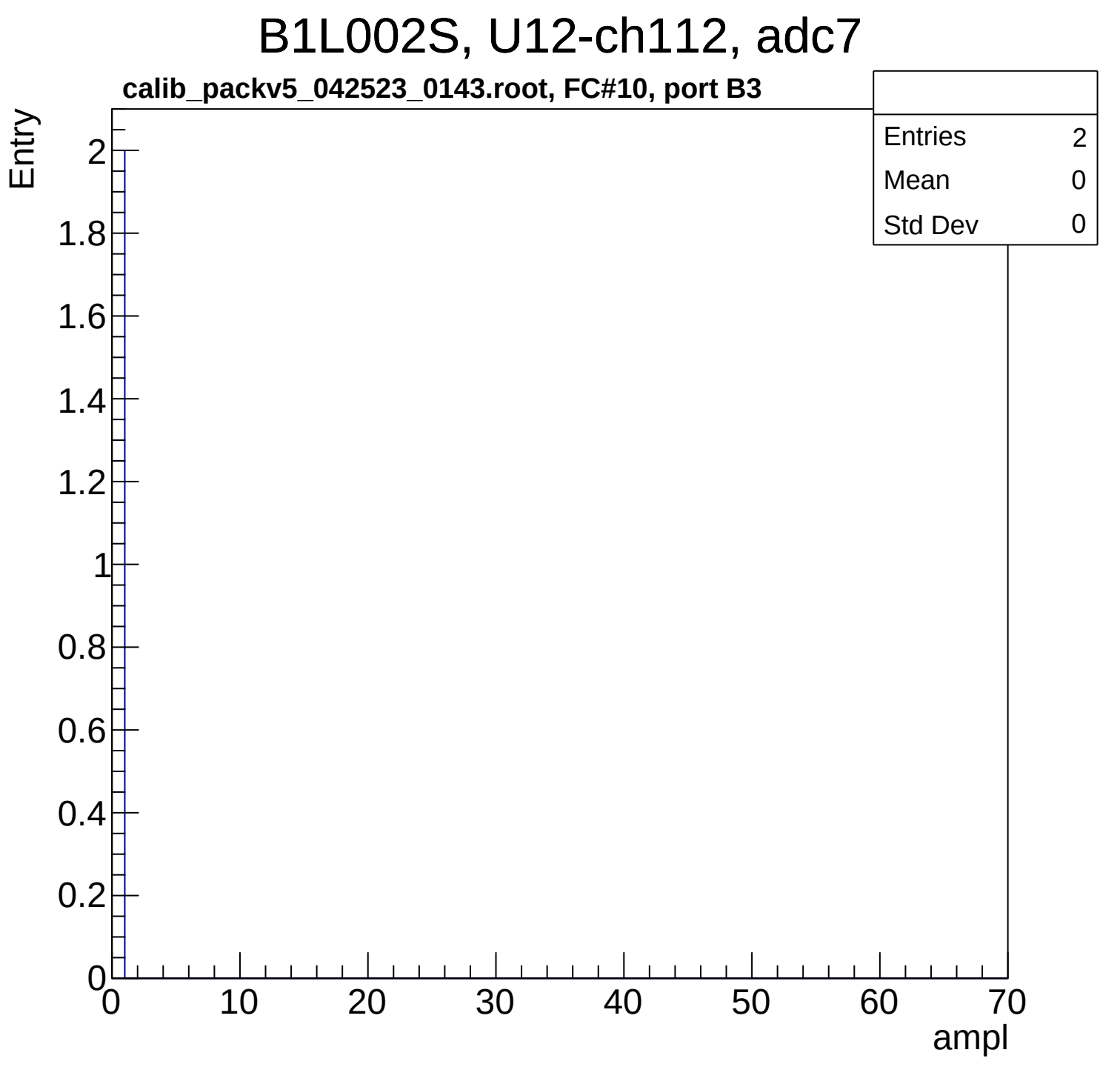

B1L002S, U12-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.5
Std Dev 13.49
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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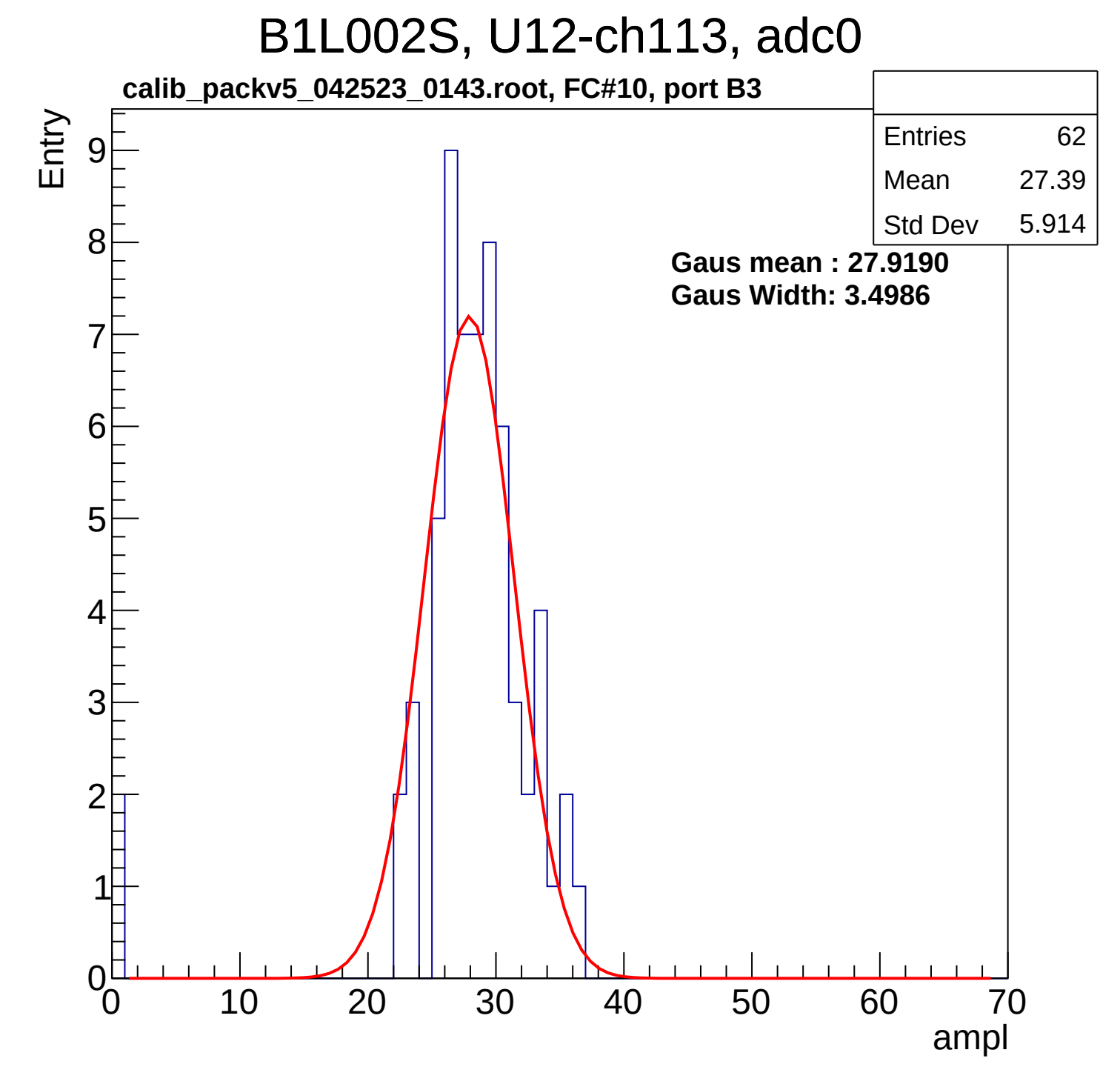
B1L002S, U12-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.39
Std Dev 5.914
Gaus mean : 27.9190
Gaus Width: 3.4986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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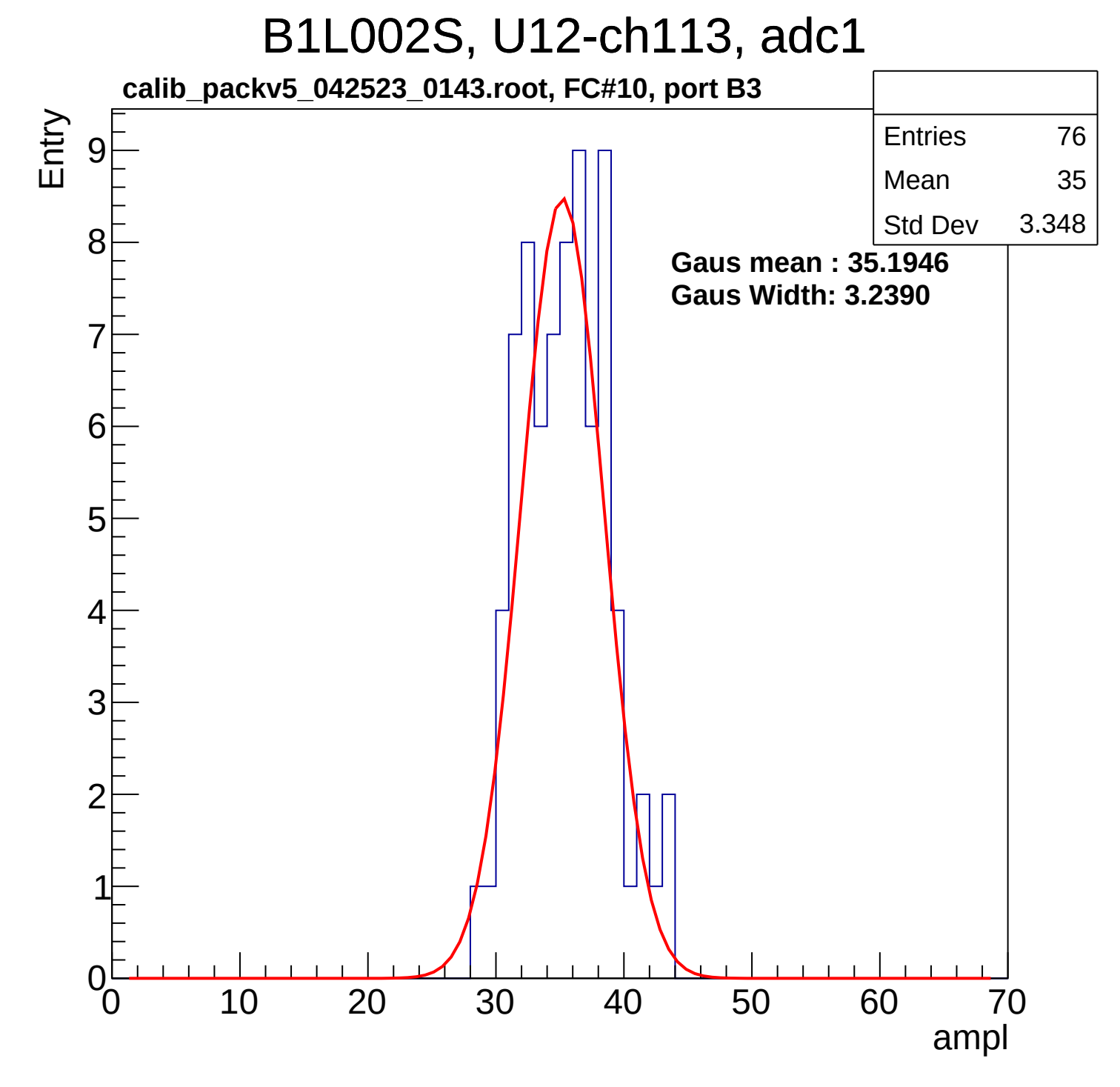
B1L002S, U12-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 35
Std Dev 3.348
Gaus mean : 35.1946
Gaus Width: 3.2390
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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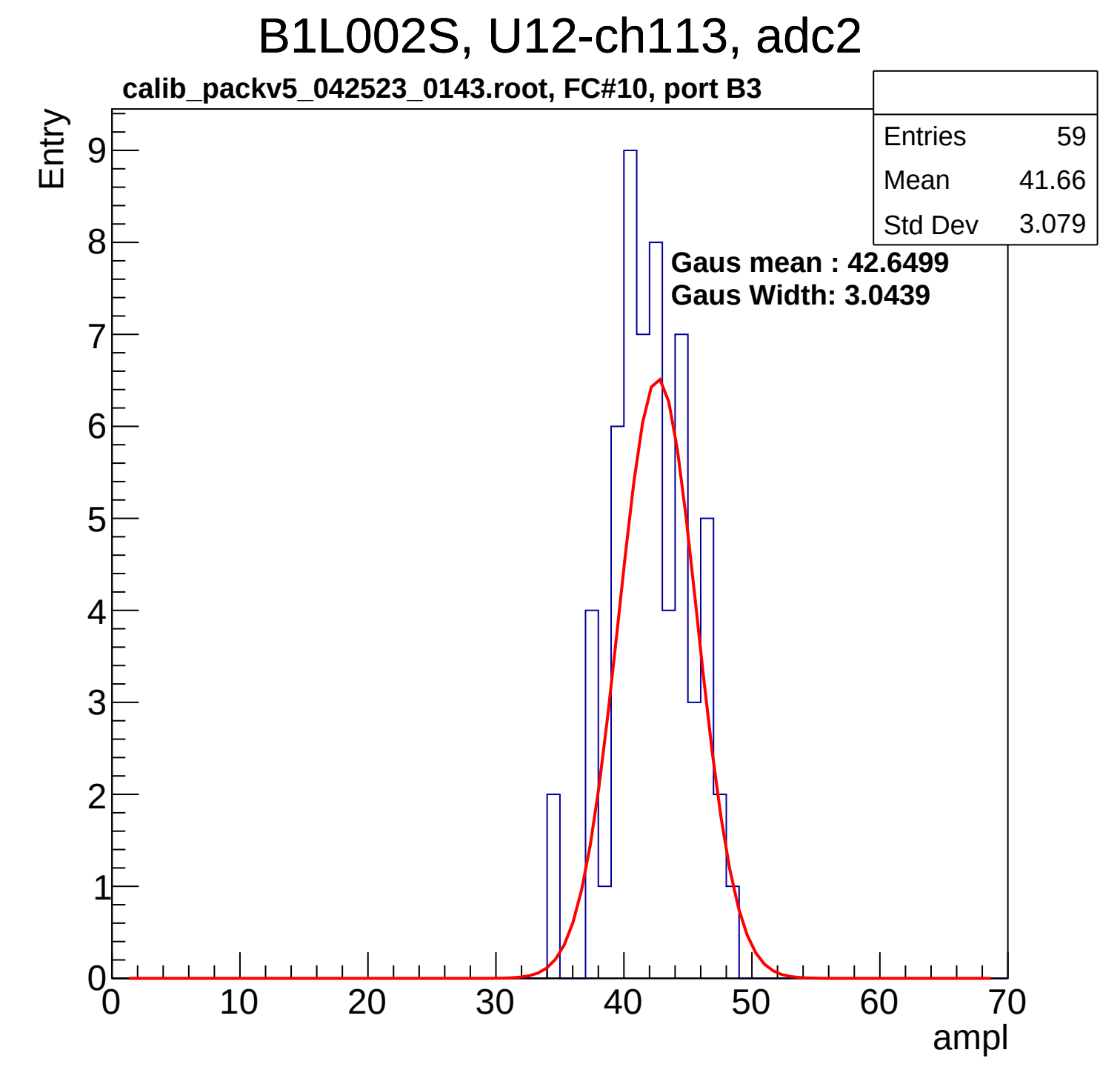
B1L002S, U12-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.66
Std Dev 3.079
Gaus mean : 42.6499
Gaus Width: 3.0439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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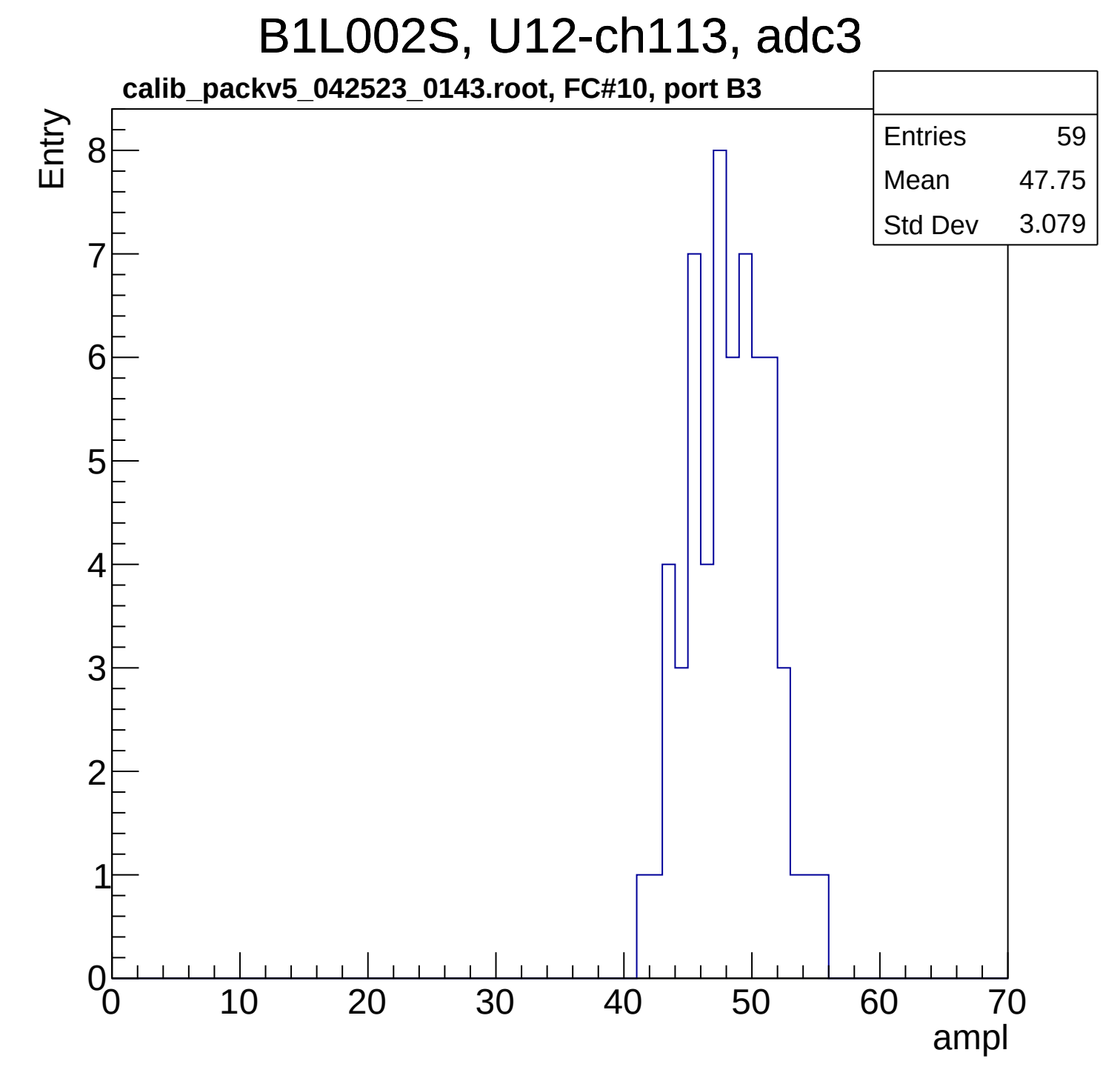
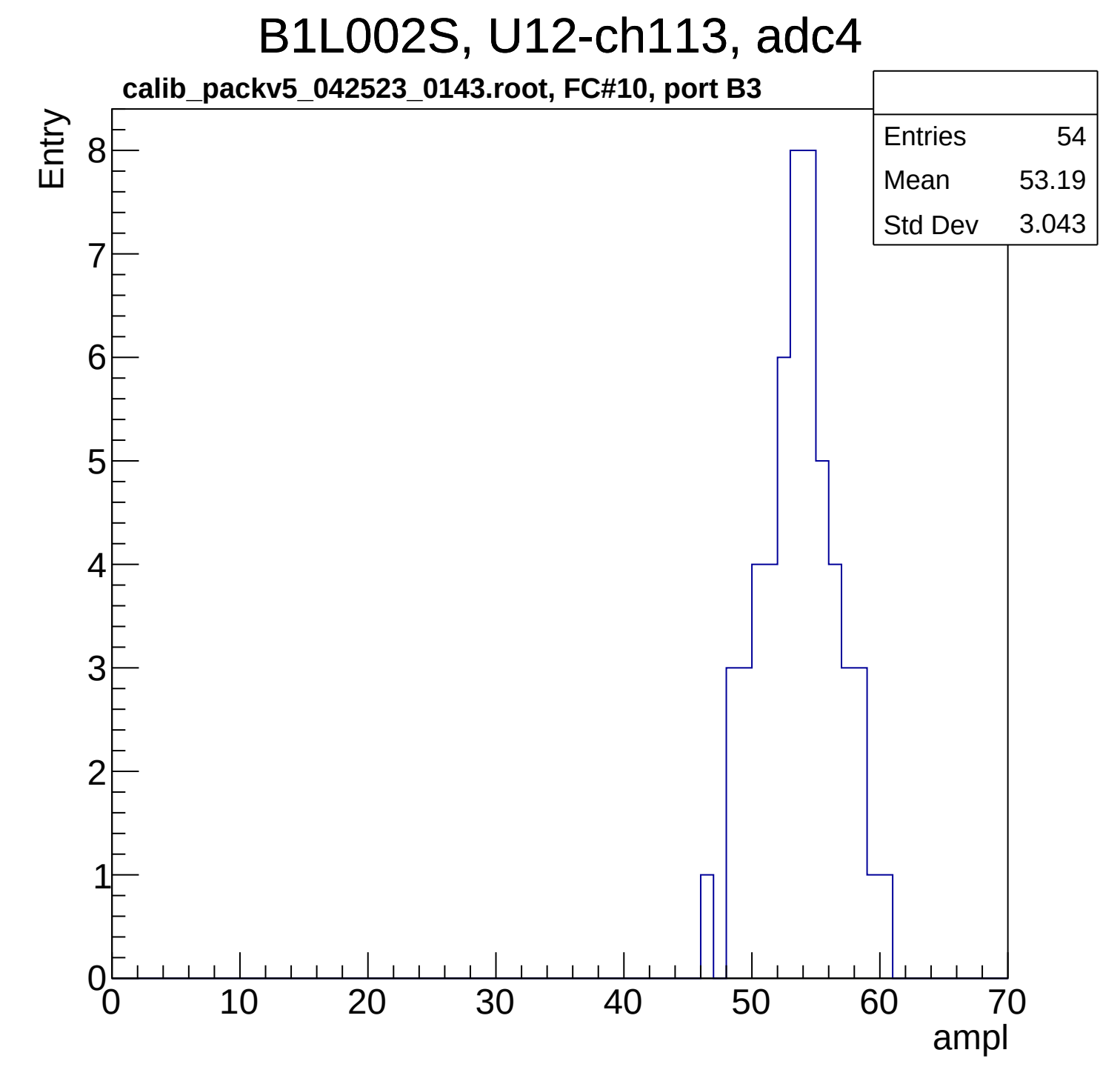

B1L002S, U12-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.75
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.19
Std Dev 3.043
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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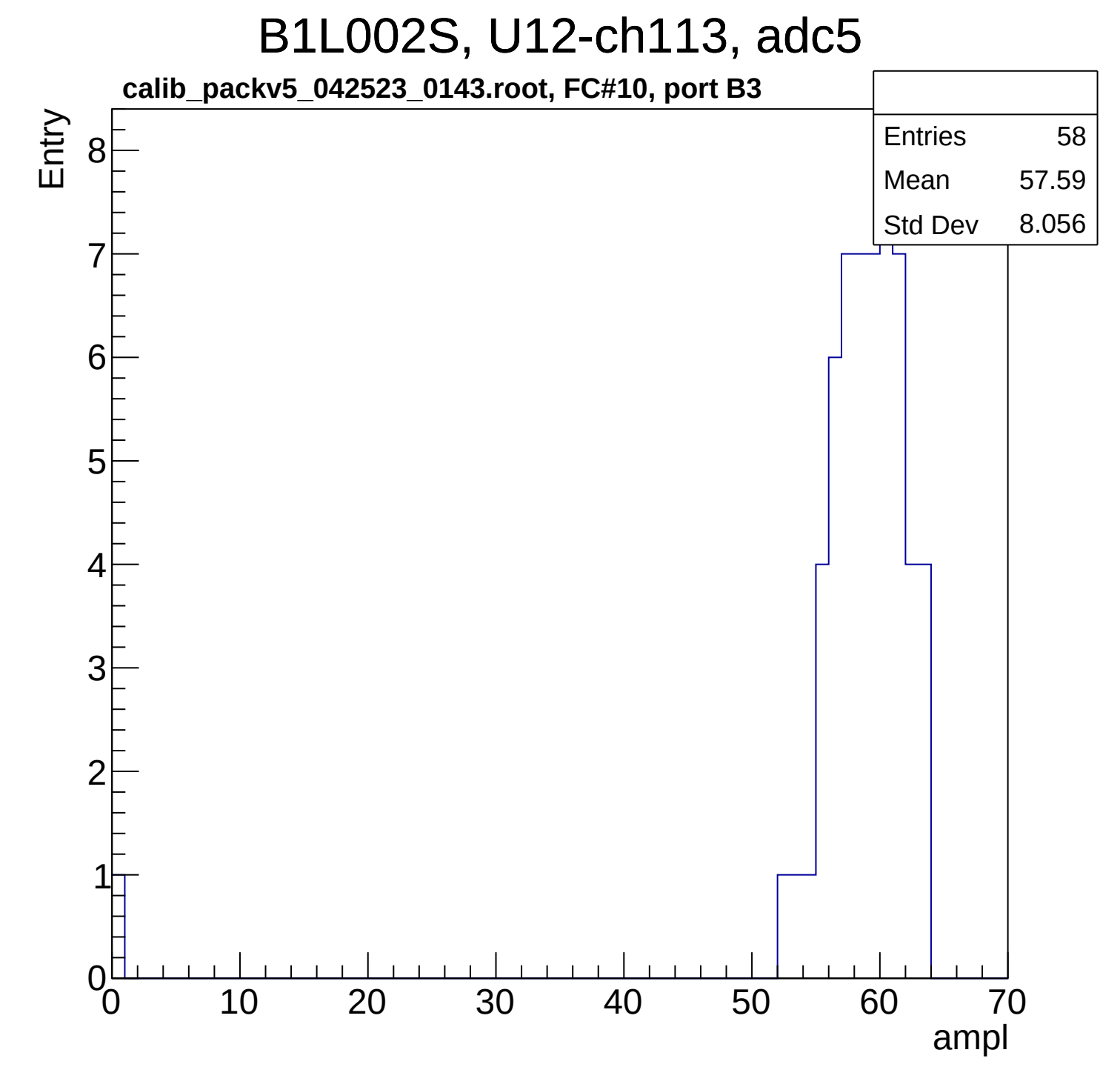
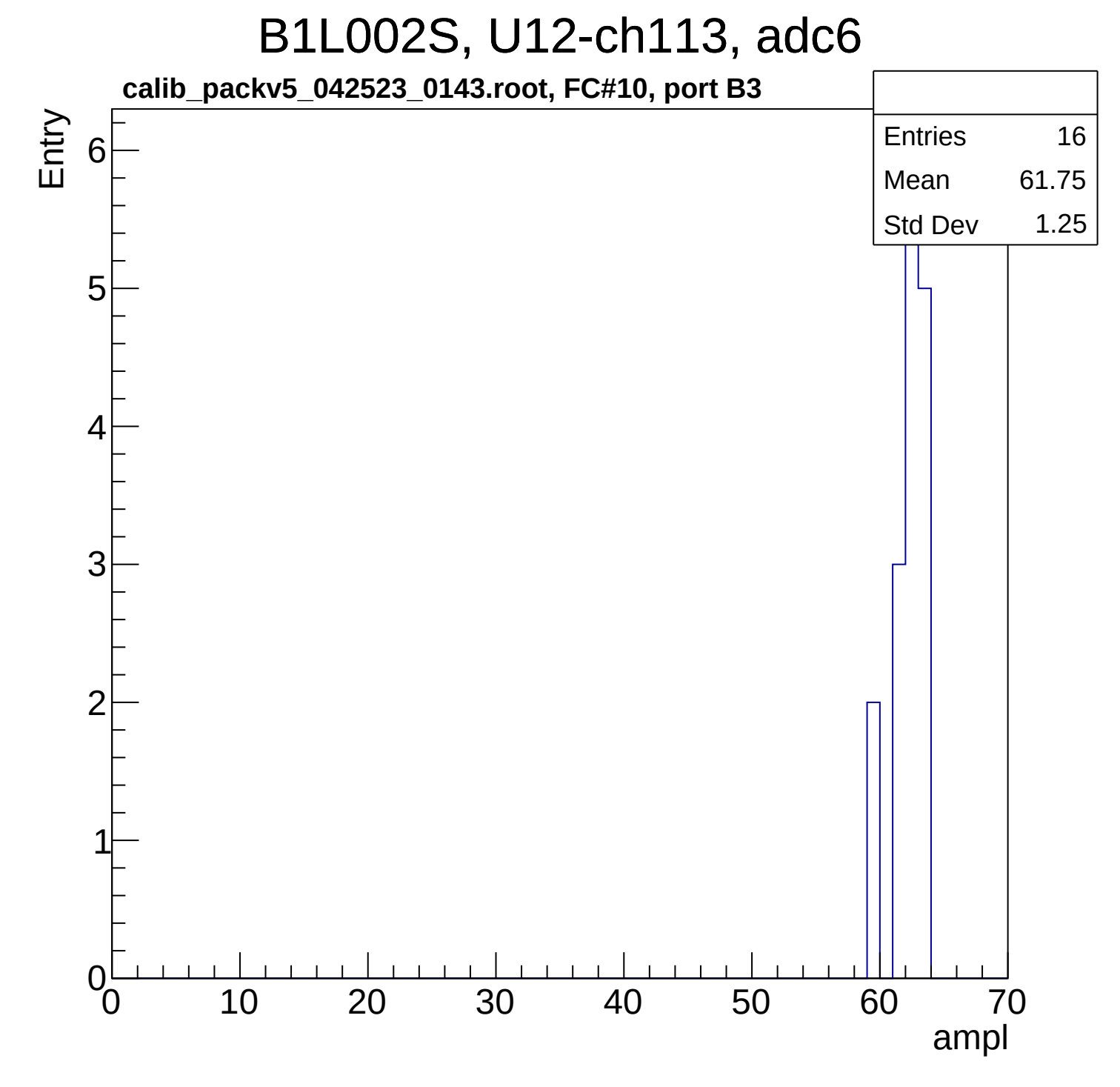
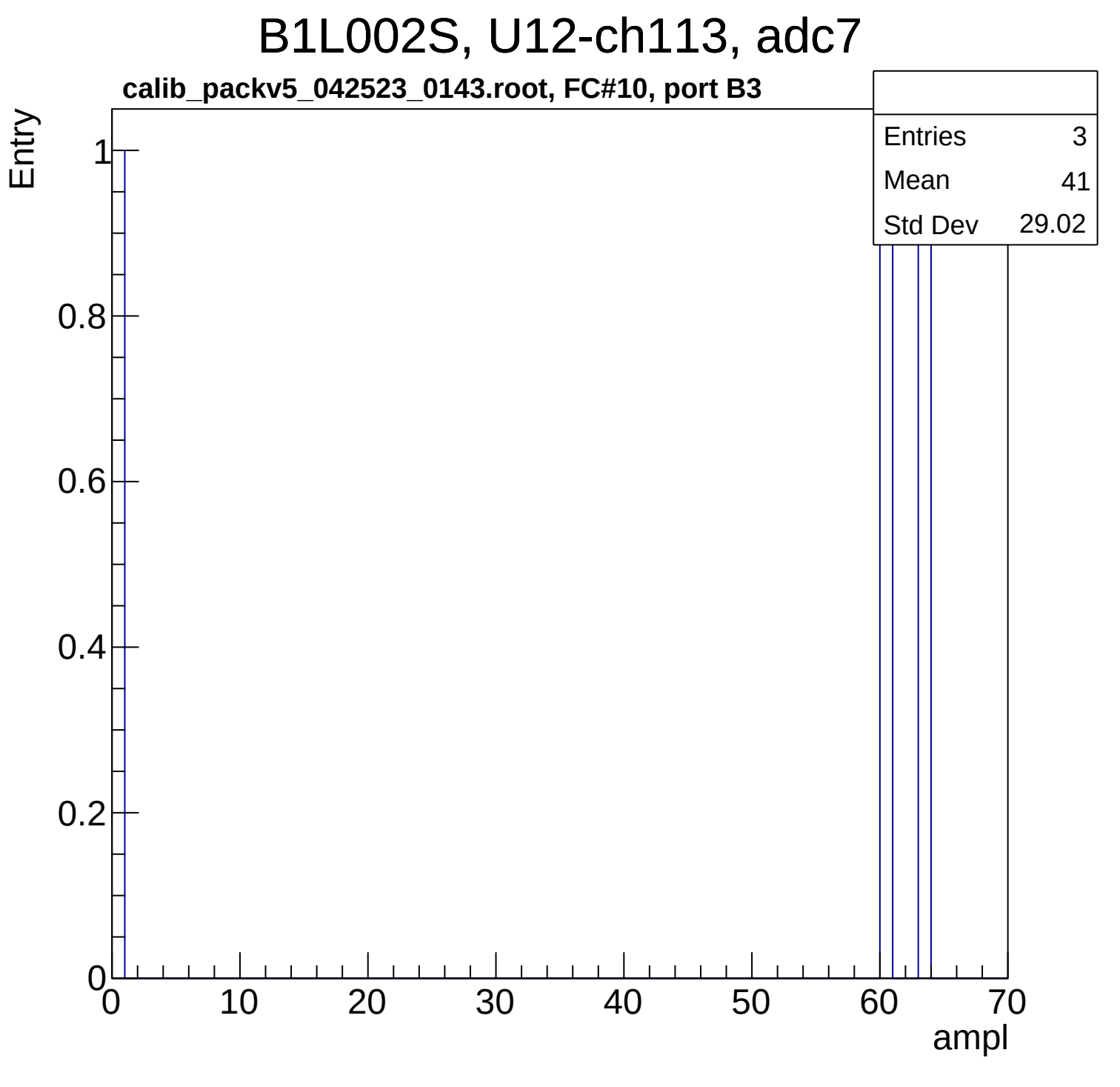

B1L002S, U12-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.59
Std Dev 8.056
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.75
Std Dev 1.25
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
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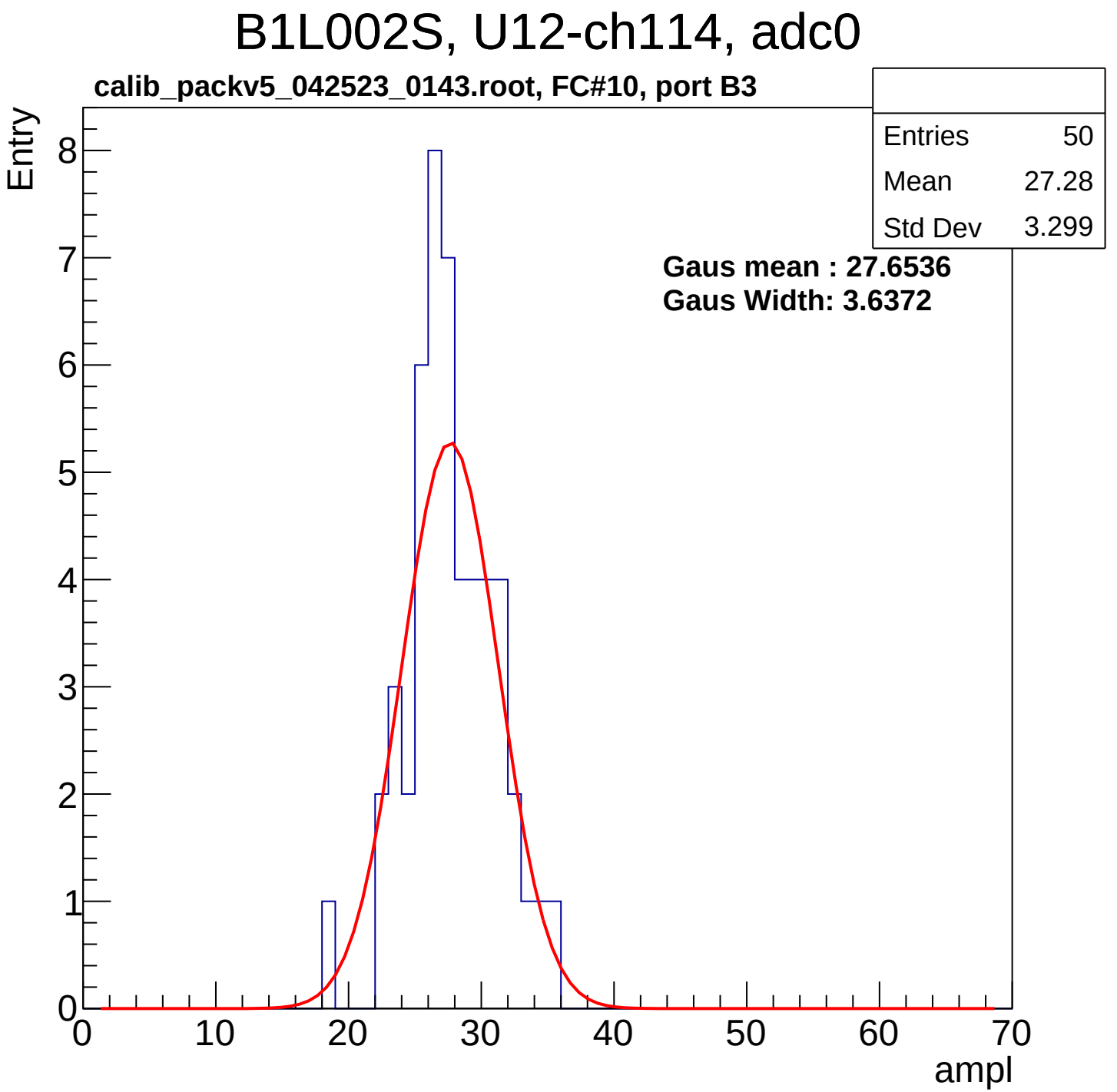
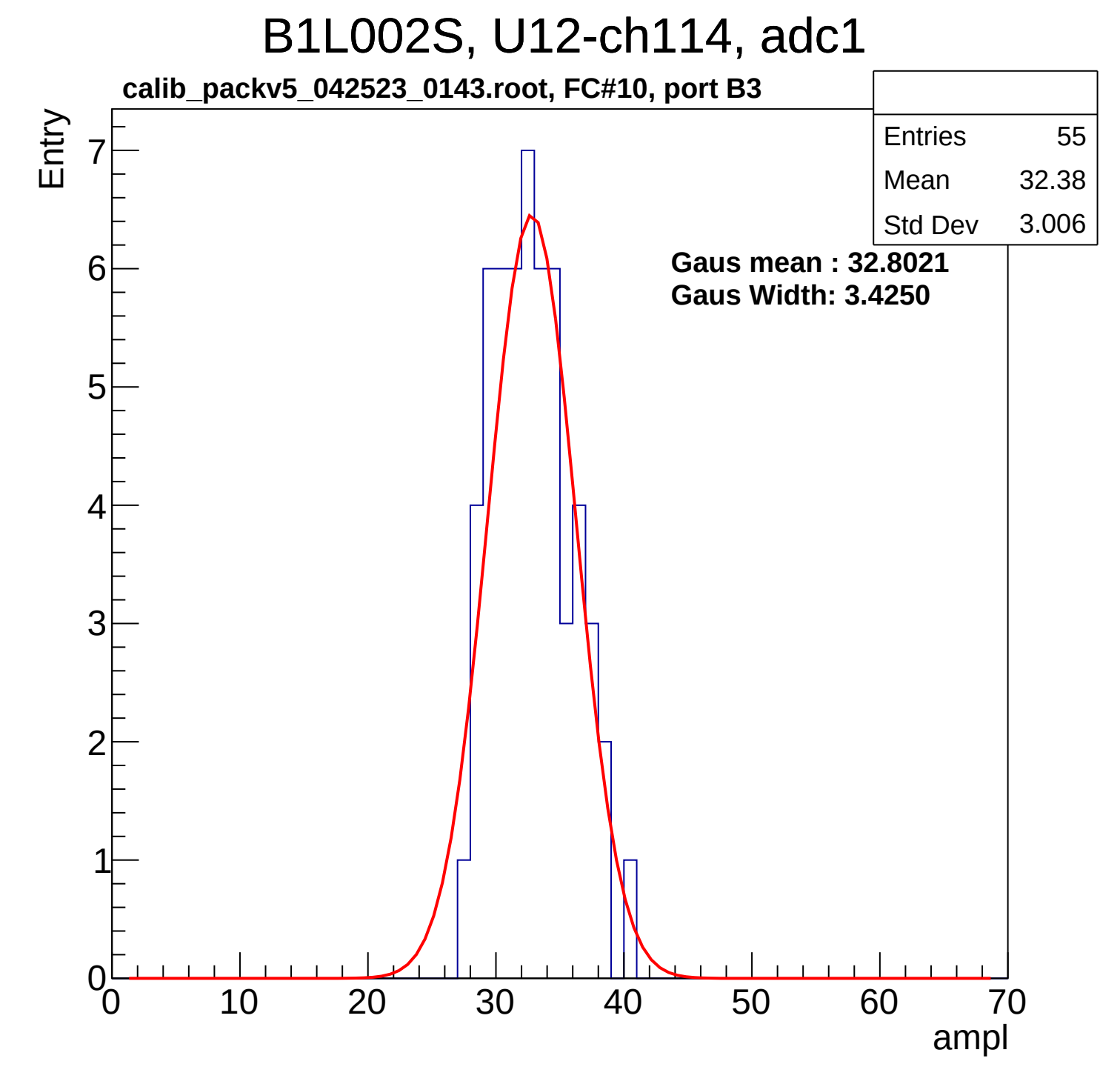
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 27.28
Std Dev 3.299
Gaus mean : 27.6536
Gaus Width: 3.6372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 32.38
Std Dev 3.006
Gaus mean : 32.8021
Gaus Width: 3.4250
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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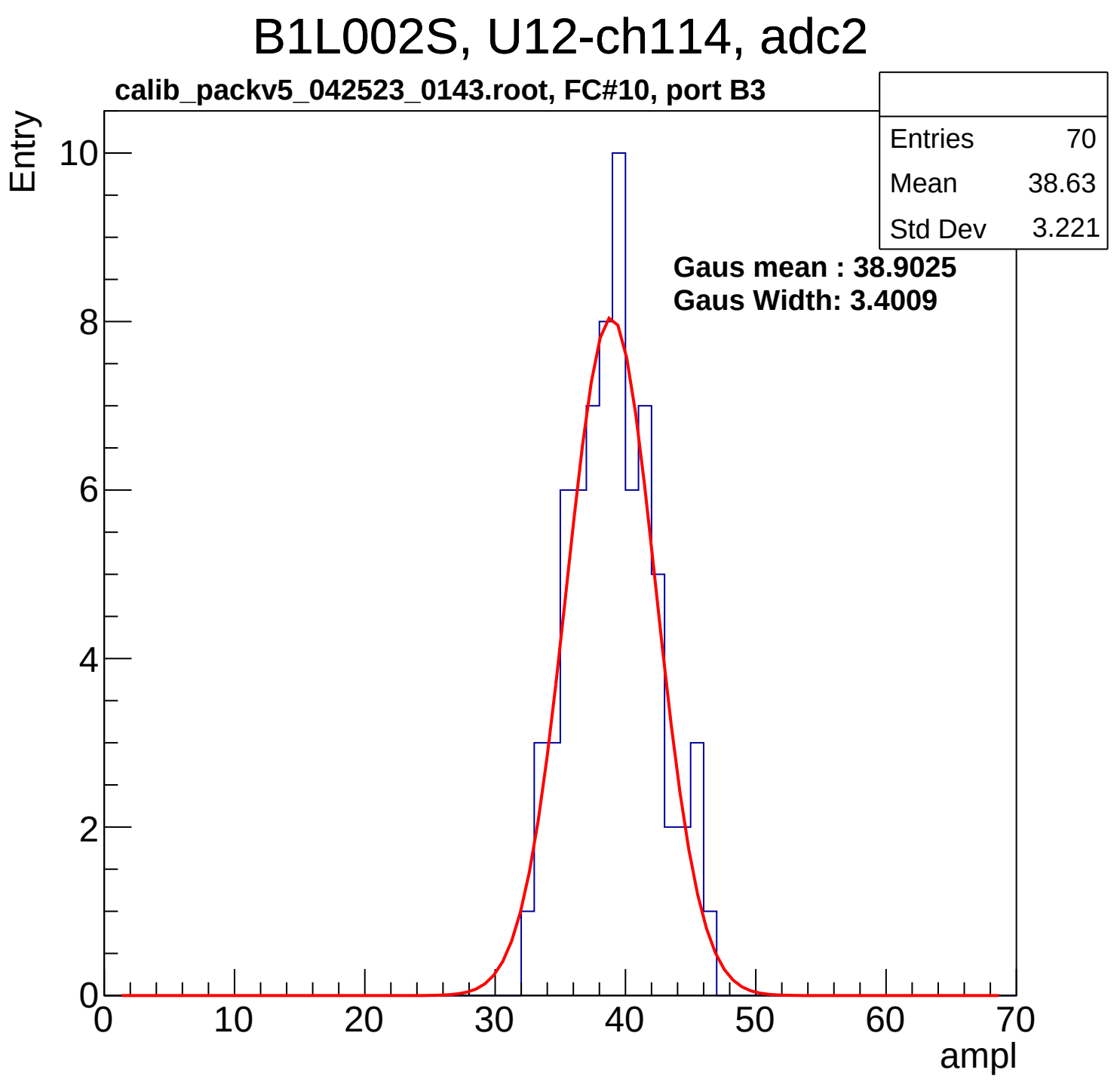
B1L002S, U12-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 38.63
Std Dev 3.221
Gaus mean : 38.9025
Gaus Width: 3.4009
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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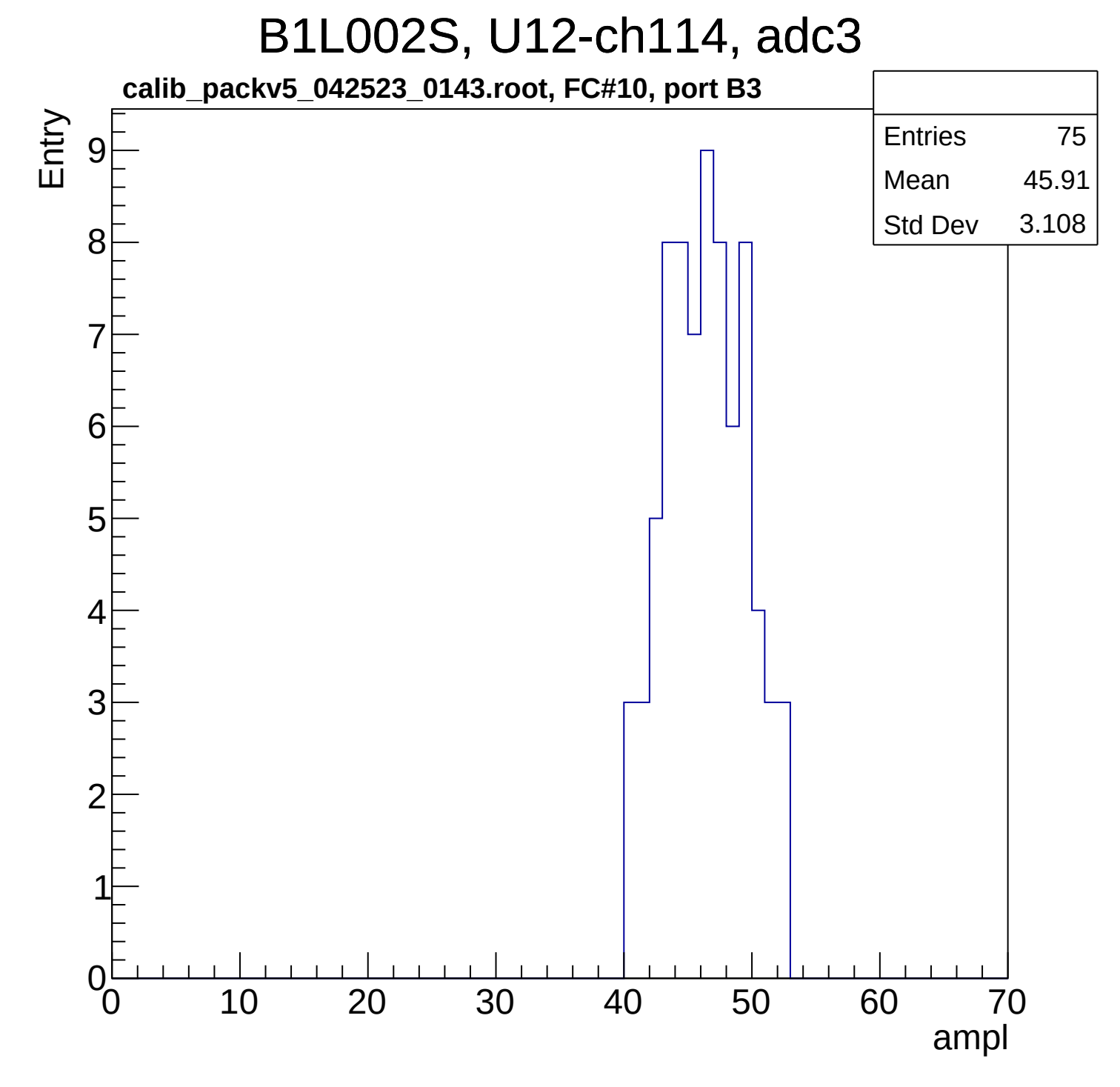
B1L002S, U12-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 45.91
Std Dev 3.108
Entry
ampl
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3
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5
6
7
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0
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60
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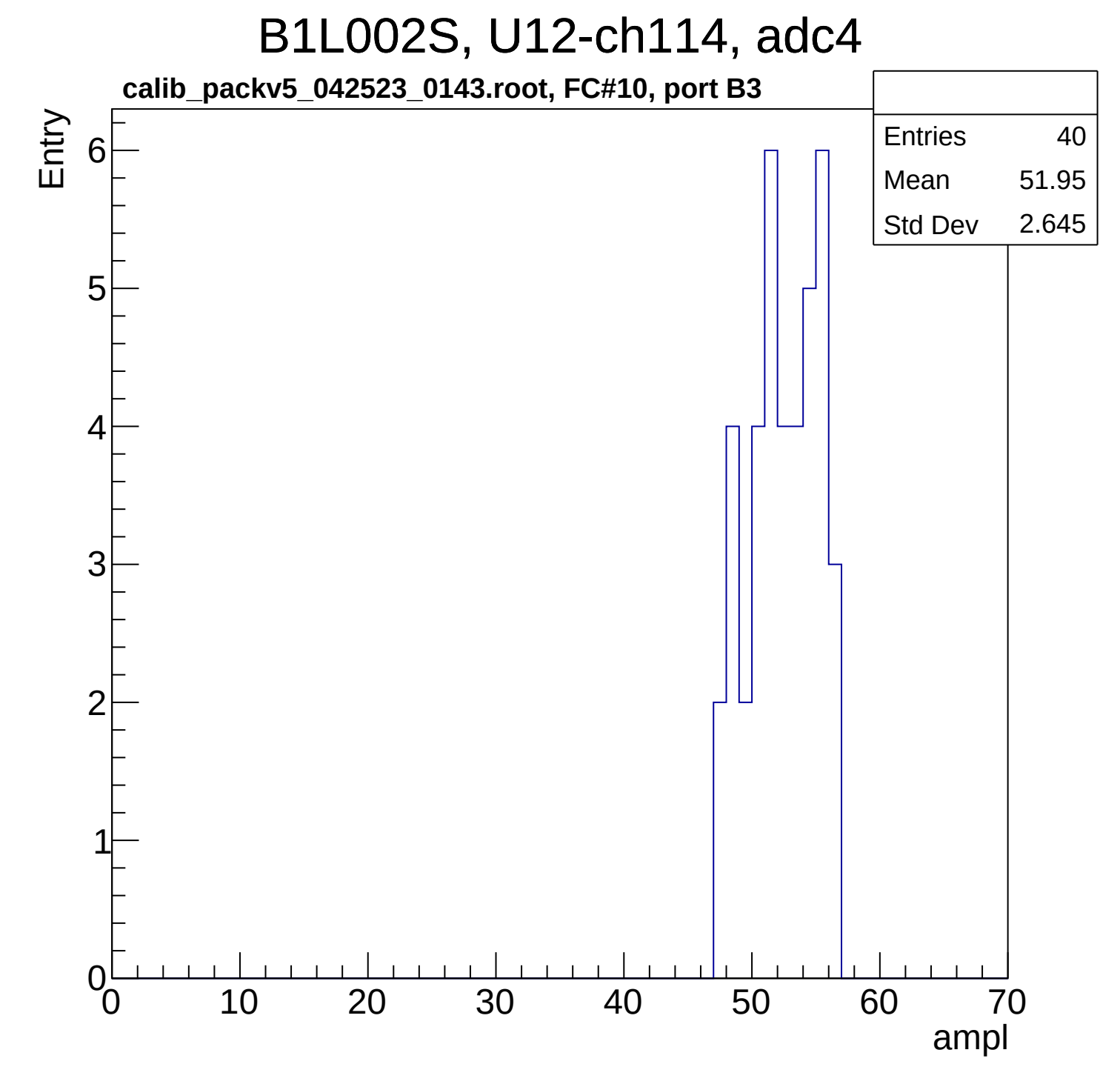
B1L002S, U12-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 51.95
Std Dev 2.645
Entry
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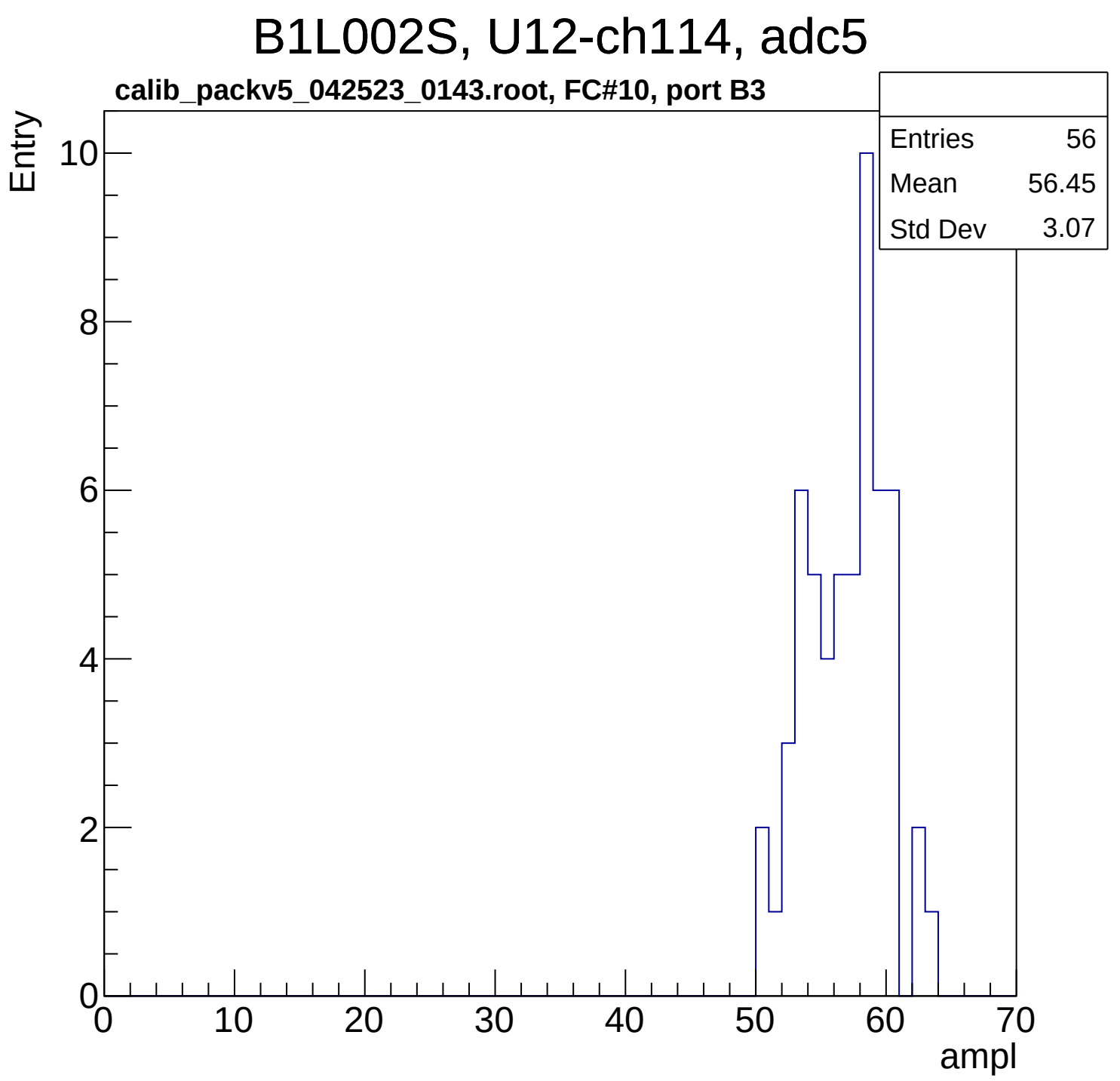
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 56.45
Std Dev 3.07
Entry
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6
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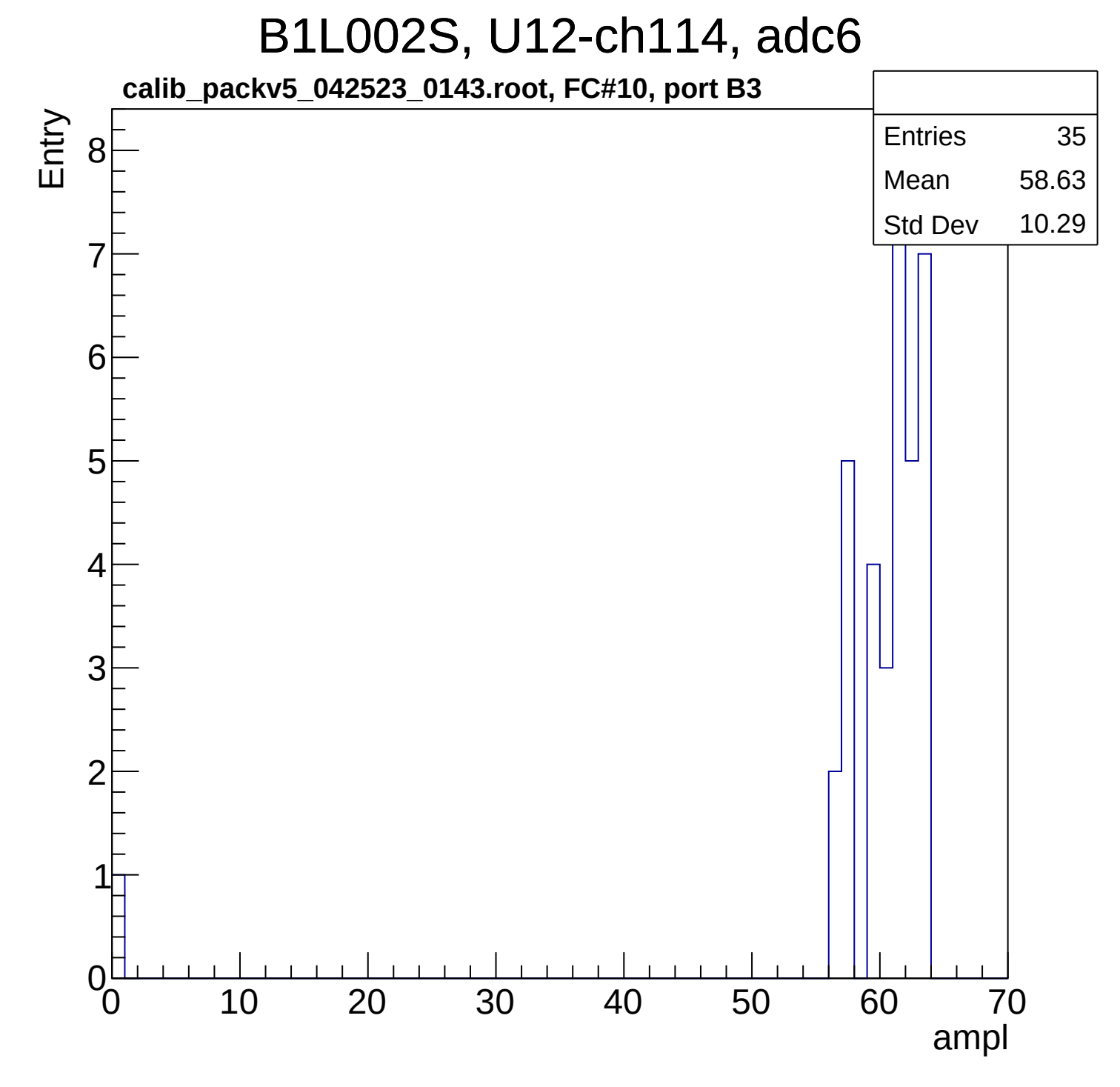
50
60
70
ampl


B1L002S, U12-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.63
Std Dev 10.29
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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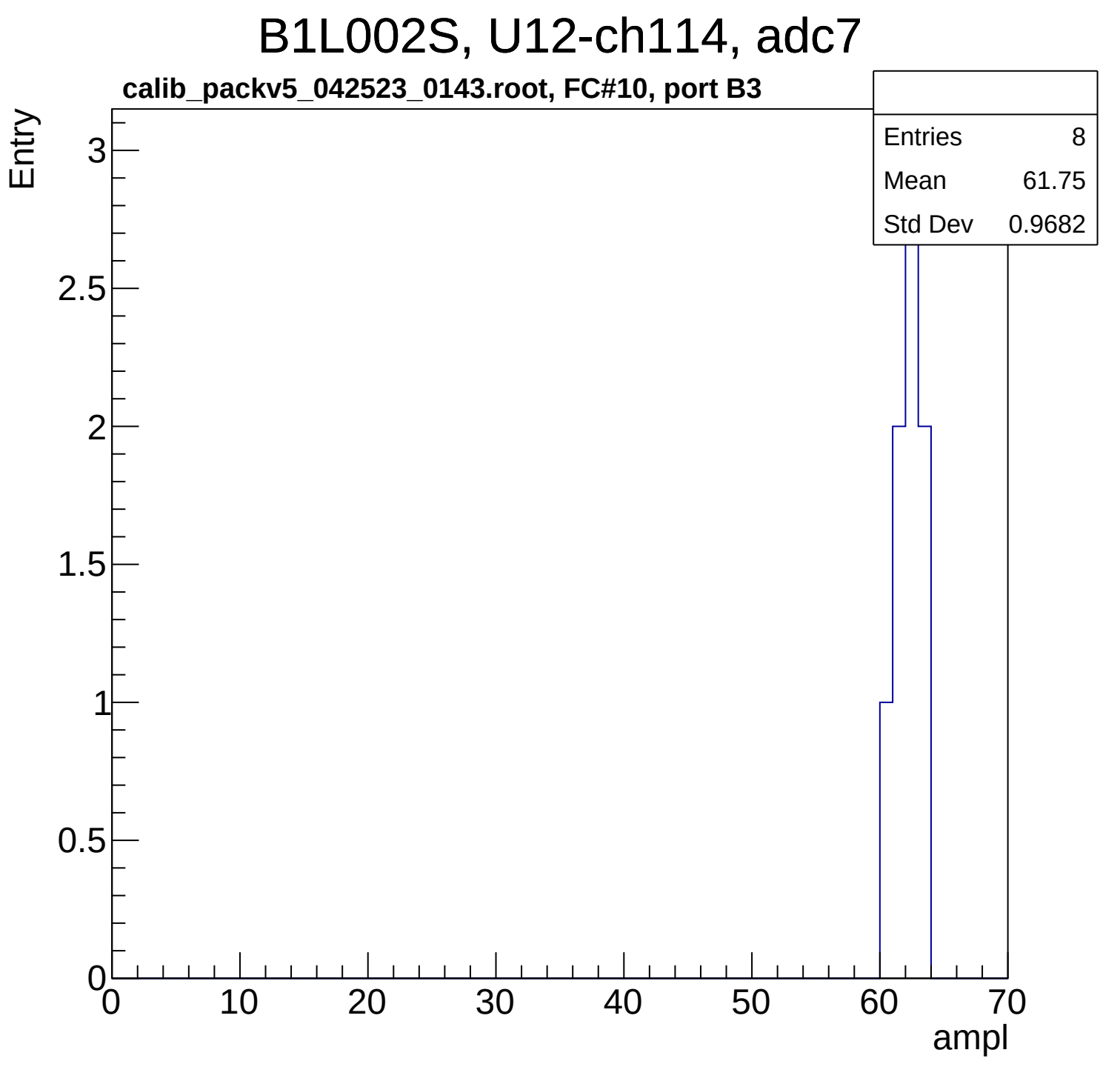
60
70


B1L002S, U12-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61.75
Std Dev 0.9682
Entry
ampl
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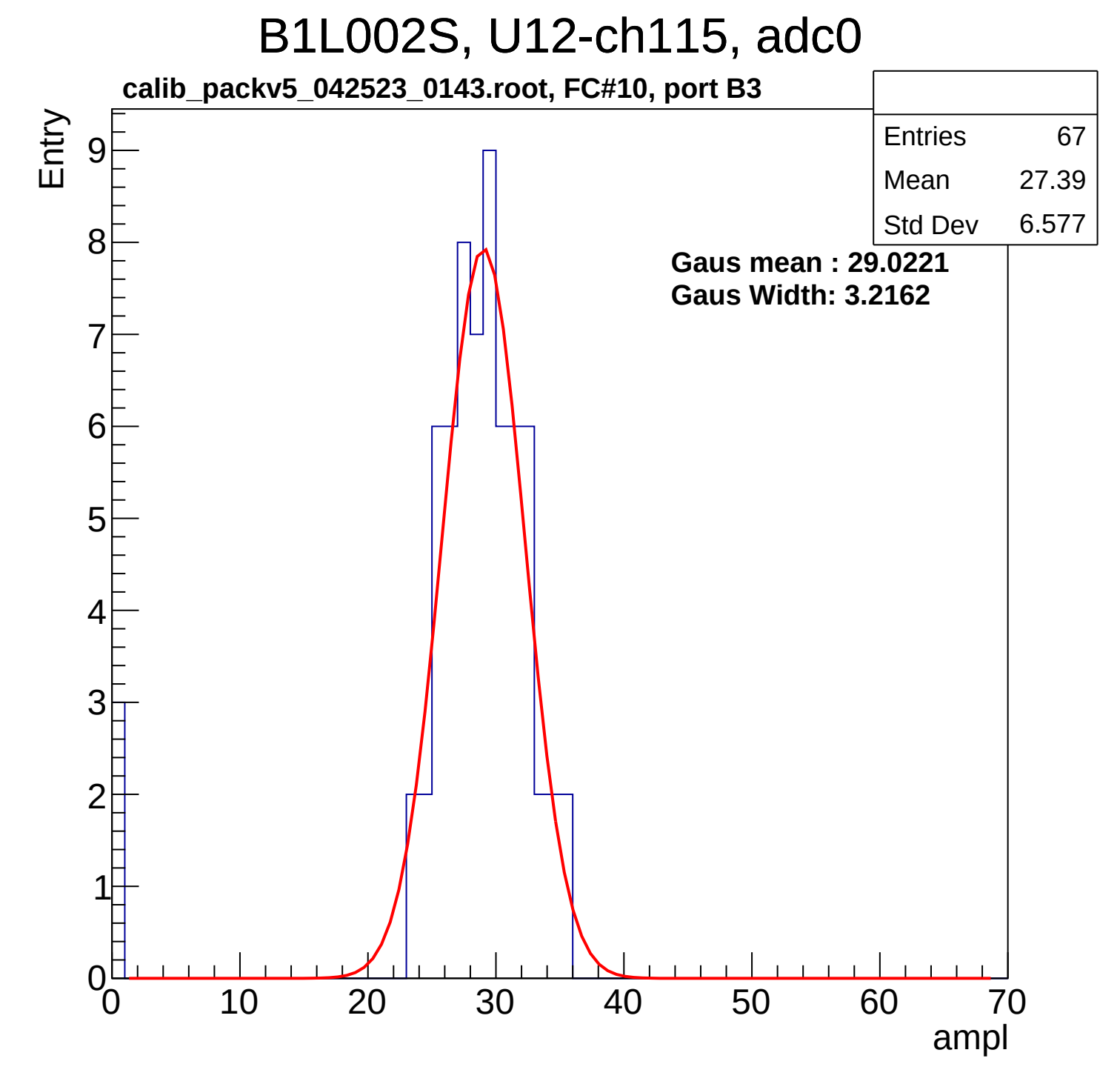
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.39
Std Dev 6.577
Gaus mean : 29.0221
Gaus Width: 3.2162
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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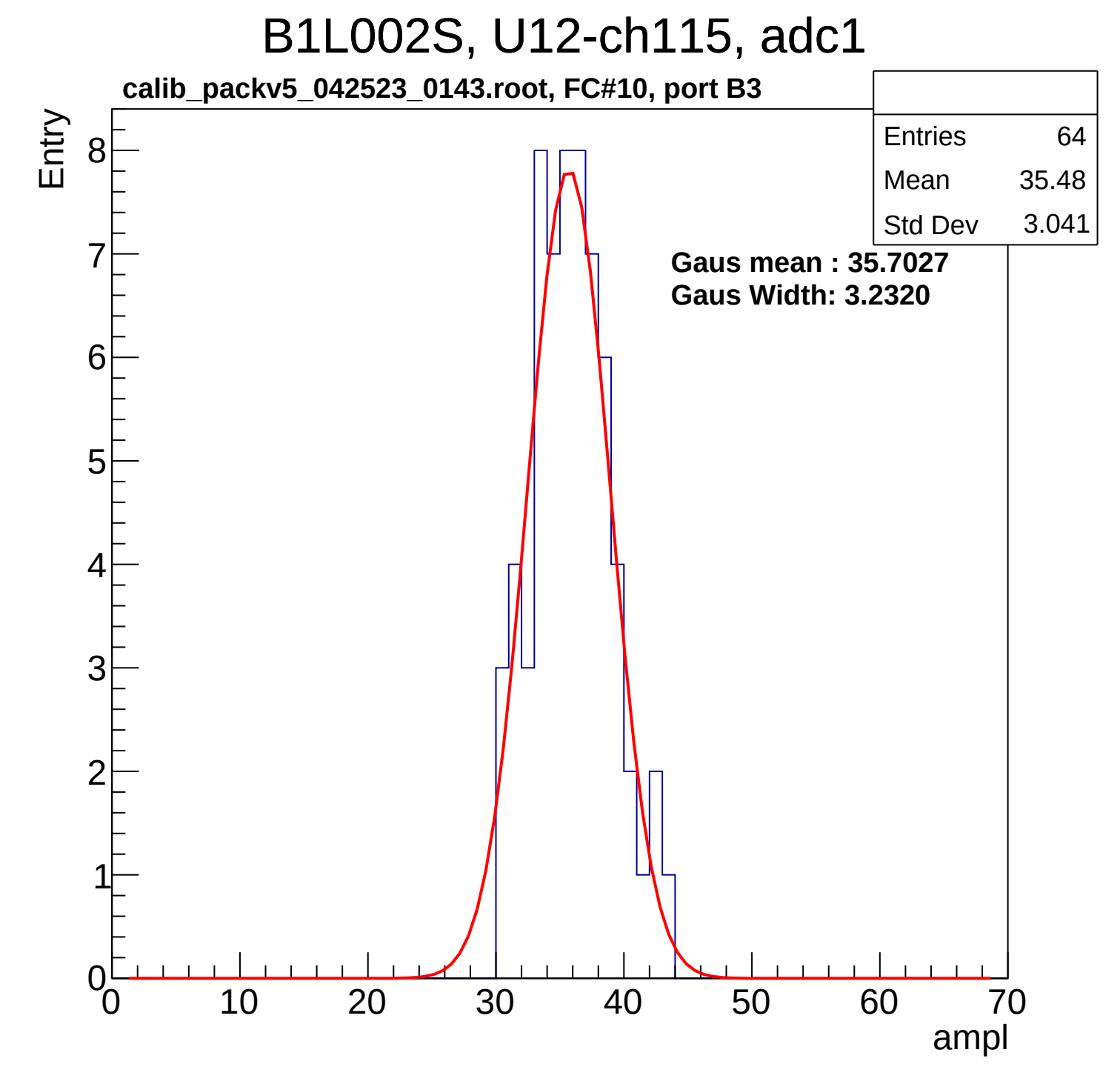
B1L002S, U12-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.48
Std Dev 3.041
Gaus mean : 35.7027
Gaus Width: 3.2320
Entry
ampl
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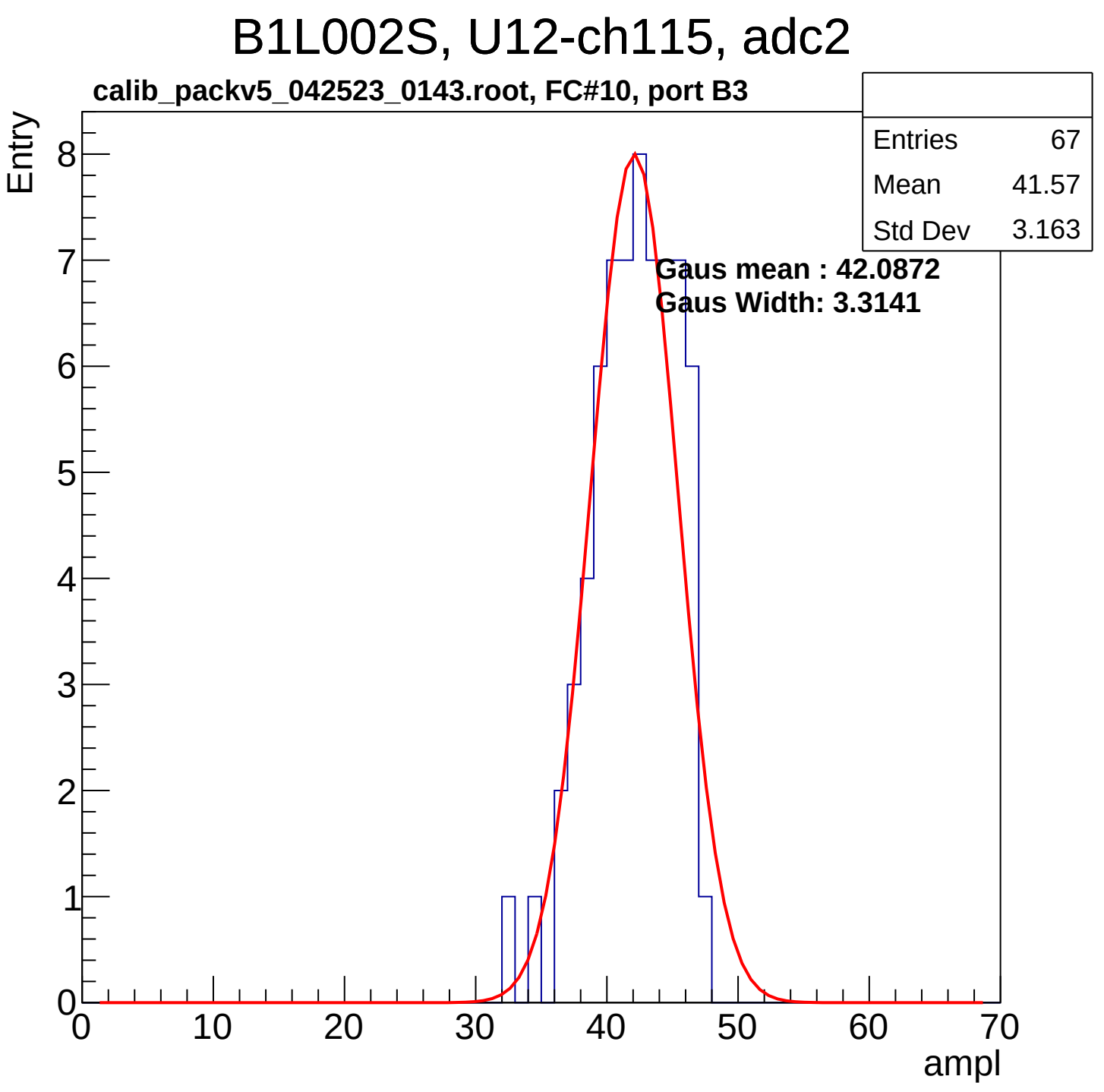
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.57
Std Dev 3.163
Gaus mean : 42.0872
Gaus Width: 3.3141
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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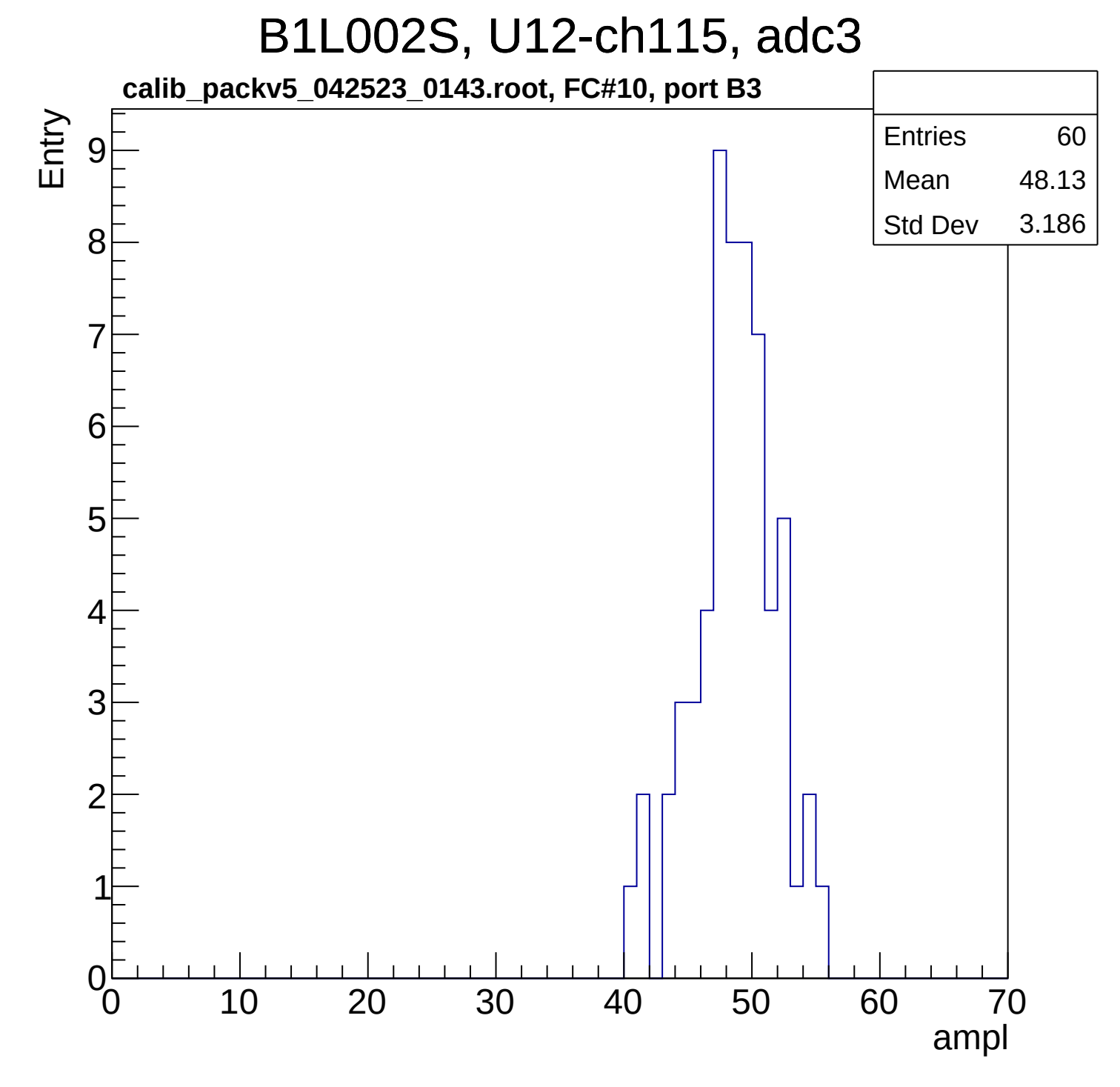
B1L002S, U12-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.13
Std Dev 3.186
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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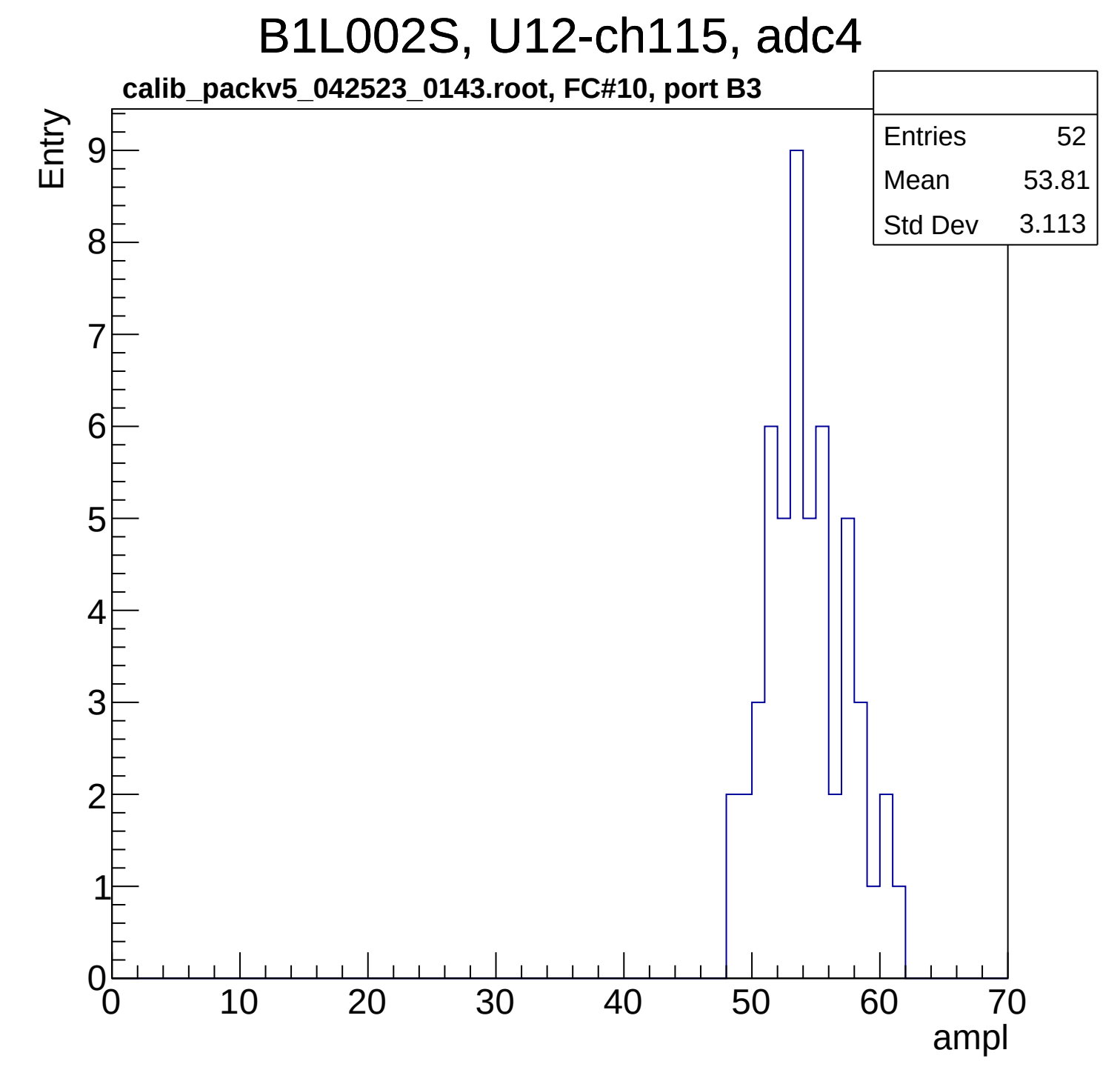
40
50
60
70


B1L002S, U12-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.81
Std Dev 3.113
Entry
ampl
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10
20
30
40
50
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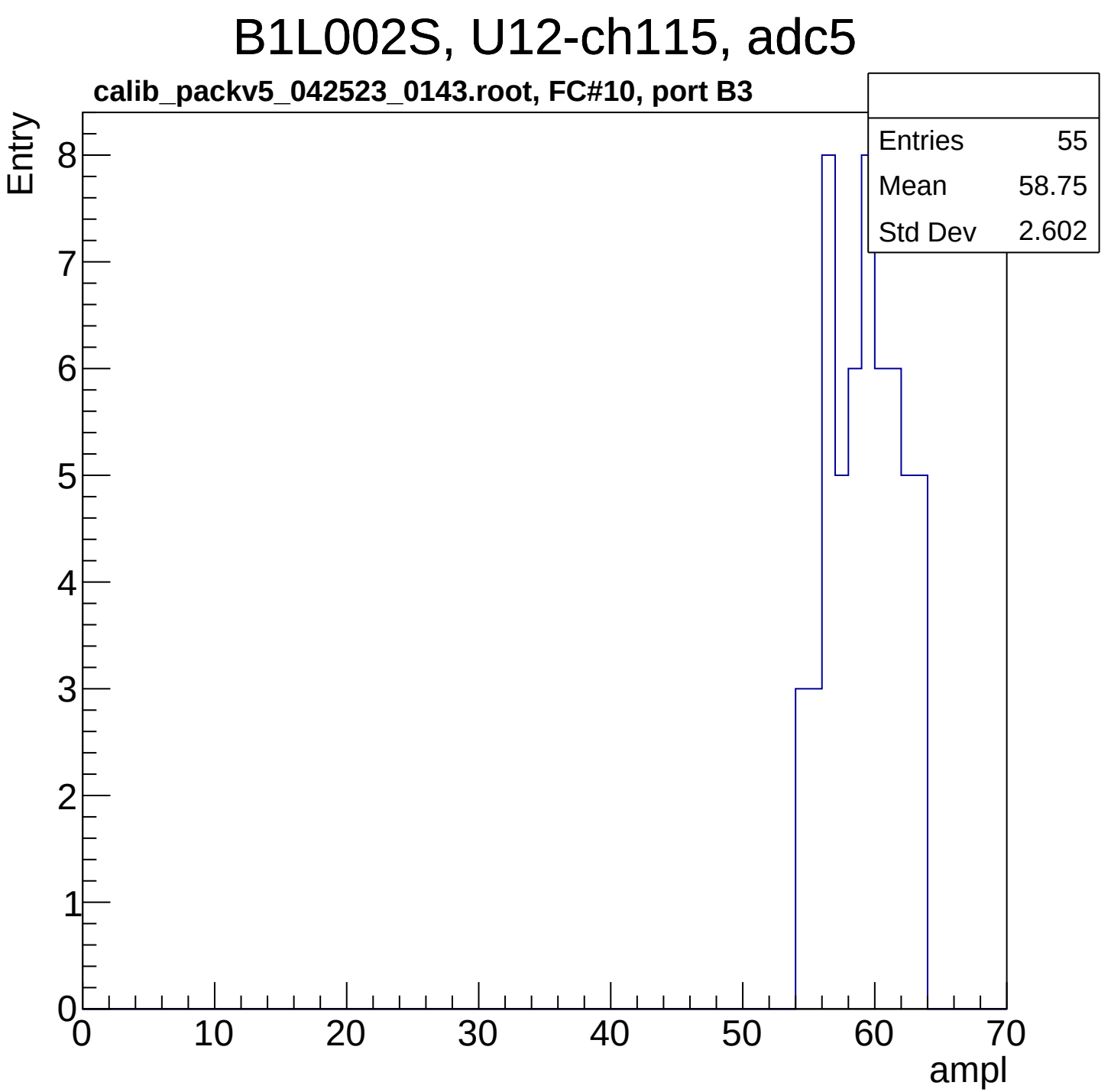
70
1
2
3
4
5
6
7
8
9


B1L002S, U12-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.75
Std Dev 2.602
Entry
ampl
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2
3
4
5
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0
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40
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60
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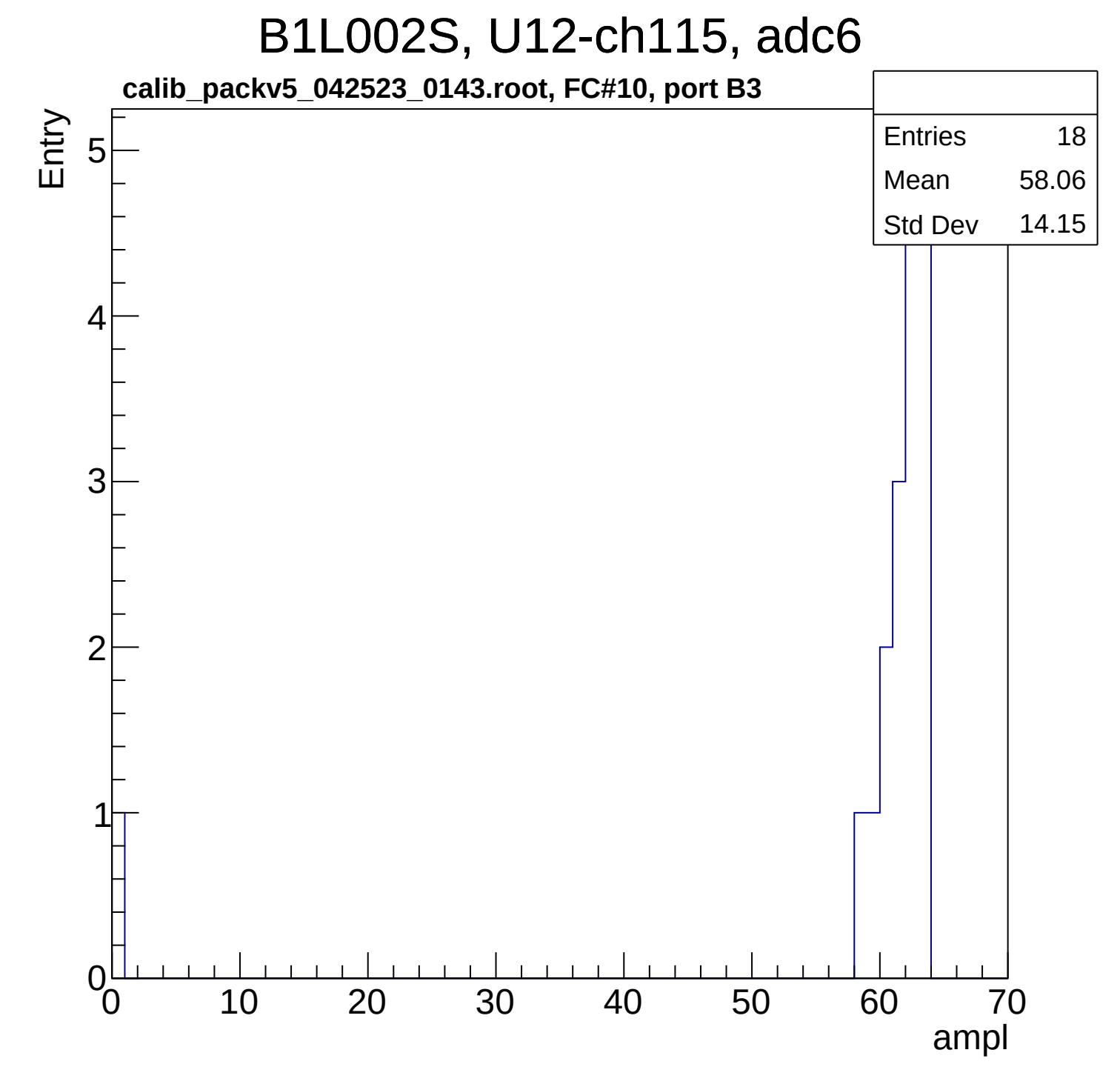
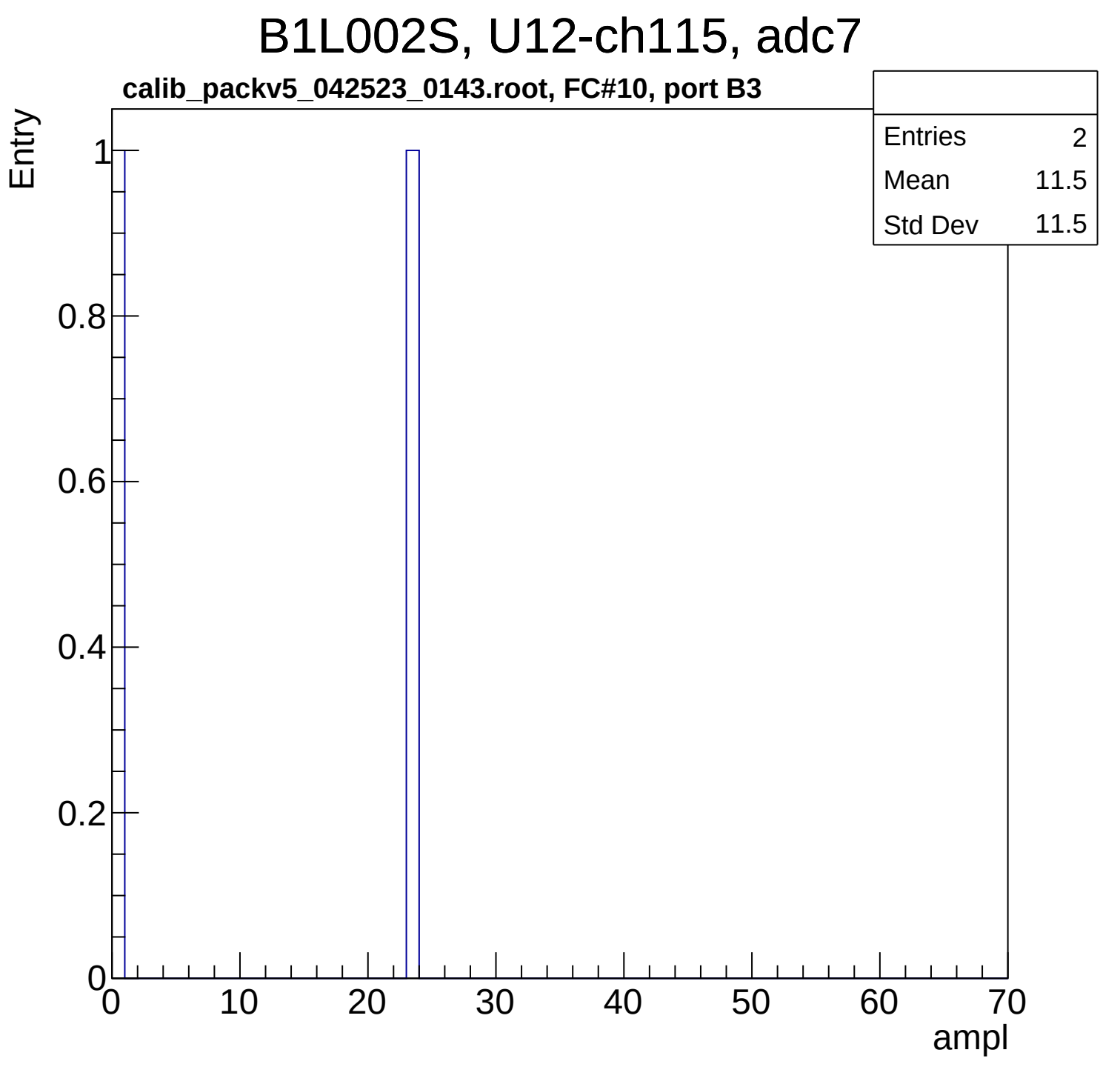
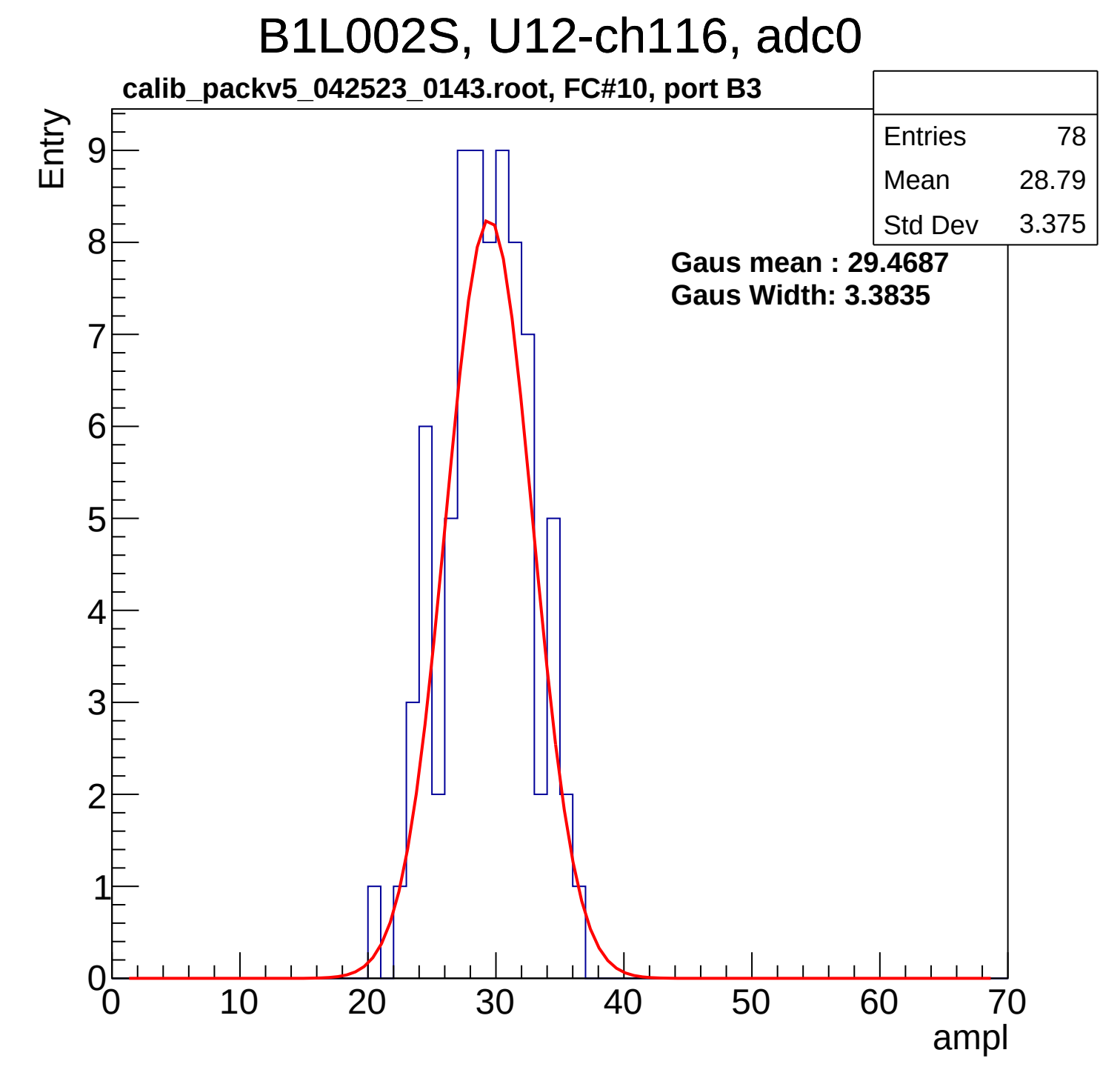

B1L002S, U12-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.06
Std Dev 14.15
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.79
Std Dev 3.375
Gaus mean : 29.4687
Gaus Width: 3.3835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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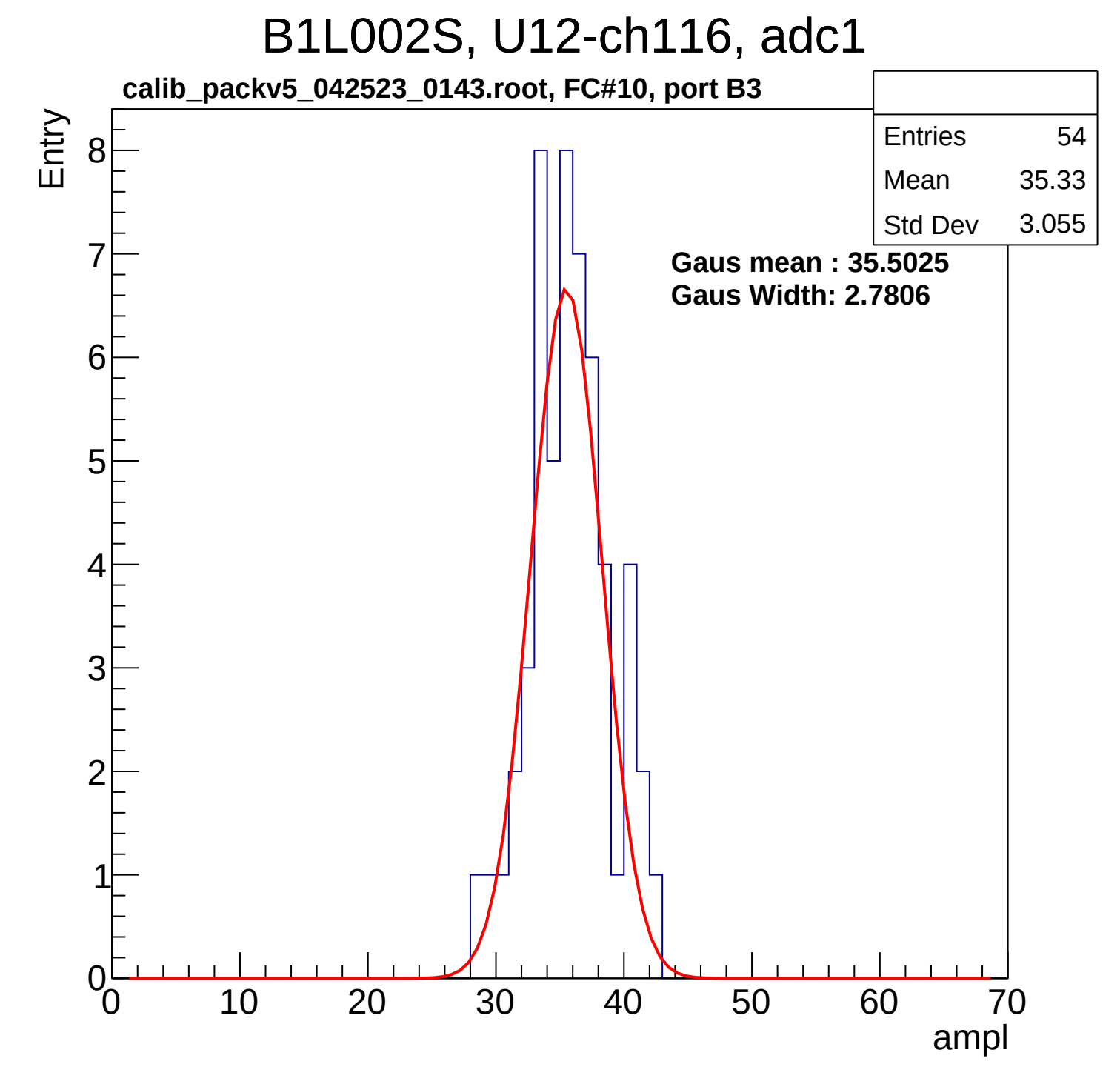
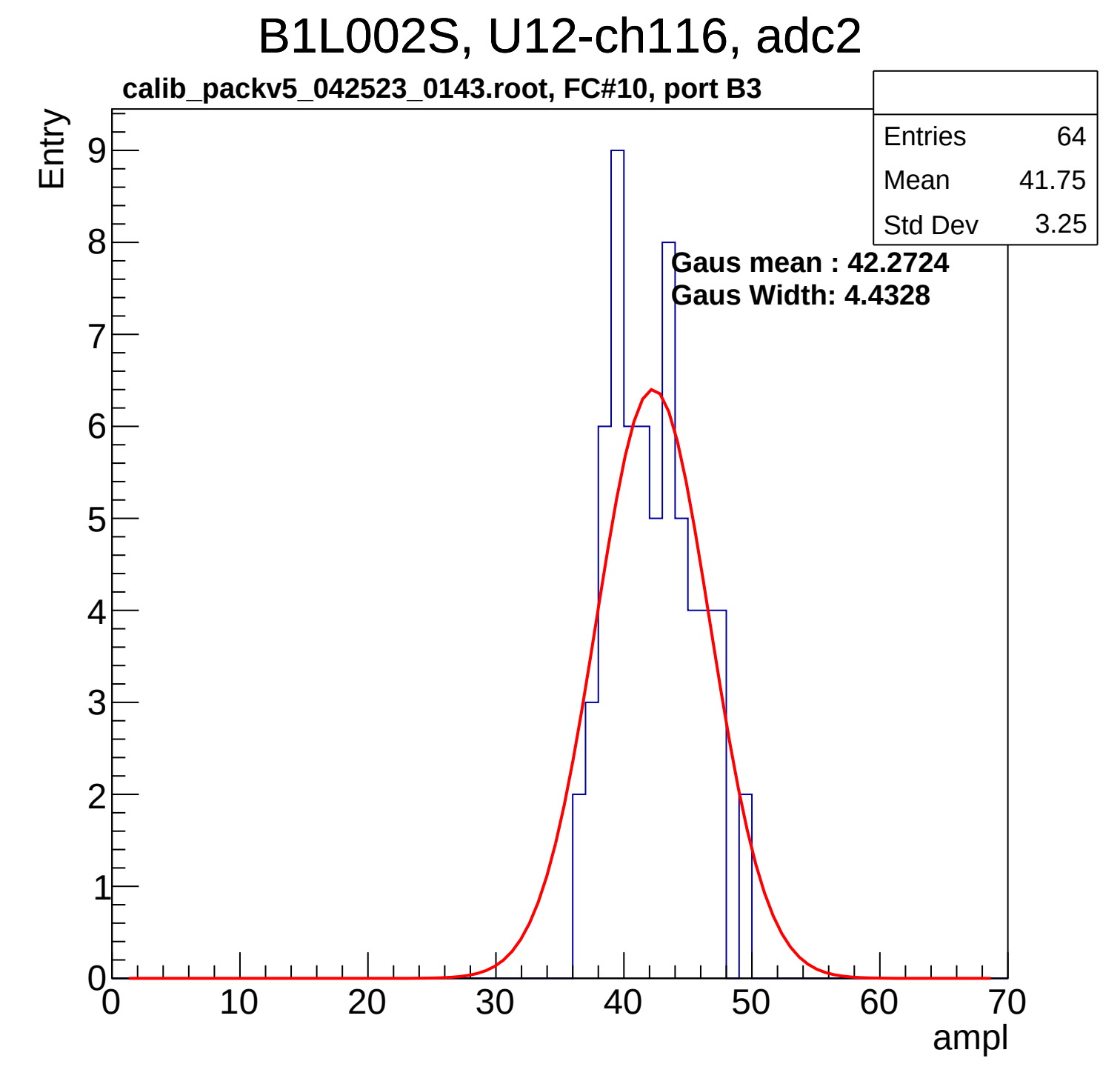
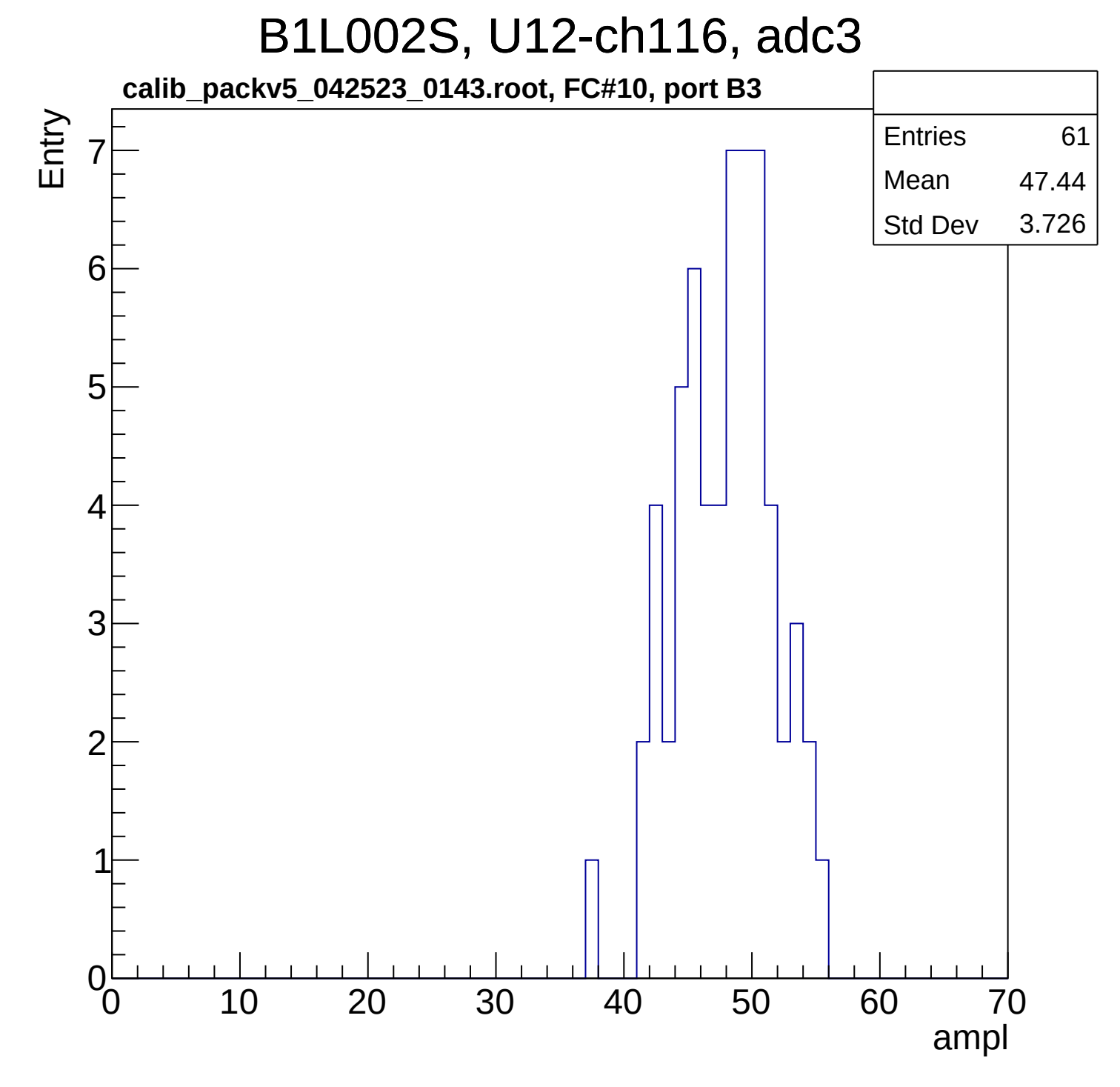
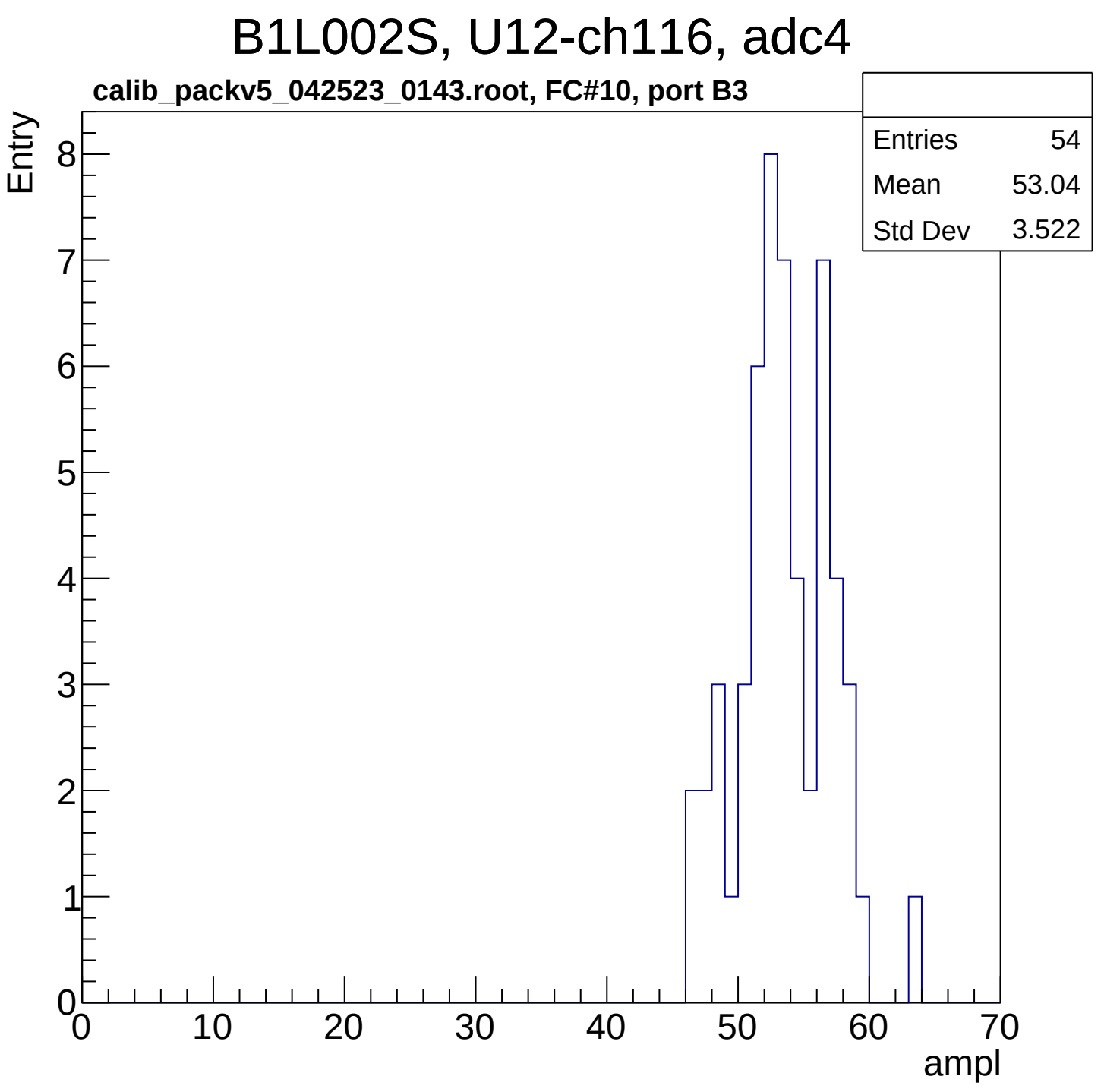
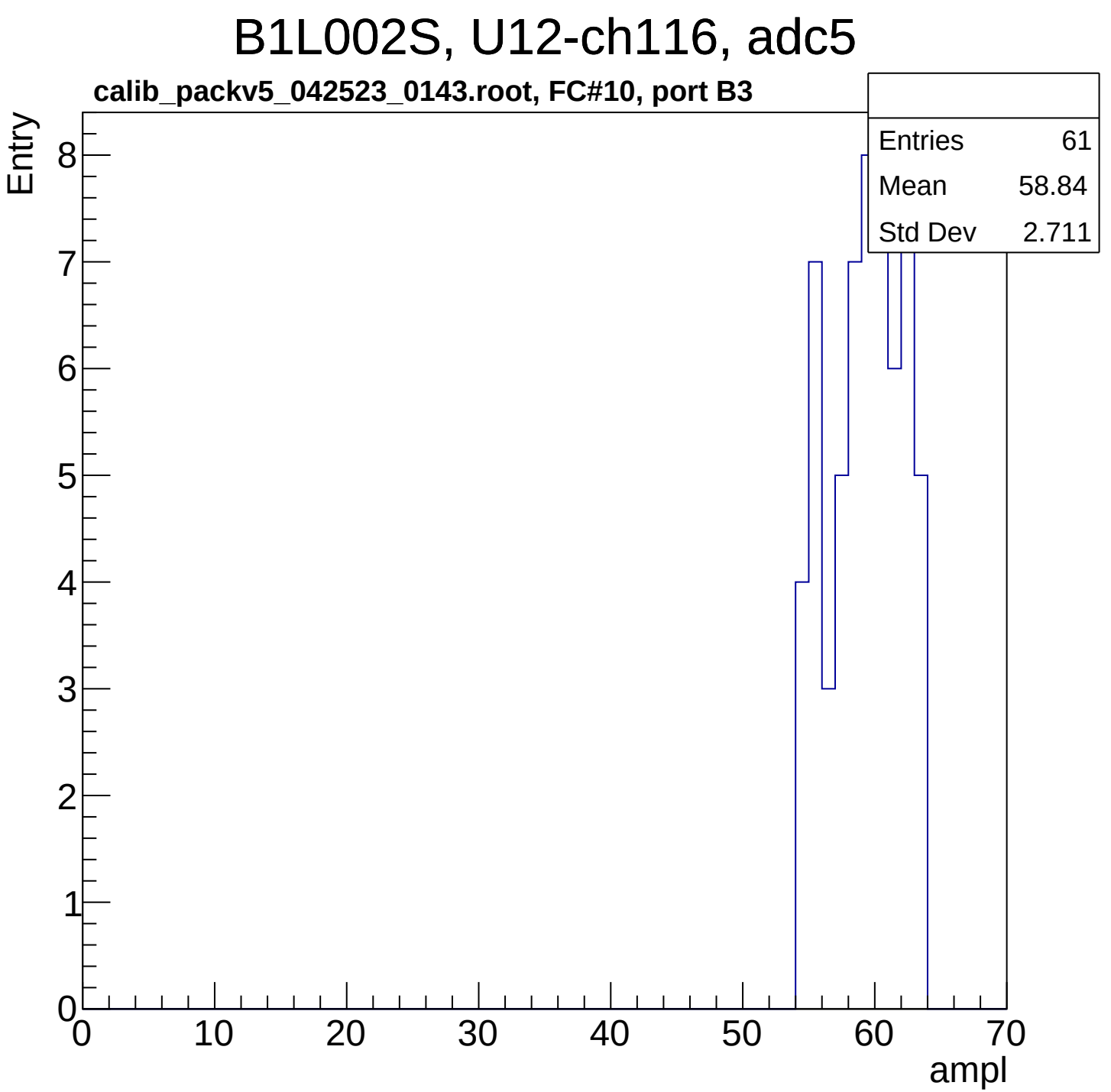

B1L002S, U12-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.33
Std Dev 3.055
Gaus mean : 35.5025
Gaus Width: 2.7806
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.75
Std Dev 3.25
Gaus mean : 42.2724
Gaus Width: 4.4328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.44
Std Dev 3.726
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.04
Std Dev 3.522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.84
Std Dev 2.711
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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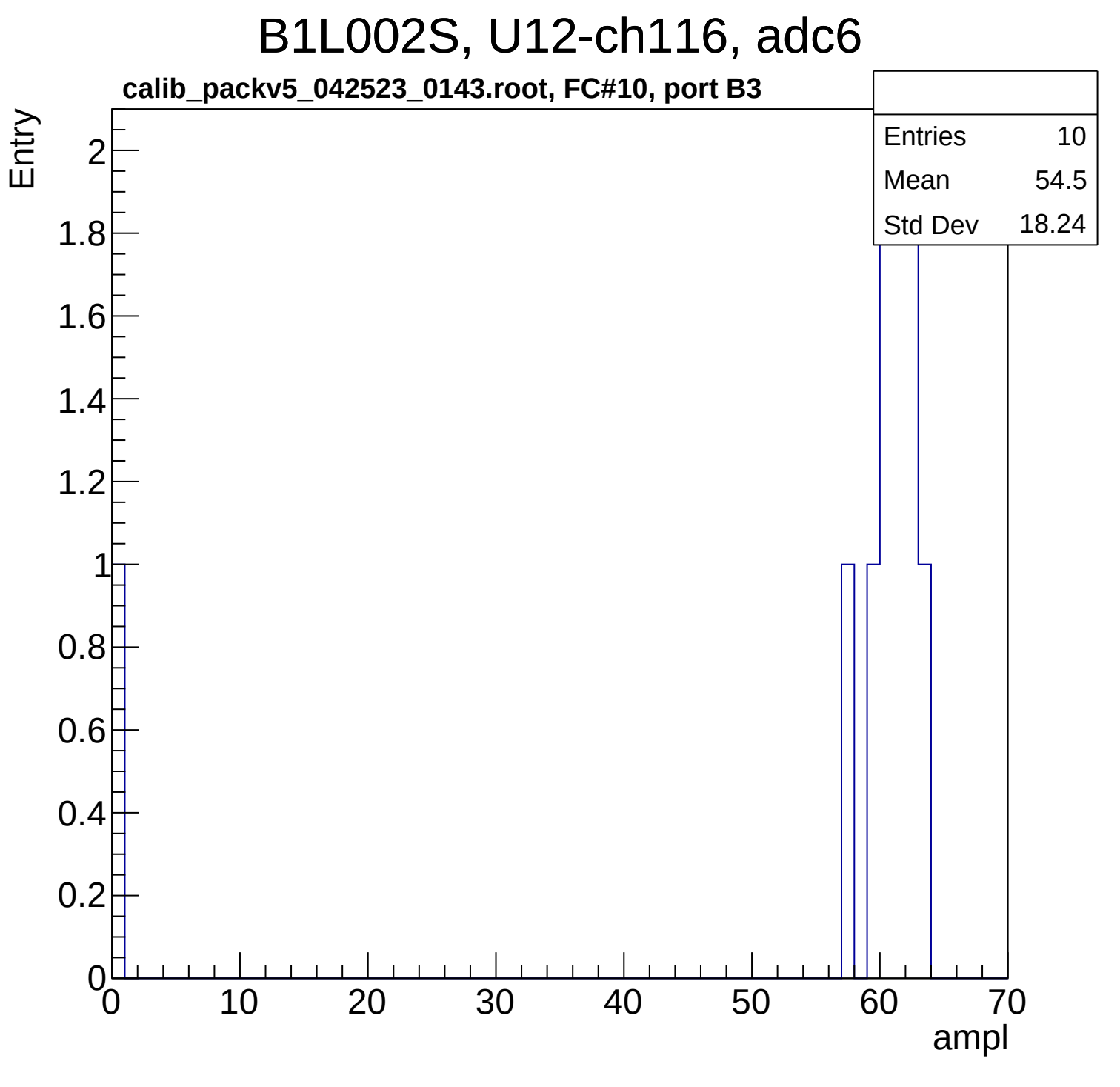
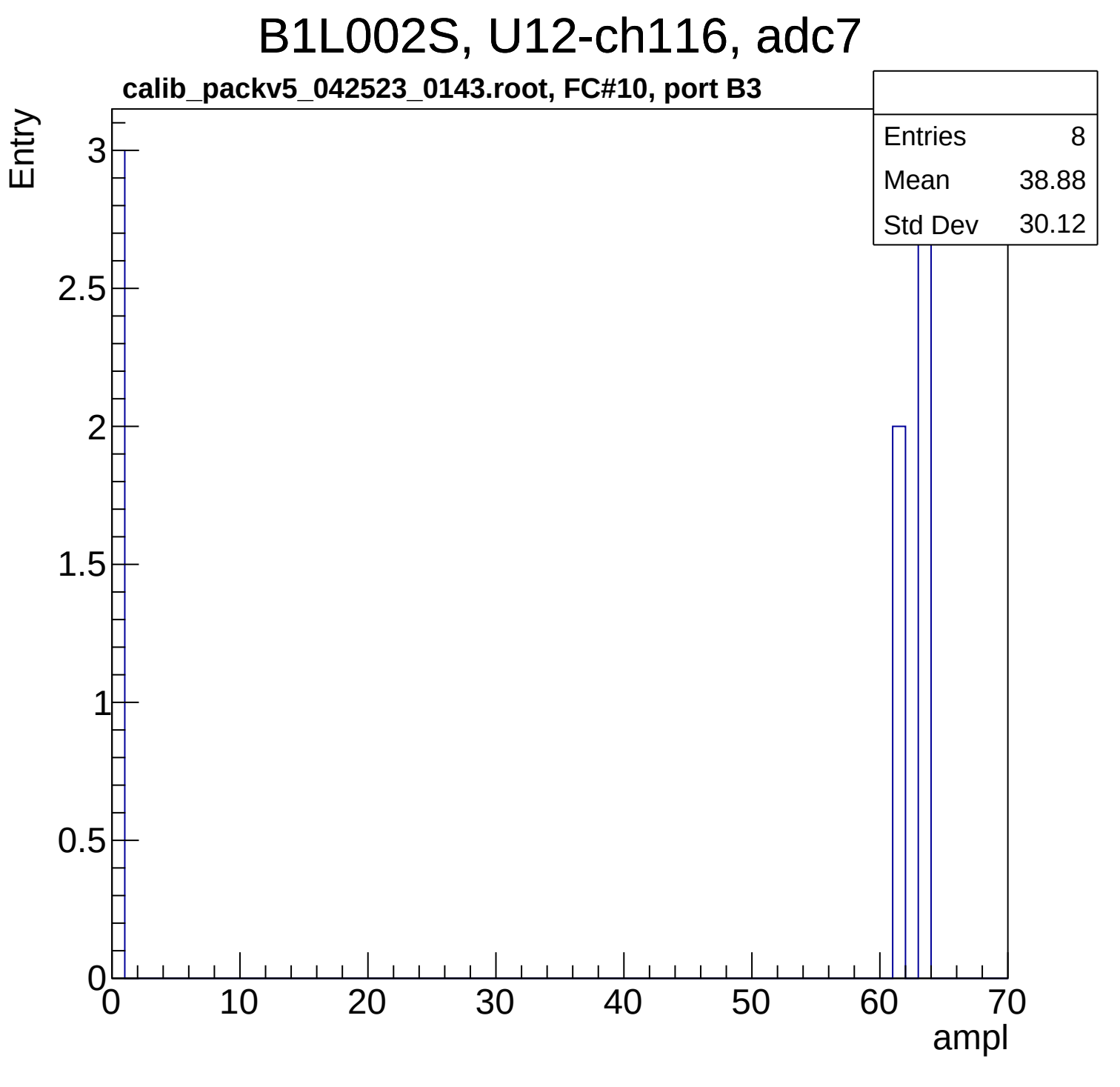
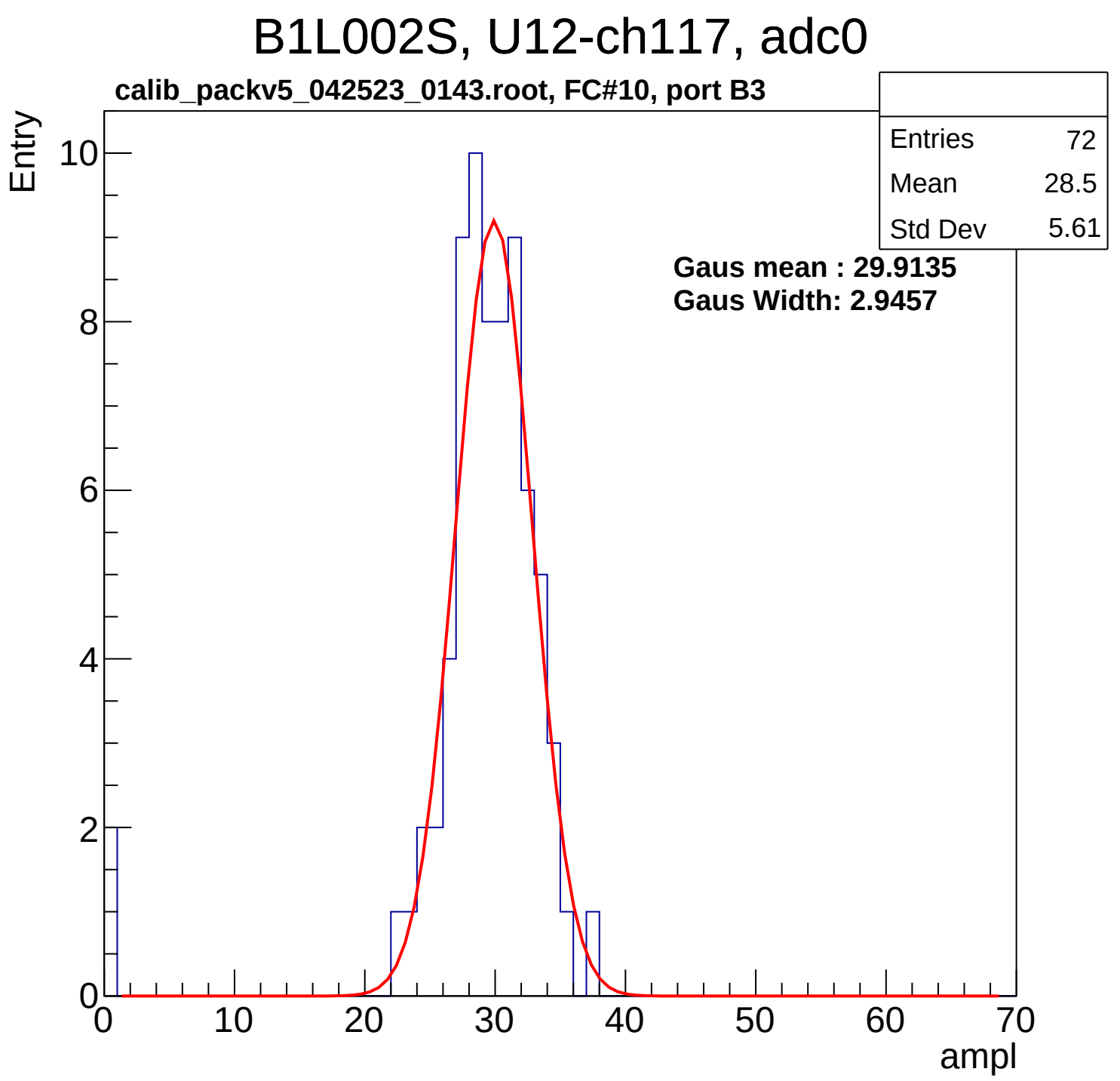
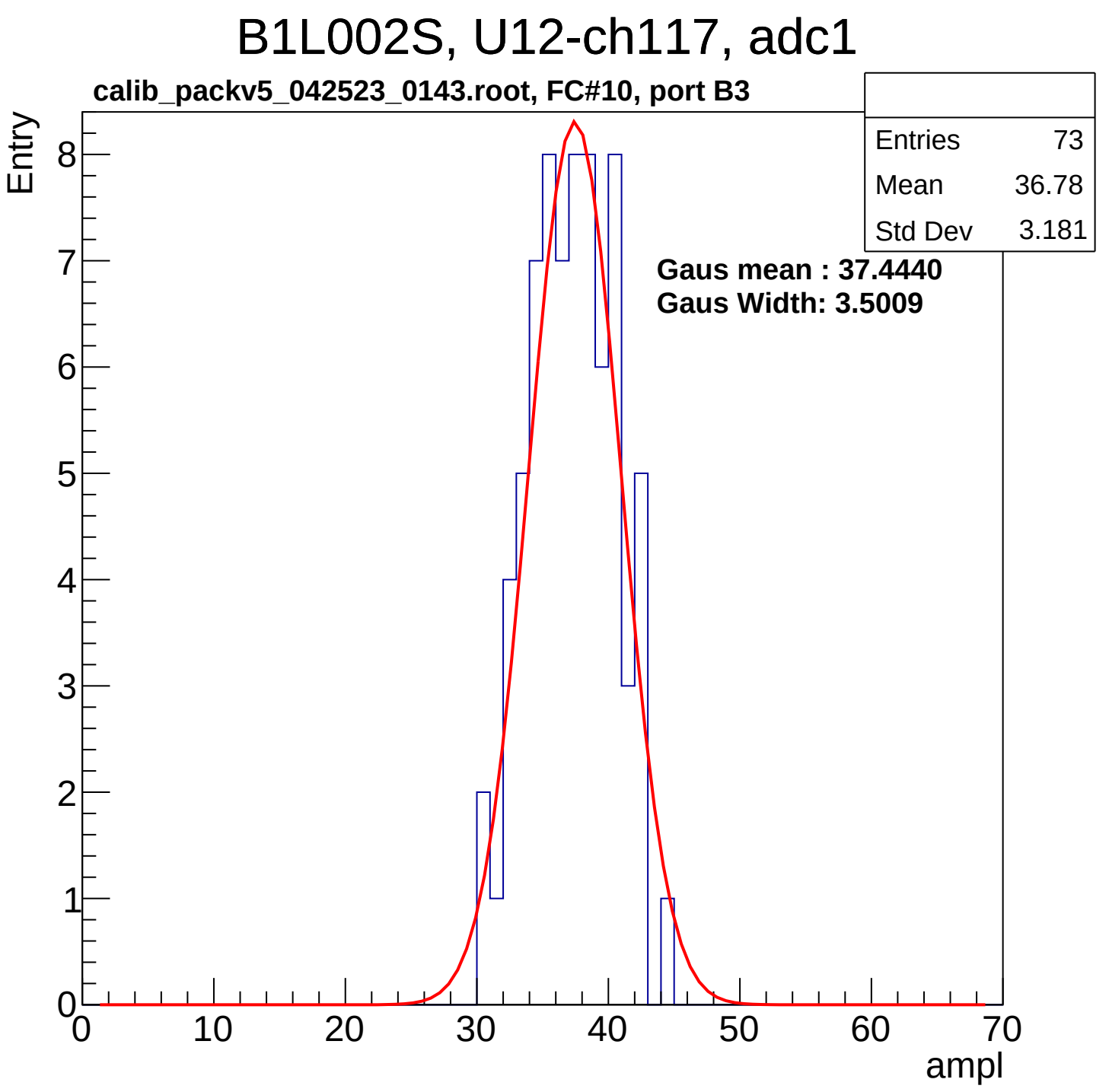
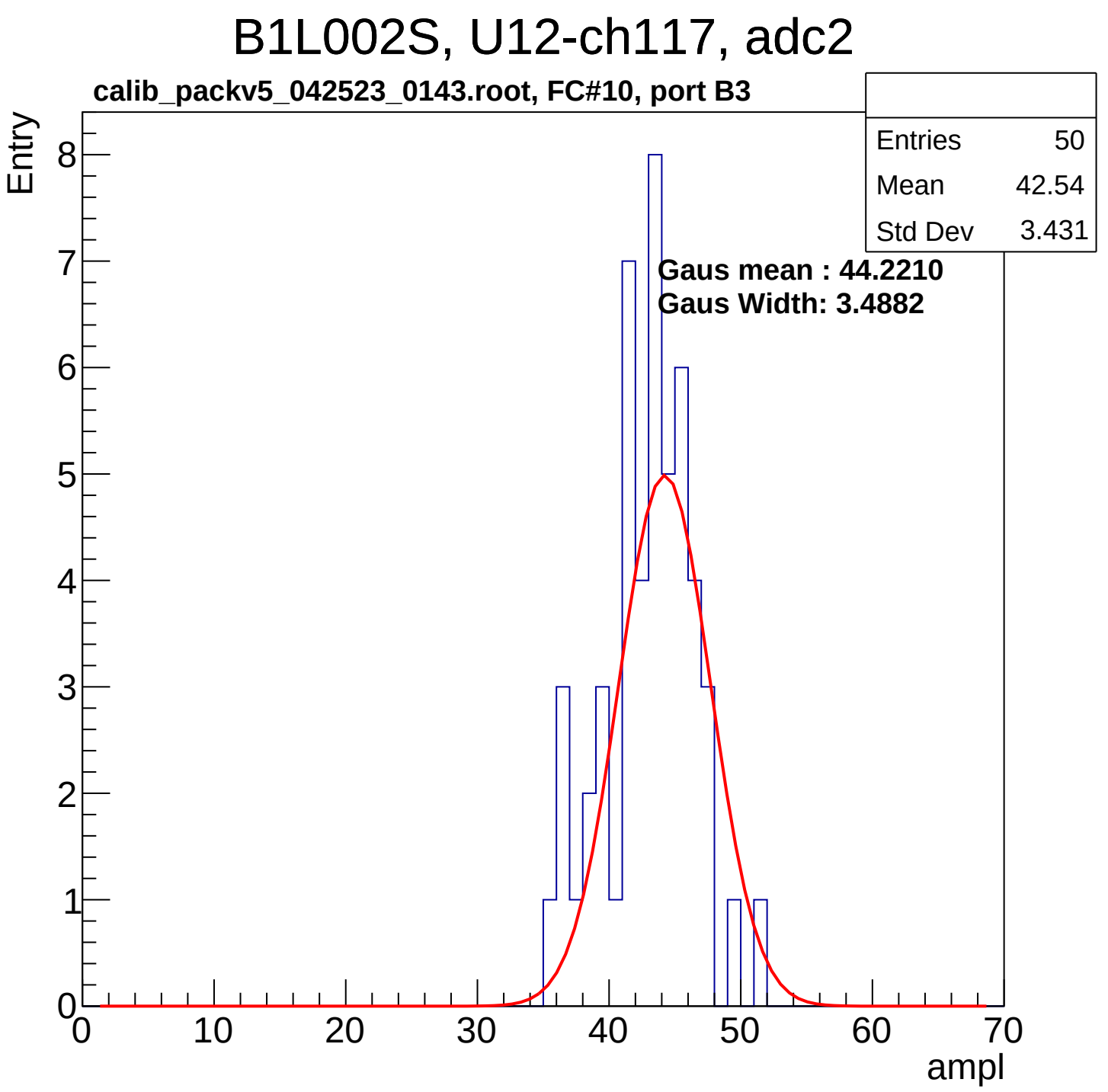

B1L002S, U12-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 54.5
Std Dev 18.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 38.88
Std Dev 30.12
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.5
Std Dev 5.61
Gaus mean : 29.9135
Gaus Width: 2.9457
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 36.78
Std Dev 3.181
Gaus mean : 37.4440
Gaus Width: 3.5009
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 42.54
Std Dev 3.431
Gaus mean : 44.2210
Gaus Width: 3.4882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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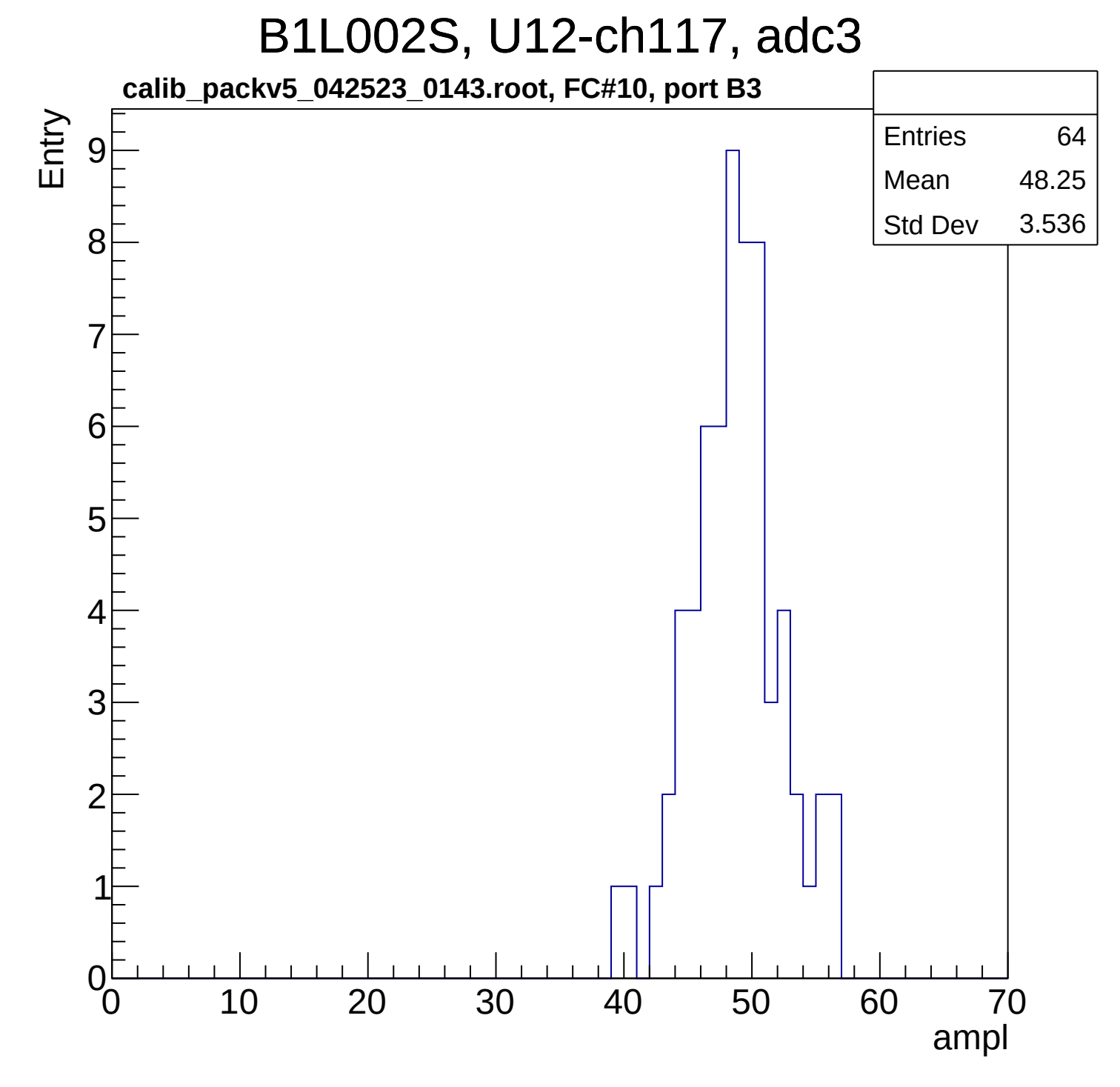
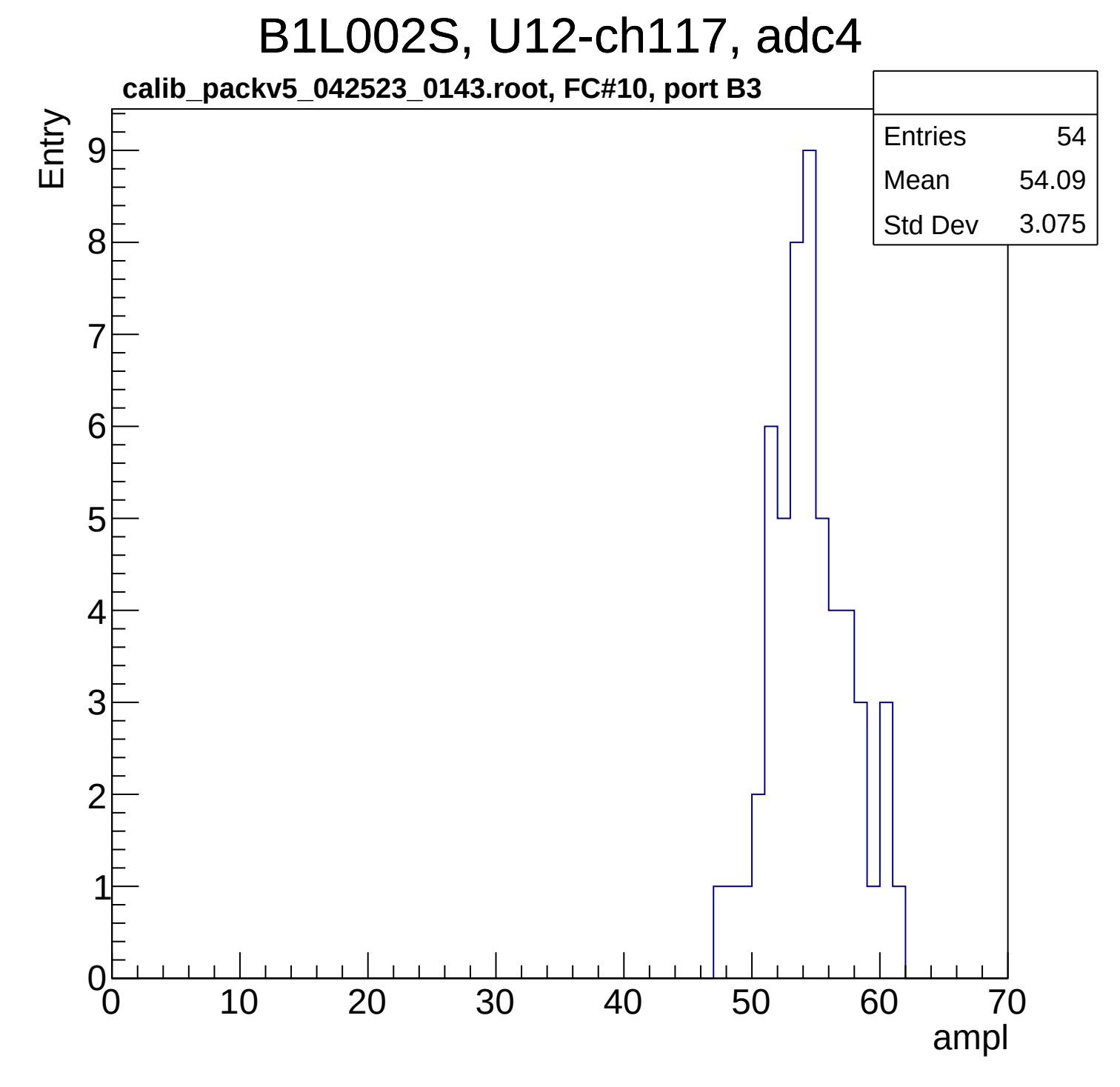

B1L002S, U12-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.25
Std Dev 3.536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.09
Std Dev 3.075
Entry
ampl
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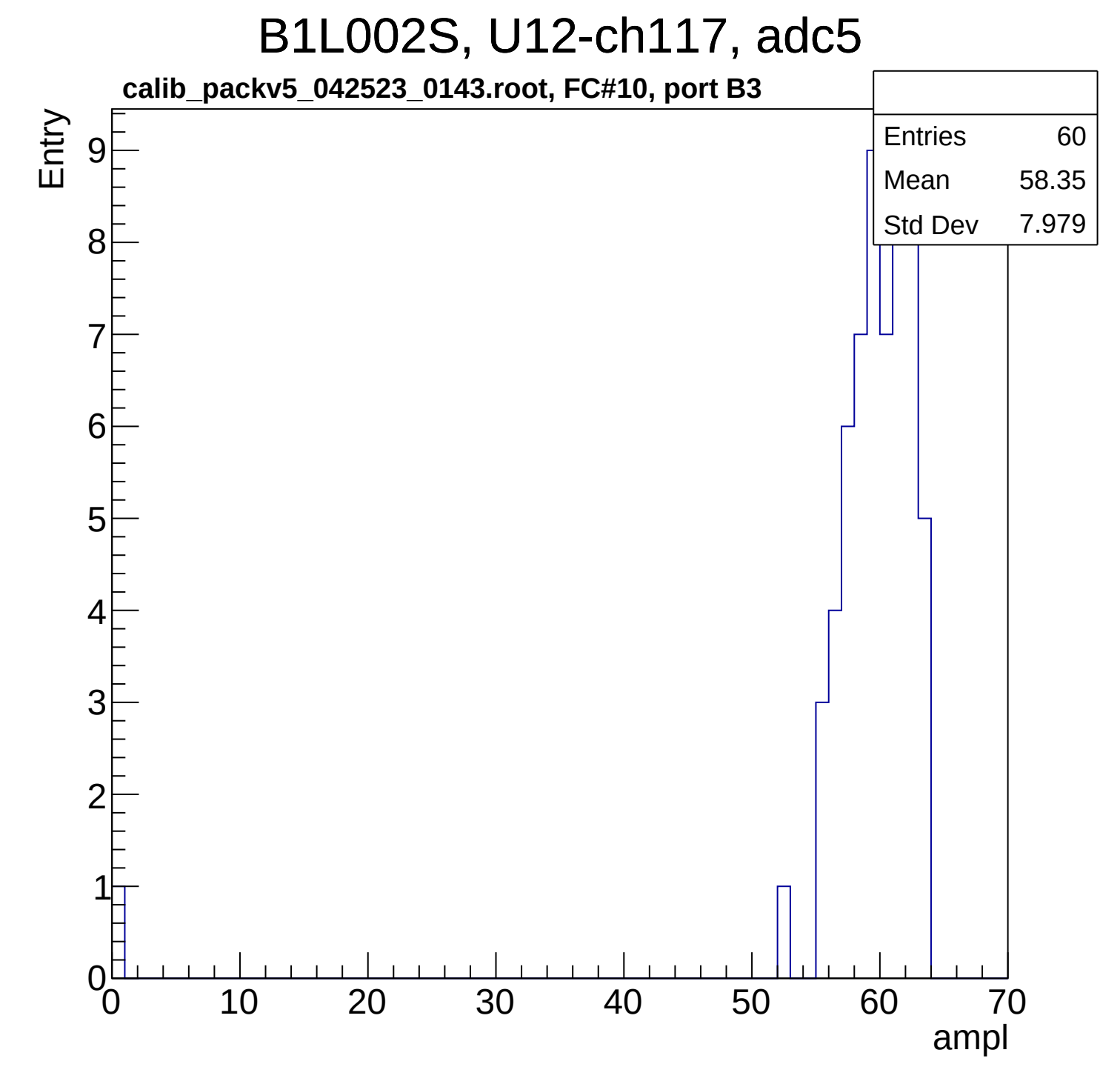
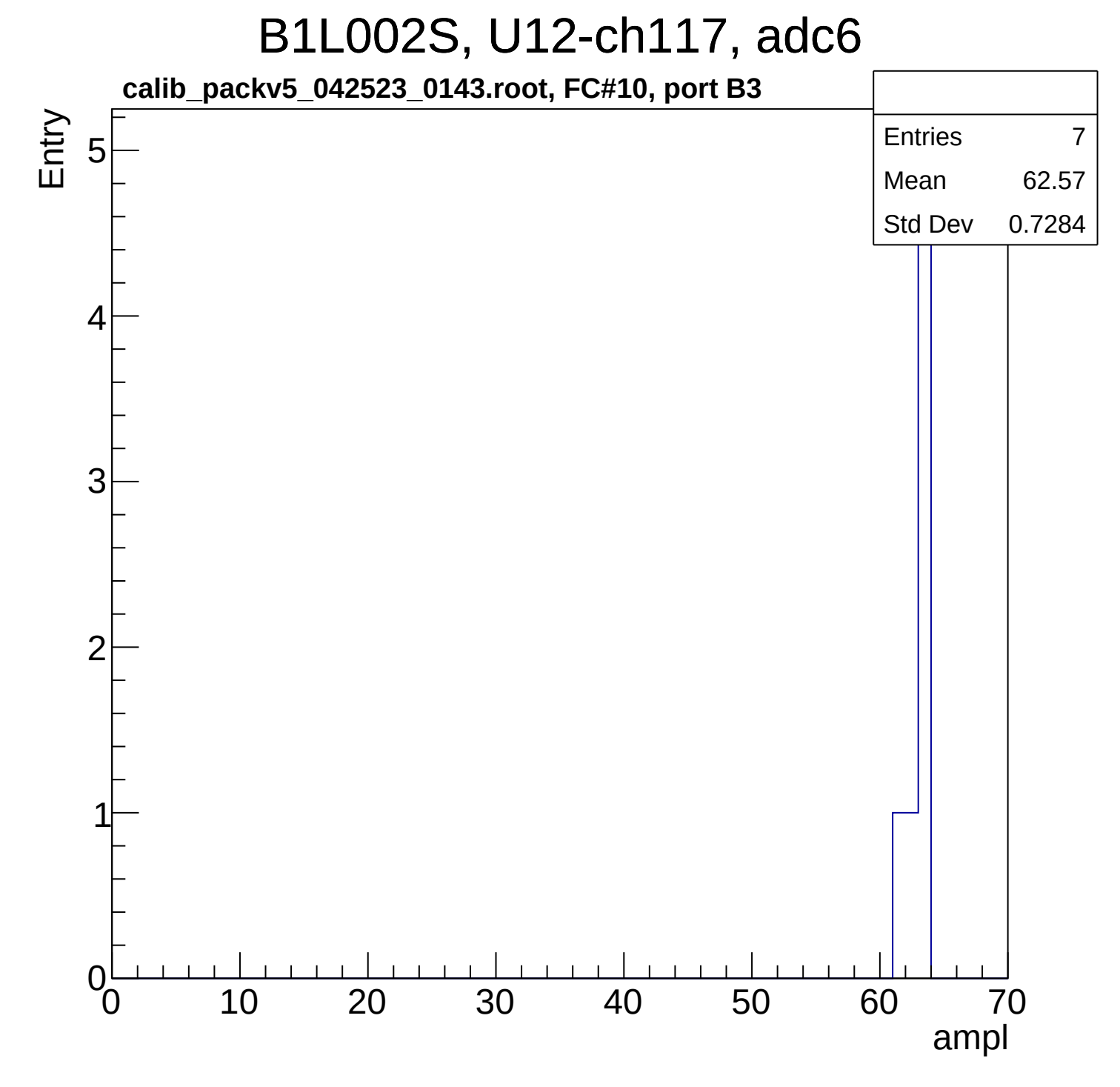
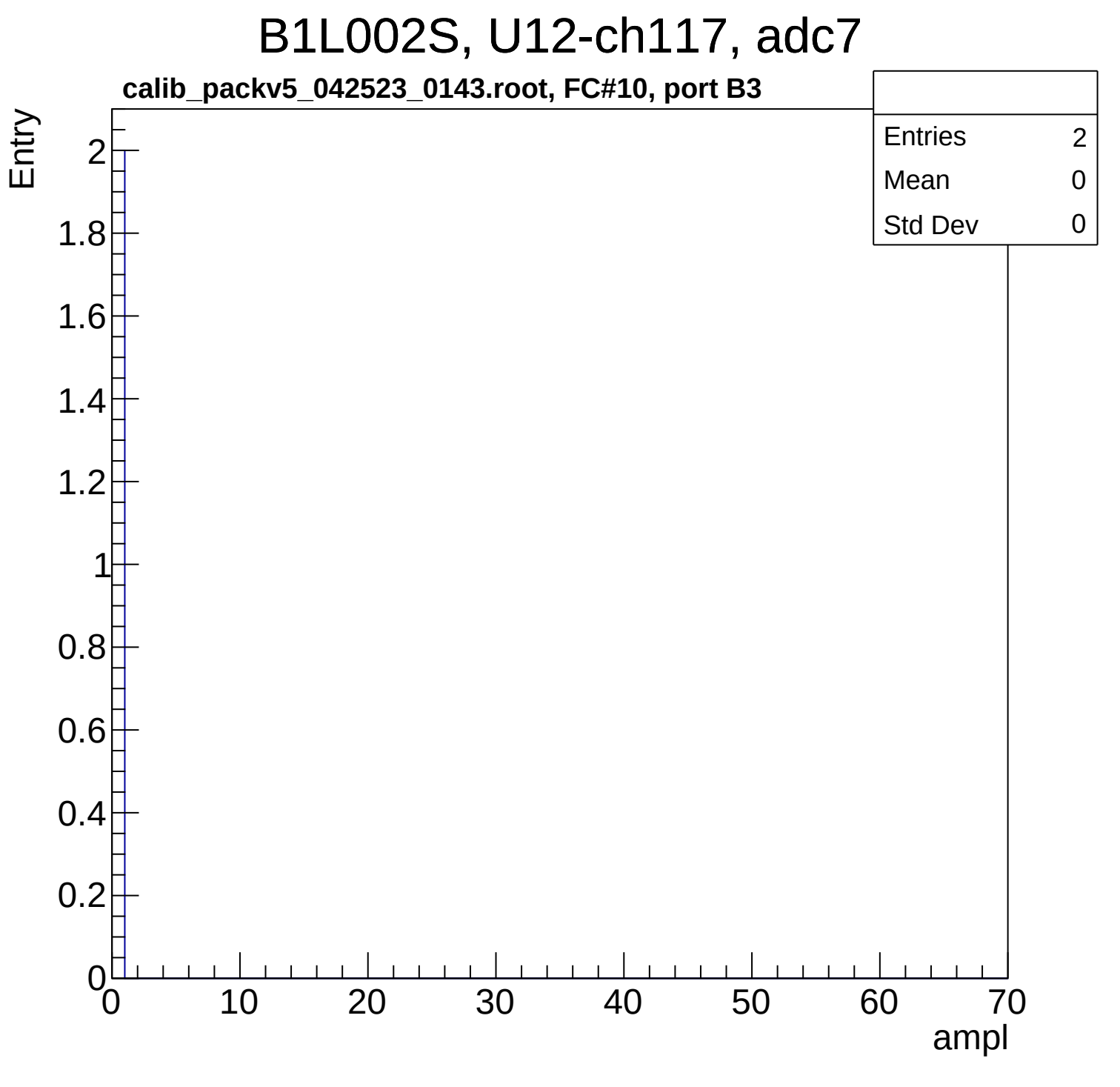
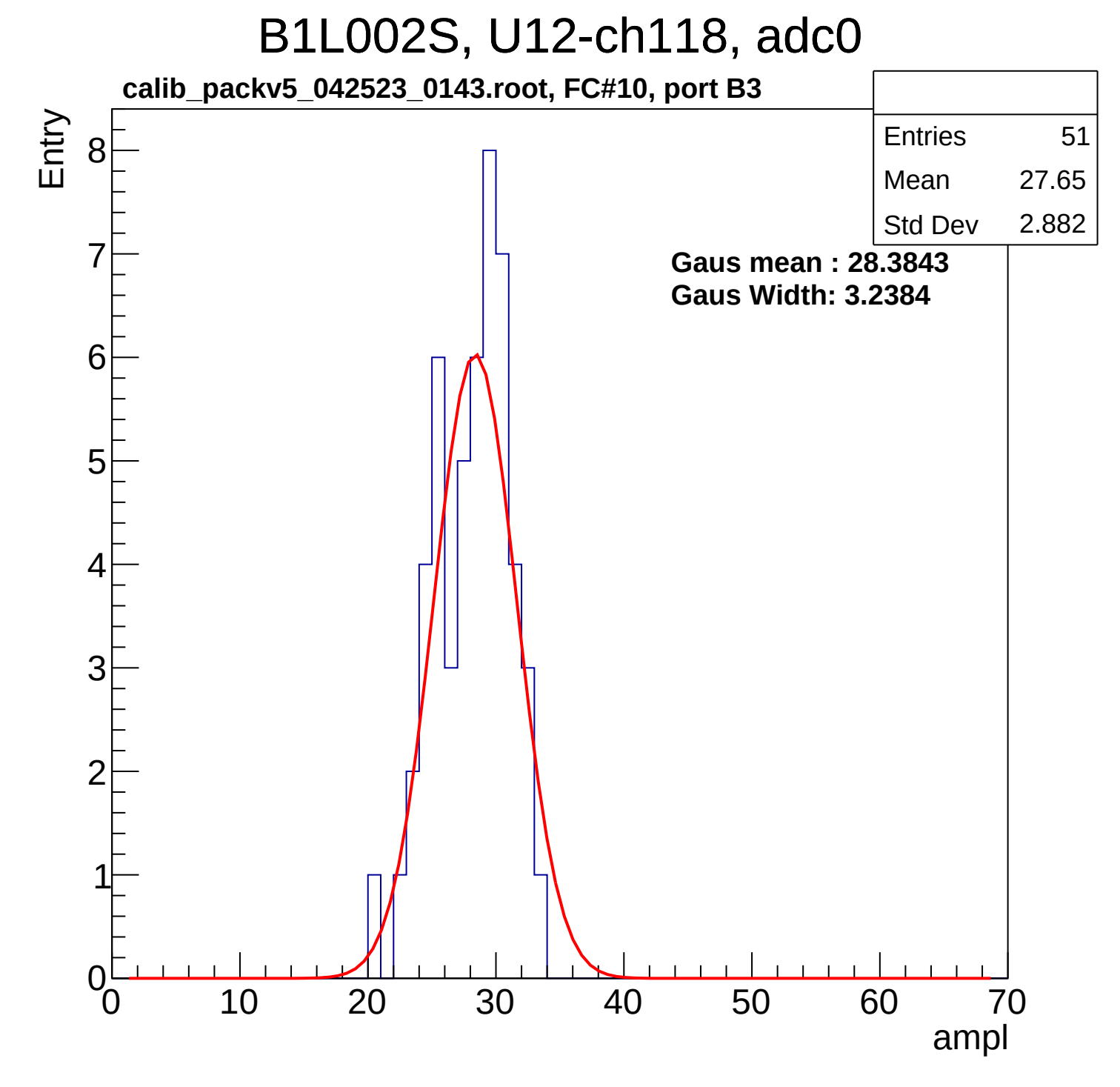
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 58.35
Std Dev 7.979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27.65
Std Dev 2.882
Gaus mean : 28.3843
Gaus Width: 3.2384
Entry
ampl
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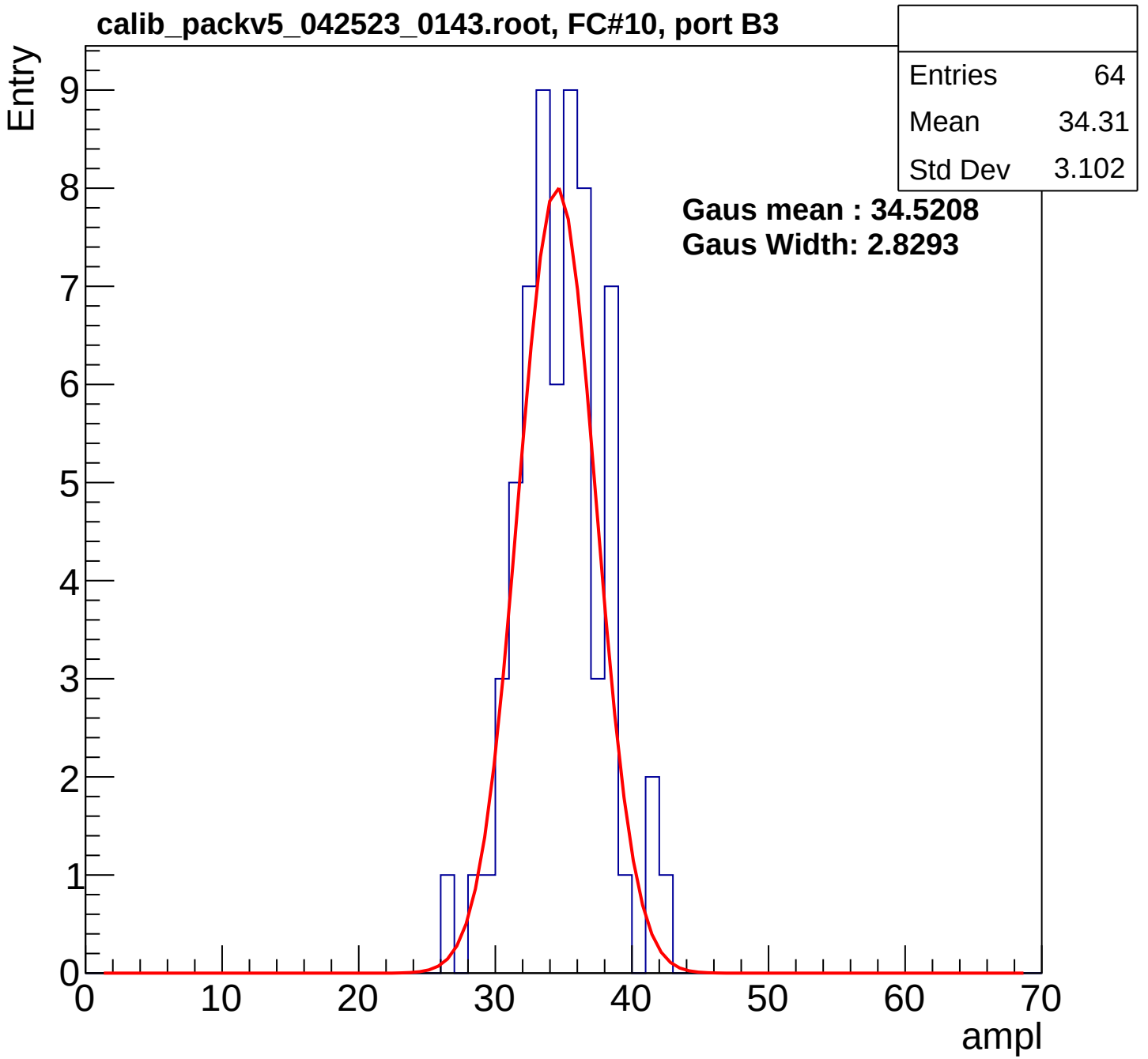
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.31
Std Dev 3.102
Gaus mean : 34.5208
Gaus Width: 2.8293
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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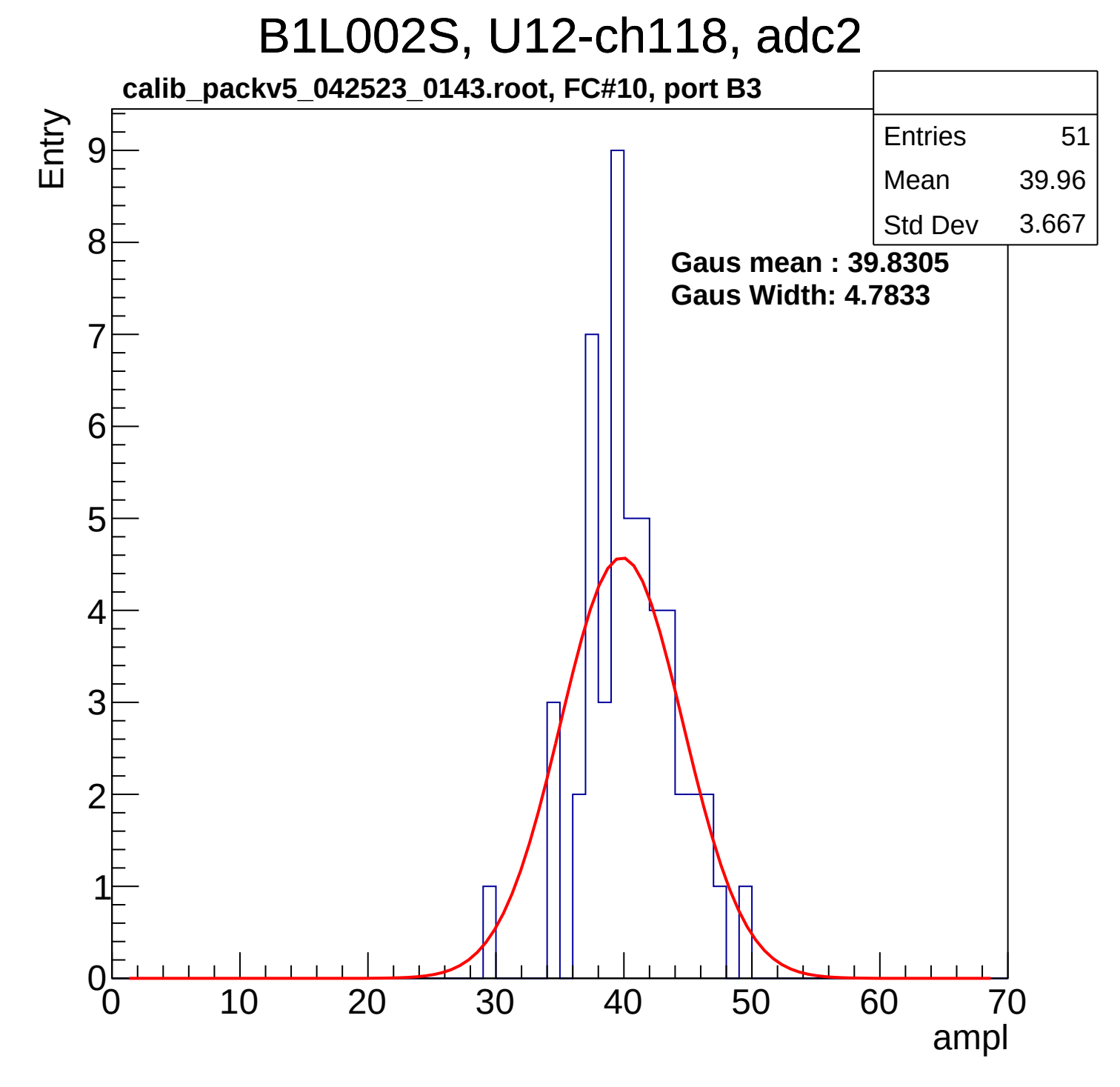
60
70


B1L002S, U12-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 39.96
Std Dev 3.667
Gaus mean : 39.8305
Gaus Width: 4.7833
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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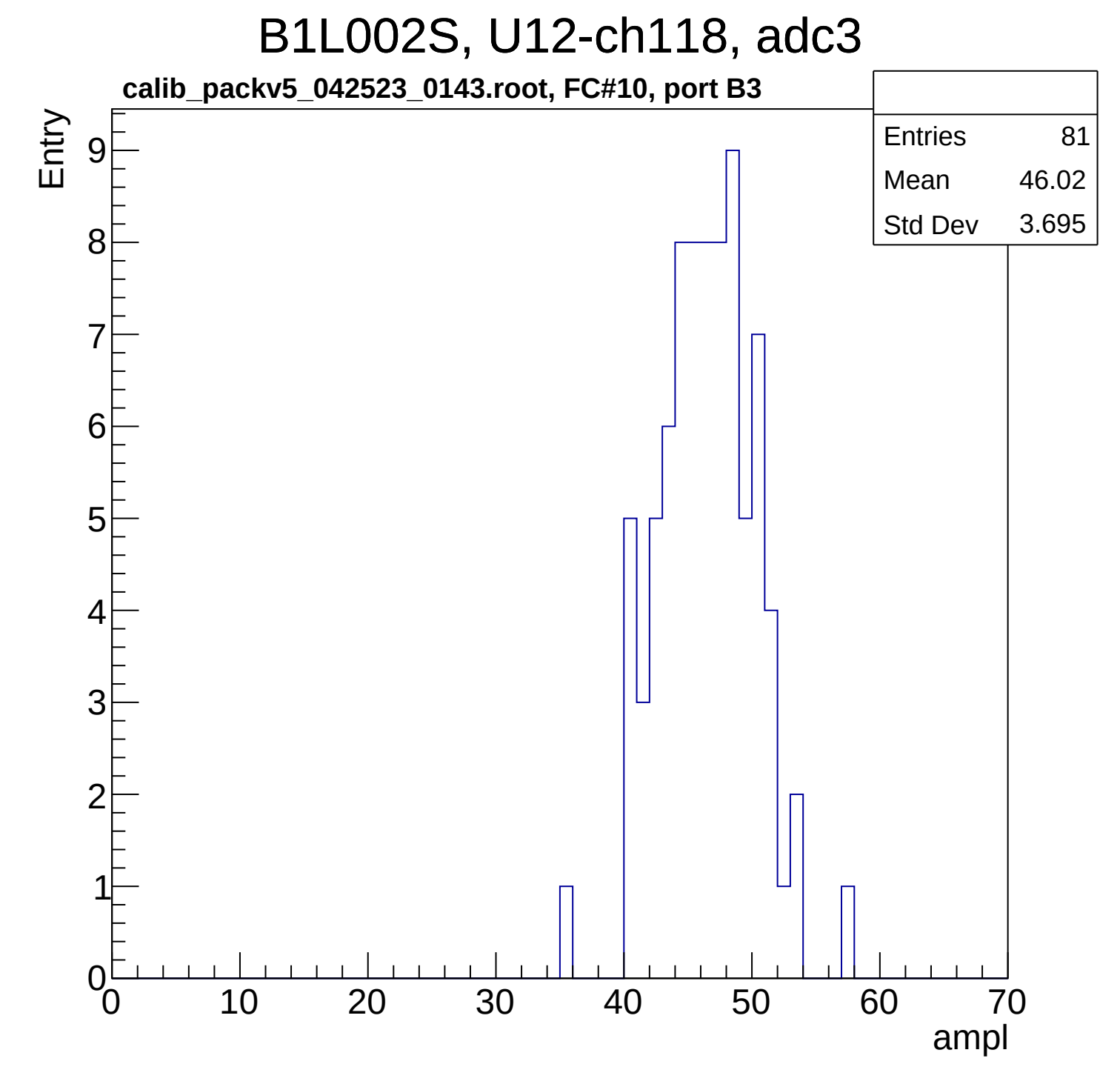
B1L002S, U12-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 46.02
Std Dev 3.695
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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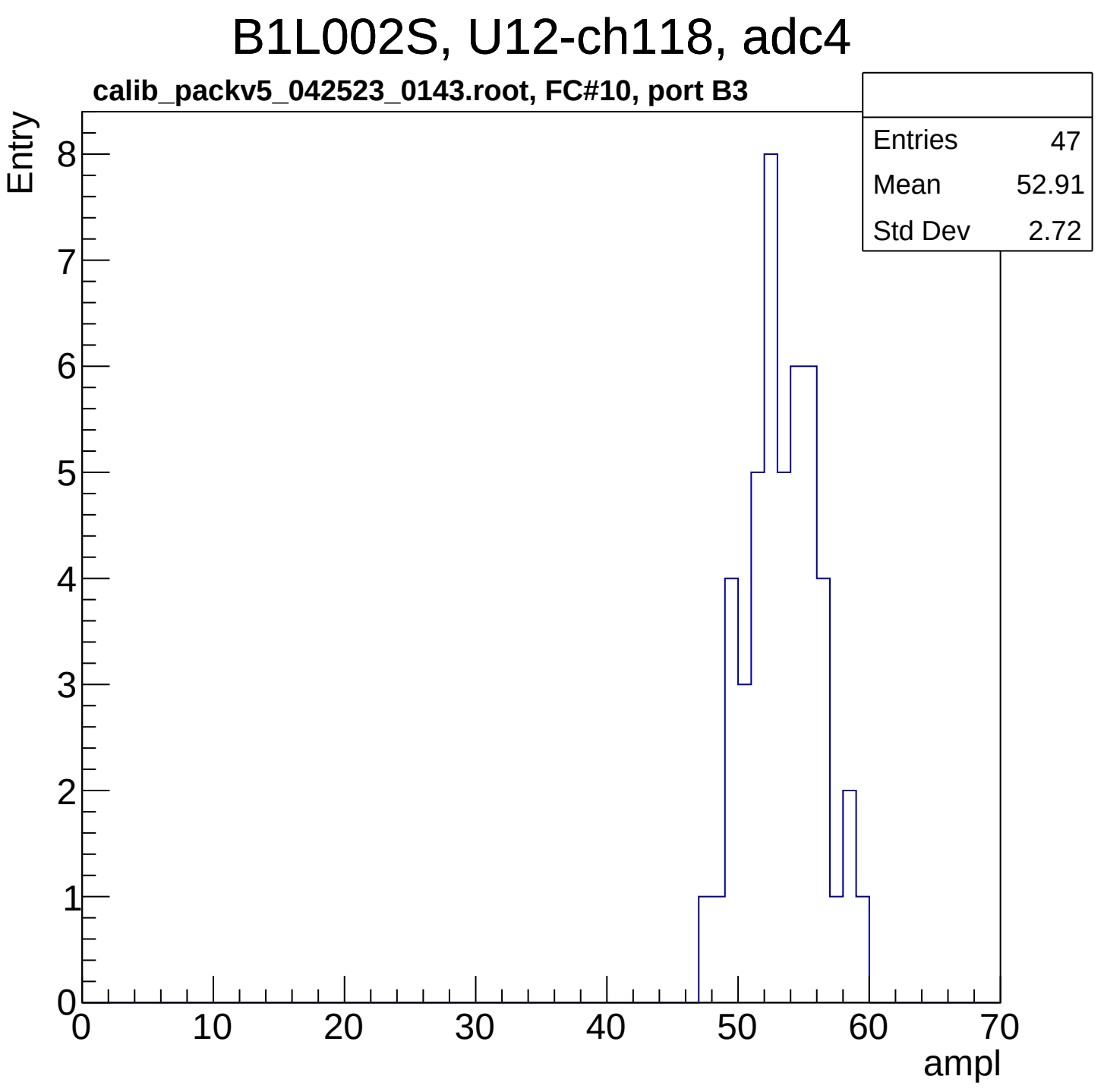
B1L002S, U12-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 52.91
Std Dev 2.72
Entry
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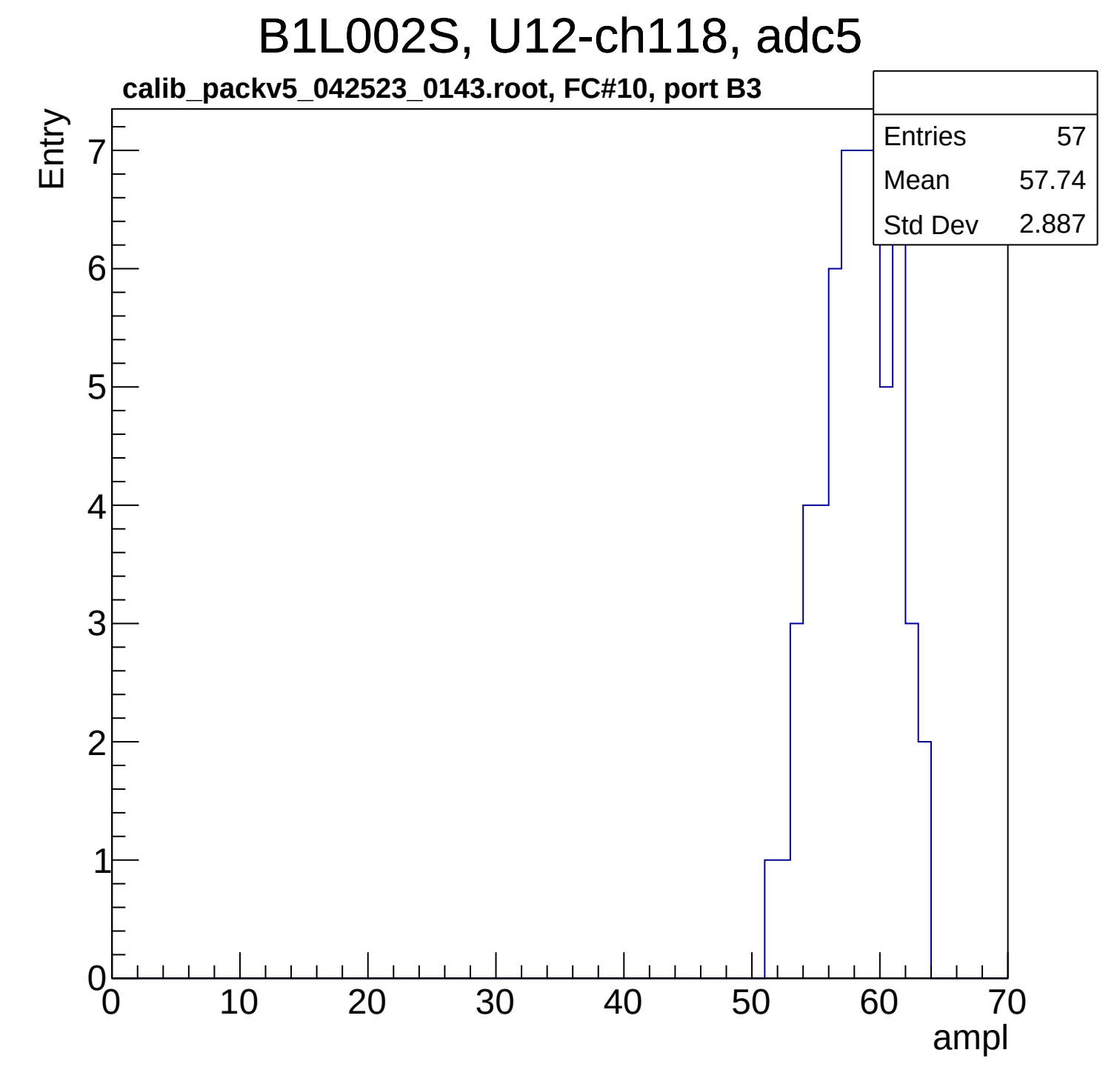
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.74
Std Dev 2.887
Entry
ampl
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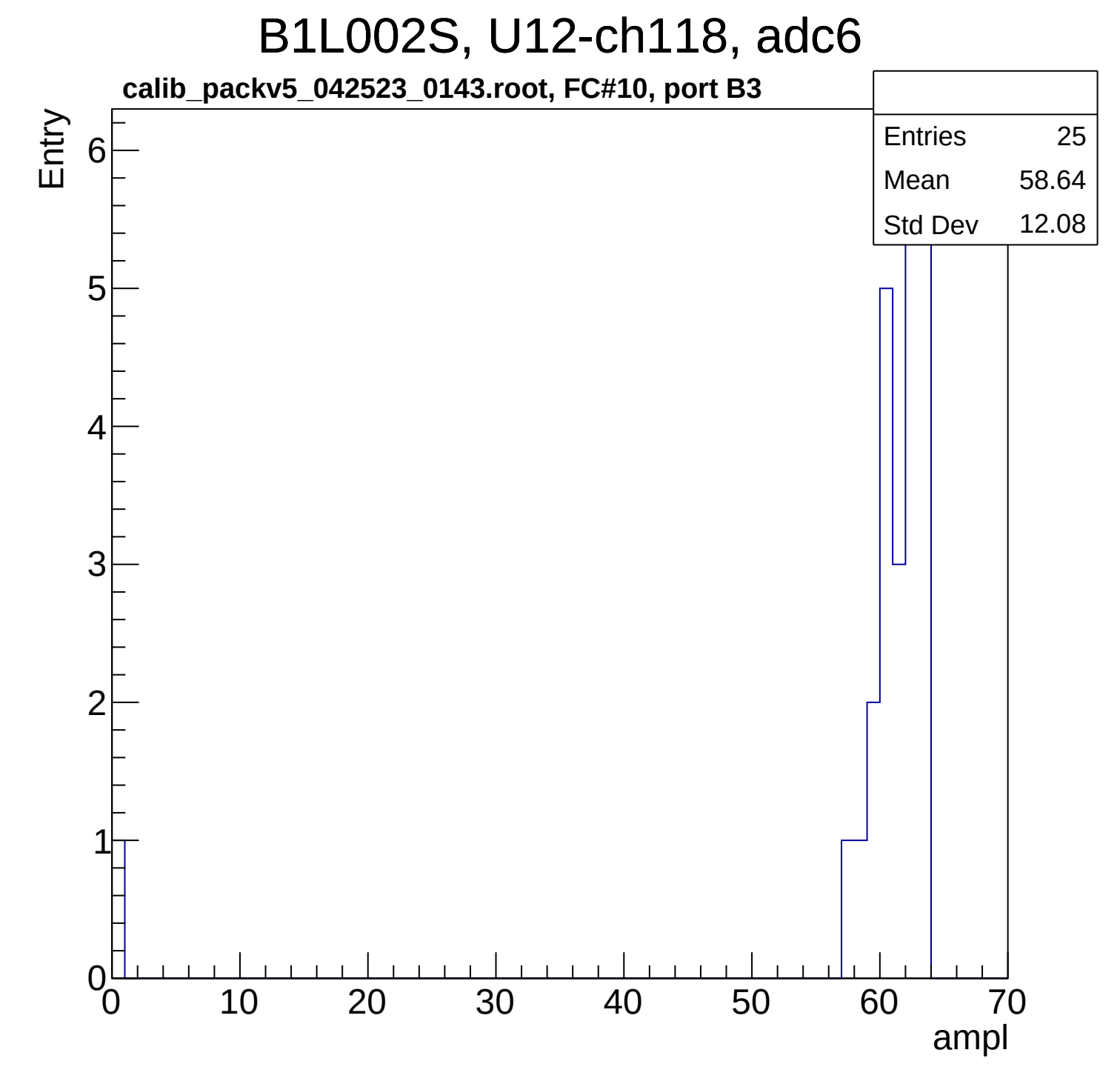
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.64
Std Dev 12.08
Entry
0
1
2
3
4
5
6
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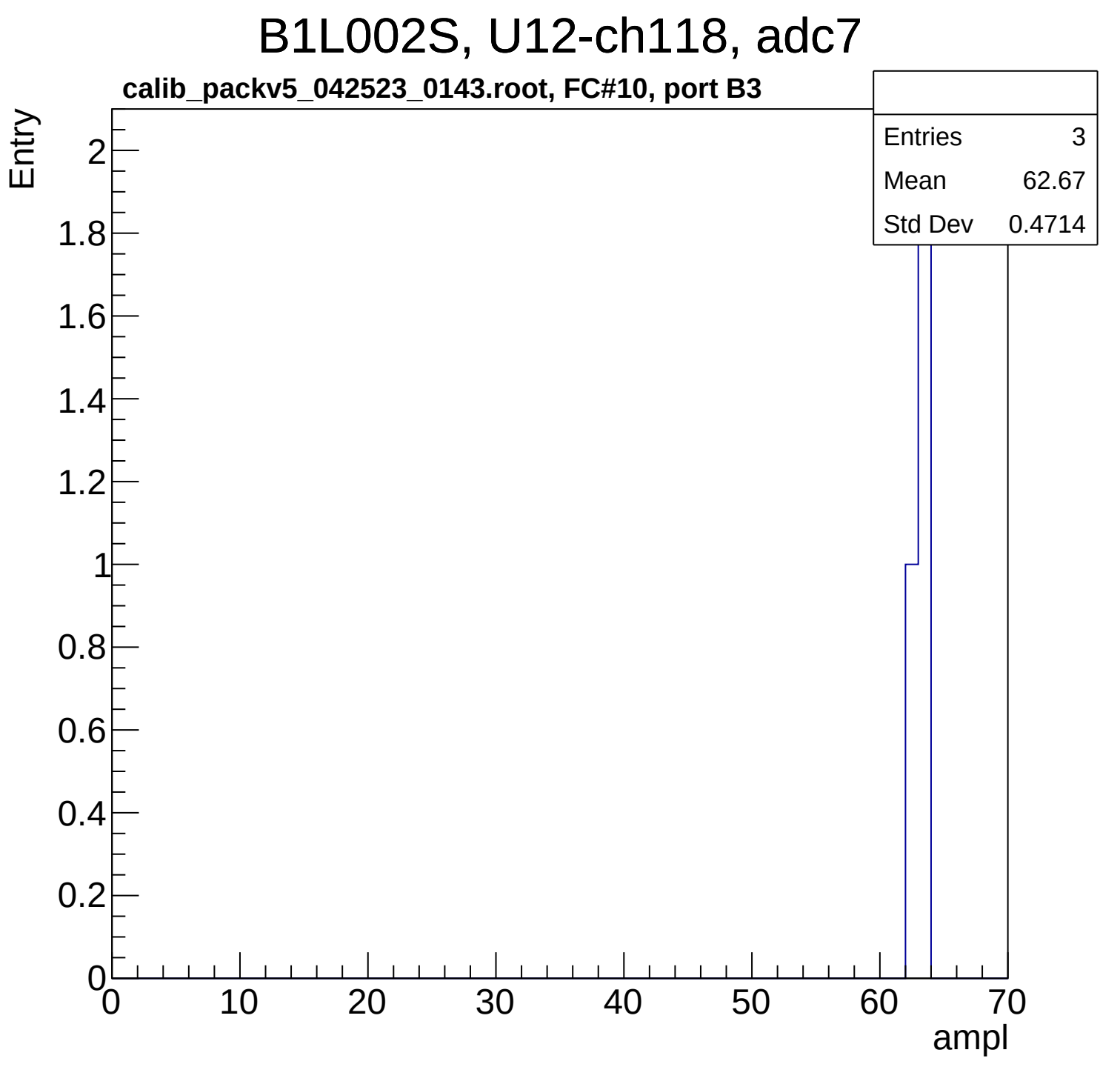
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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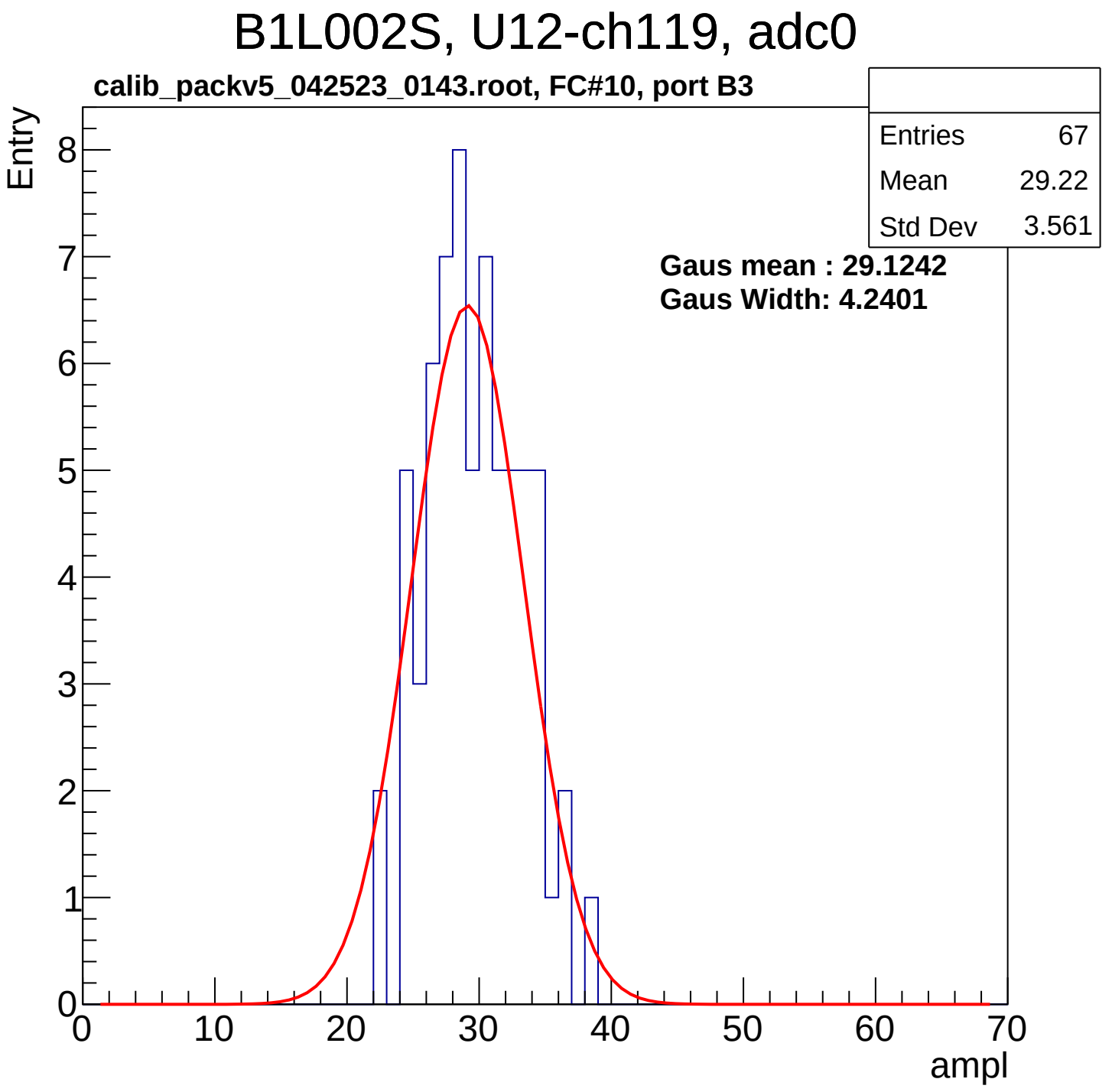
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U12-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.22
Std Dev 3.561
Gaus mean : 29.1242
Gaus Width: 4.2401
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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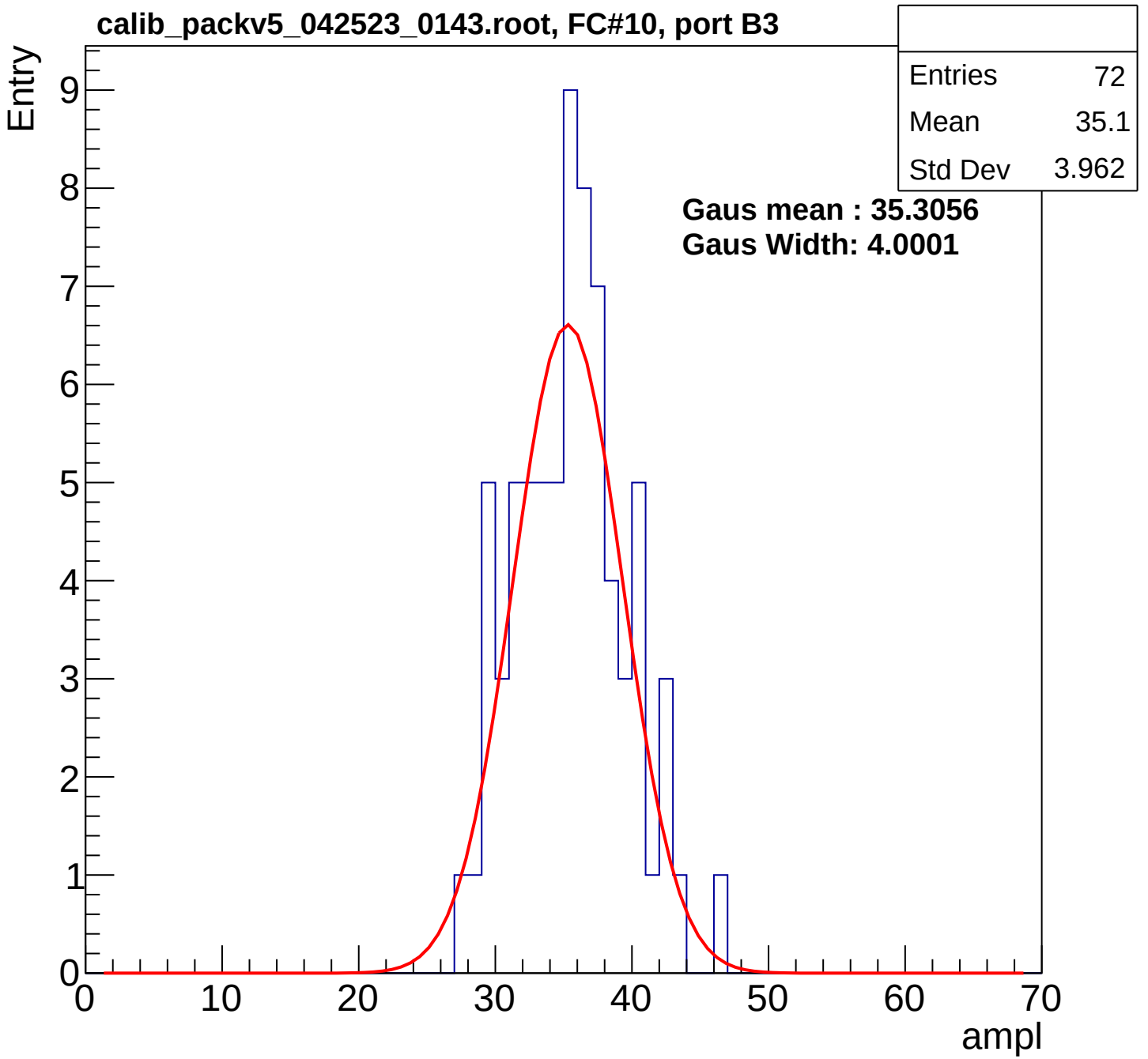
B1L002S, U12-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.1
Std Dev 3.962
Gaus mean : 35.3056
Gaus Width: 4.0001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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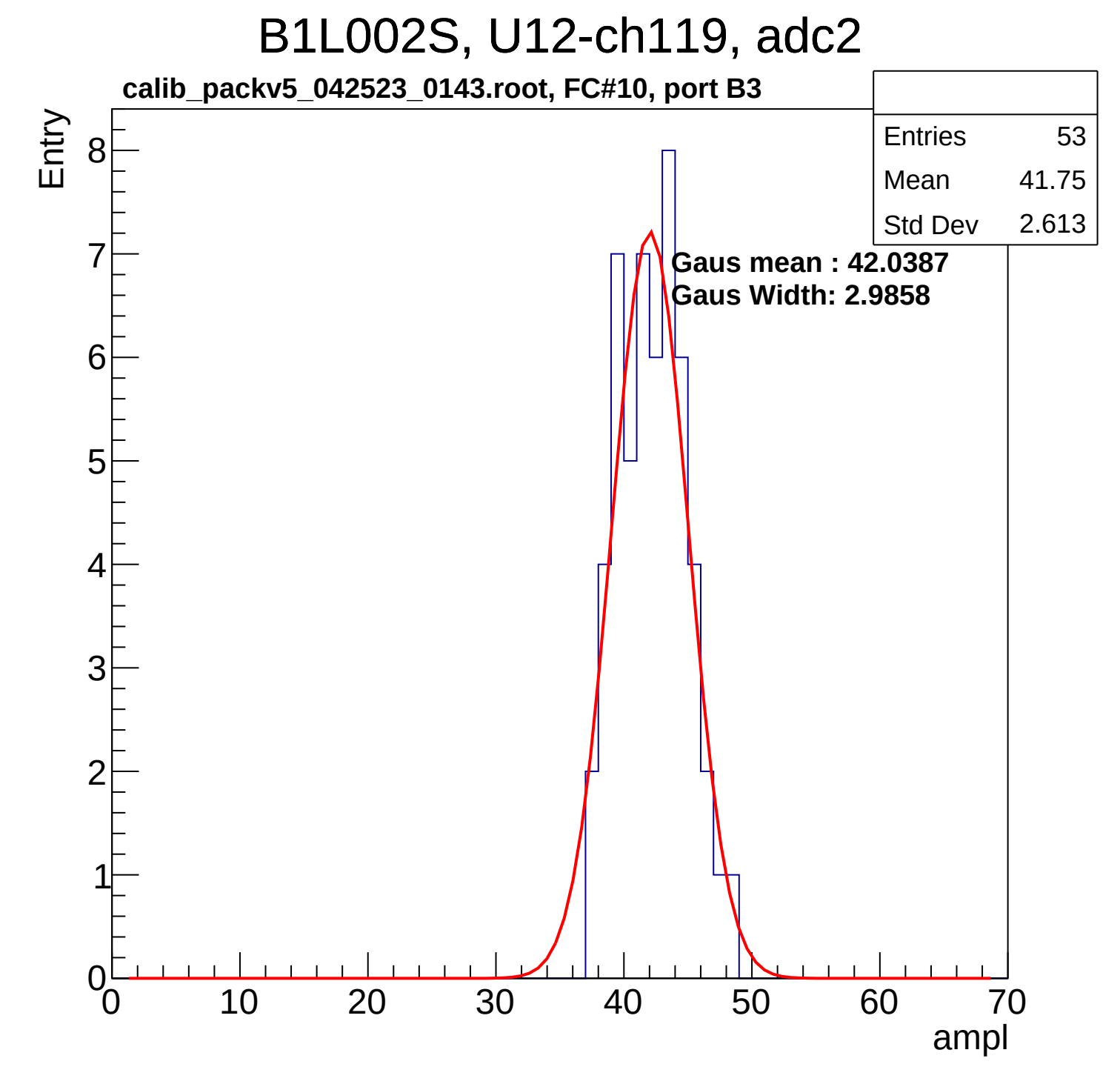
B1L002S, U12-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.75
Std Dev 2.613
Gaus mean : 42.0387
Gaus Width: 2.9858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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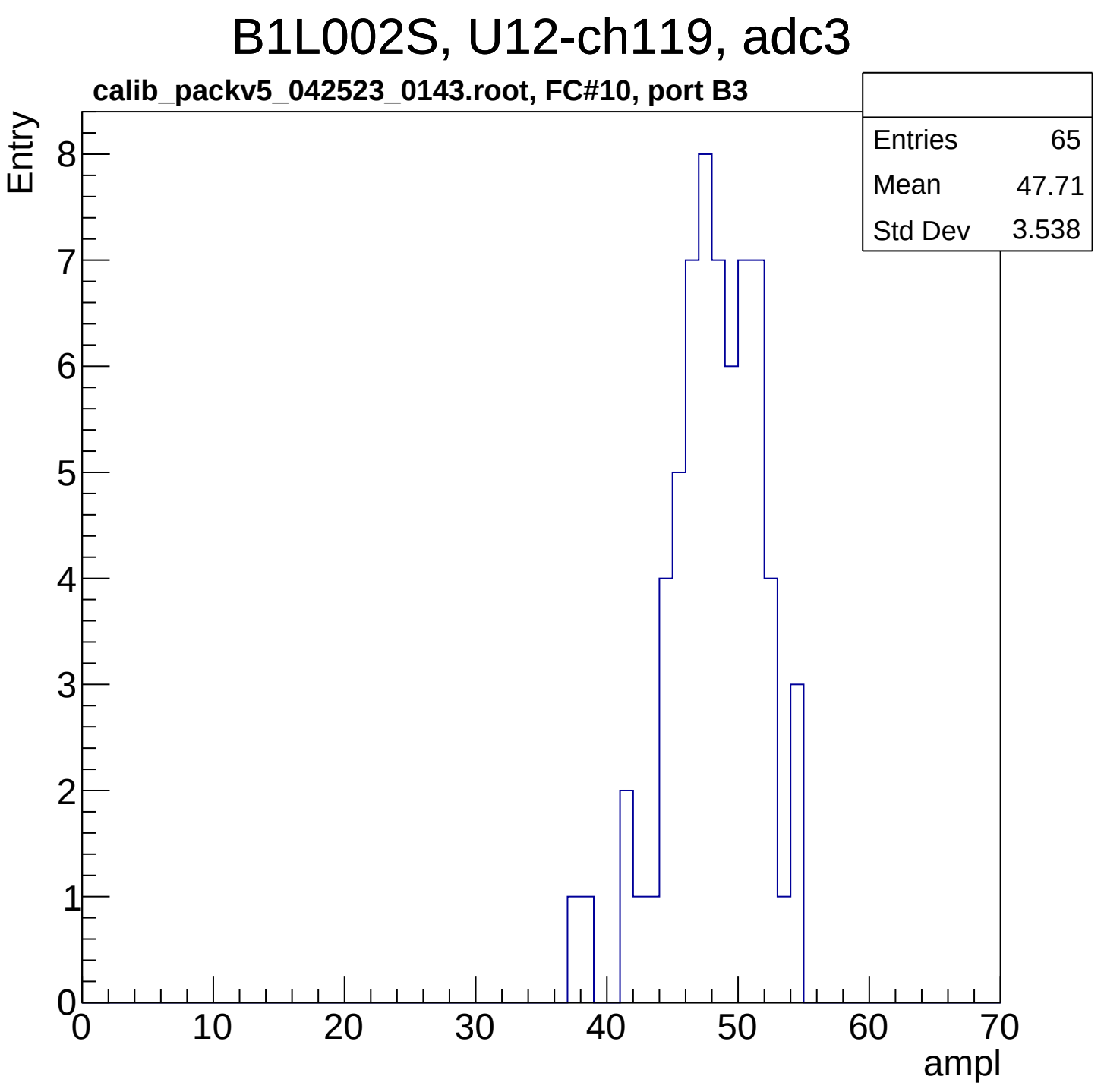
B1L002S, U12-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.71
Std Dev 3.538
Entry
ampl
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3
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20
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60
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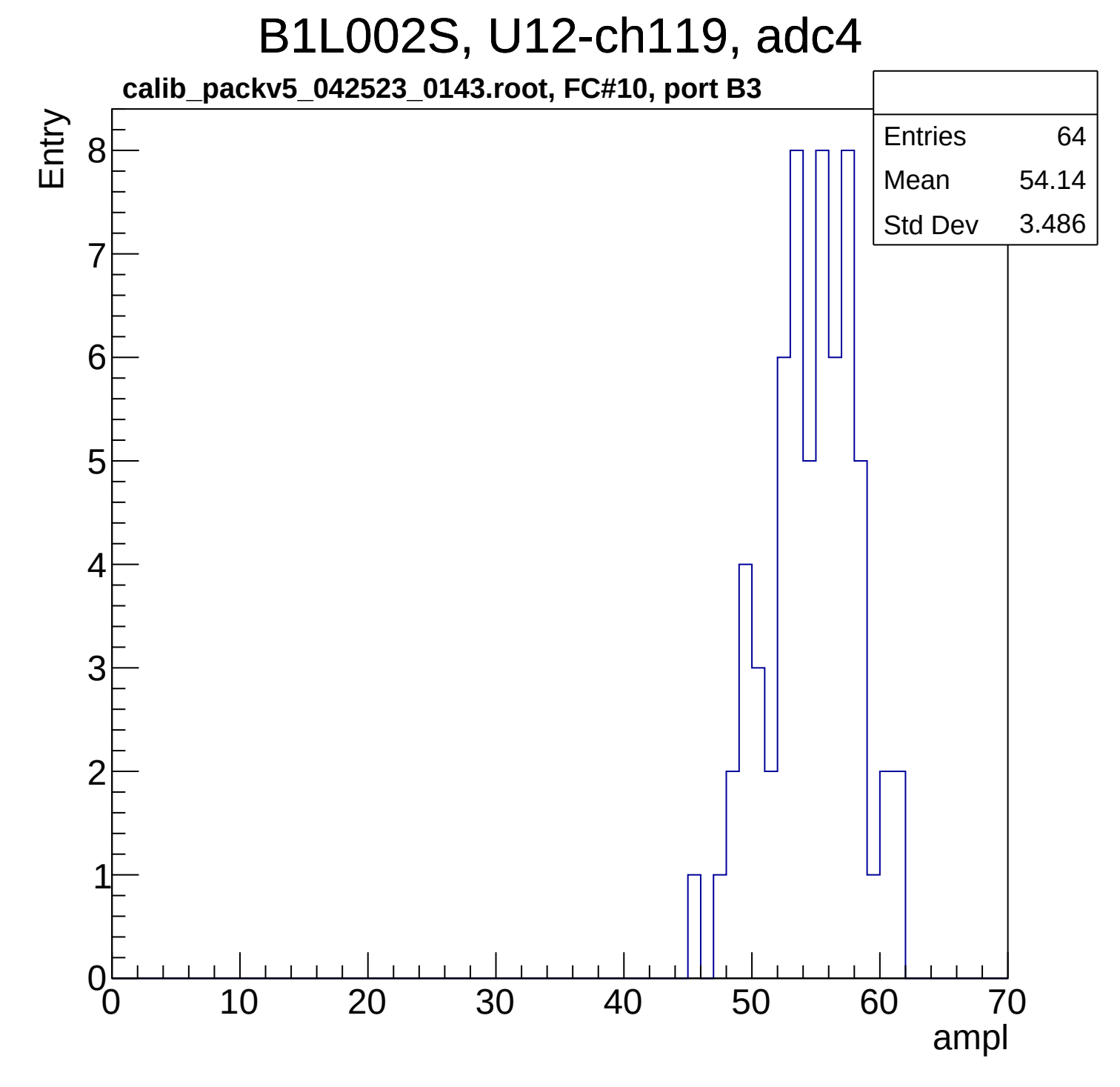
B1L002S, U12-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.14
Std Dev 3.486
Entry
ampl
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7
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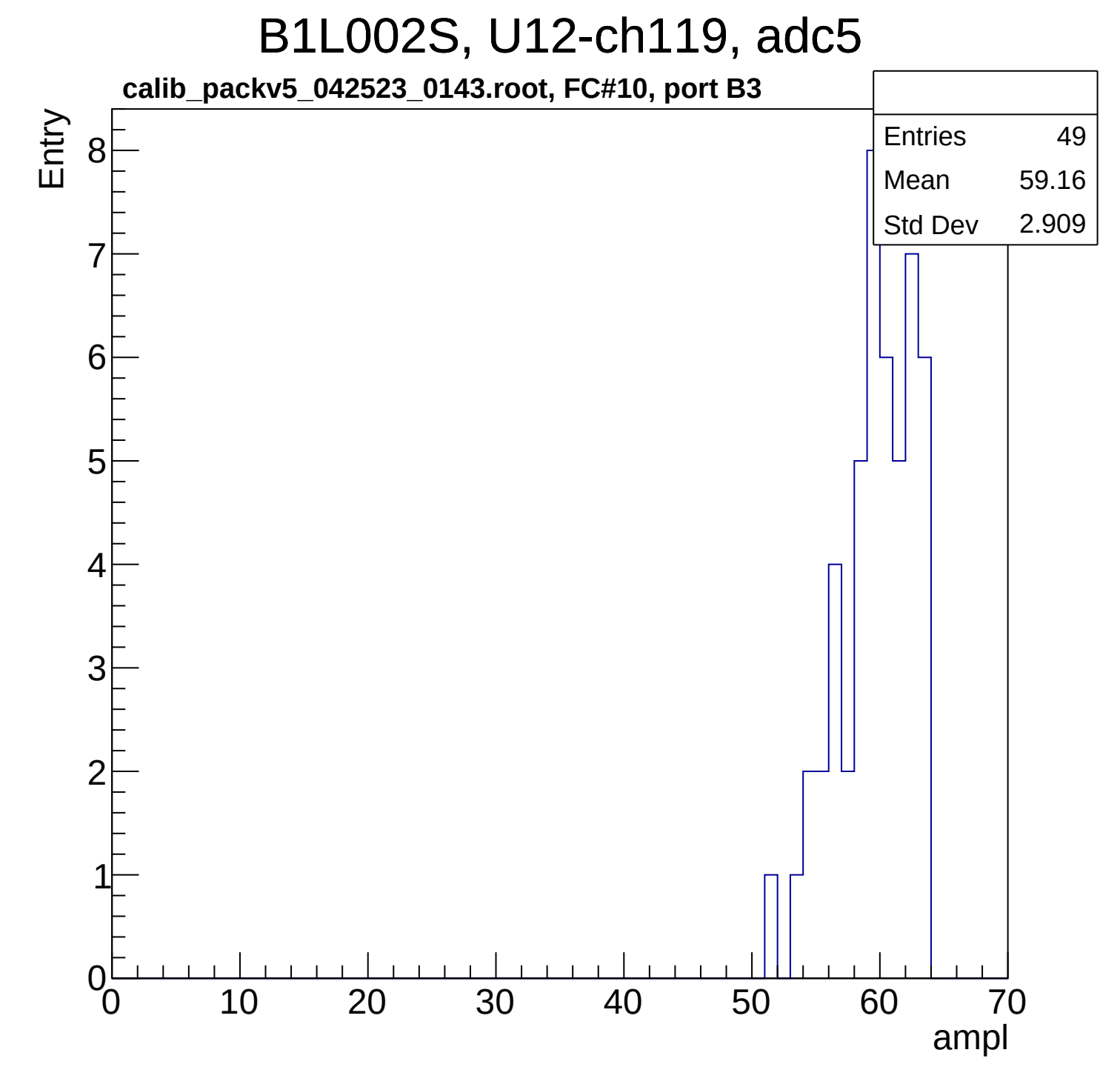
0
10
20
30
40
50
60
70


B1L002S, U12-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 59.16
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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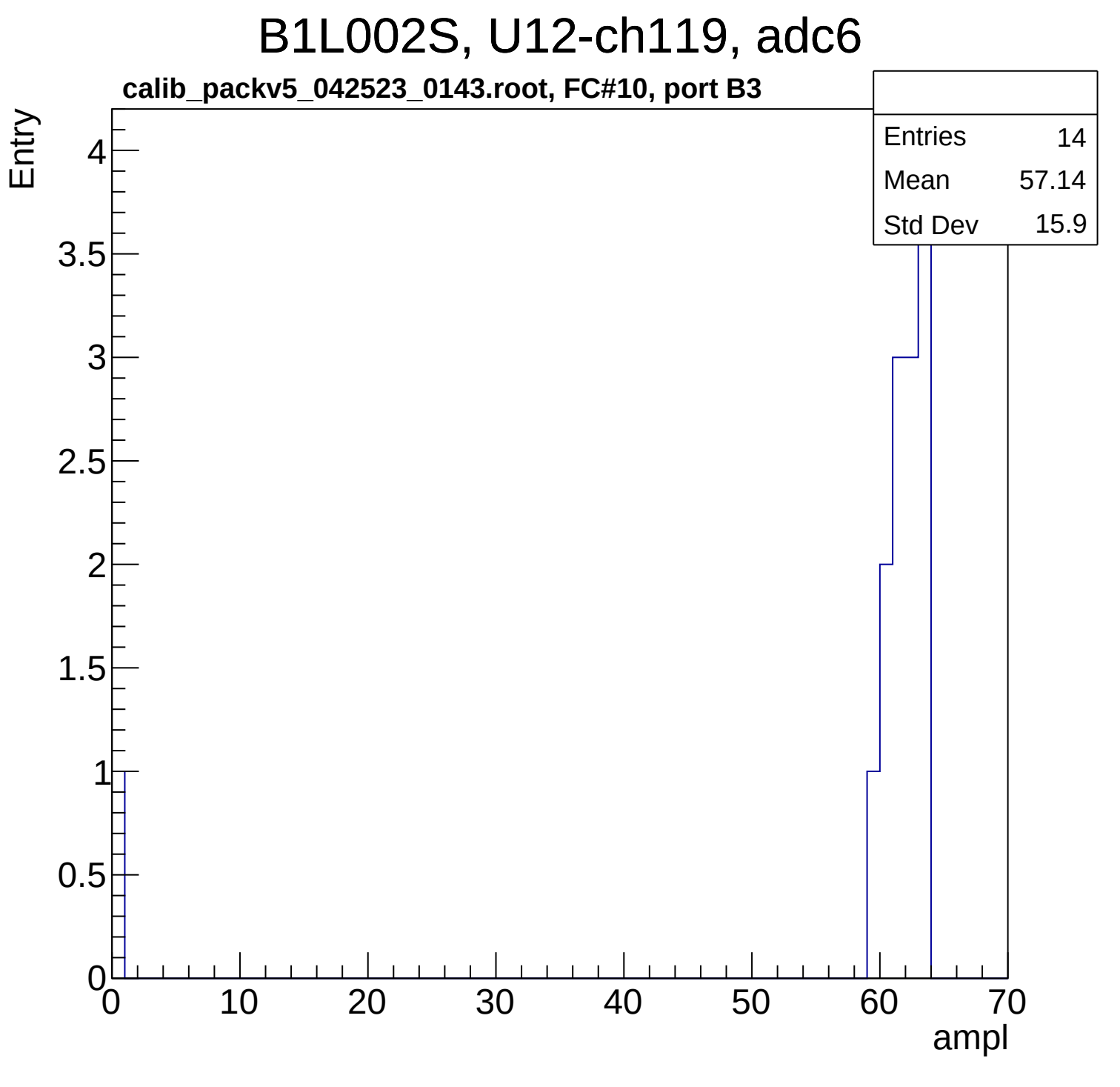
B1L002S, U12-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.14
Std Dev 15.9
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
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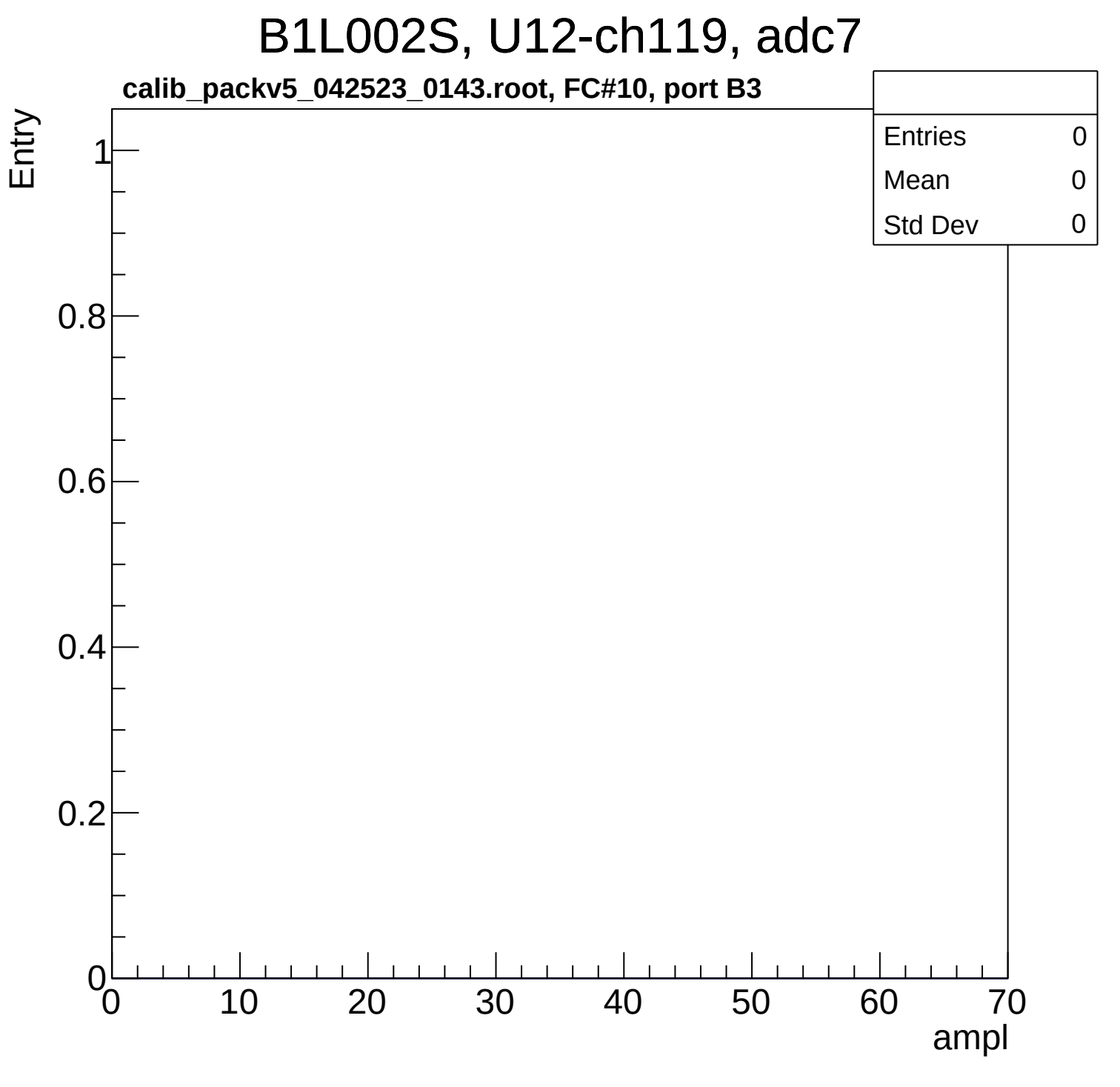
60
70
ampl


B1L002S, U12-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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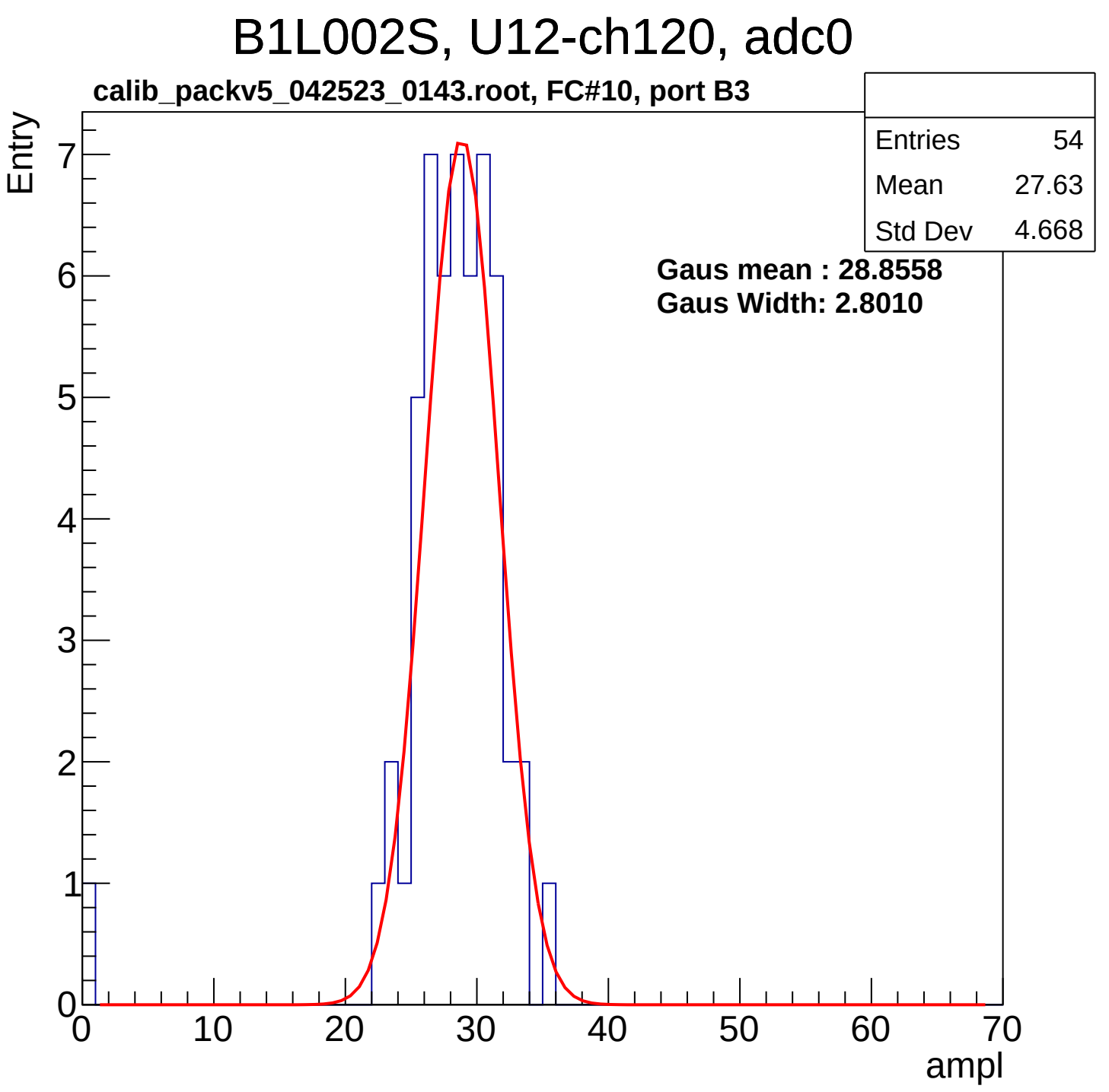
40
50
60
70
ampl


B1L002S, U12-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.63
Std Dev 4.668
Gaus mean : 28.8558
Gaus Width: 2.8010
Entry
ampl
0
1
2
3
4
5
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0
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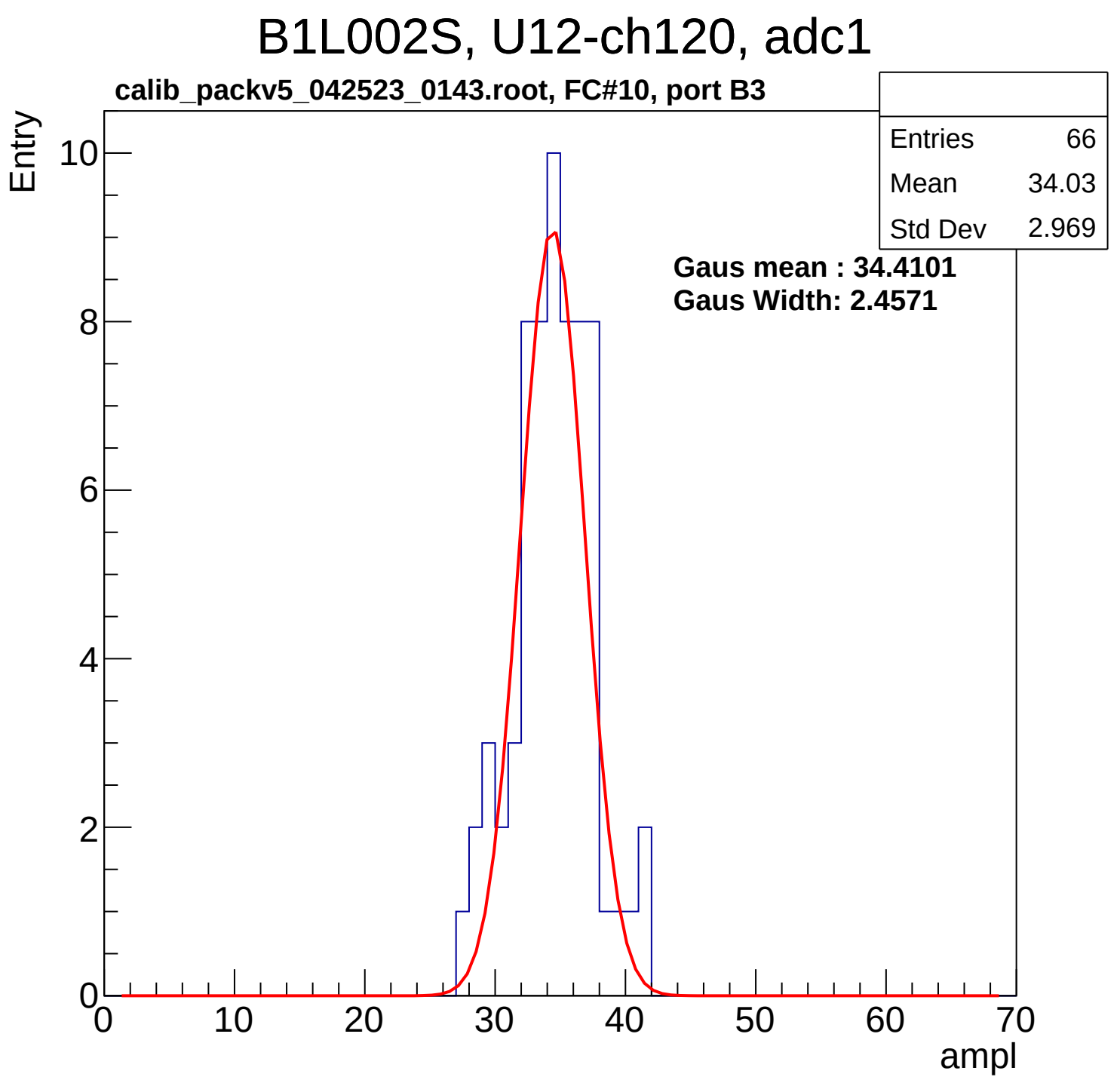
B1L002S, U12-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.03
Std Dev 2.969
Gaus mean : 34.4101
Gaus Width: 2.4571
Entry
ampl
0
2
4
6
8
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0
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40
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60
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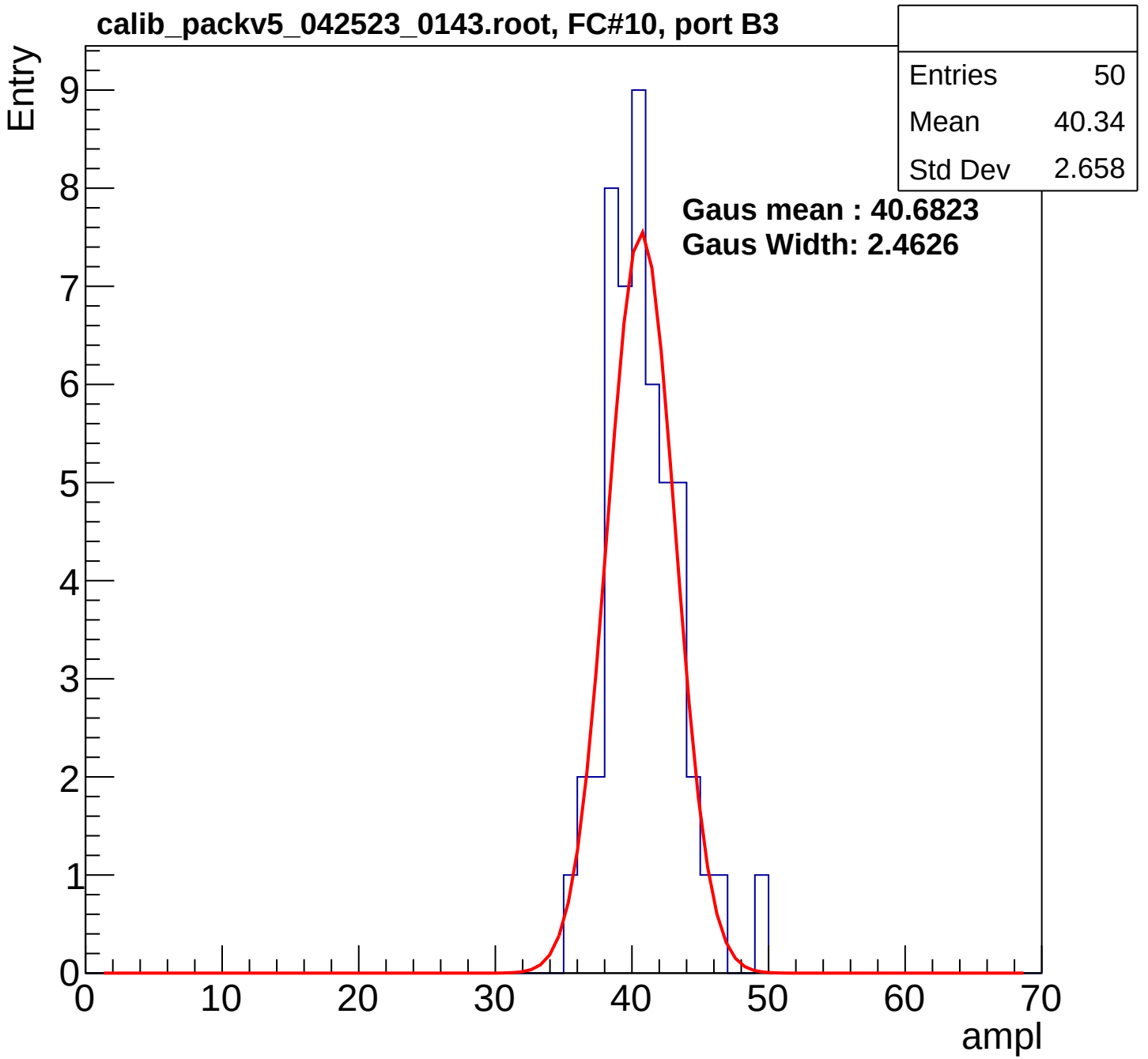
B1L002S, U12-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.34
Std Dev 2.658
Gaus mean : 40.6823
Gaus Width: 2.4626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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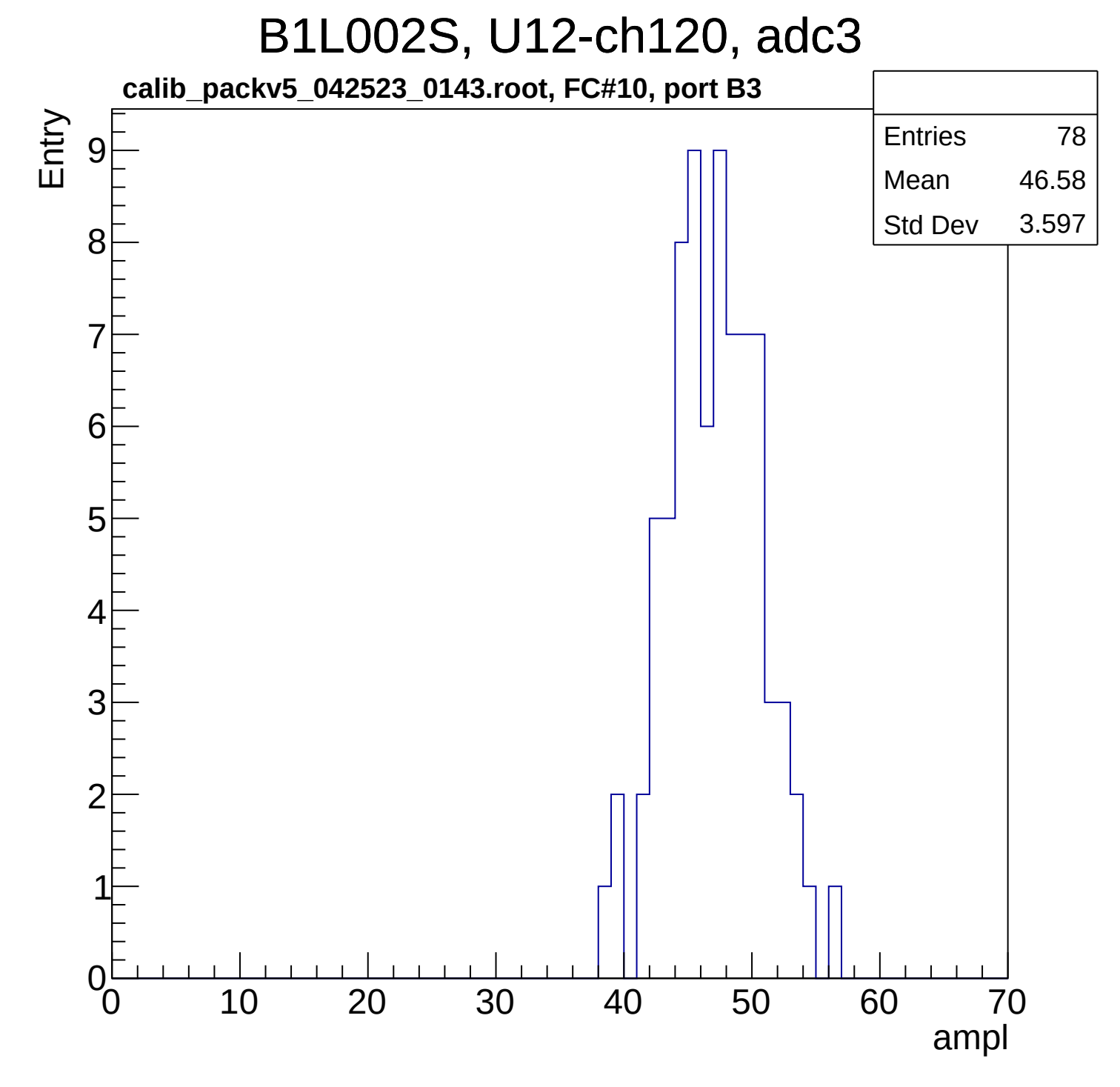
B1L002S, U12-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 46.58
Std Dev 3.597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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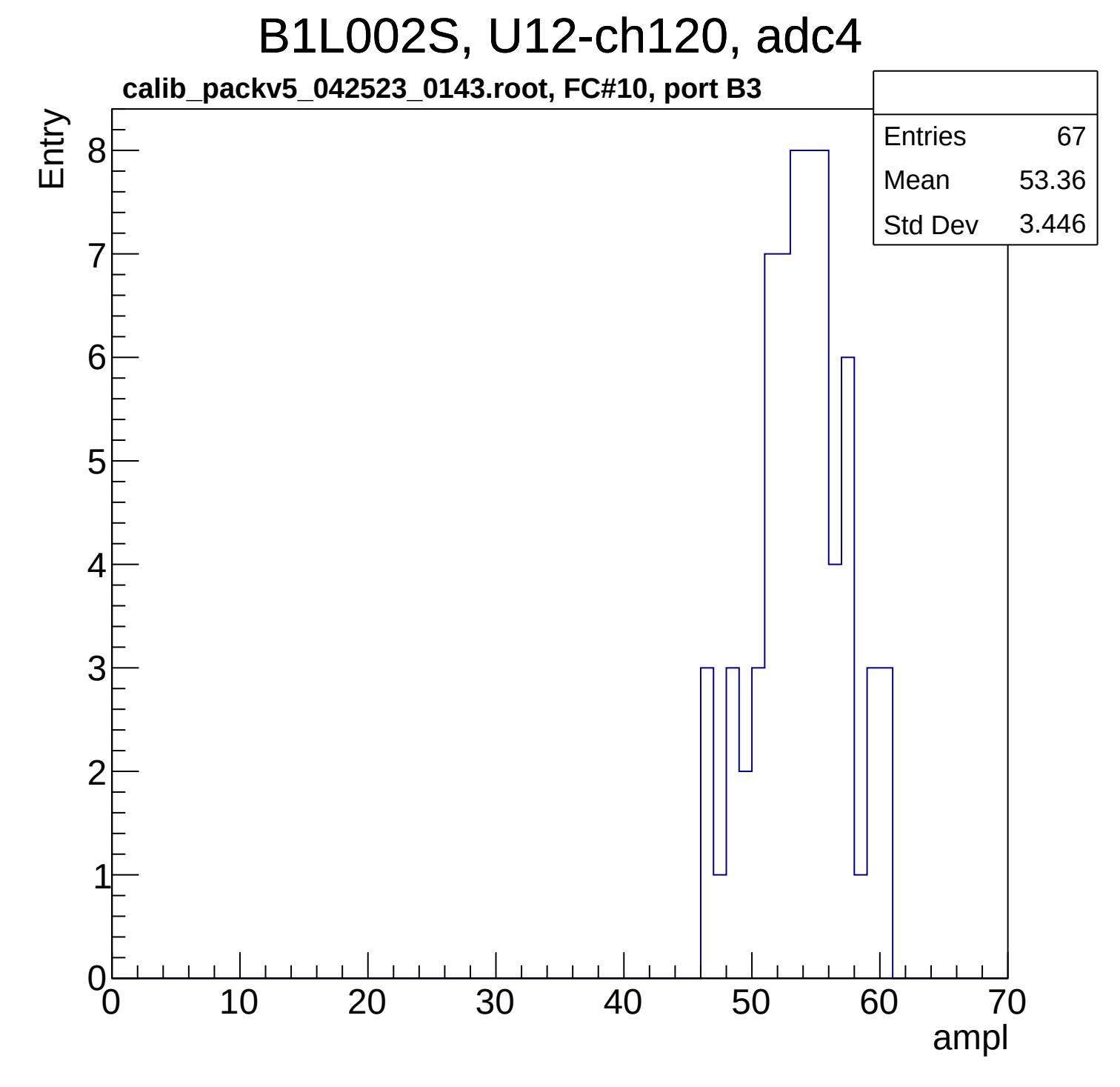
B1L002S, U12-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.36
Std Dev 3.446
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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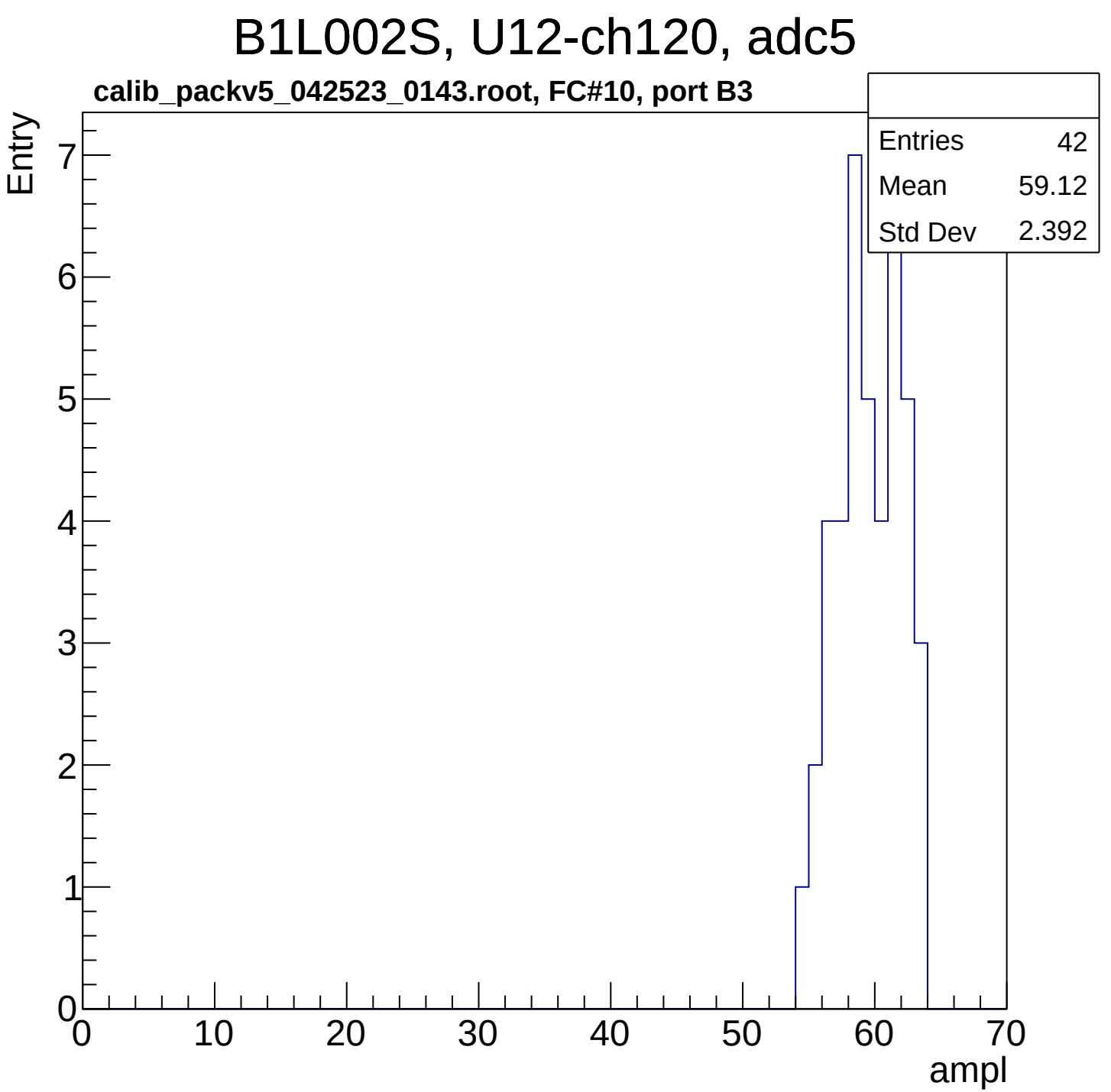
B1L002S, U12-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.12
Std Dev 2.392
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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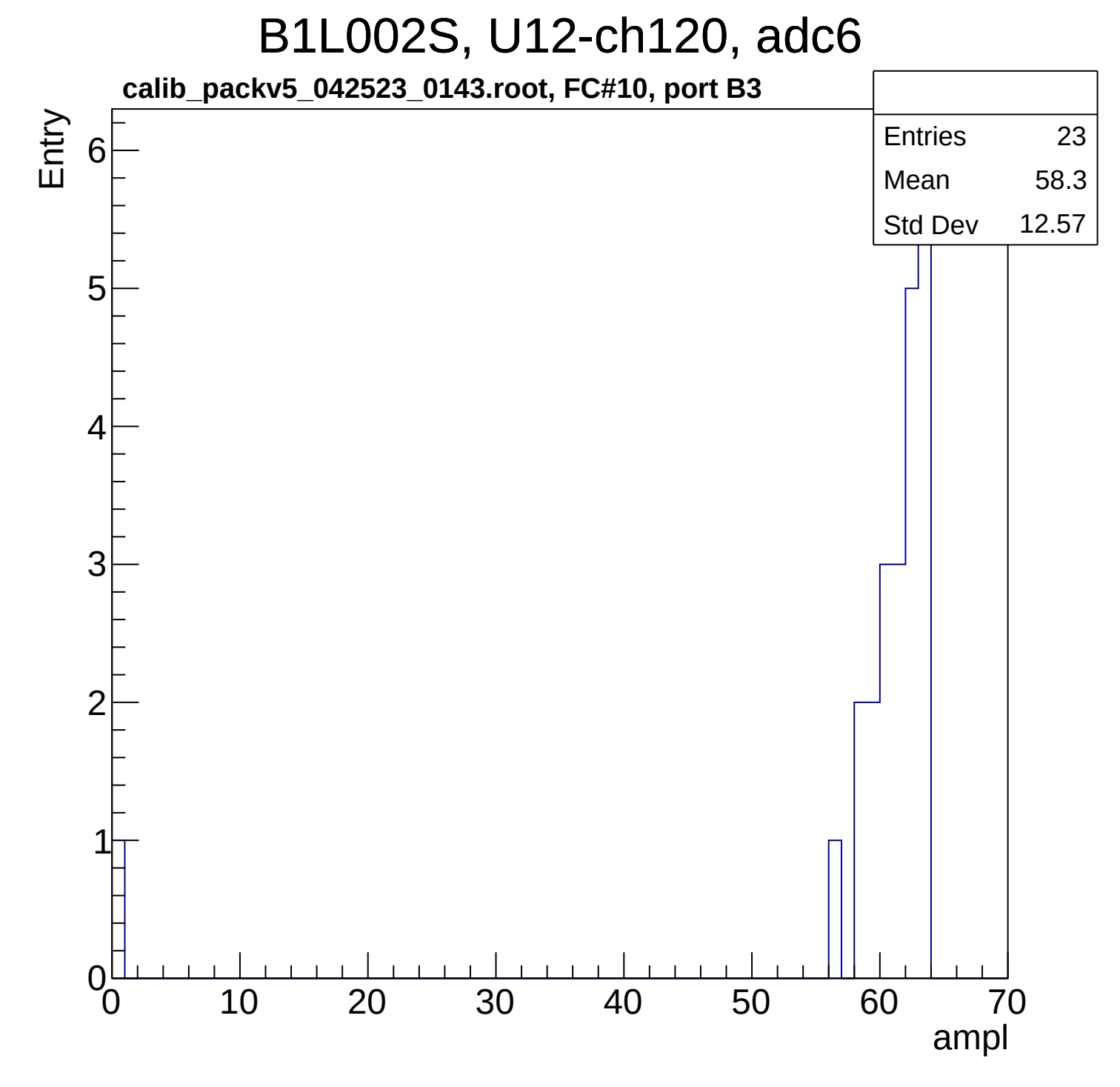
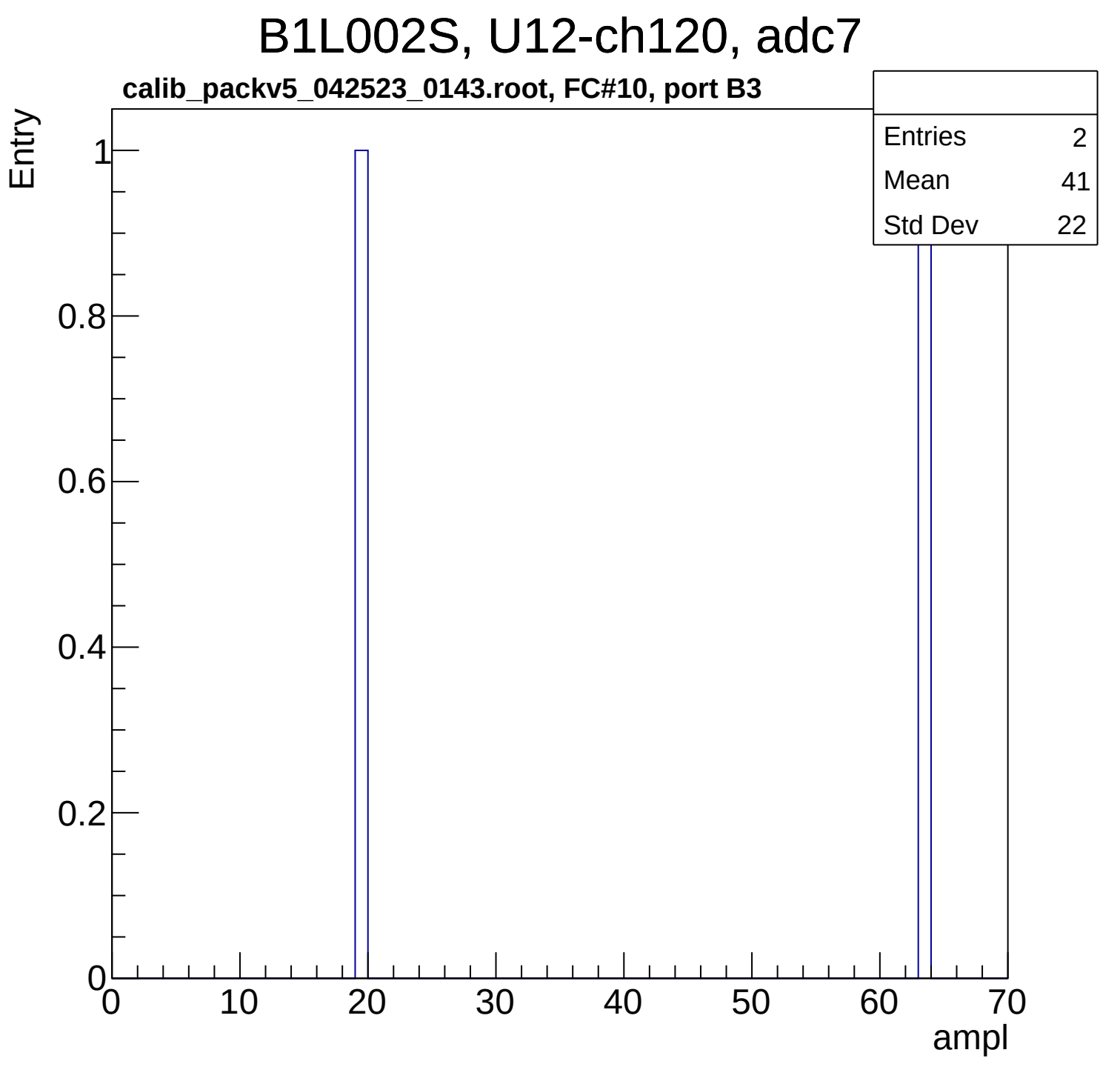

B1L002S, U12-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.3
Std Dev 12.57
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 22
Entry
ampl
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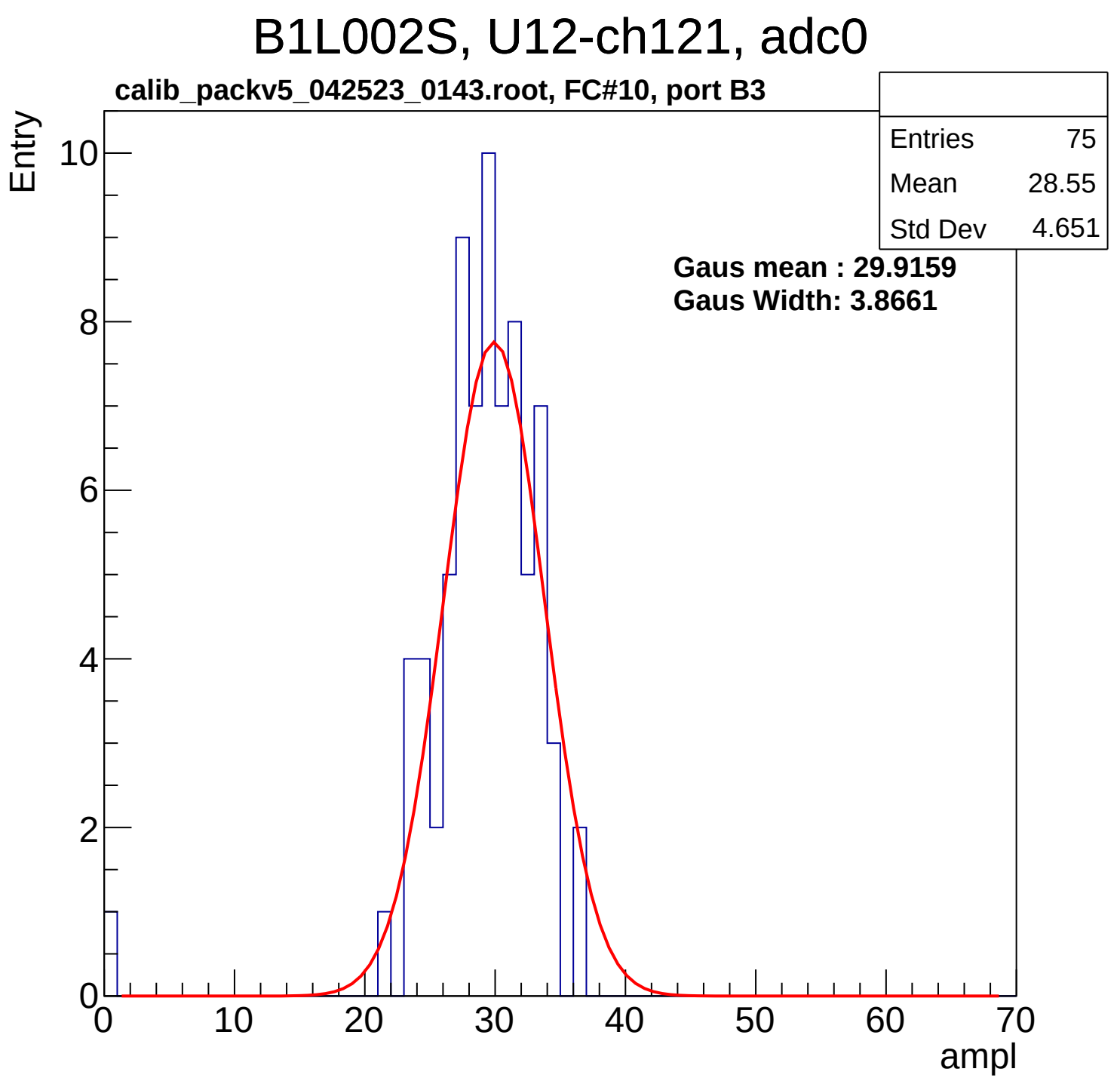
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U12-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.55
Std Dev 4.651
Gaus mean : 29.9159
Gaus Width: 3.8661
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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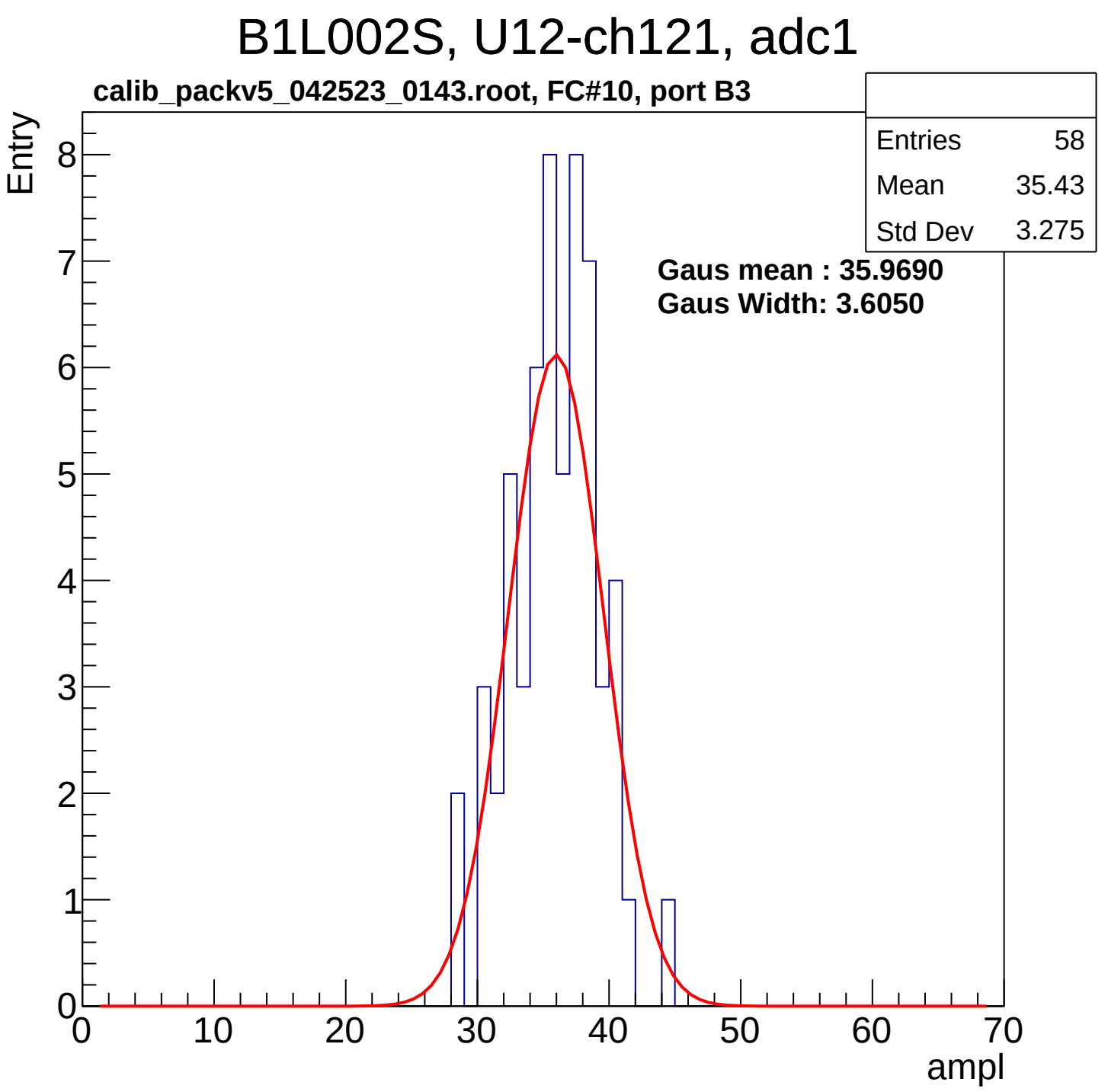
B1L002S, U12-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.43
Std Dev 3.275
Gaus mean : 35.9690
Gaus Width: 3.6050
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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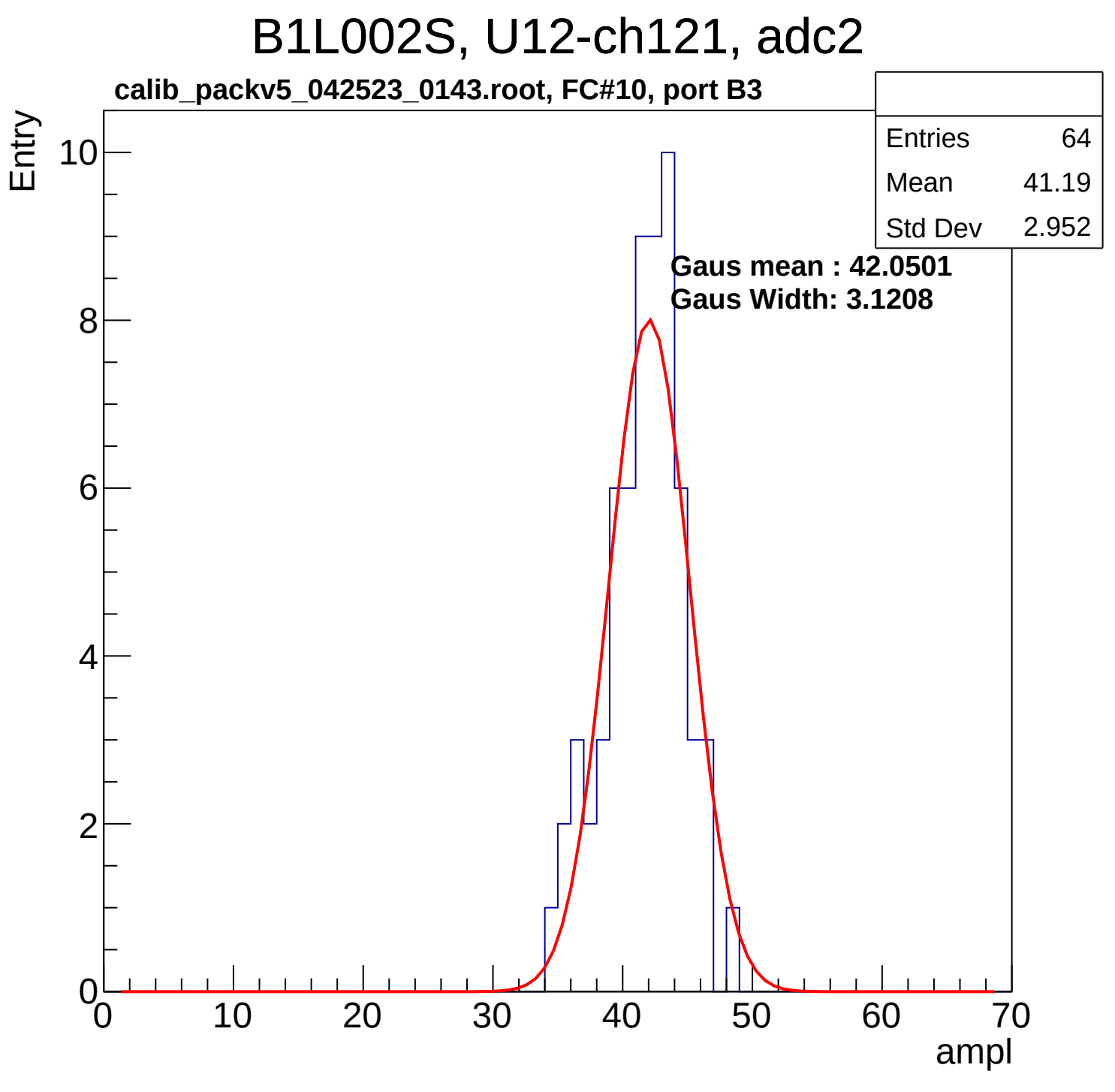
B1L002S, U12-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.19
Std Dev 2.952
Gaus mean : 42.0501
Gaus Width: 3.1208
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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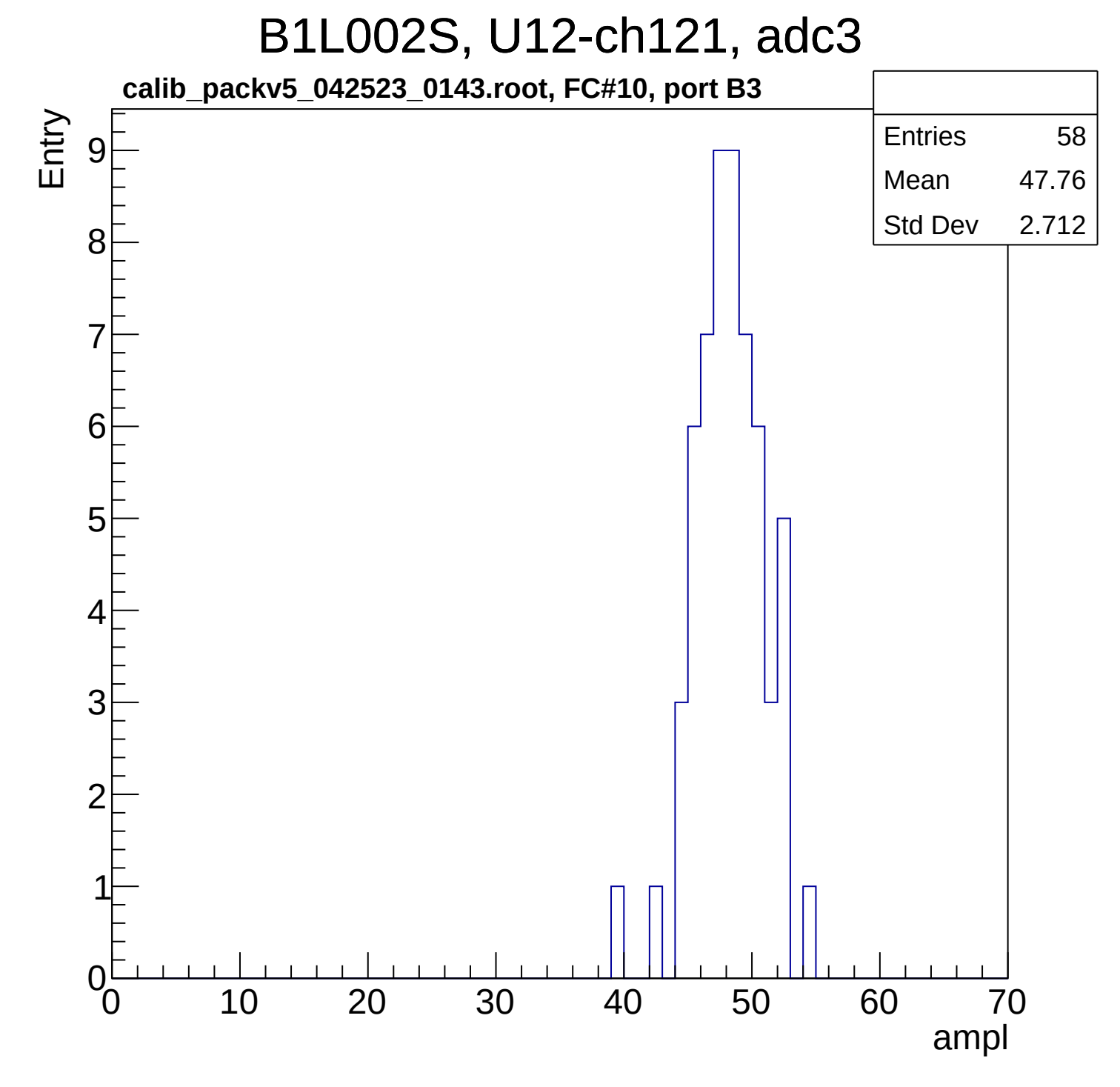
B1L002S, U12-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.76
Std Dev 2.712
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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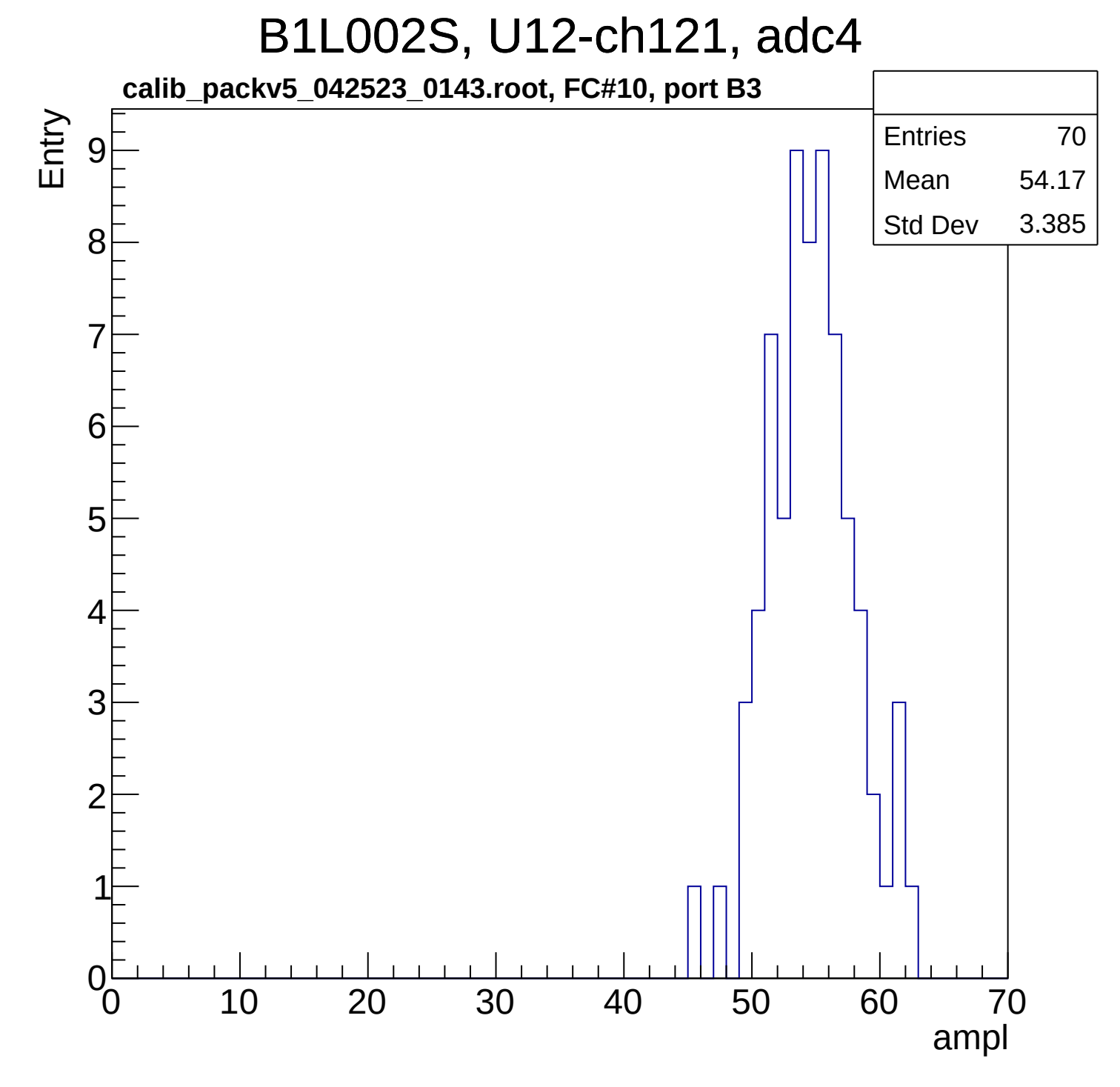
B1L002S, U12-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 54.17
Std Dev 3.385
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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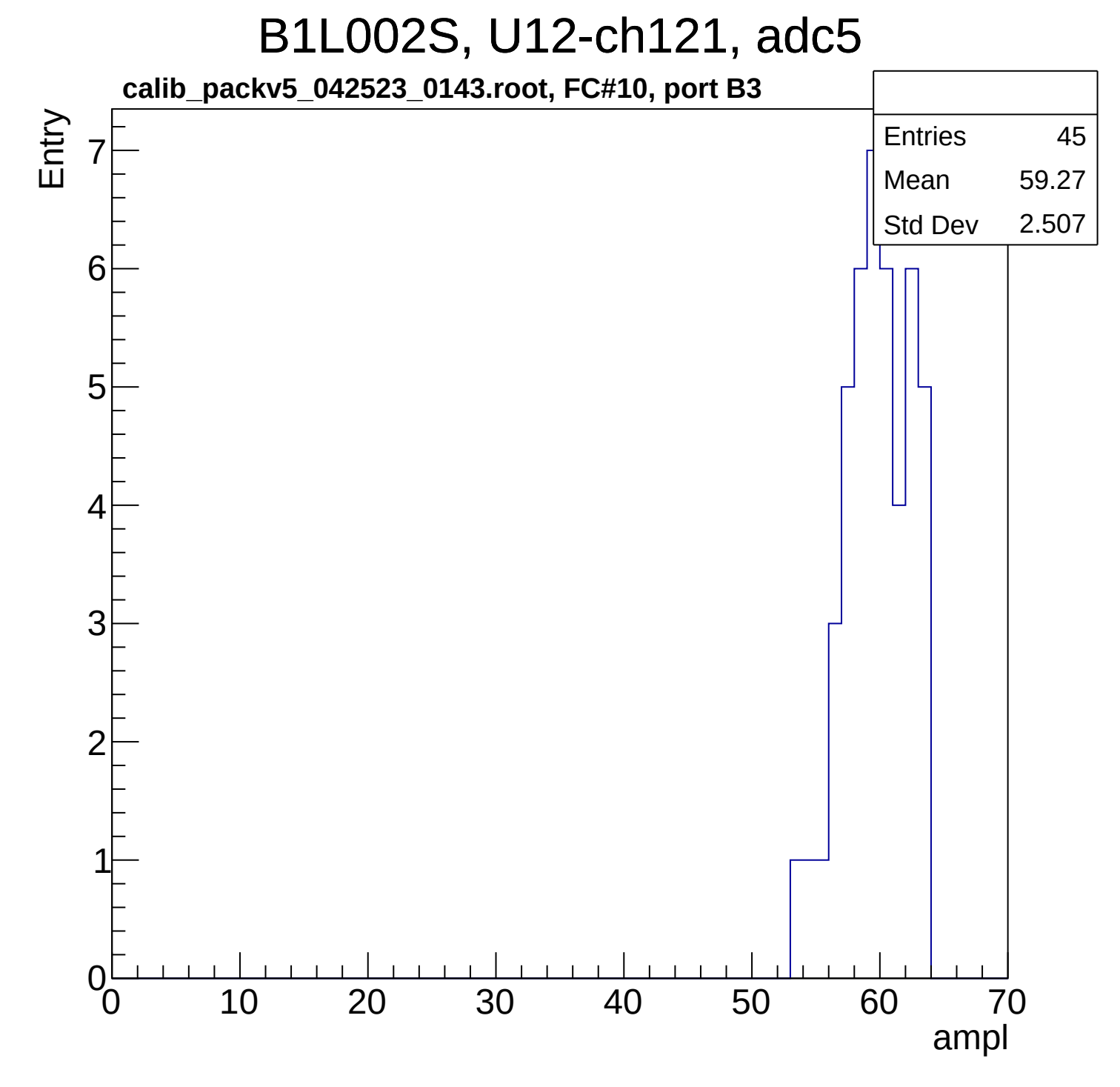
B1L002S, U12-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 59.27
Std Dev 2.507
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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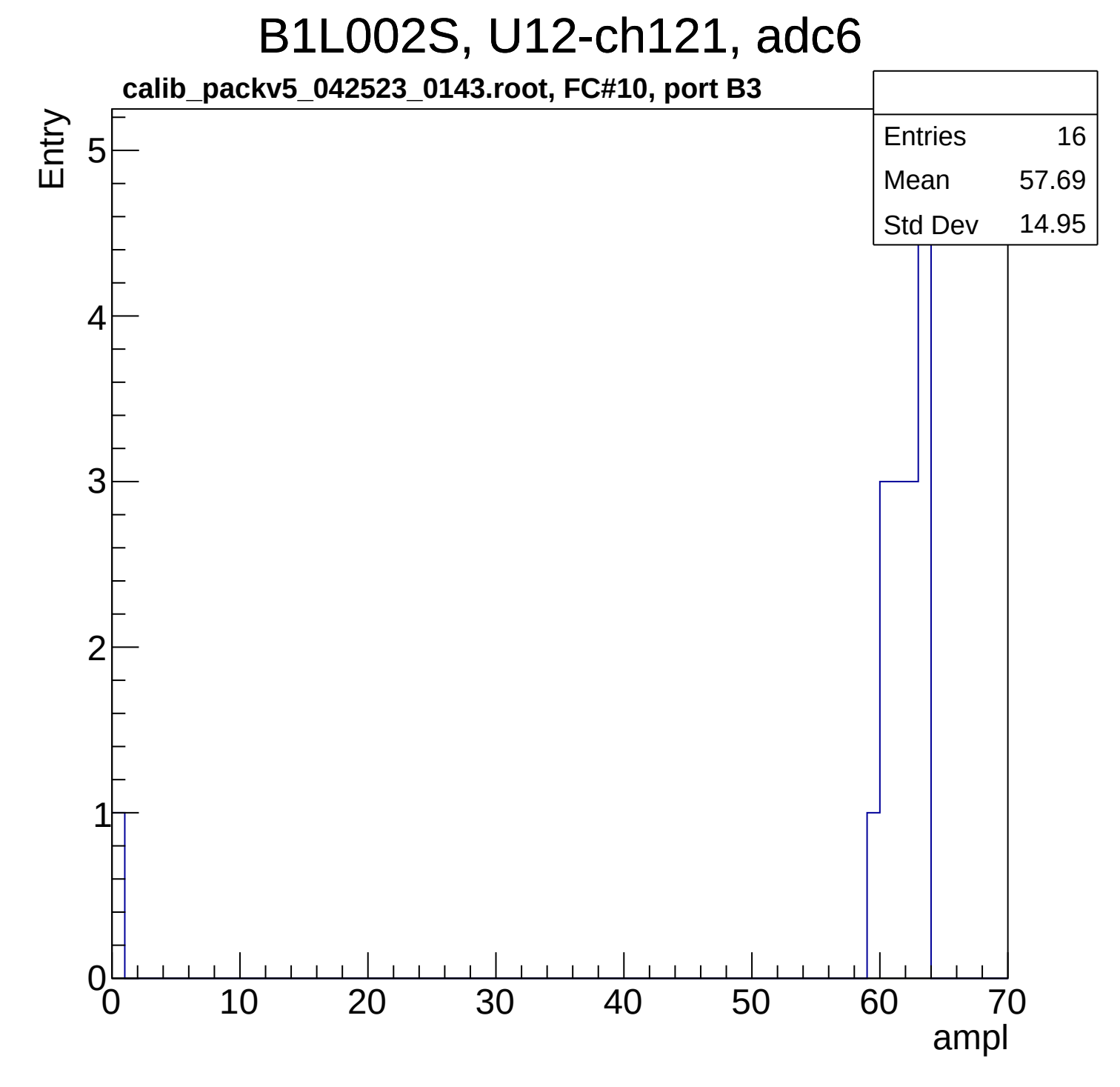
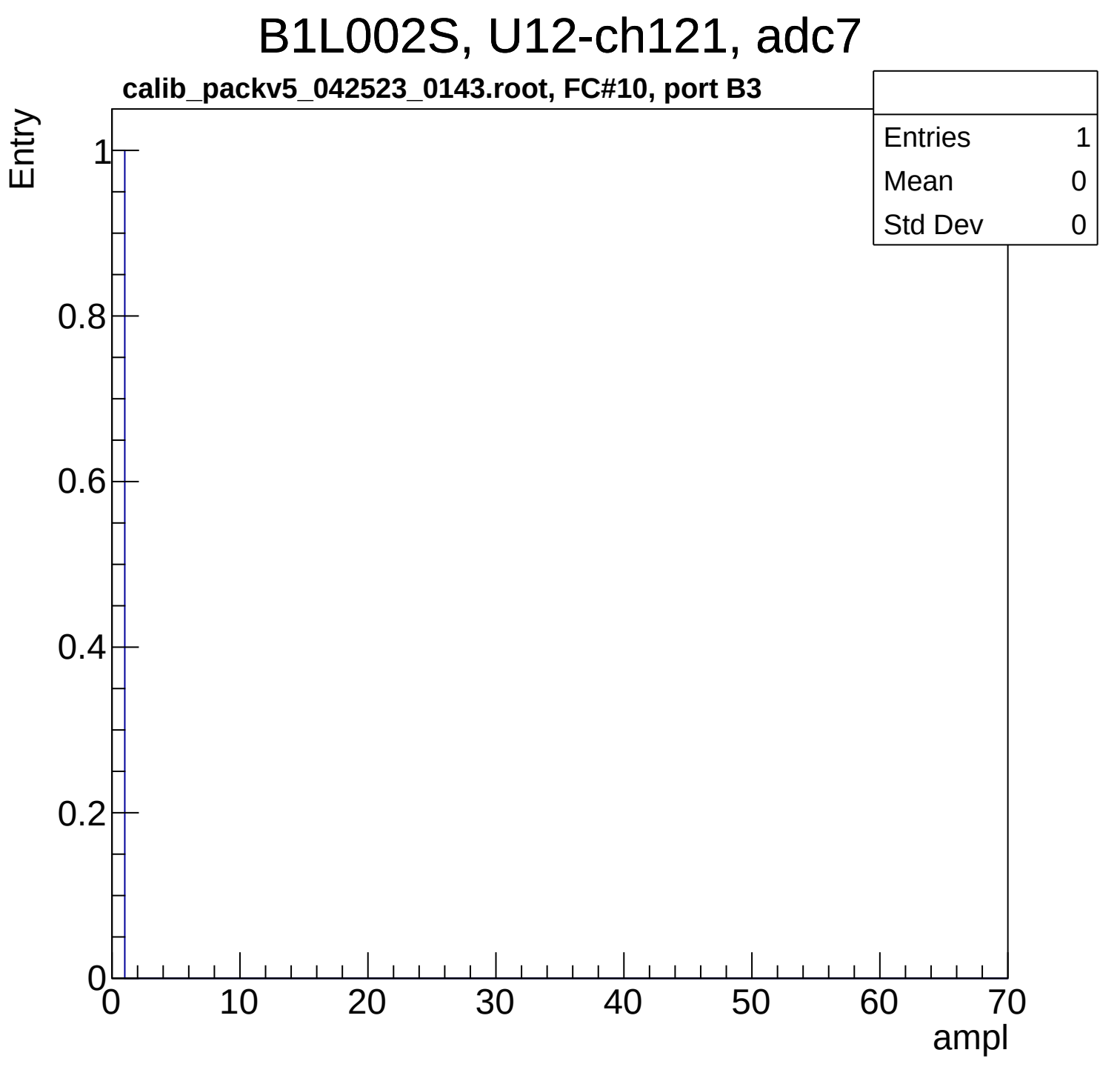
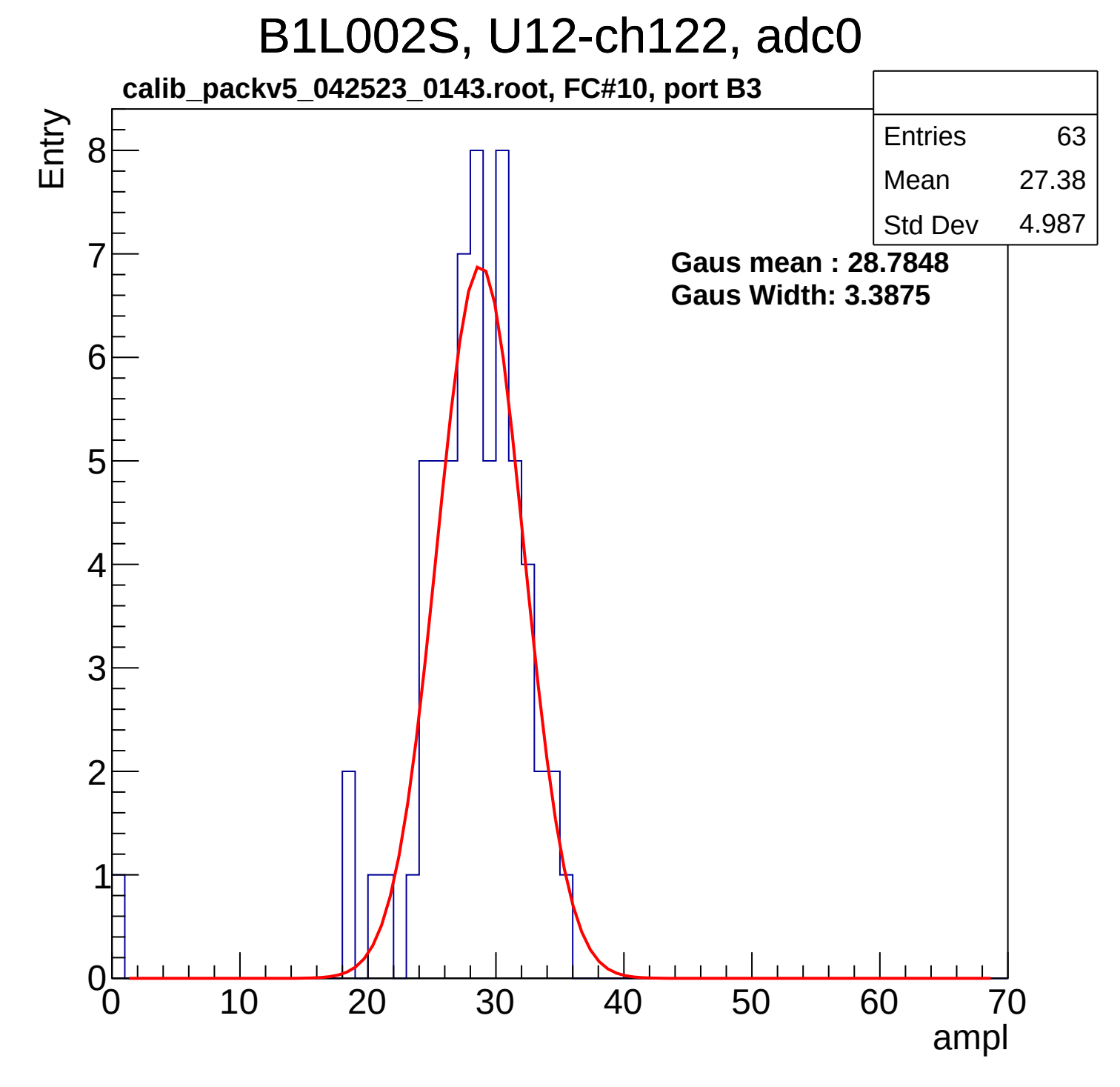

B1L002S, U12-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.69
Std Dev 14.95
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.38
Std Dev 4.987
Gaus mean : 28.7848
Gaus Width: 3.3875
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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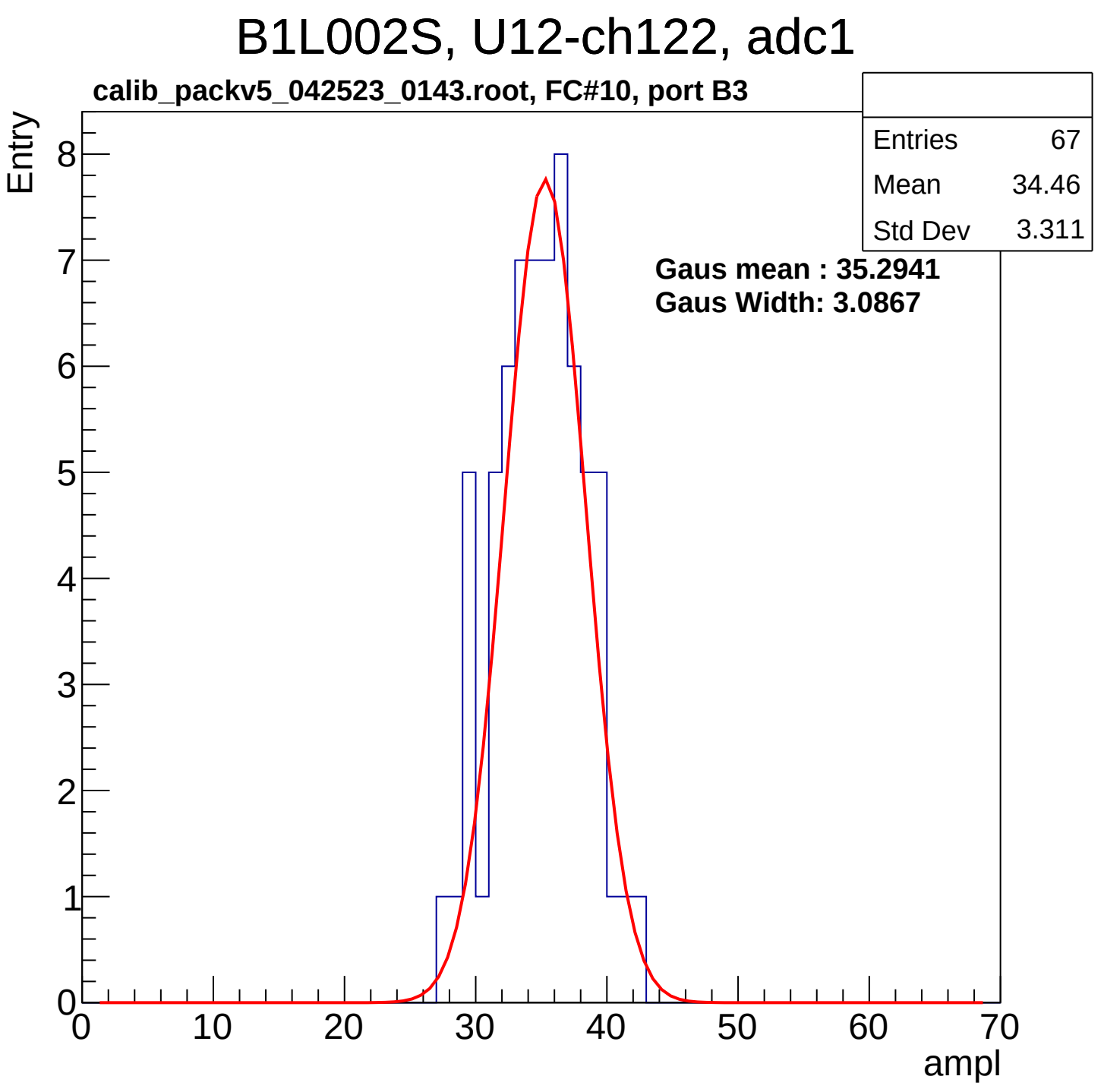
B1L002S, U12-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.46
Std Dev 3.311
Gaus mean : 35.2941
Gaus Width: 3.0867
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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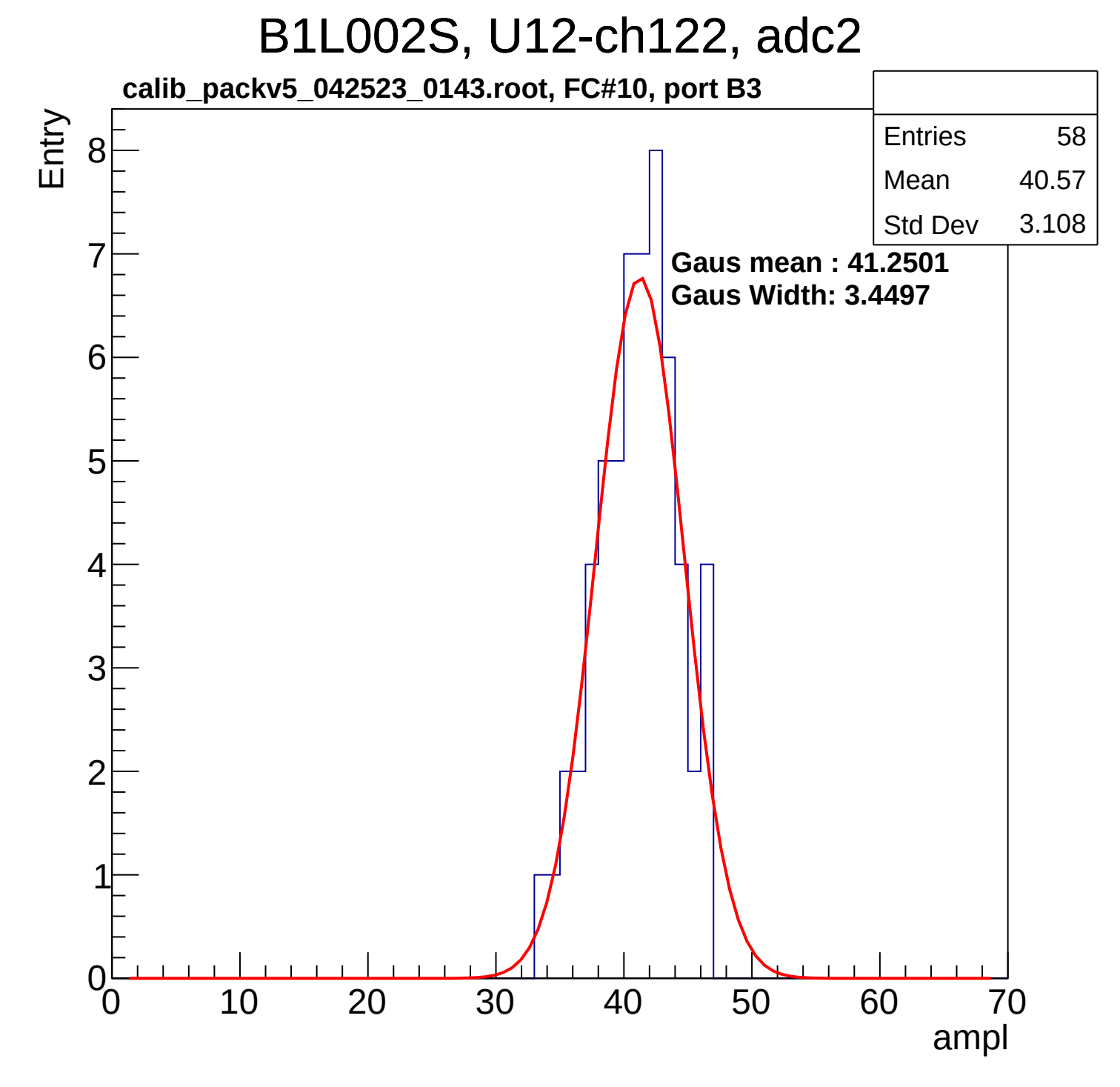
B1L002S, U12-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.57
Std Dev 3.108
Gaus mean : 41.2501
Gaus Width: 3.4497
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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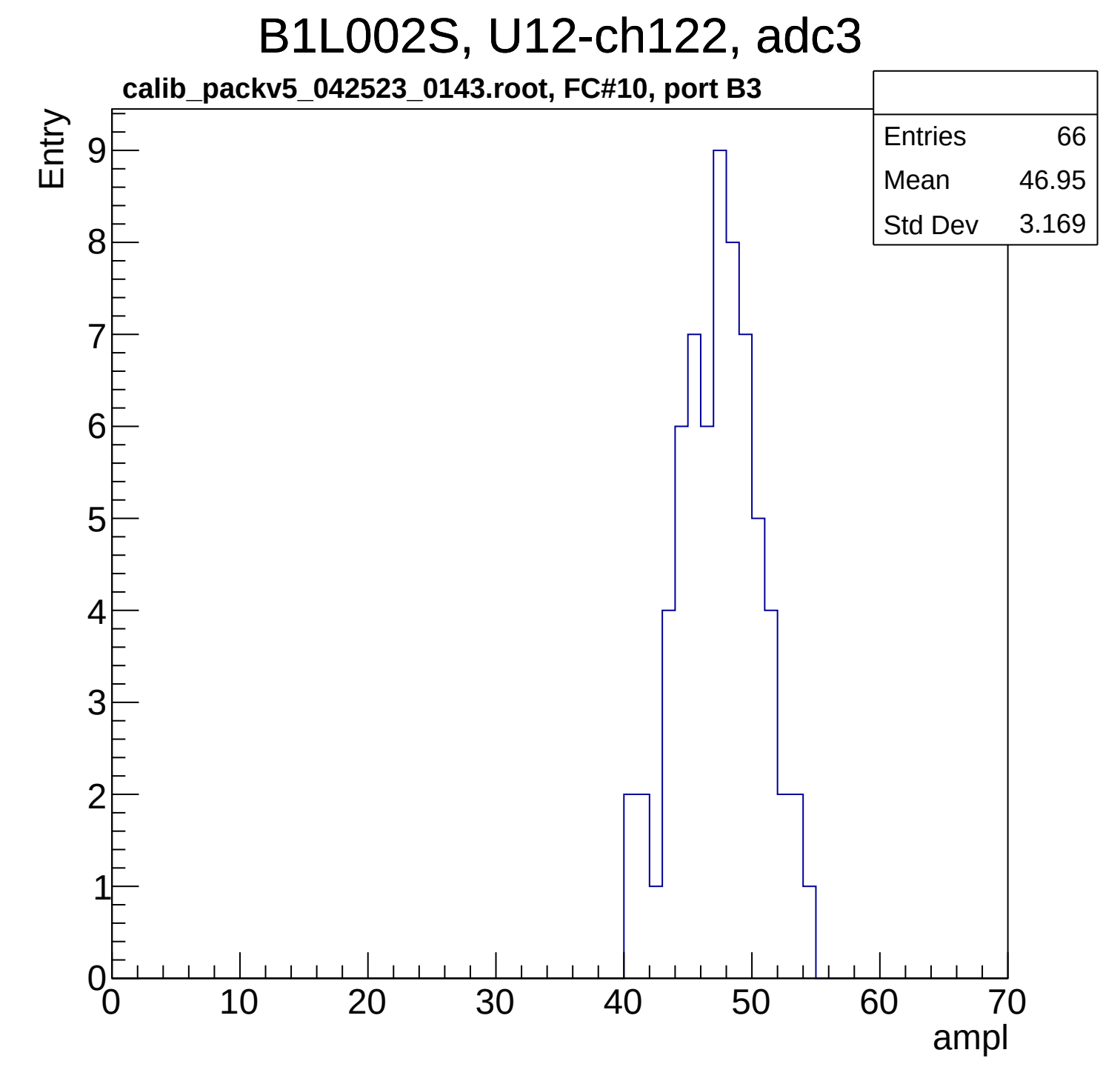
B1L002S, U12-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.95
Std Dev 3.169
Entry
ampl
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3
4
5
6
7
8
9
0
10
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30
40
50
60
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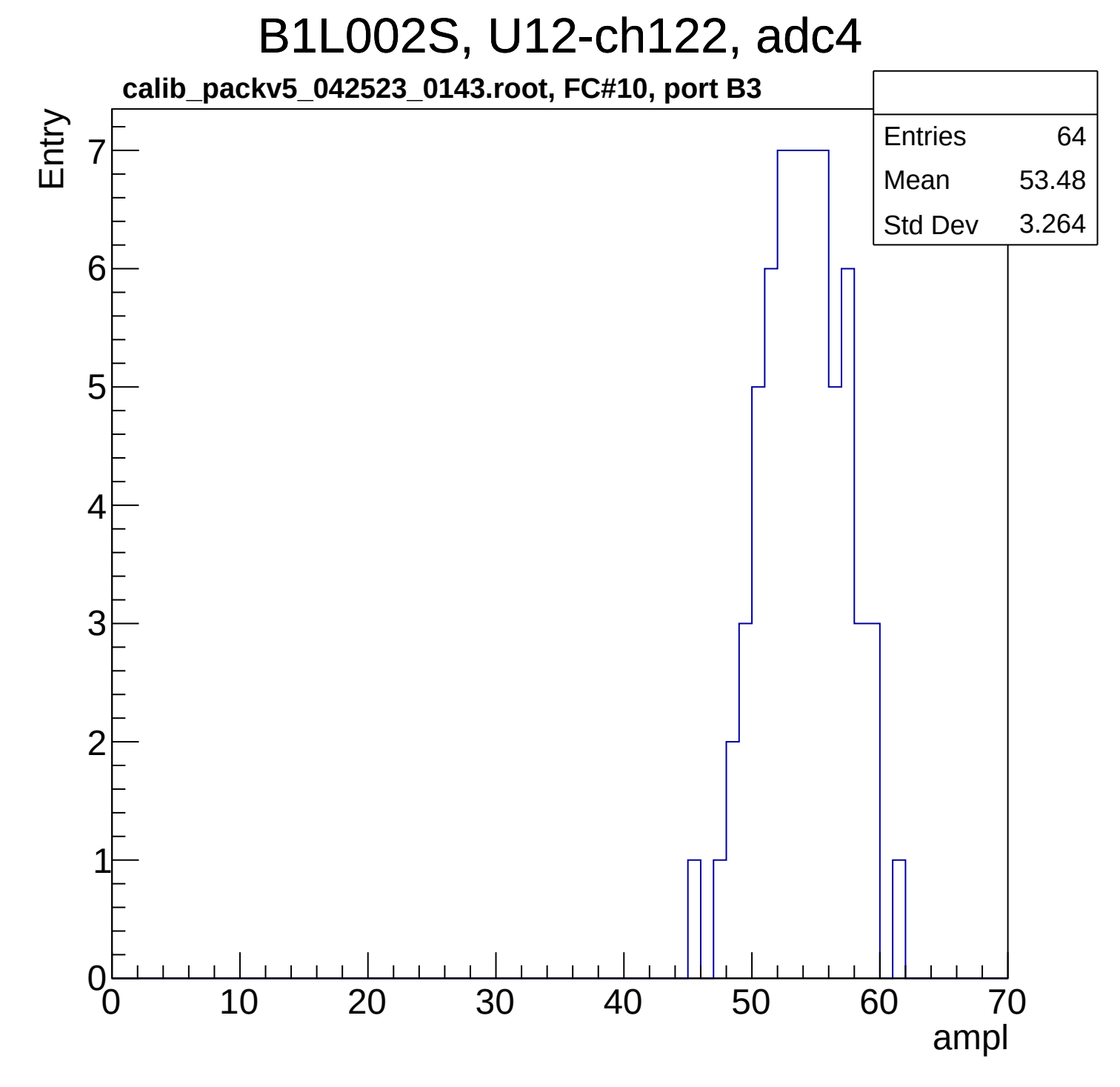
B1L002S, U12-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.48
Std Dev 3.264
Entry
ampl
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3
4
5
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0
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60
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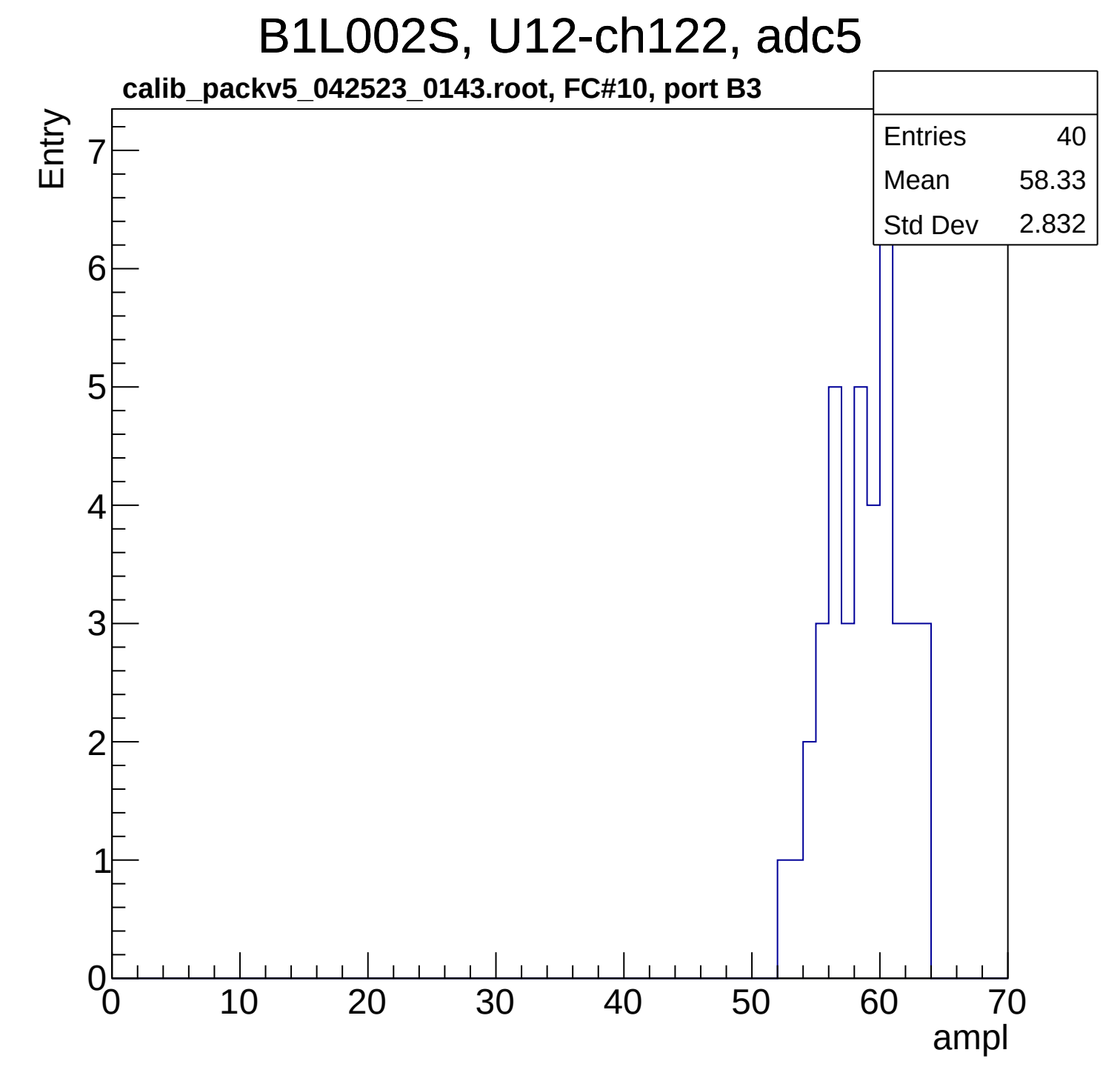
B1L002S, U12-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 58.33
Std Dev 2.832
Entry
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2
3
4
5
6
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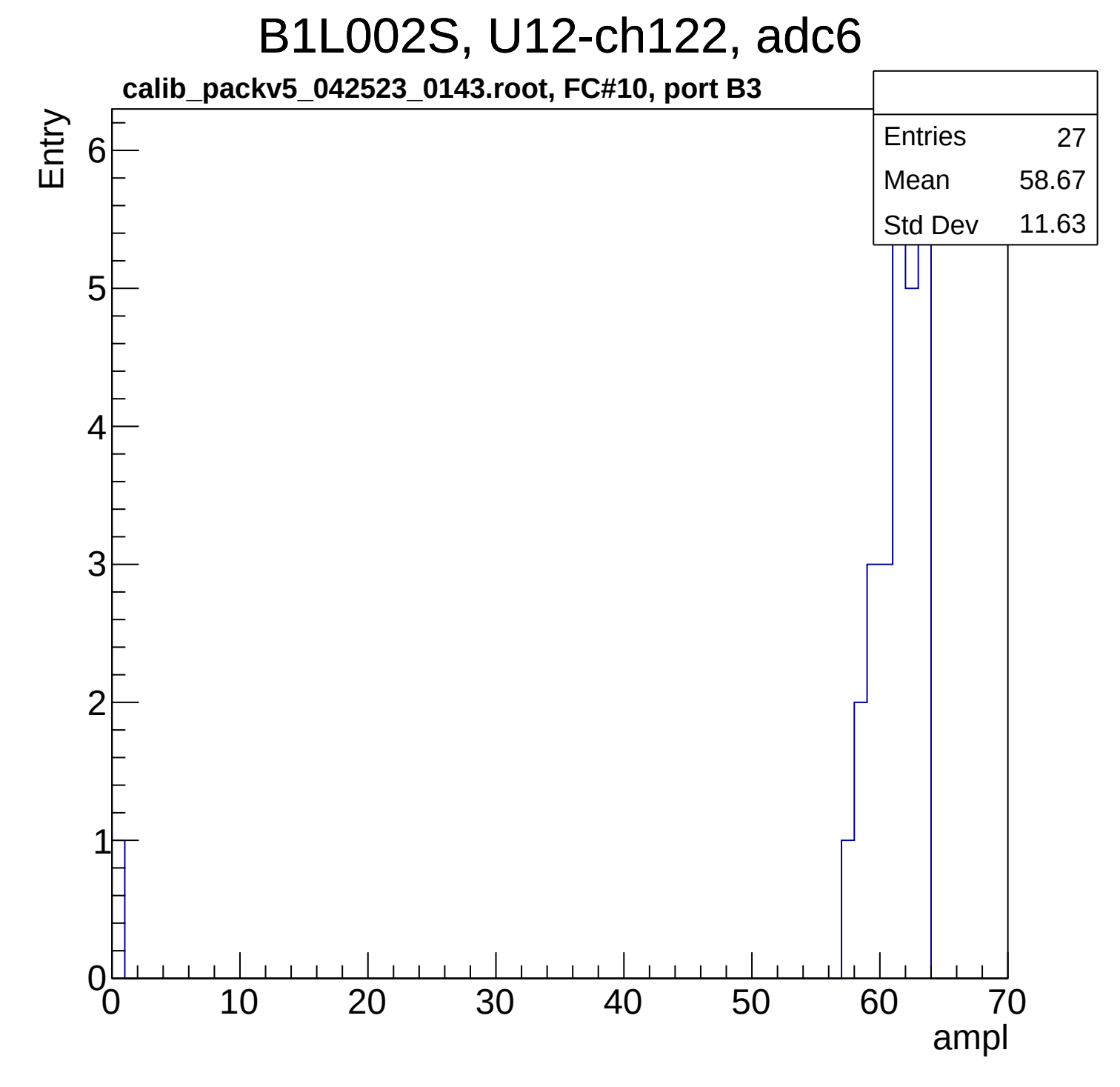
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.67
Std Dev 11.63
Entry
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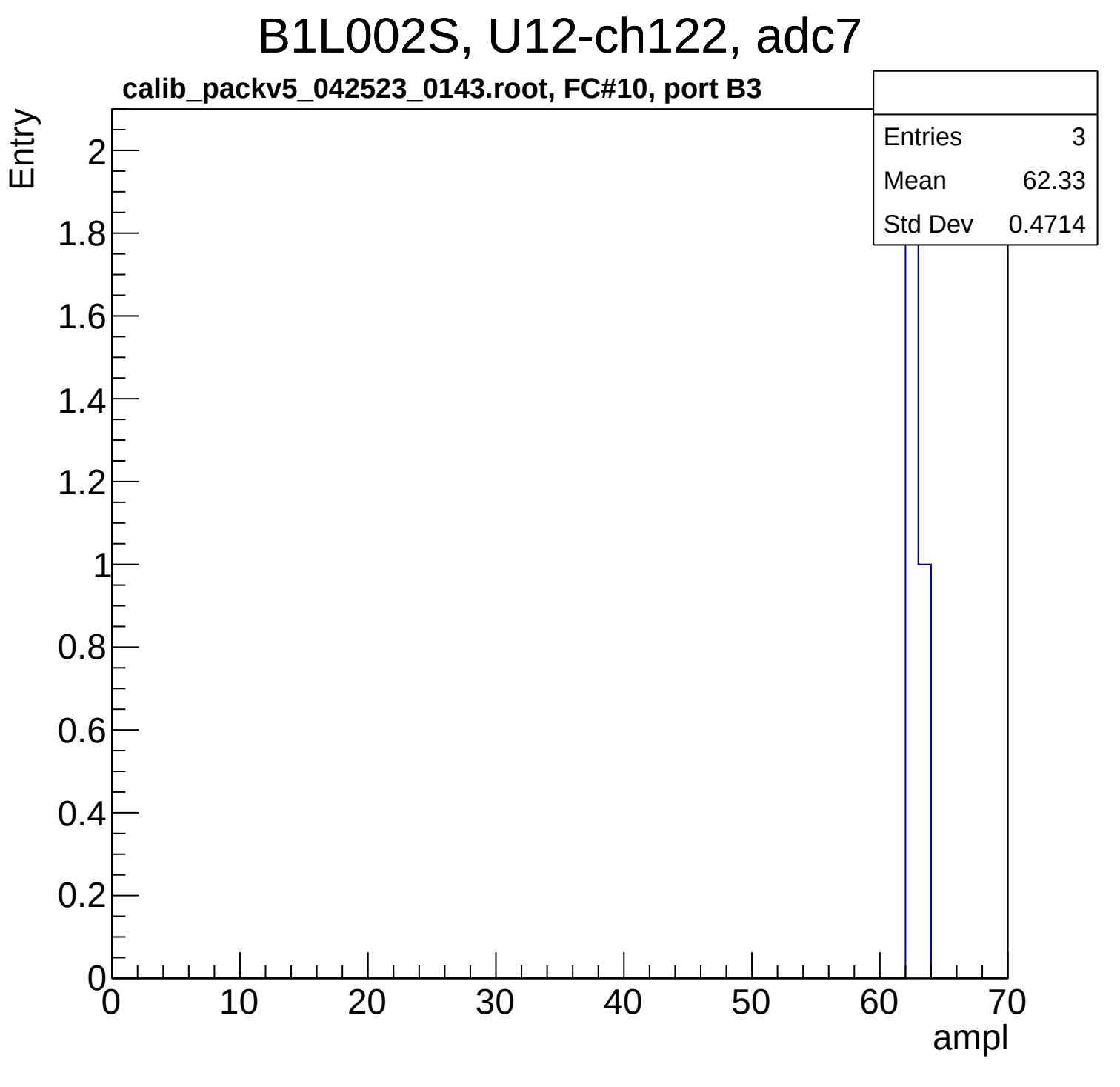
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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30
40
50
60
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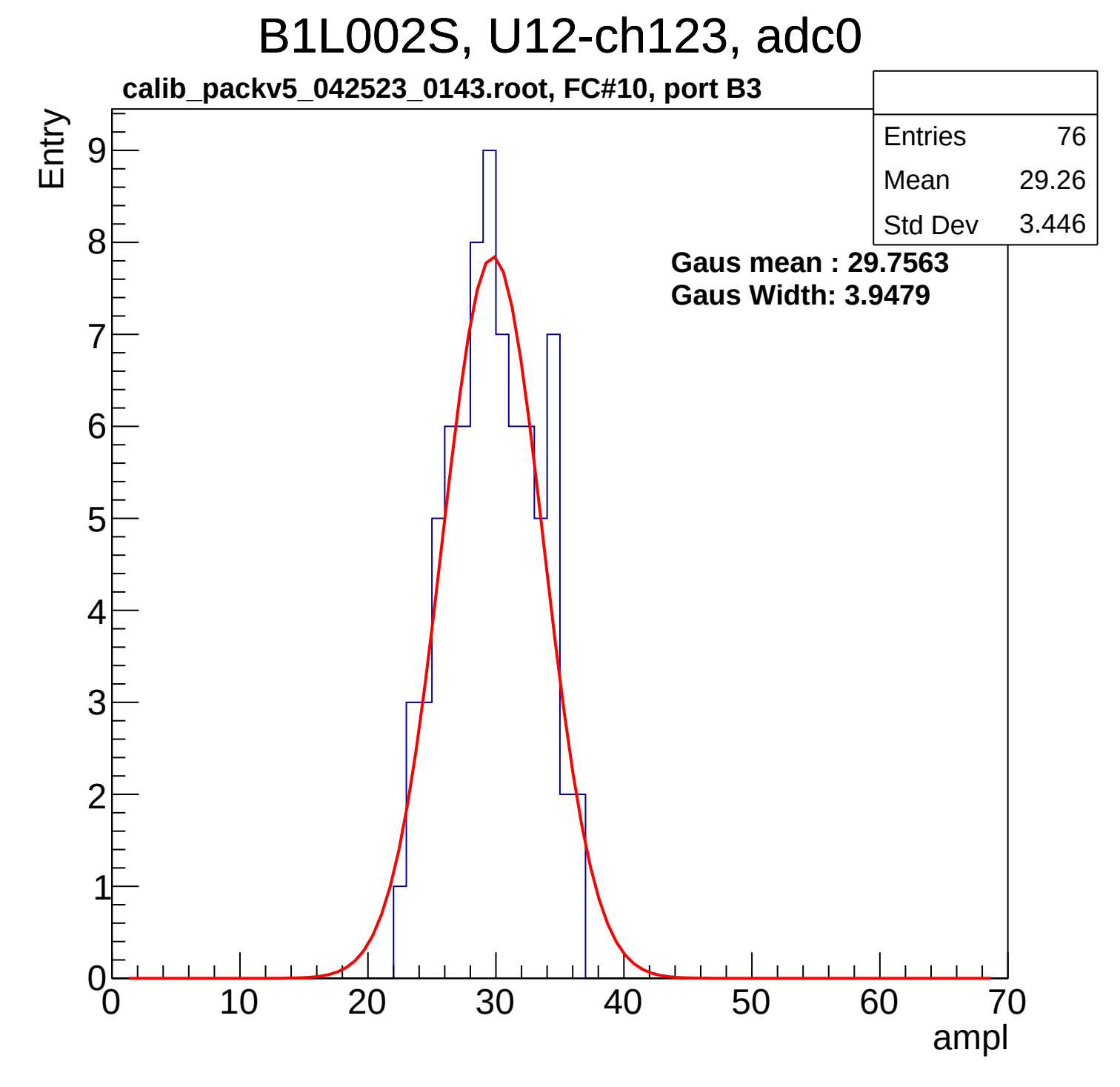
B1L002S, U12-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 29.26
Std Dev 3.446
Gaus mean : 29.7563
Gaus Width: 3.9479
Entry
ampl
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2
3
4
5
6
7
8
9
0
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50
60
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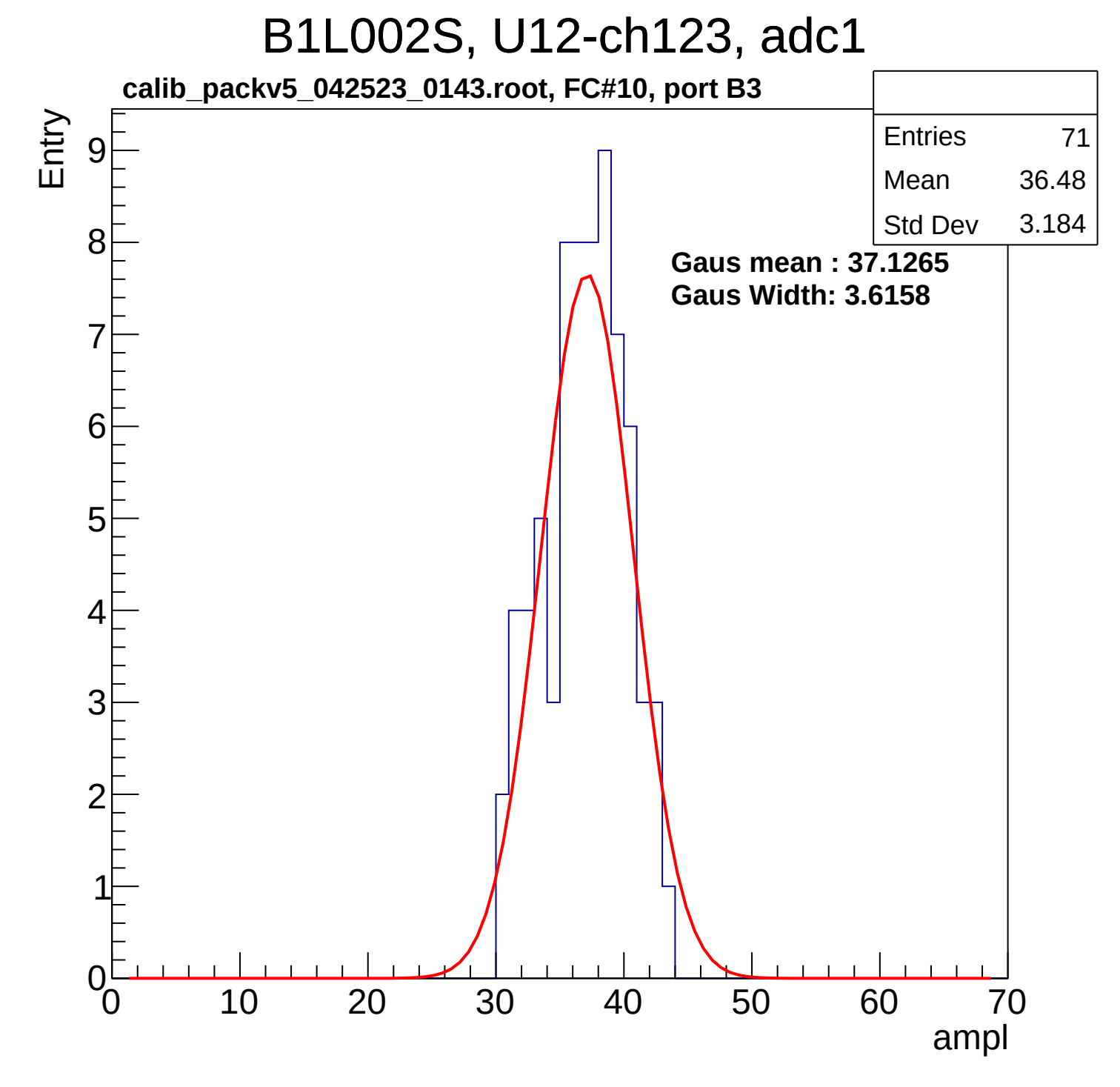
B1L002S, U12-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 36.48
Std Dev 3.184
Gaus mean : 37.1265
Gaus Width: 3.6158
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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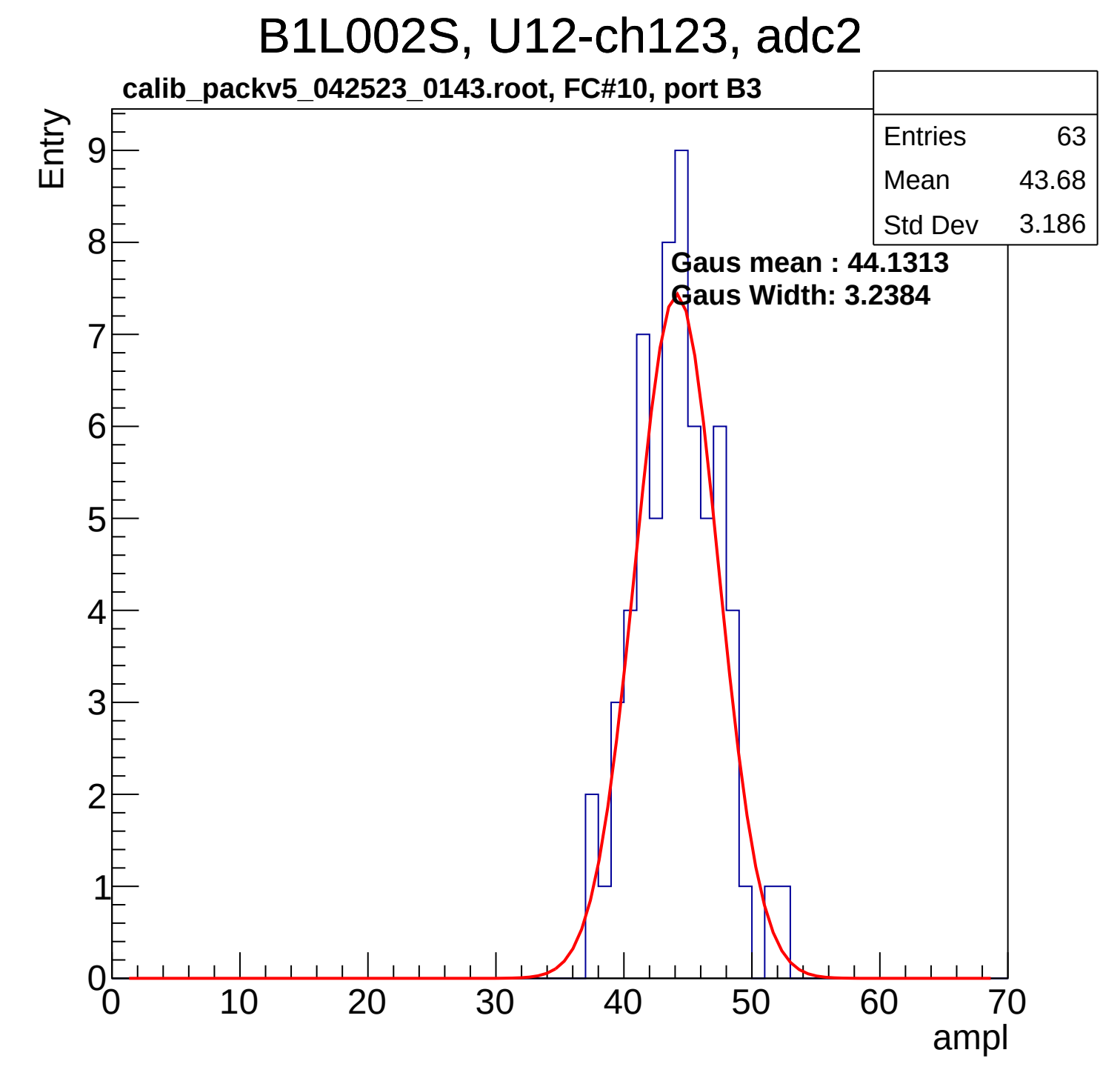
B1L002S, U12-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 43.68
Std Dev 3.186
Gaus mean : 44.1313
Gaus Width: 3.2384
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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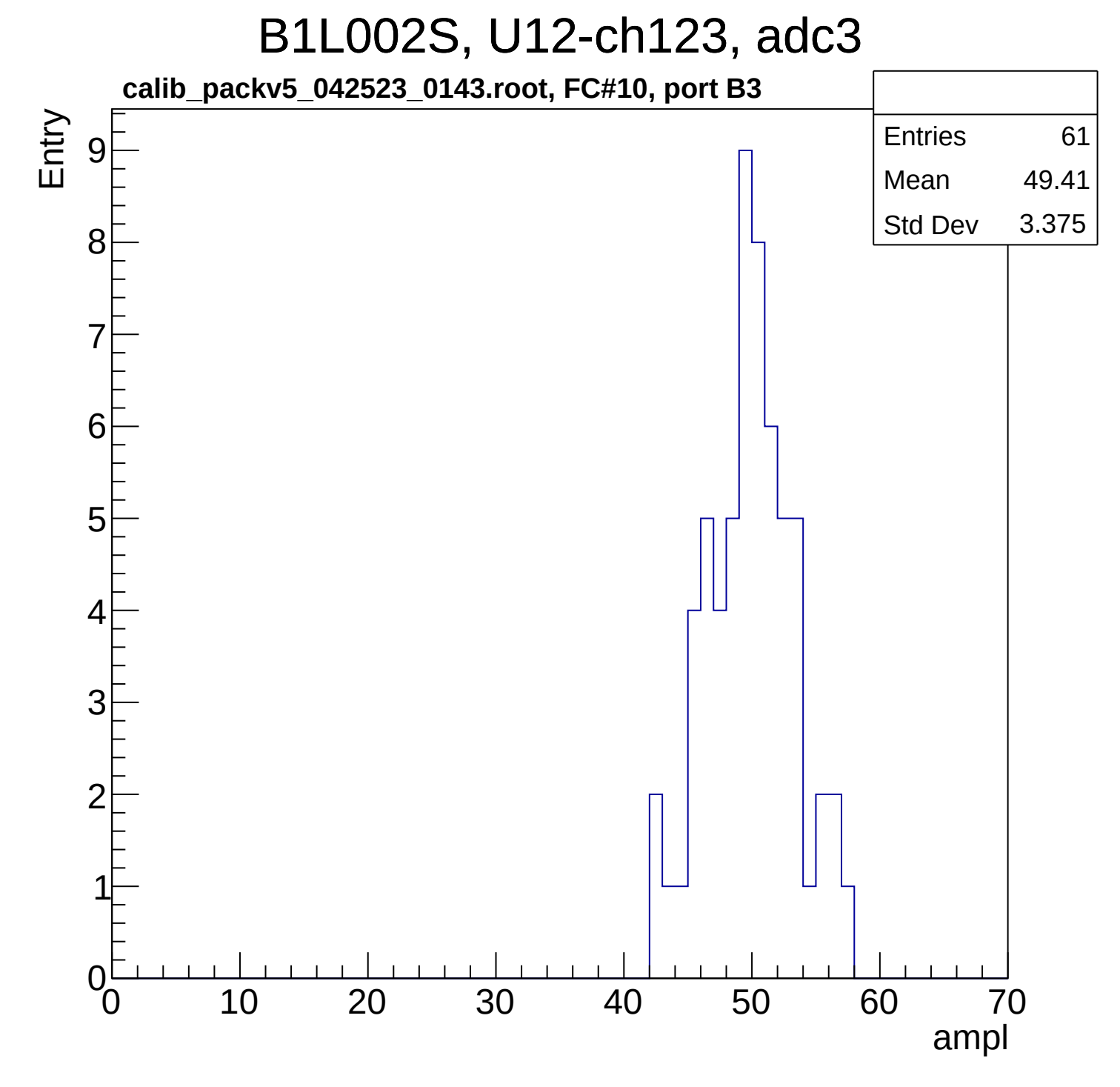
B1L002S, U12-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 49.41
Std Dev 3.375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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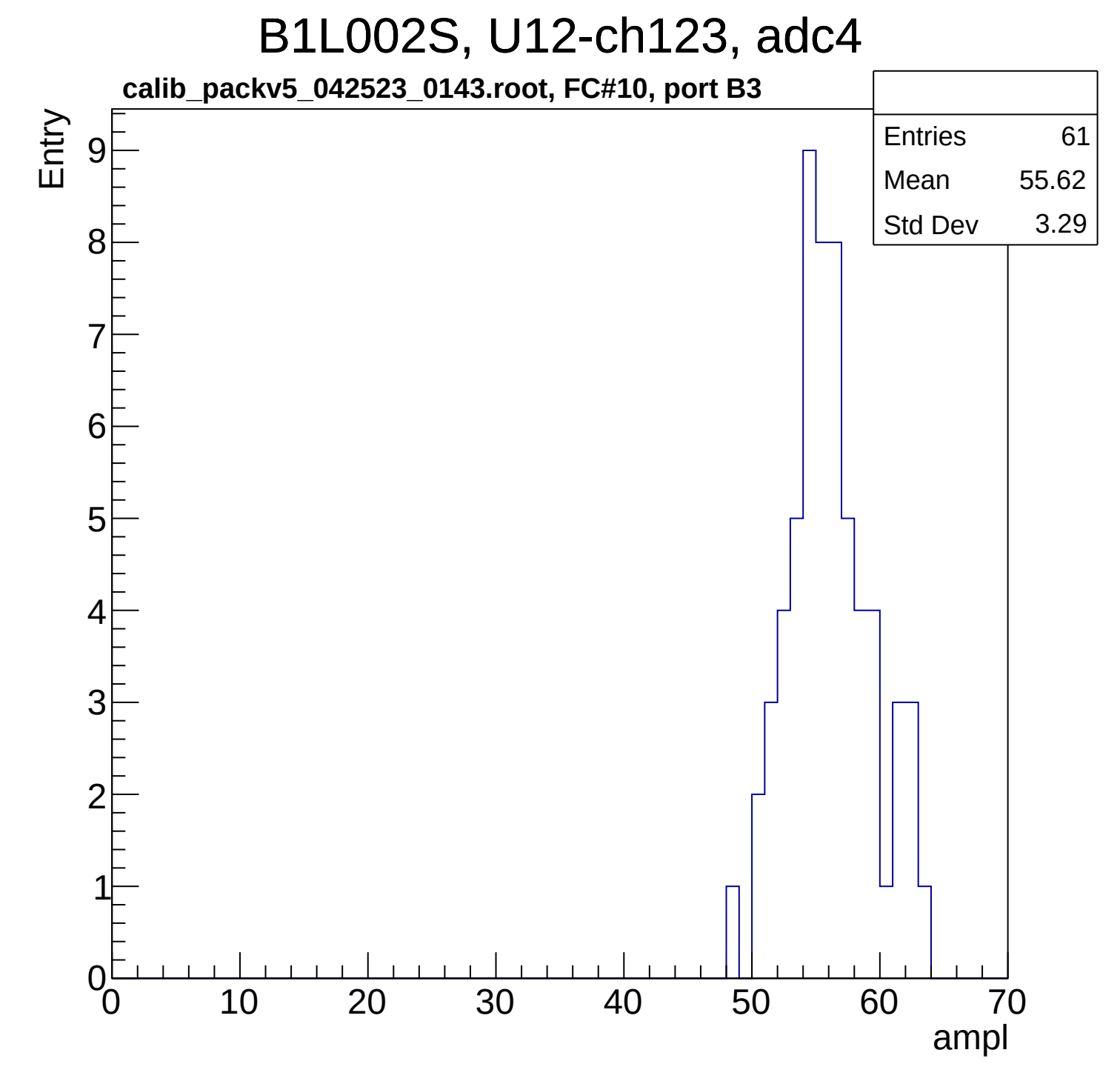
B1L002S, U12-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 55.62
Std Dev 3.29
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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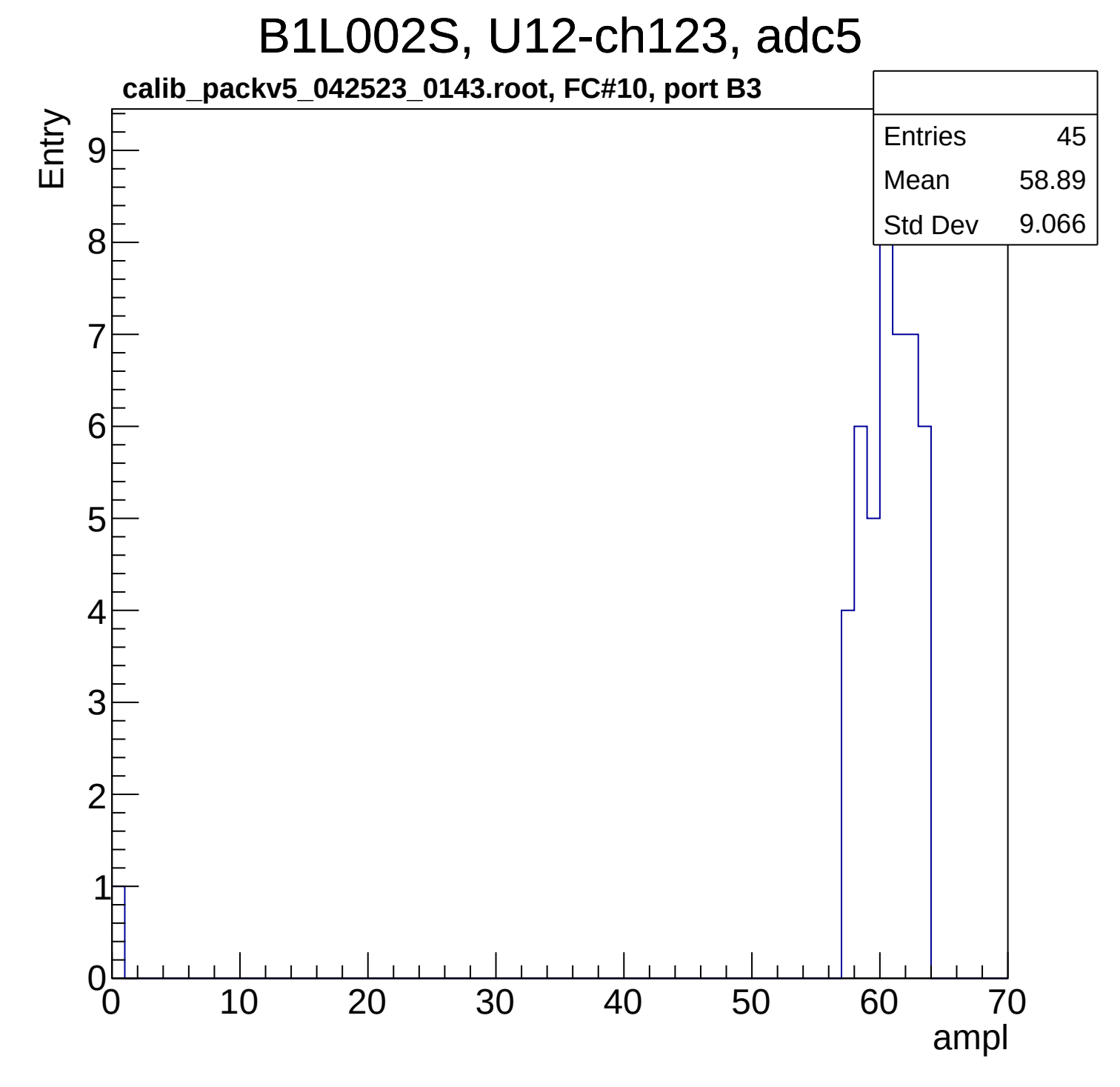
B1L002S, U12-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.89
Std Dev 9.066
Entry
ampl
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3
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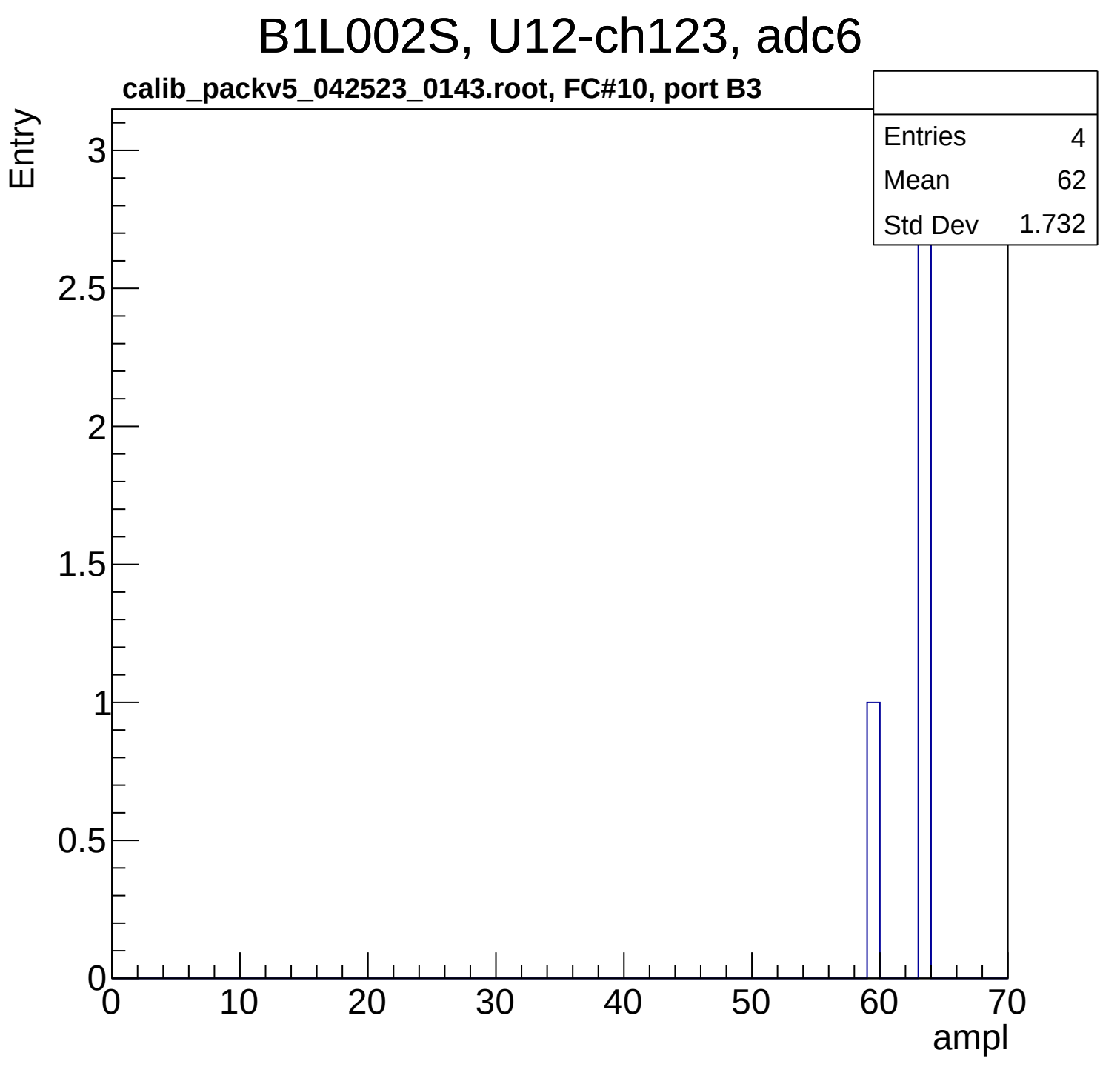
B1L002S, U12-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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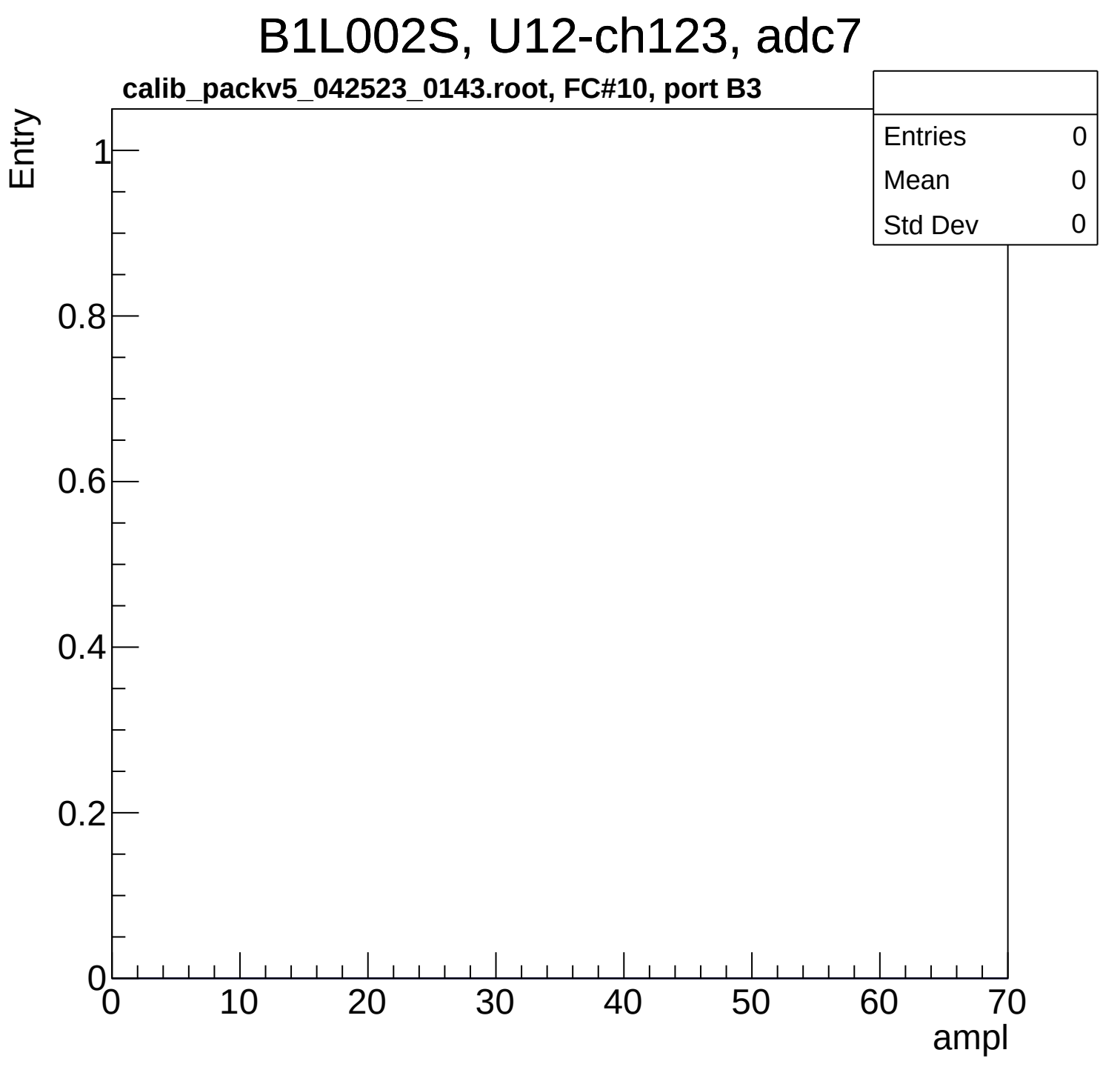
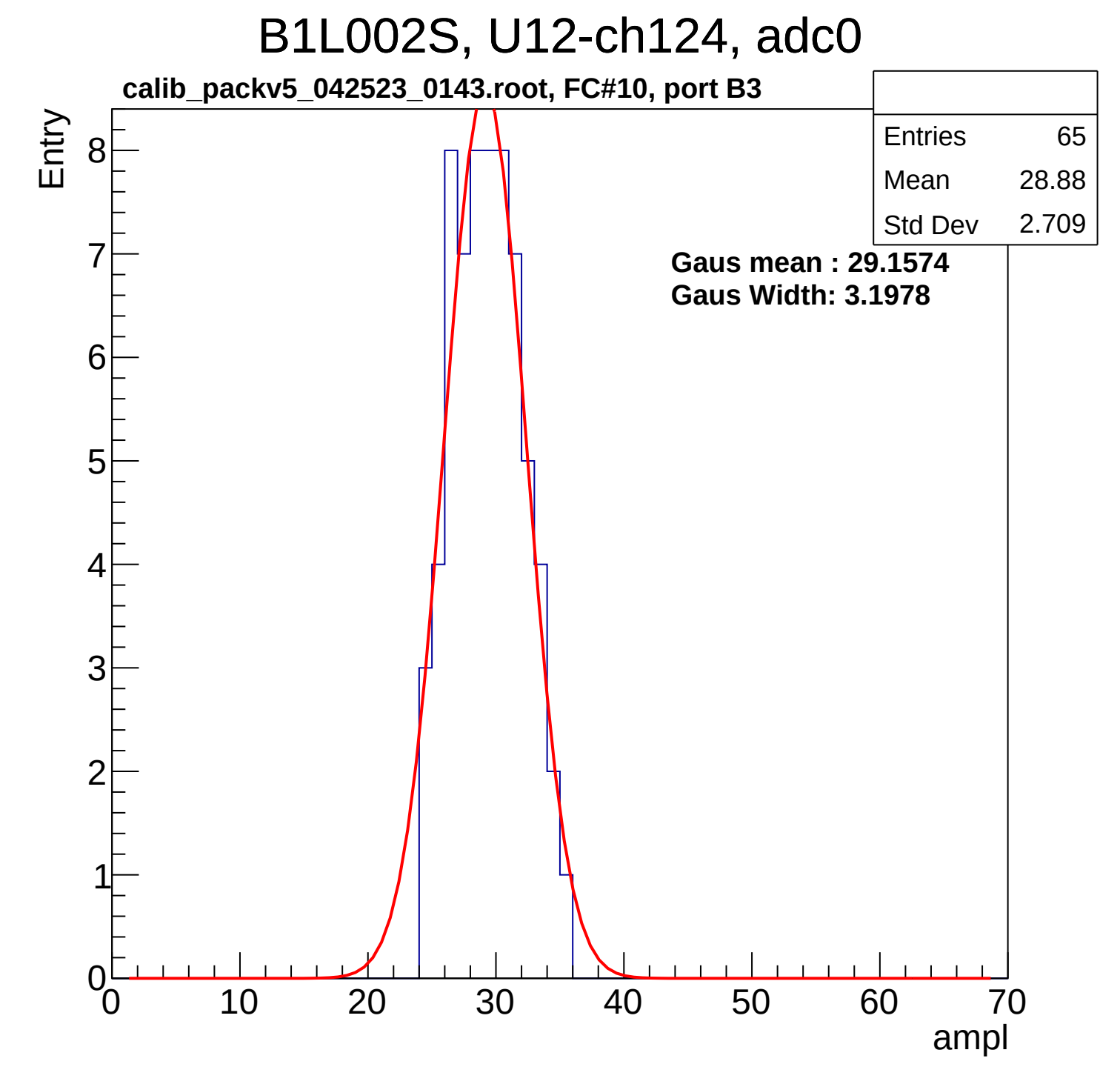

B1L002S, U12-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.88
Std Dev 2.709
Gaus mean : 29.1574
Gaus Width: 3.1978
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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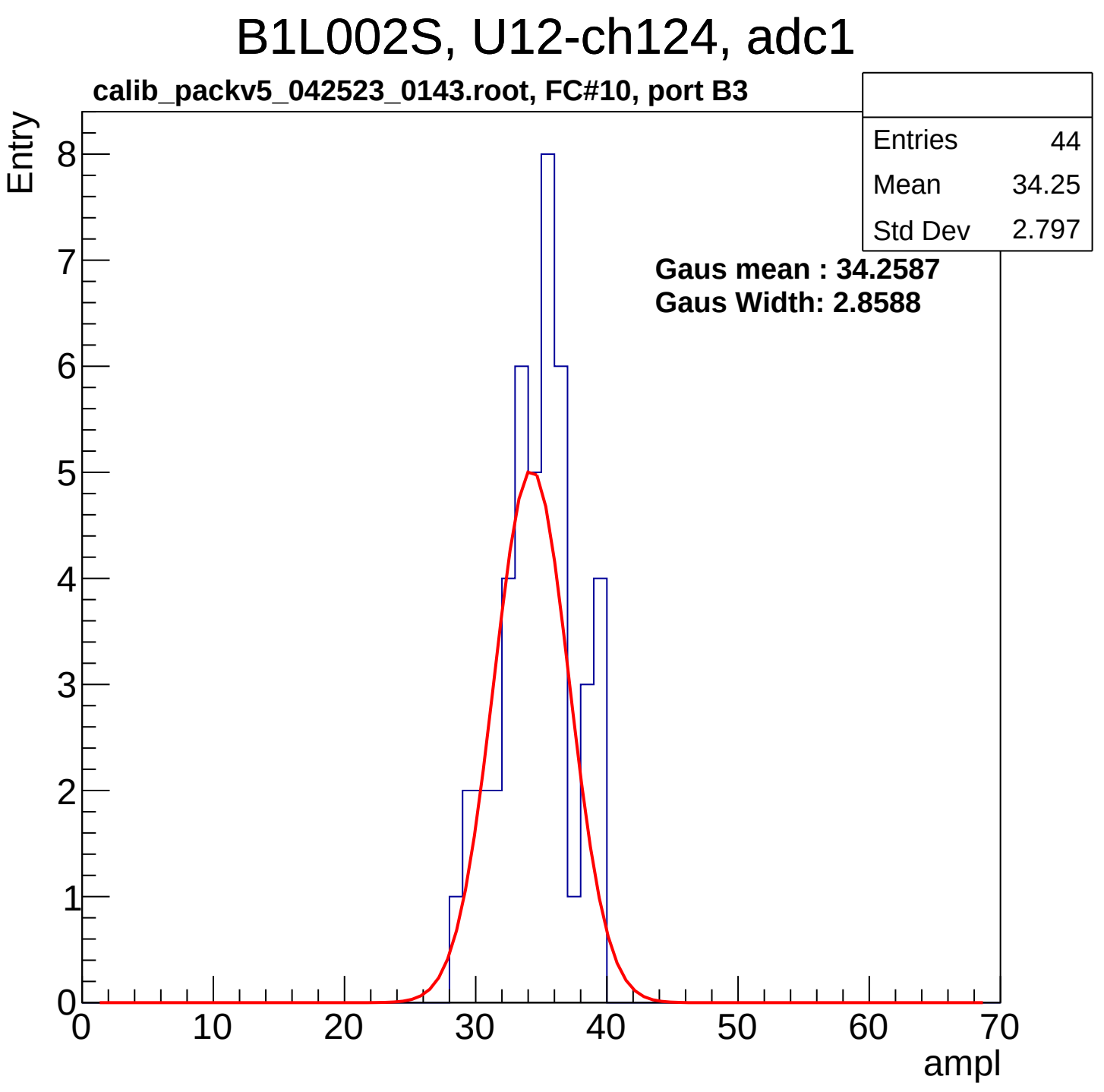
B1L002S, U12-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 34.25
Std Dev 2.797
Gaus mean : 34.2587
Gaus Width: 2.8588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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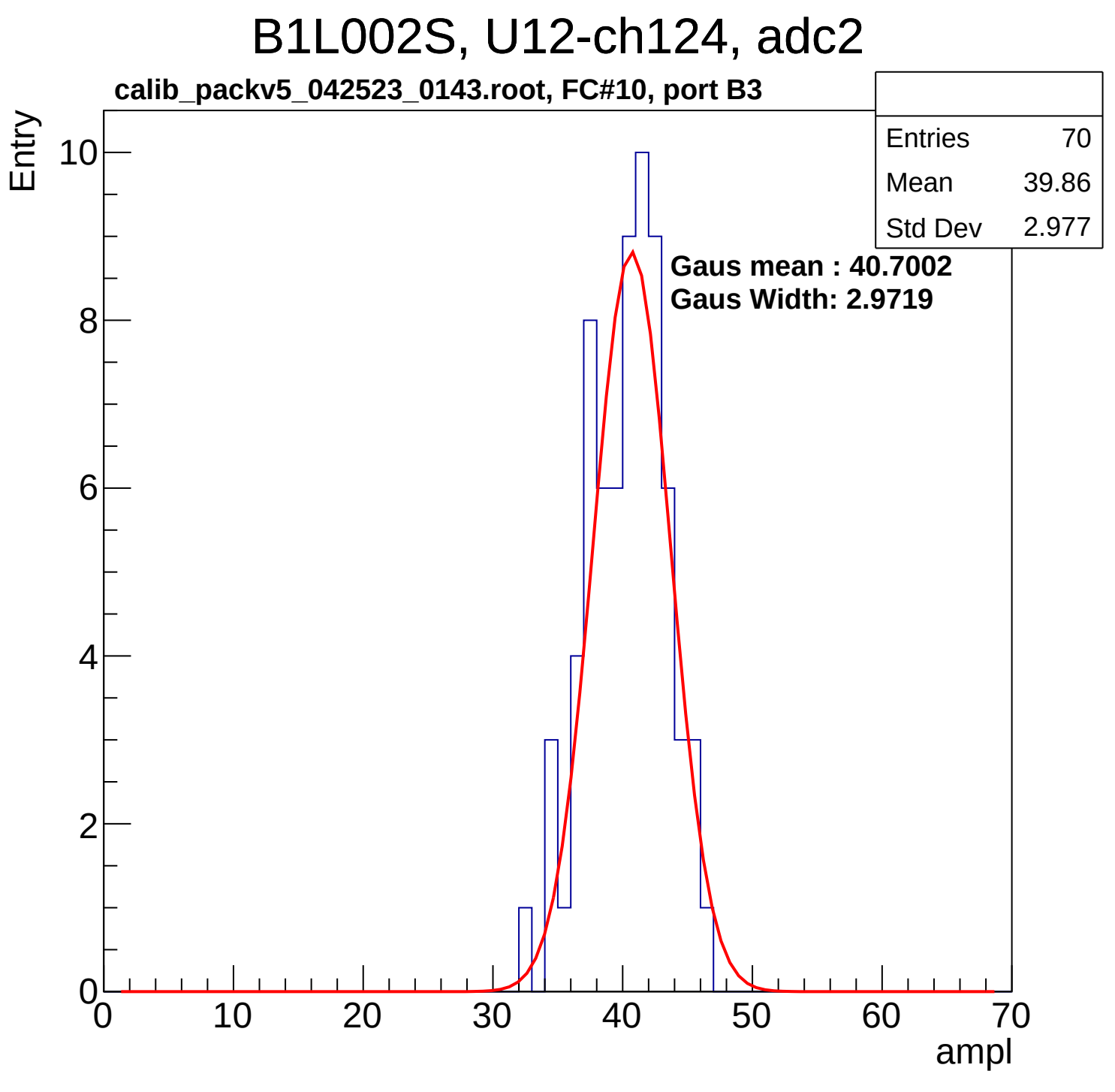
B1L002S, U12-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 39.86
Std Dev 2.977
Gaus mean : 40.7002
Gaus Width: 2.9719
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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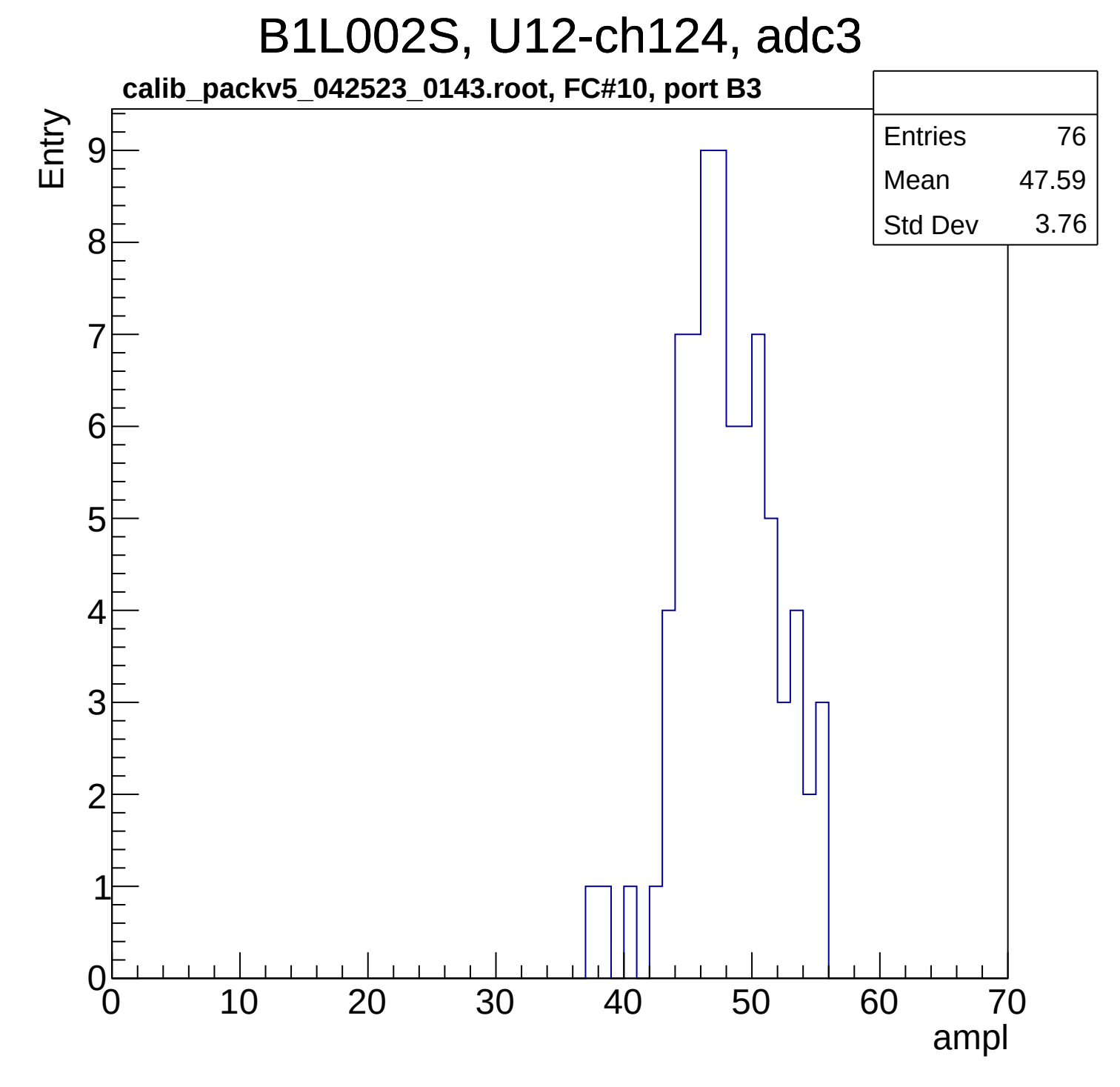
B1L002S, U12-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.59
Std Dev 3.76
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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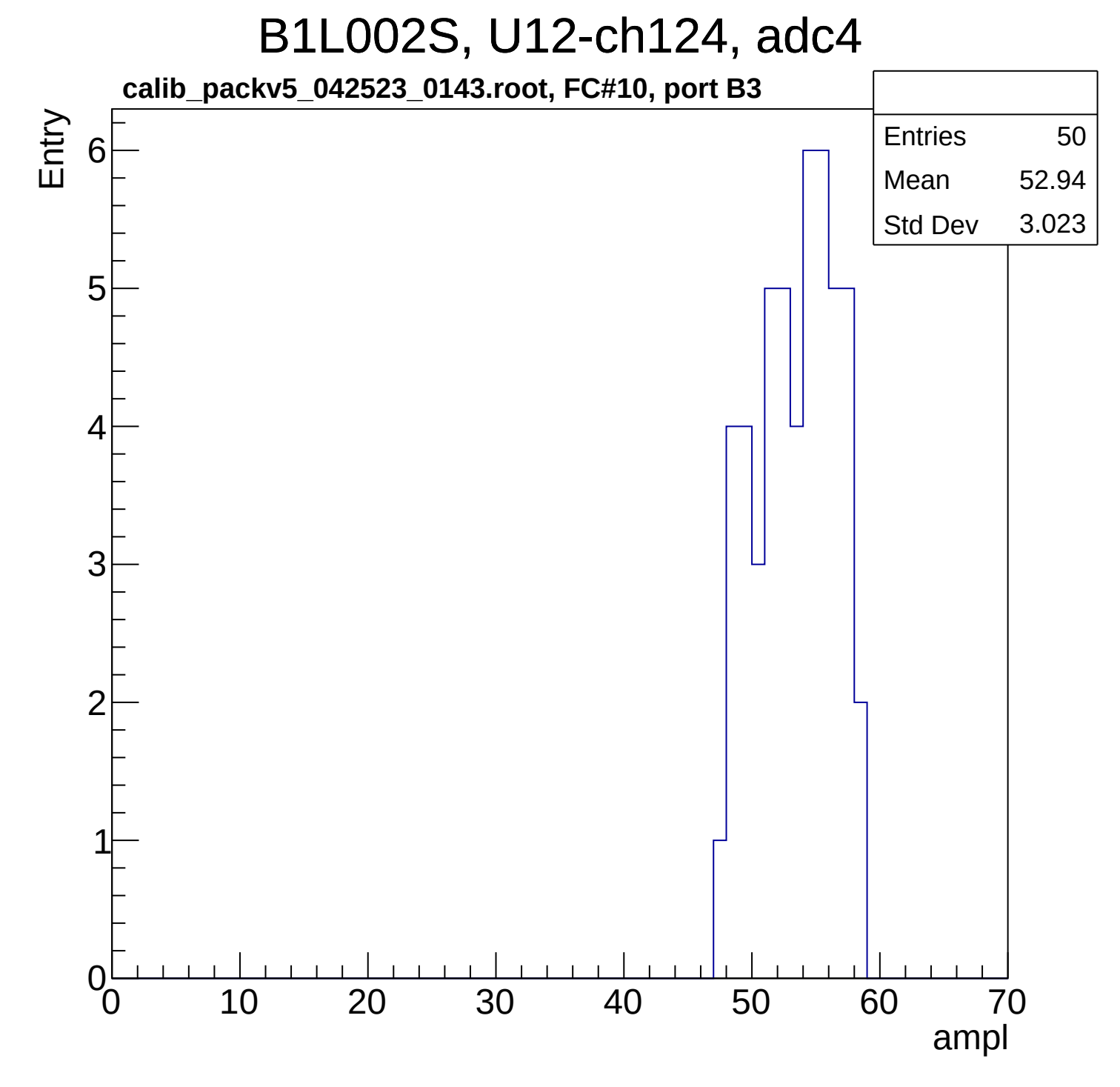
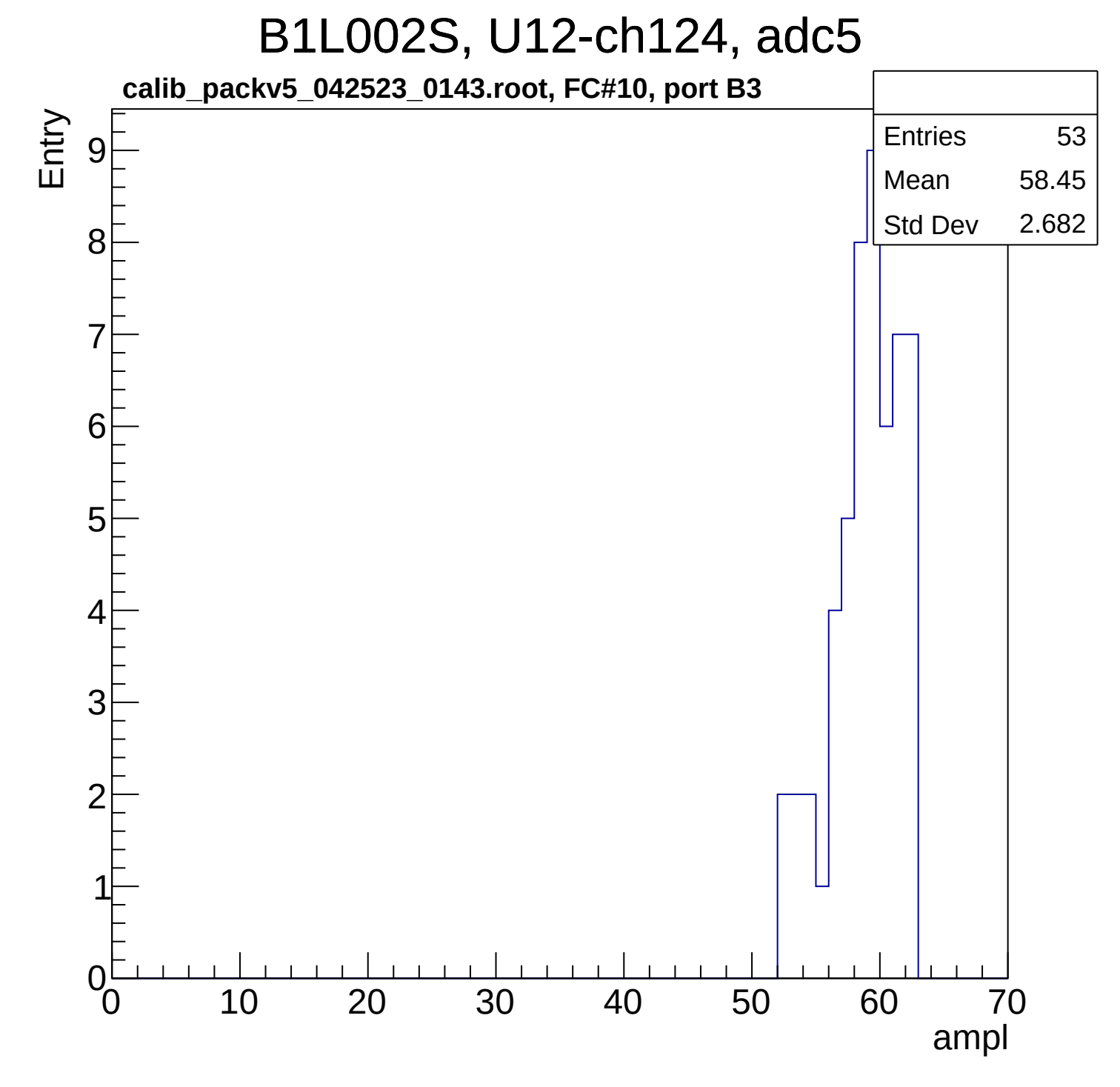
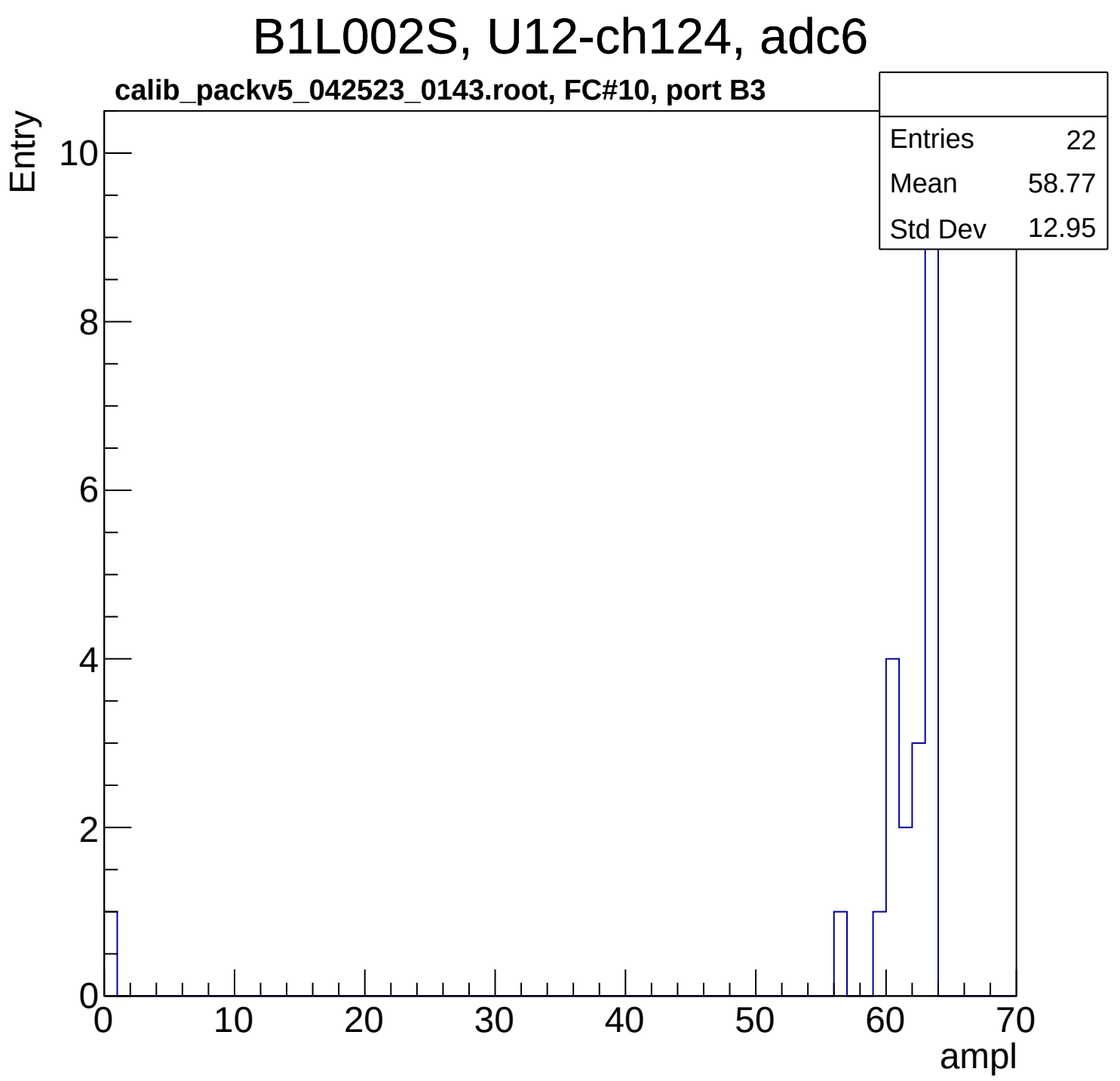
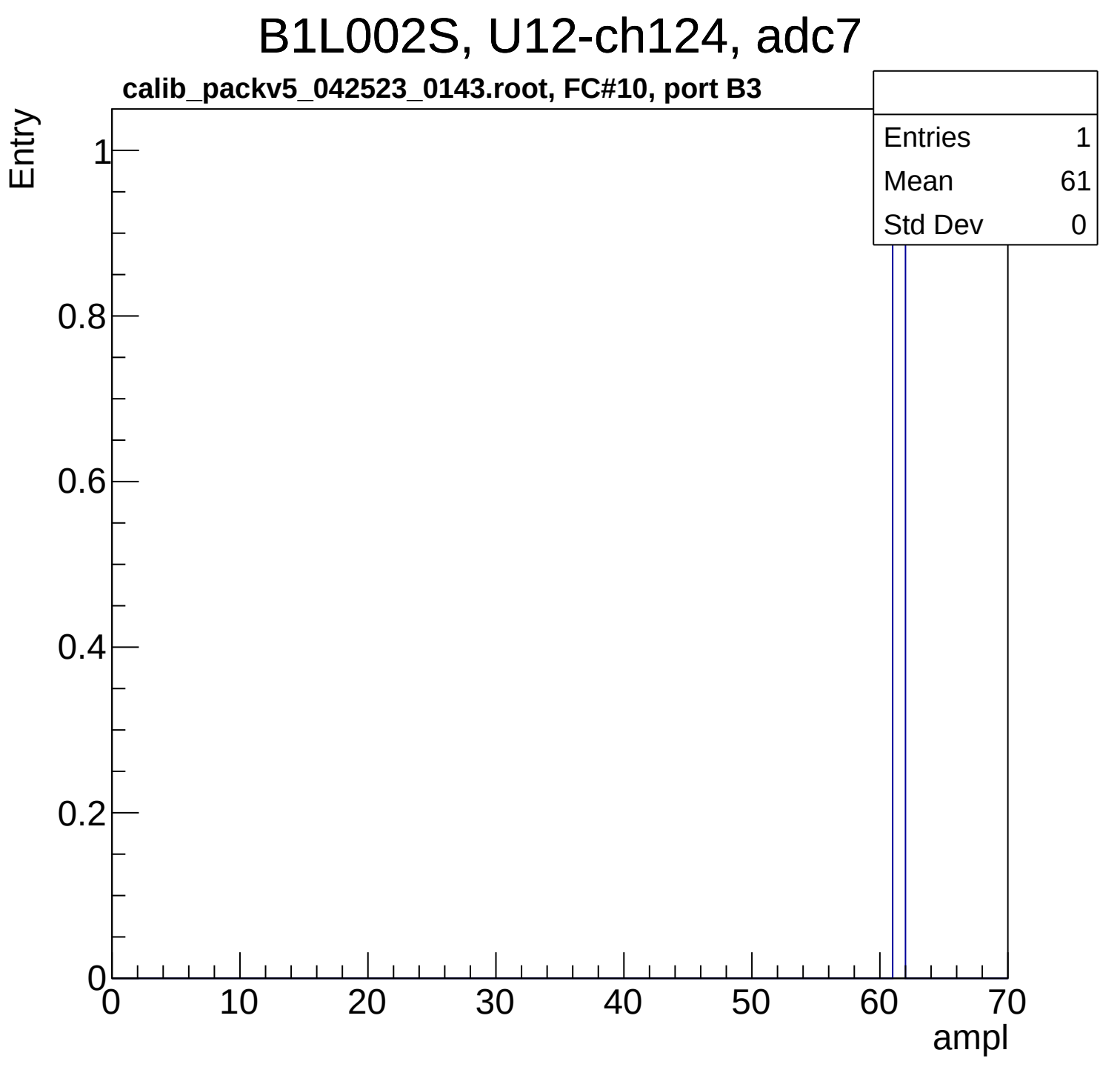

B1L002S, U12-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.94
Std Dev 3.023
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.45
Std Dev 2.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.77
Std Dev 12.95
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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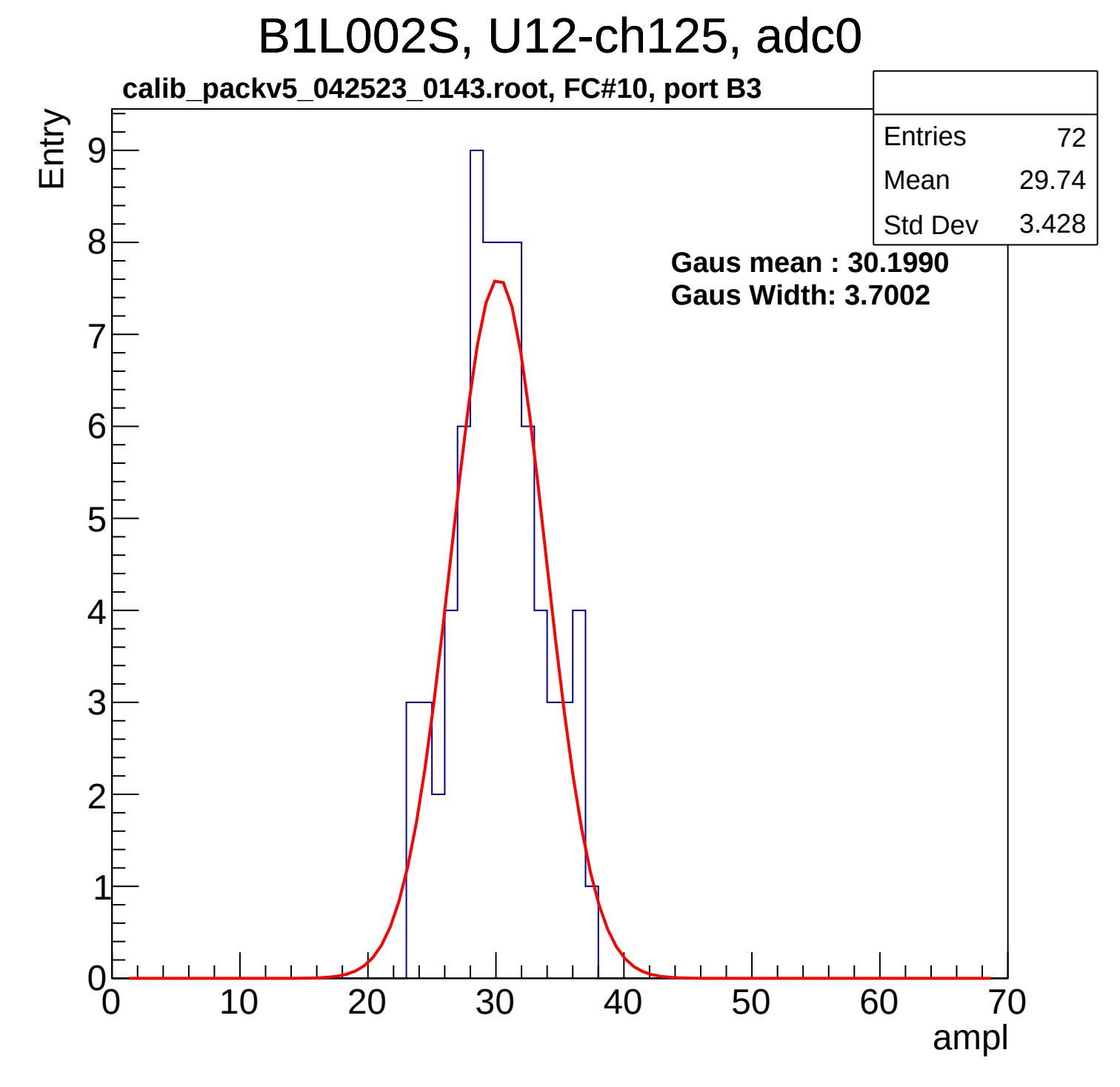
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 29.74
Std Dev 3.428
Gaus mean : 30.1990
Gaus Width: 3.7002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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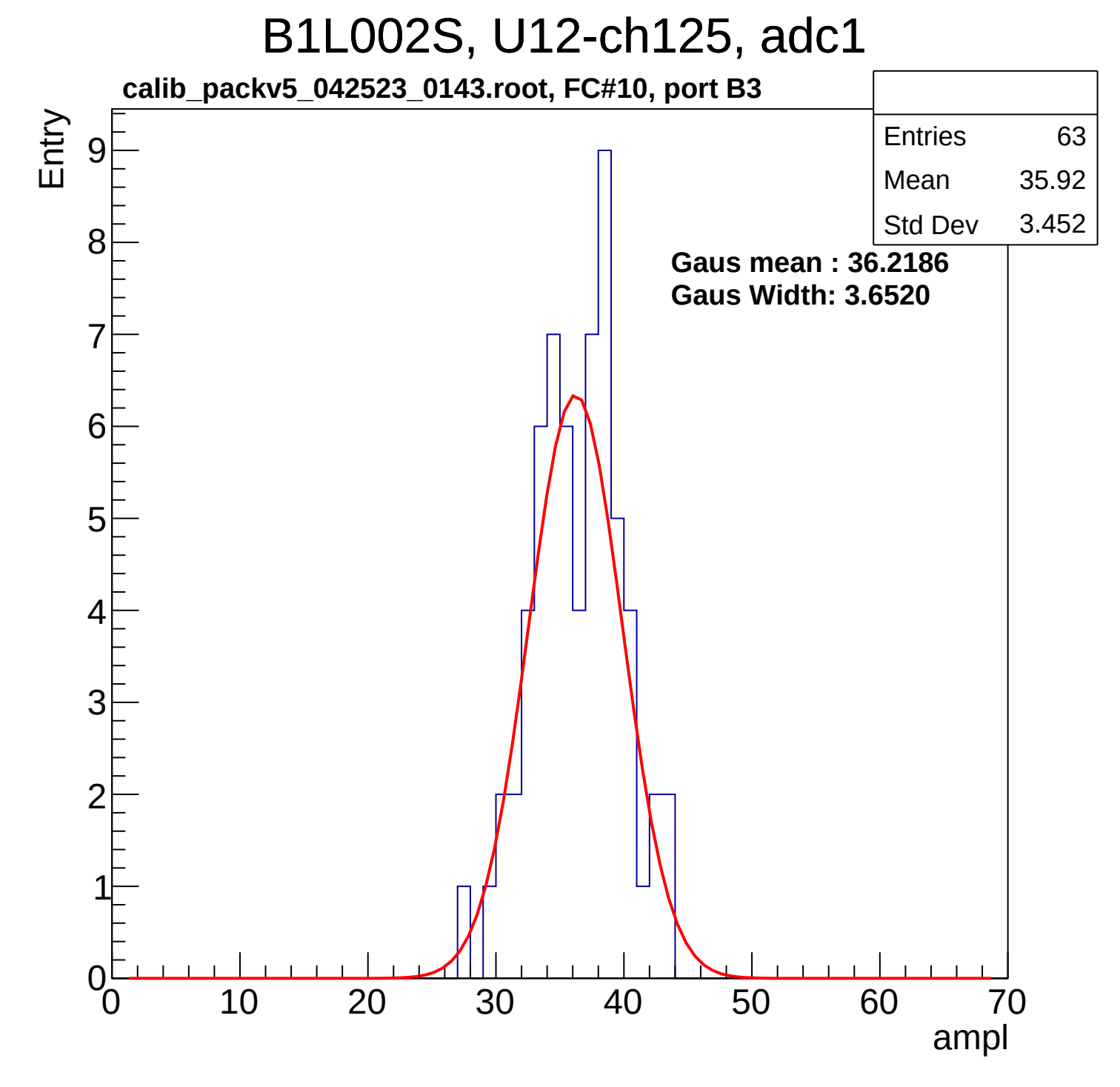
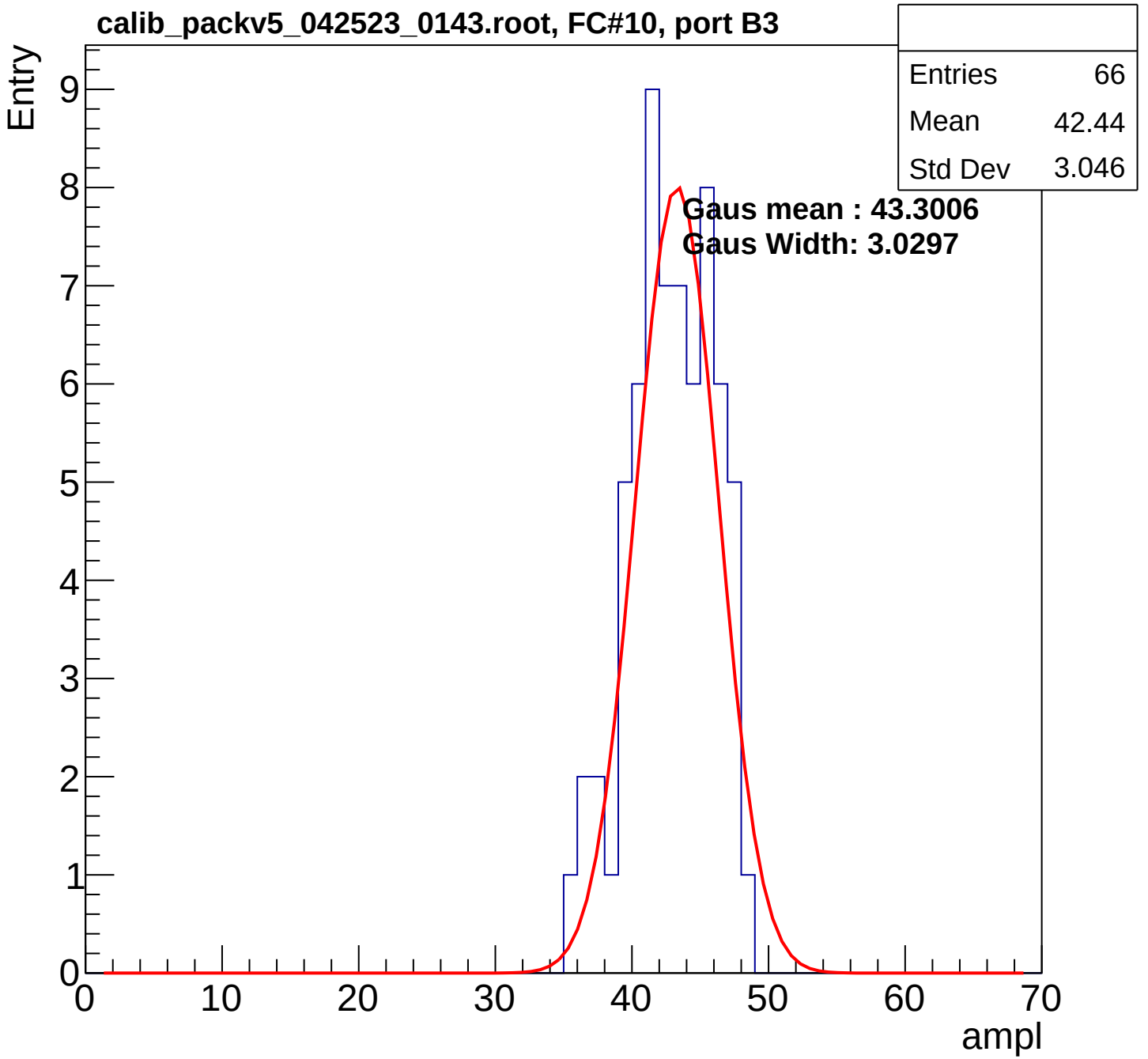

B1L002S, U12-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.92
Std Dev 3.452
Gaus mean : 36.2186
Gaus Width: 3.6520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.44
Std Dev 3.046
Gaus mean : 43.3006
Gaus Width: 3.0297
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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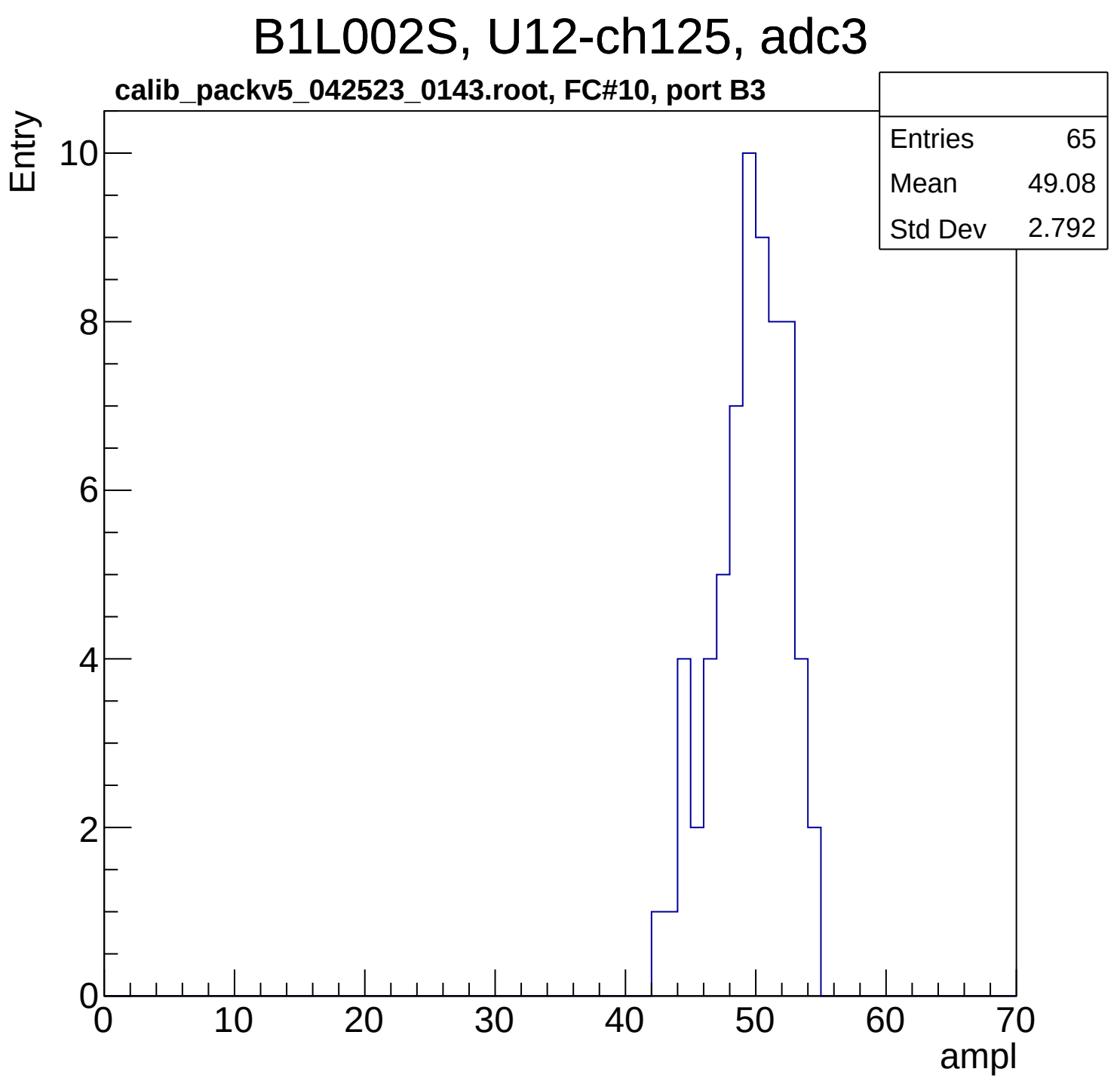
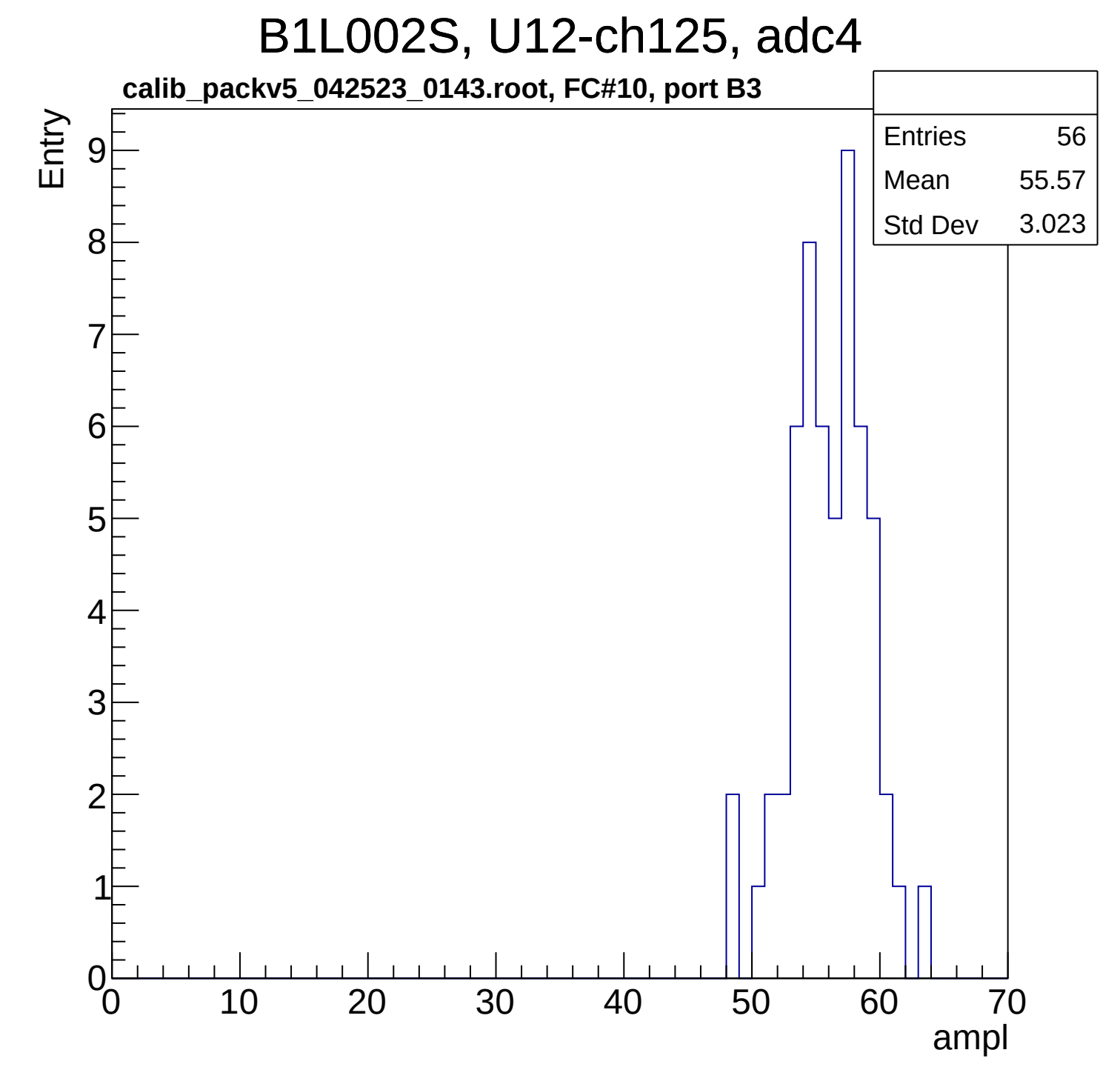

B1L002S, U12-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 49.08
Std Dev 2.792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.57
Std Dev 3.023
Entry
ampl
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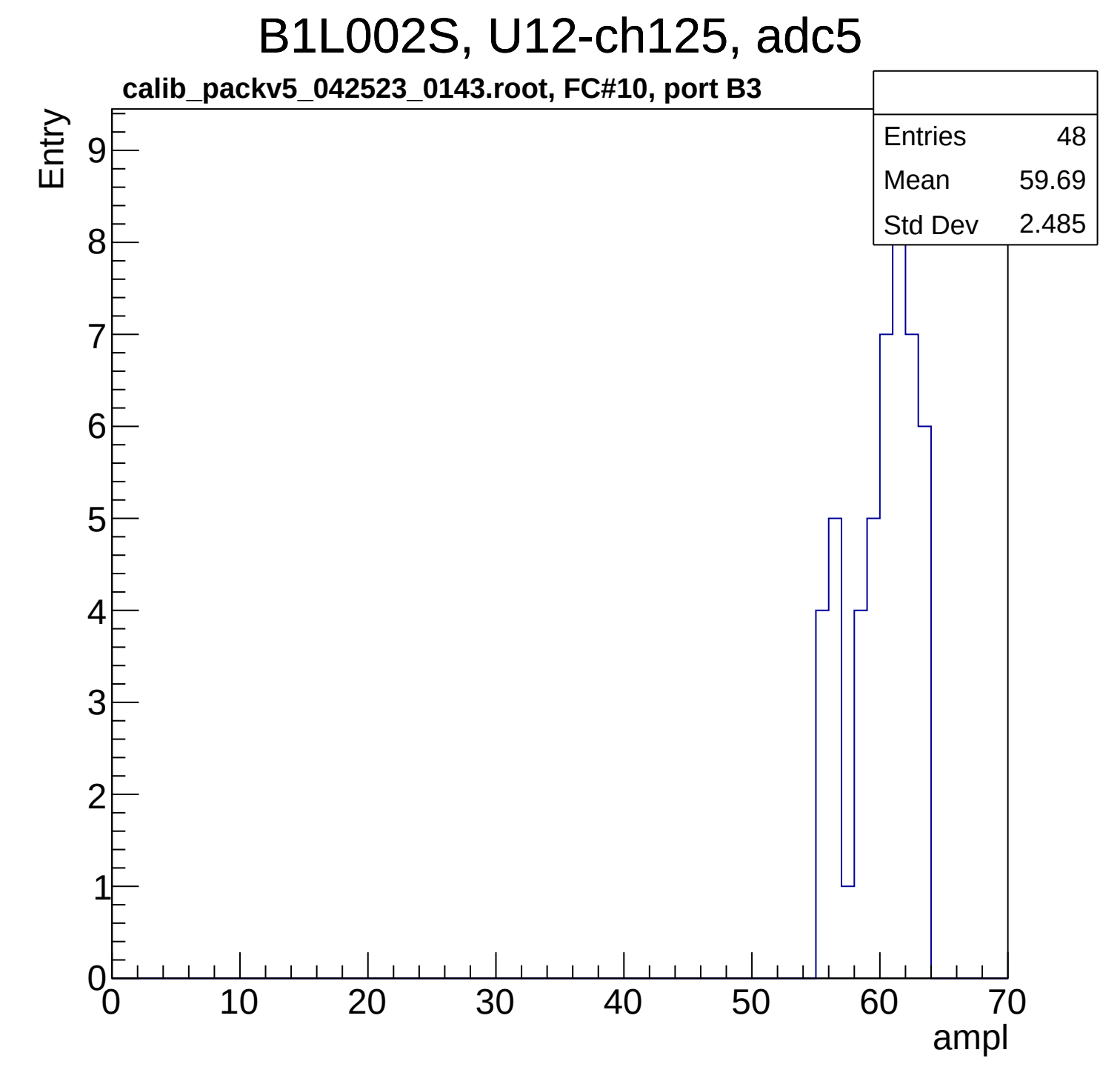
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.69
Std Dev 2.485
Entry
ampl
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2
3
4
5
6
7
8
9
0
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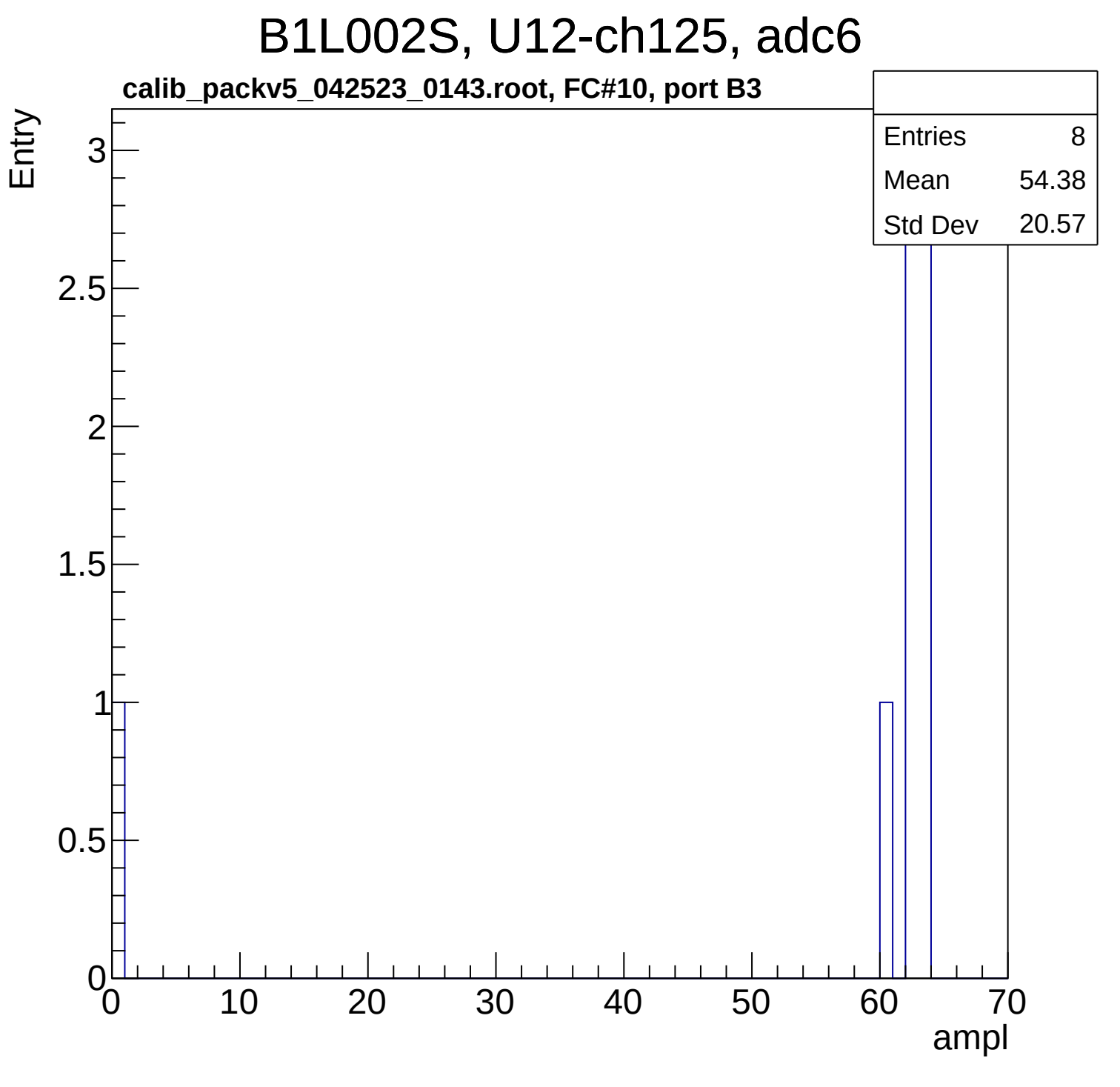
20
30
40
50
60
70


B1L002S, U12-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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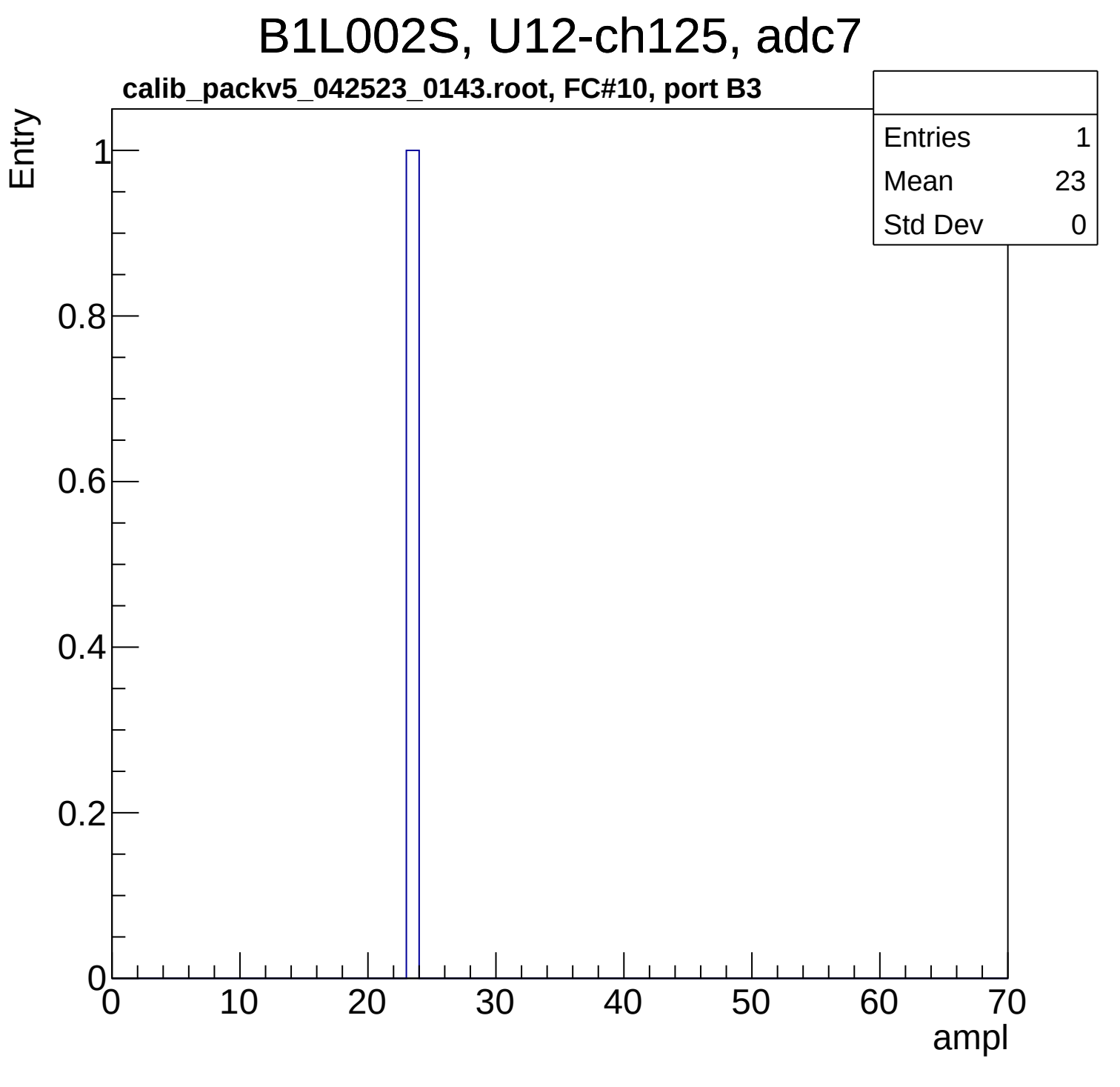
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U12-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
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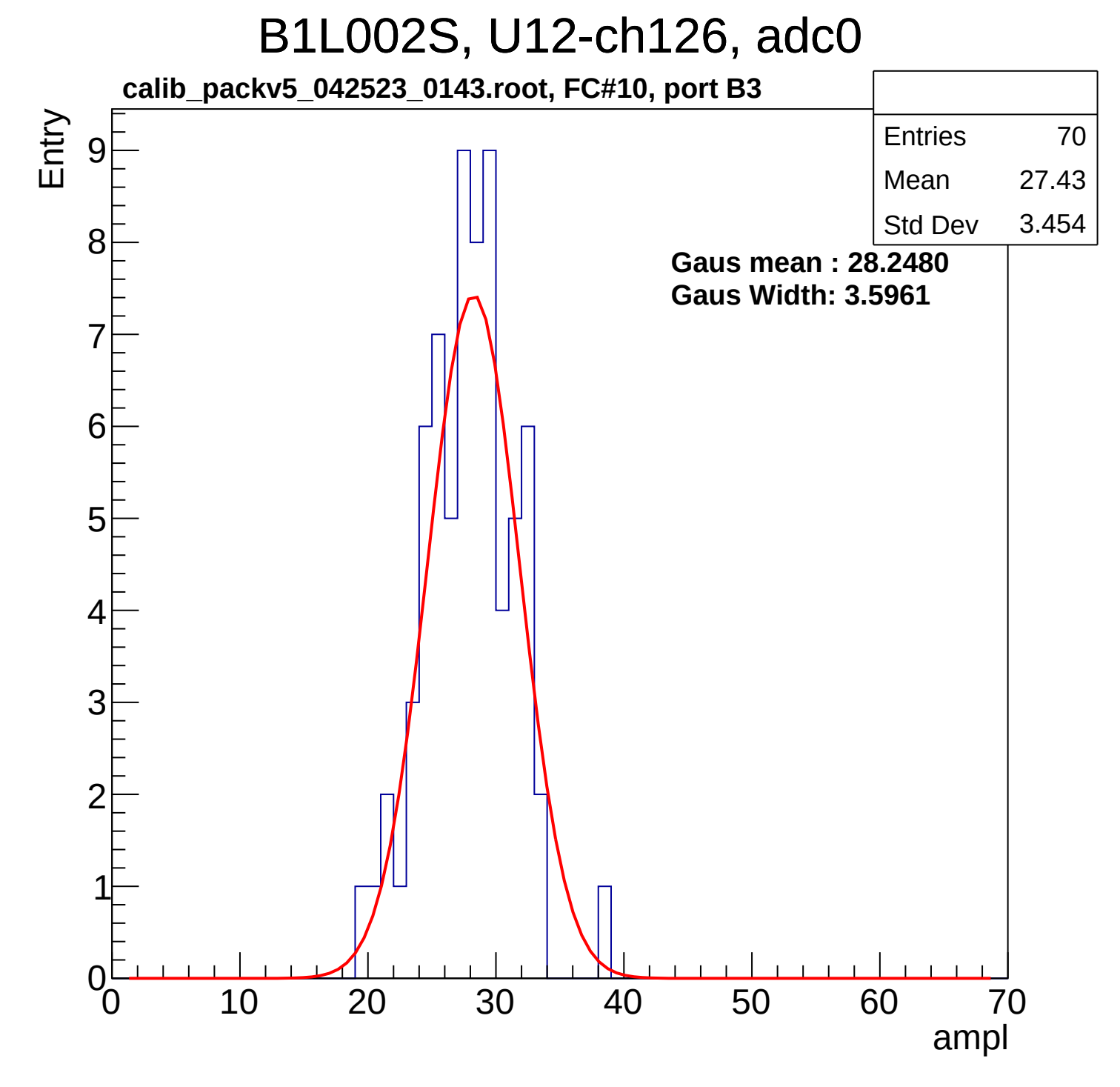
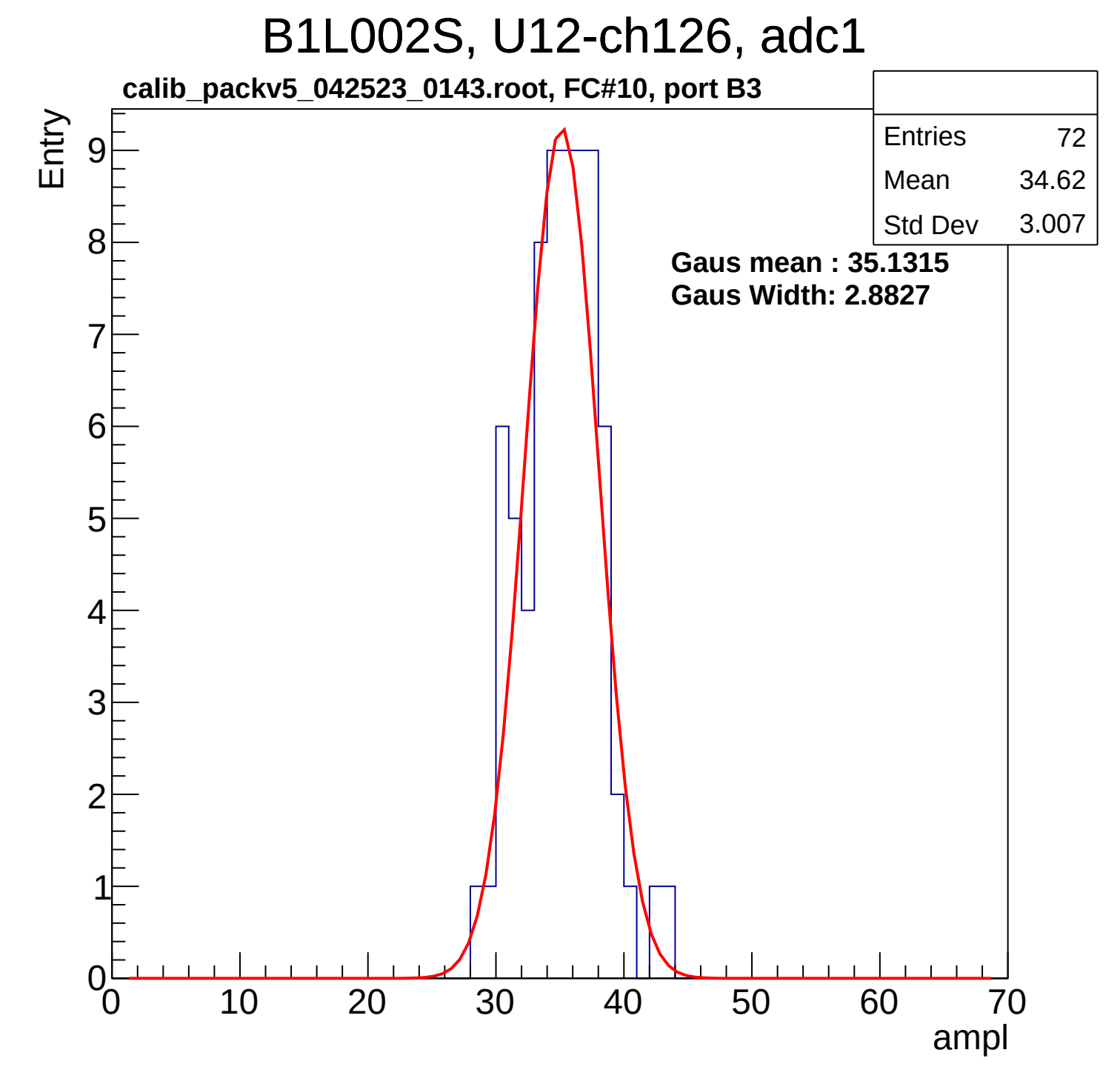
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.43
Std Dev 3.454
Gaus mean : 28.2480
Gaus Width: 3.5961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U12-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.62
Std Dev 3.007
Gaus mean : 35.1315
Gaus Width: 2.8827
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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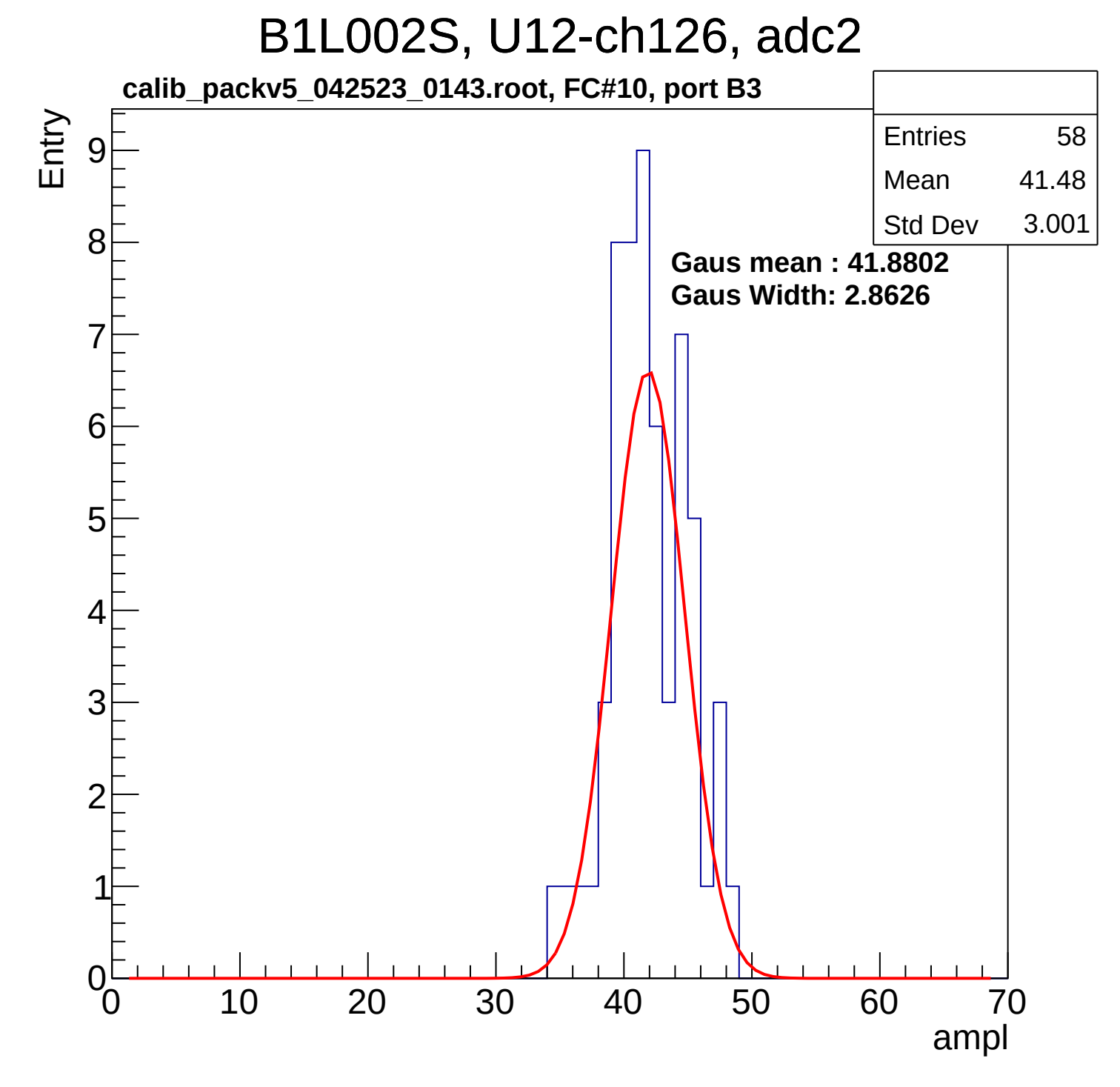
20
30
40
50
60
70


B1L002S, U12-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.48
Std Dev 3.001
Gaus mean : 41.8802
Gaus Width: 2.8626
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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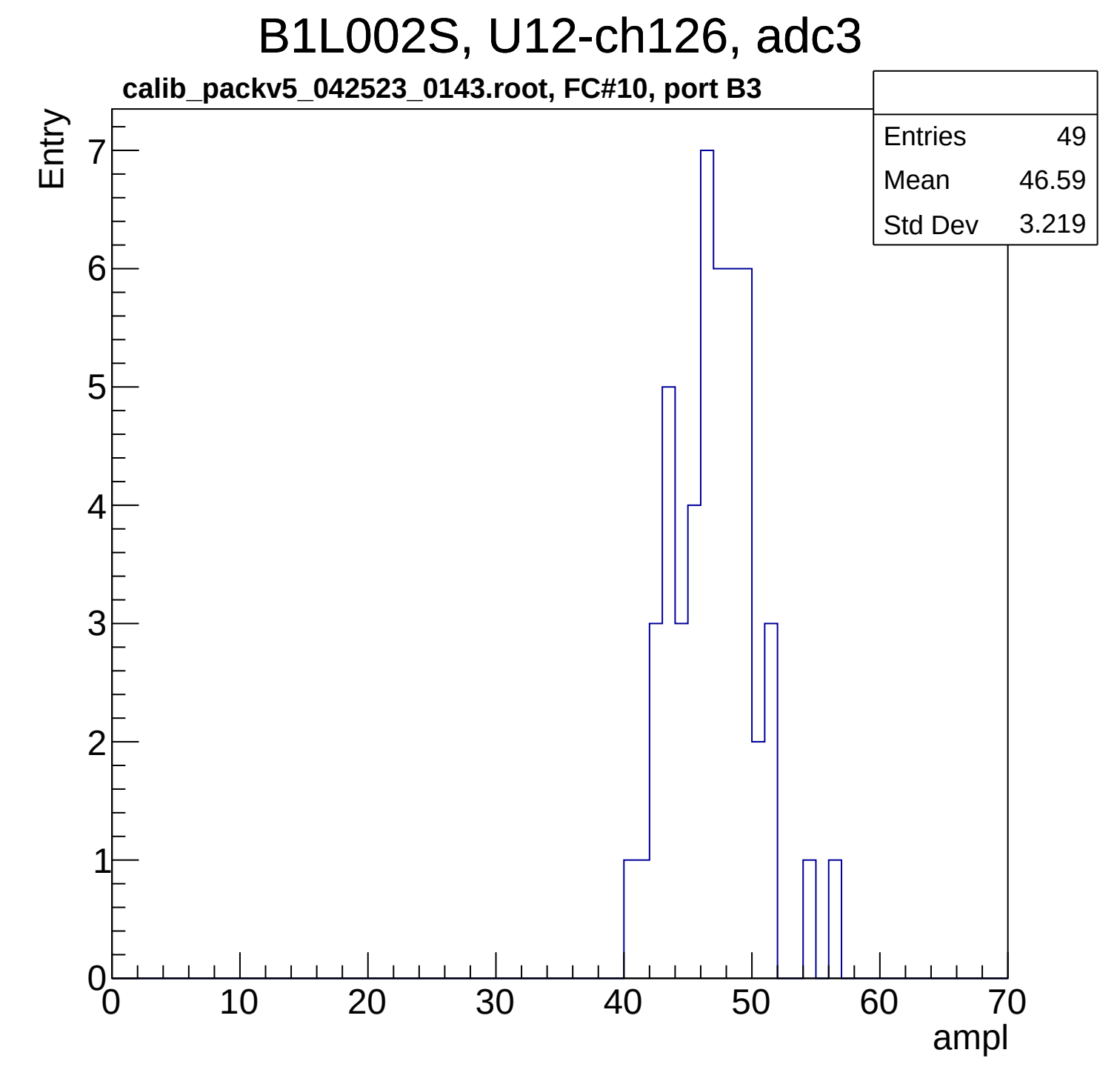
B1L002S, U12-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 46.59
Std Dev 3.219
Entry
ampl
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3
4
5
6
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0
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20
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40
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60
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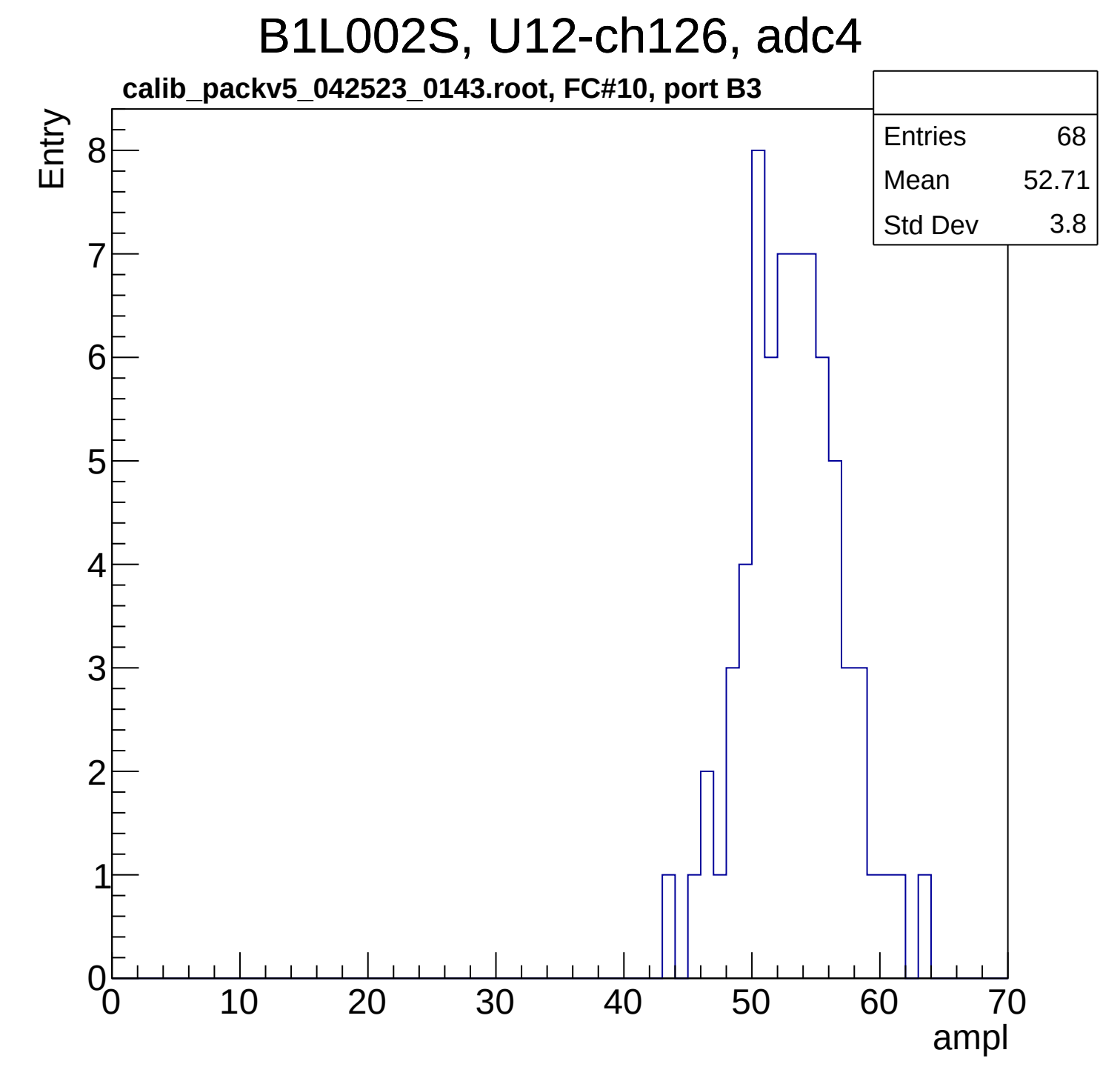
B1L002S, U12-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52.71
Std Dev 3.8
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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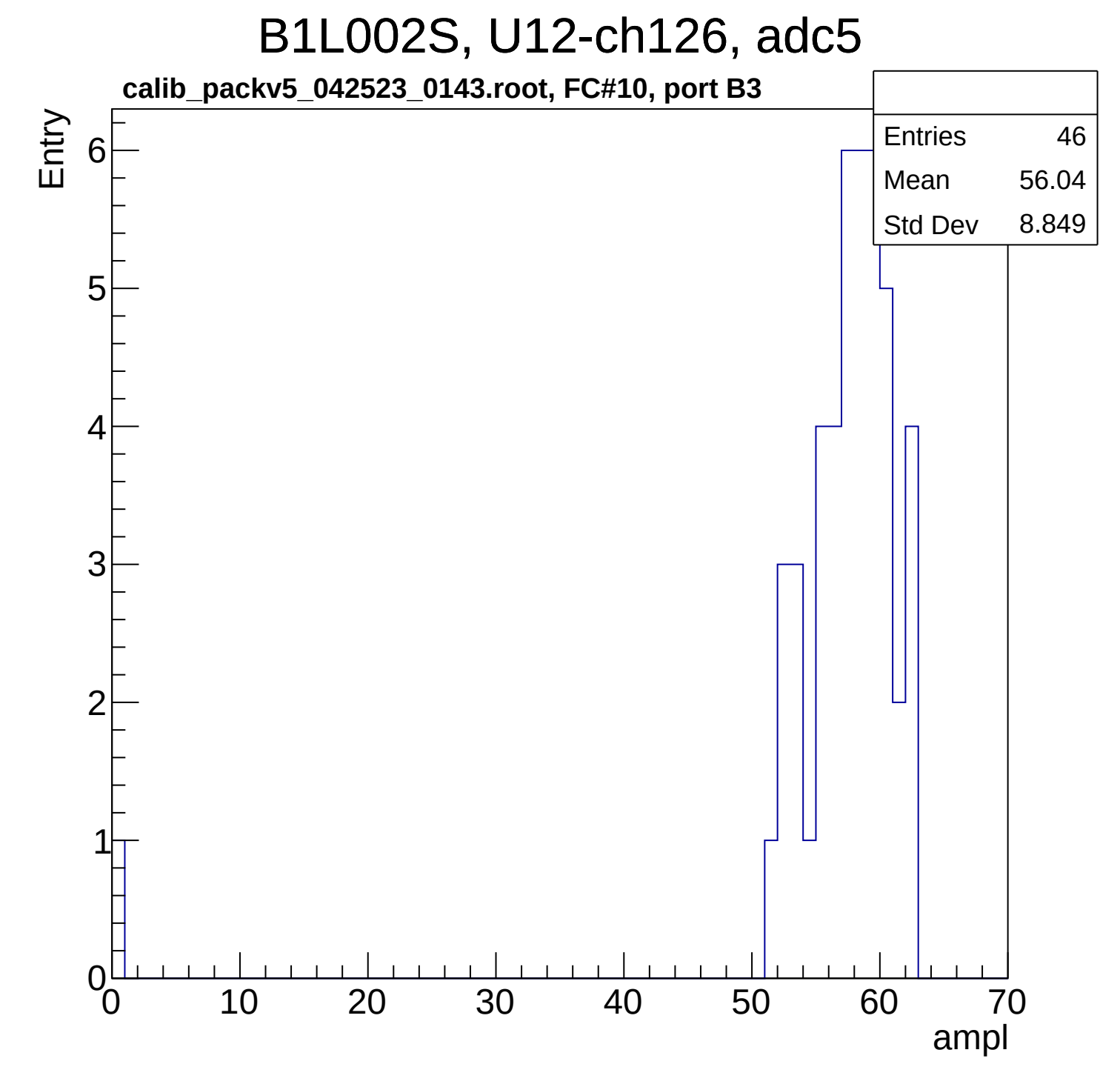
B1L002S, U12-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 56.04
Std Dev 8.849
Entry
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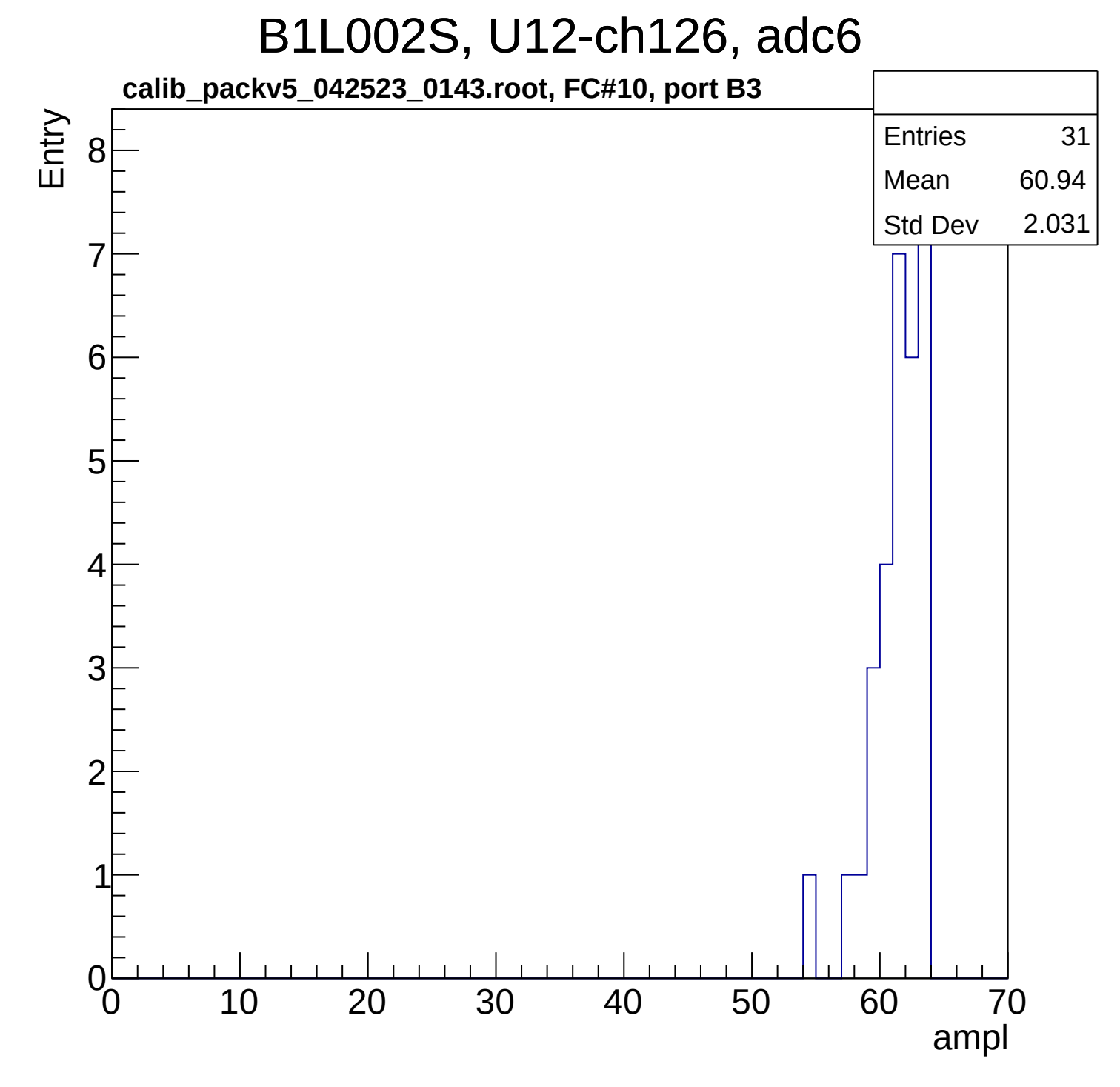
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 60.94
Std Dev 2.031
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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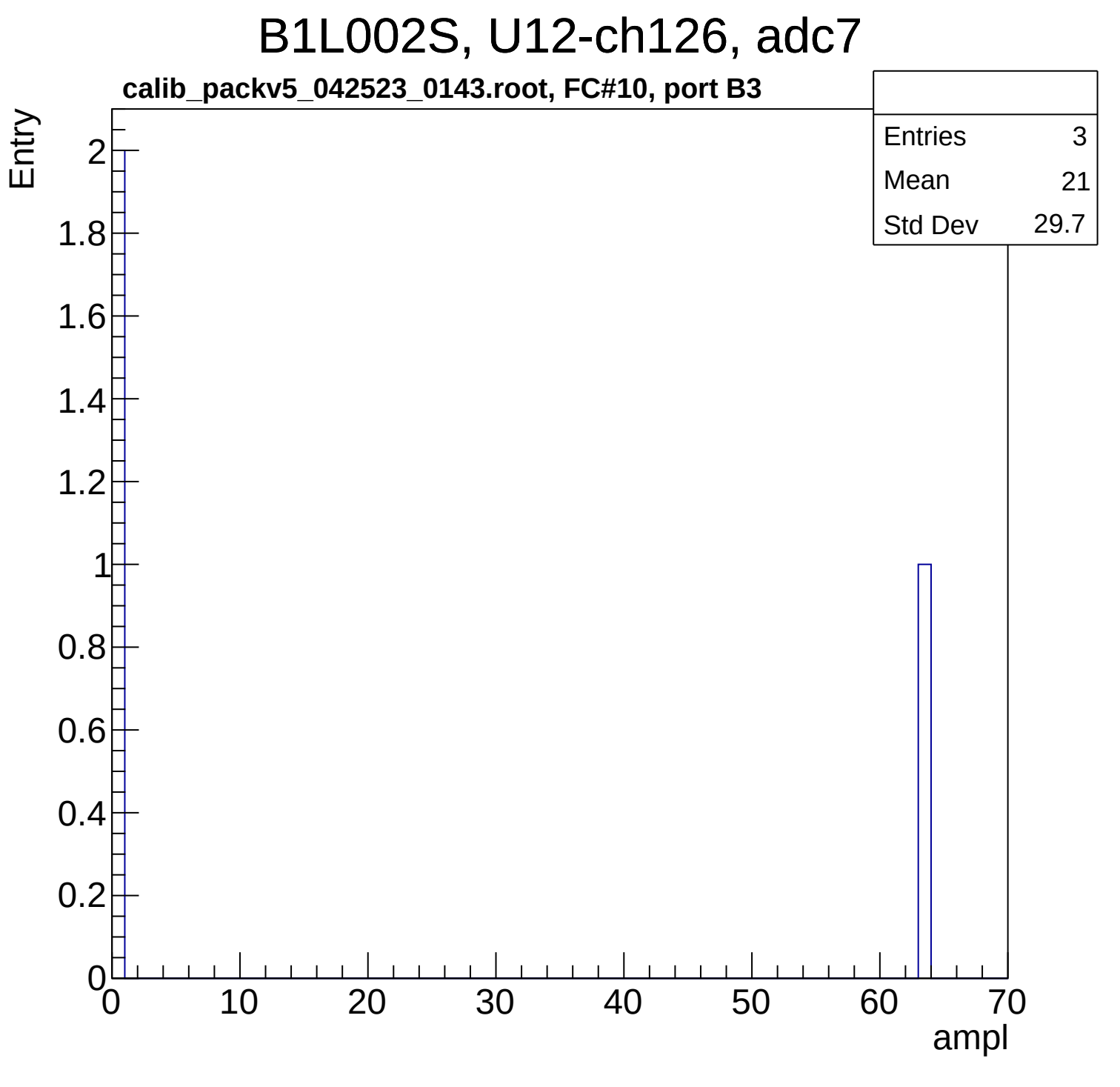
B1L002S, U12-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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40
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60
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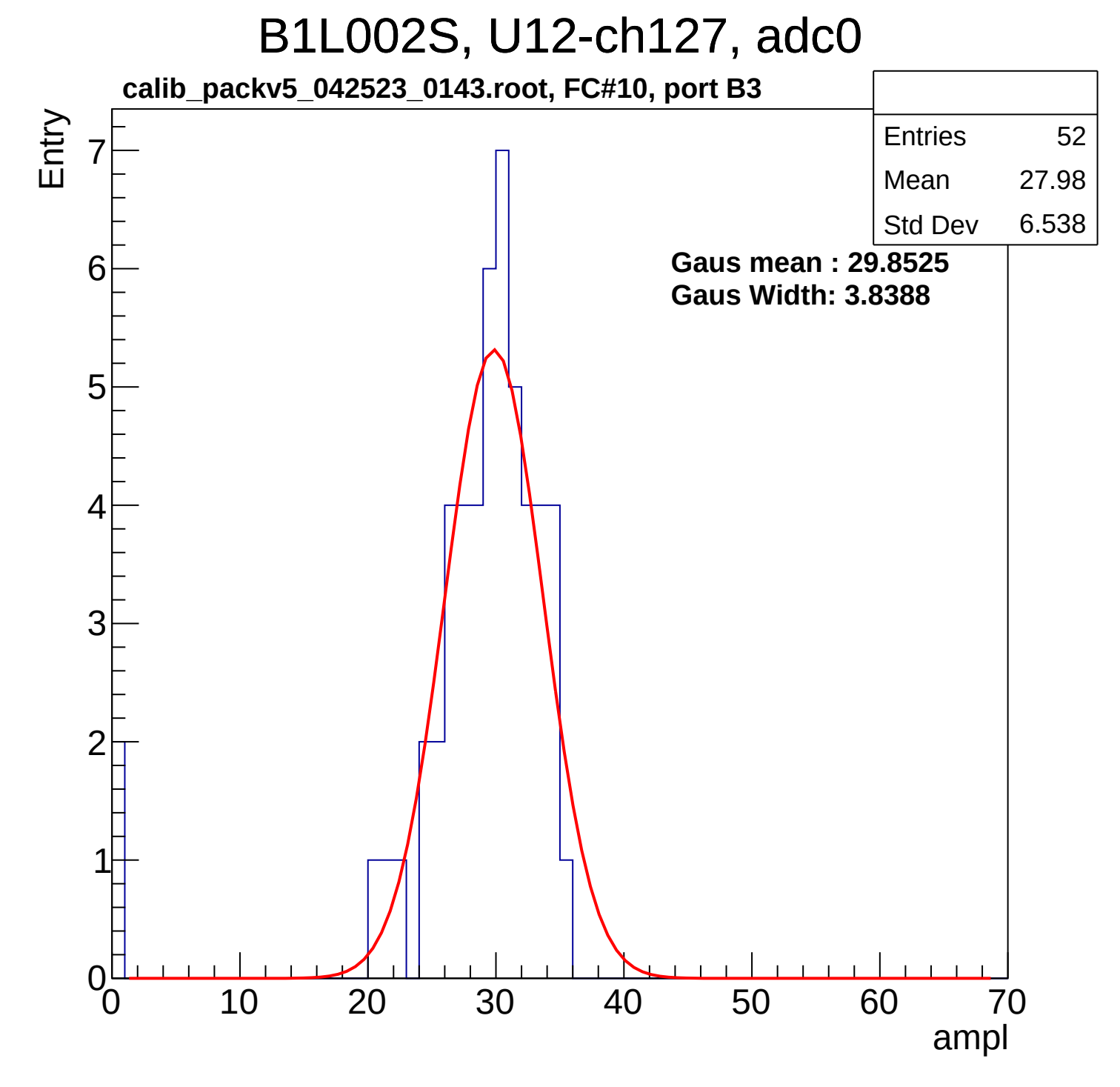
B1L002S, U12-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.98
Std Dev 6.538
Gaus mean : 29.8525
Gaus Width: 3.8388
Entry
ampl
0
1
2
3
4
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6
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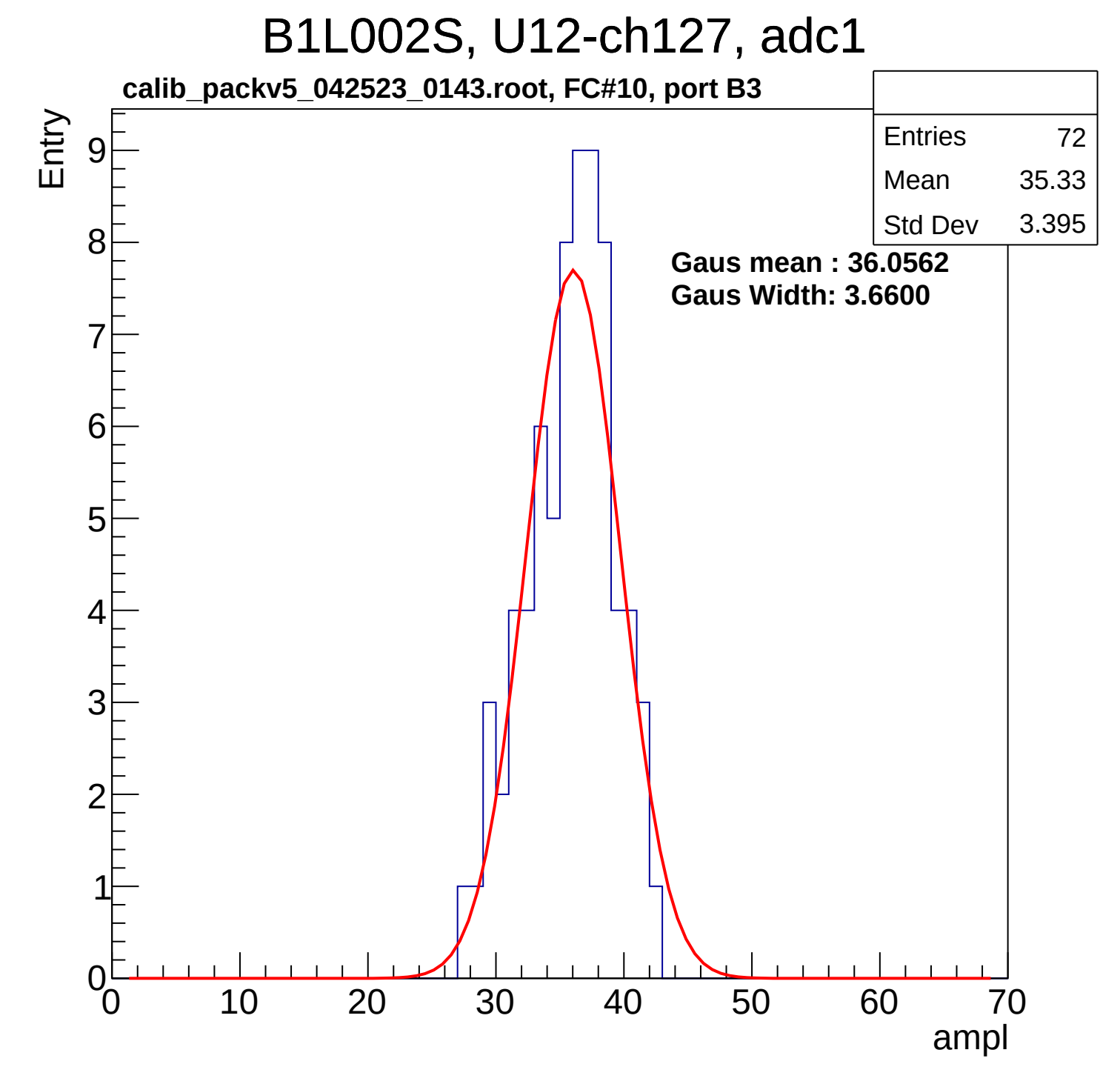
B1L002S, U12-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 35.33
Std Dev 3.395
Gaus mean : 36.0562
Gaus Width: 3.6600
Entry
ampl
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1
2
3
4
5
6
7
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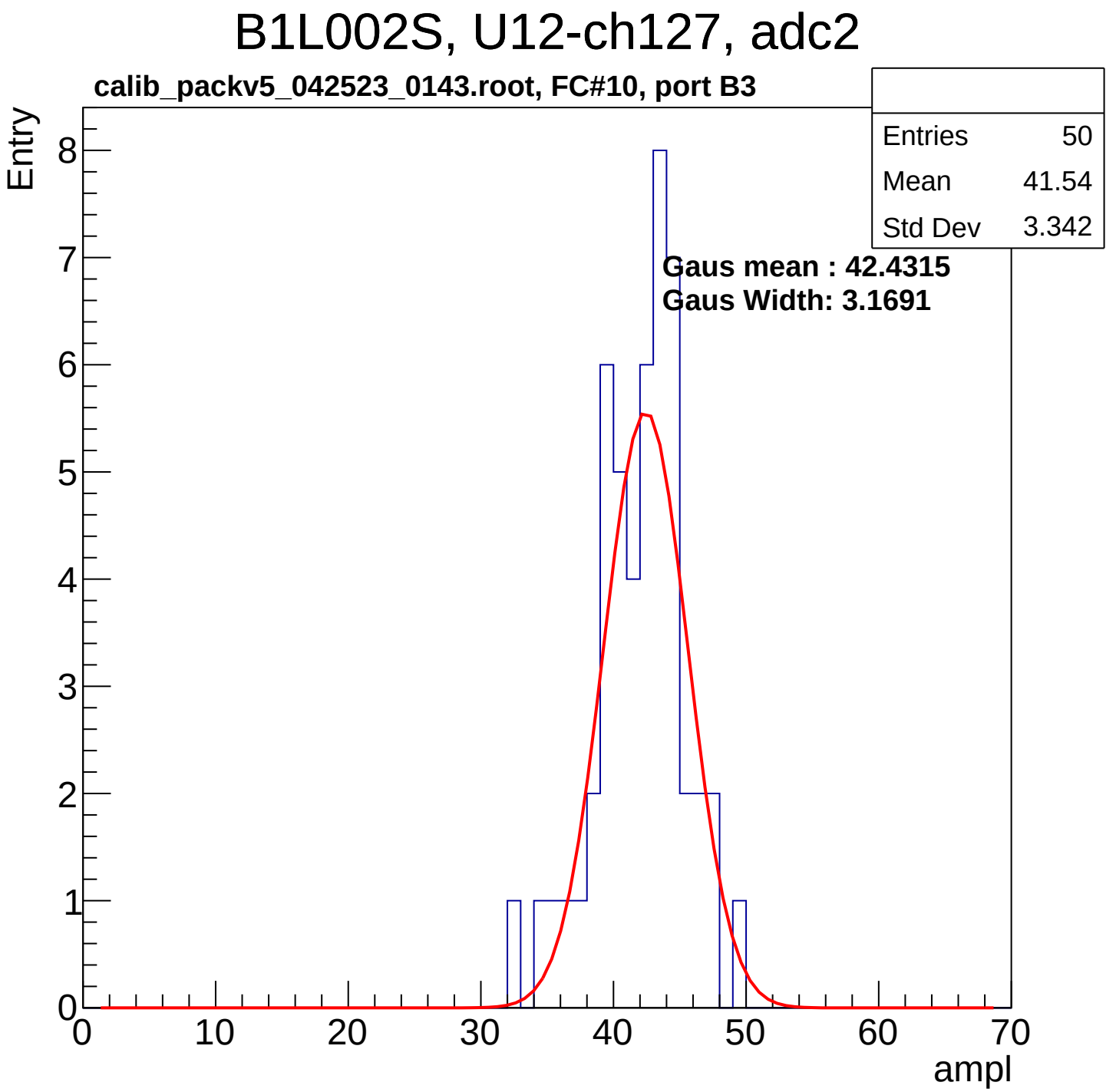
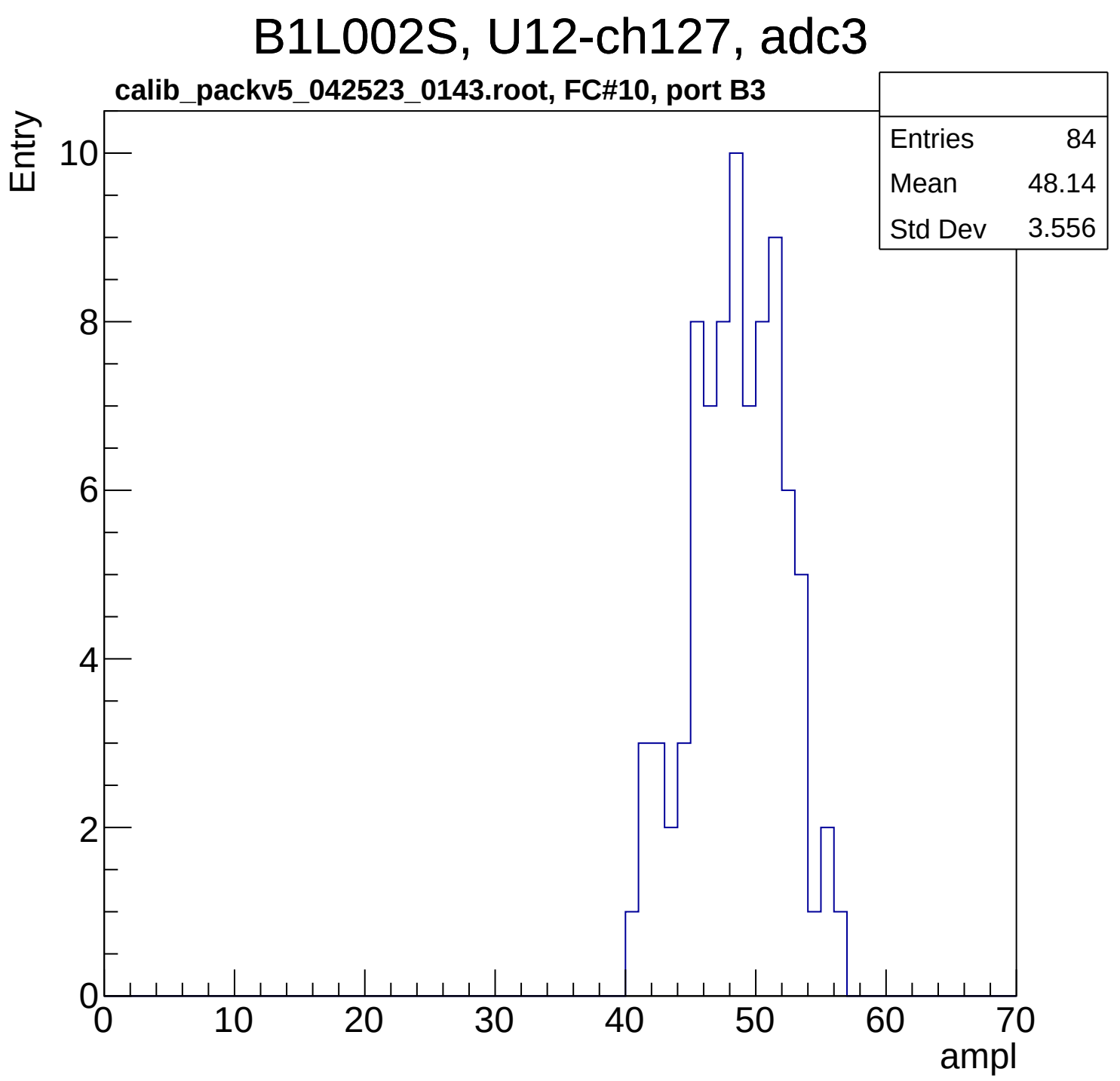
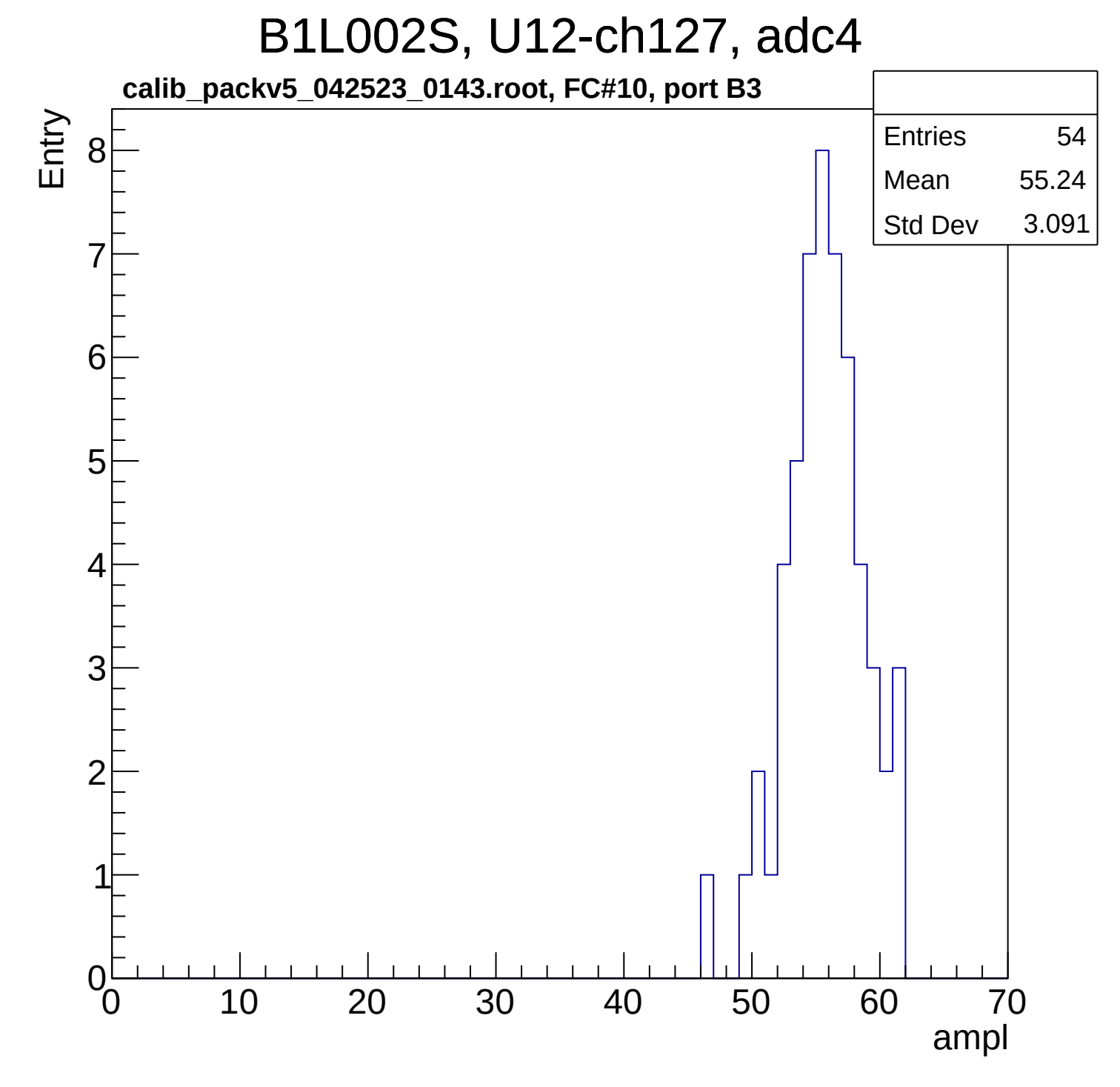
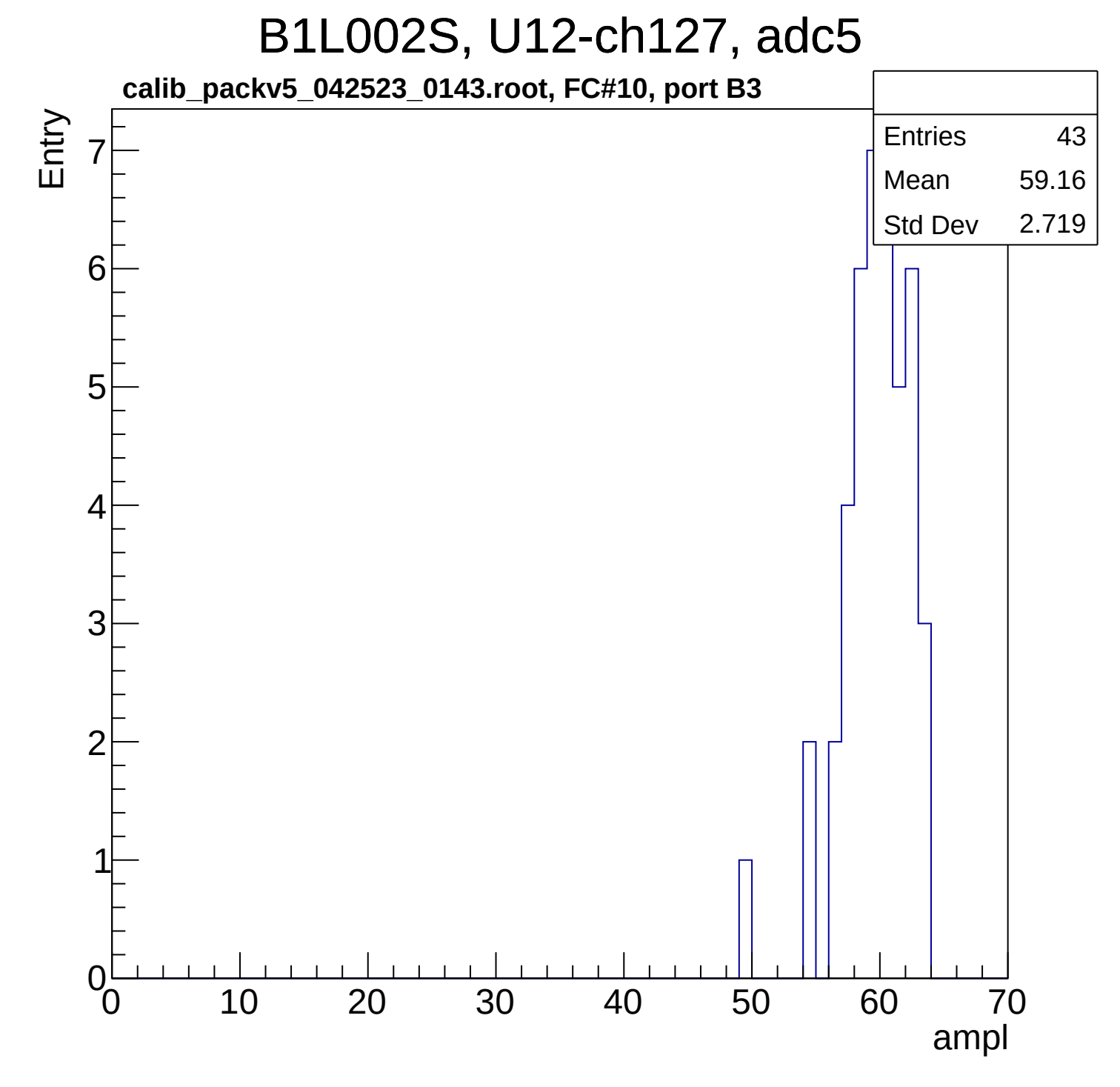
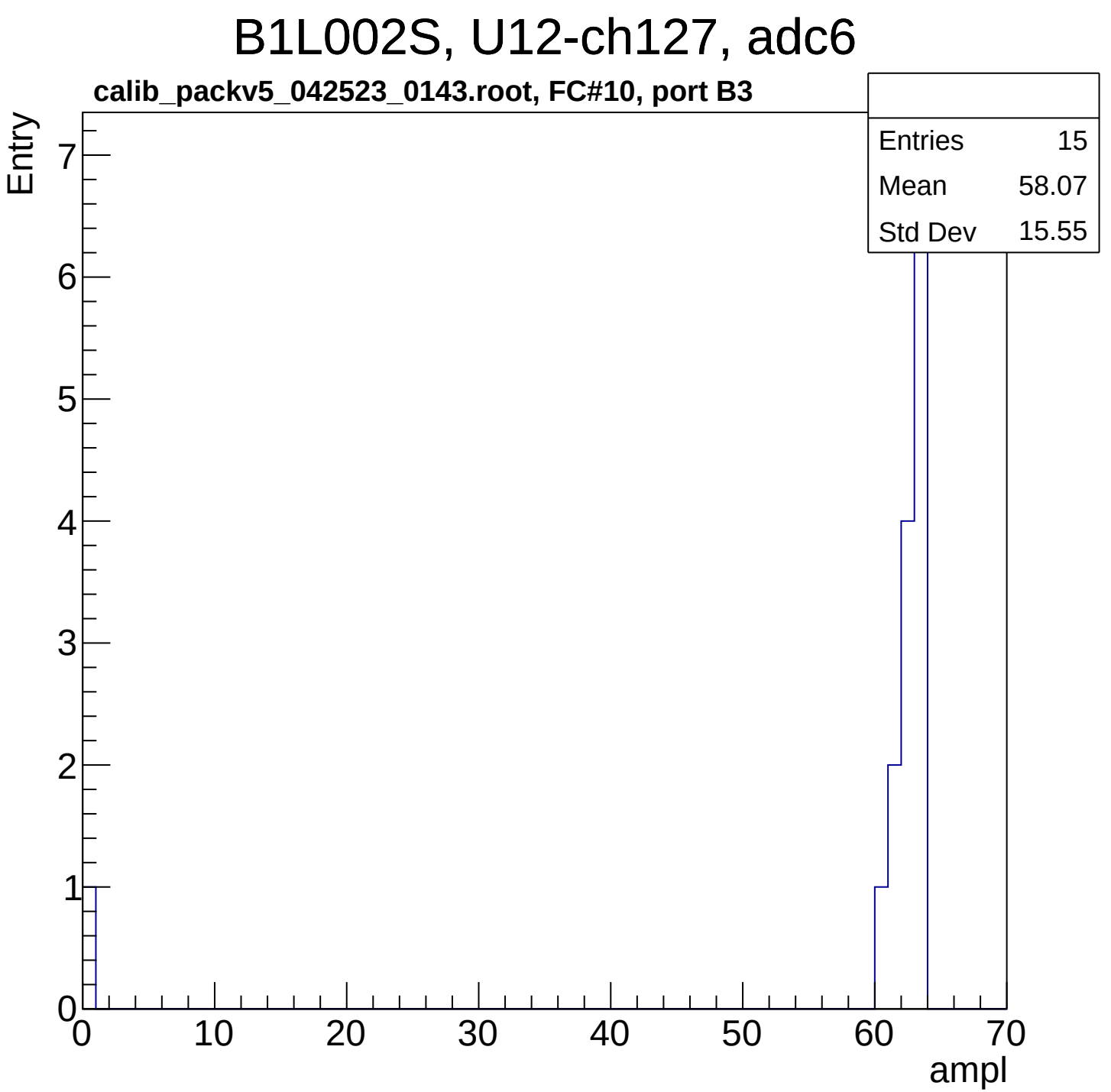
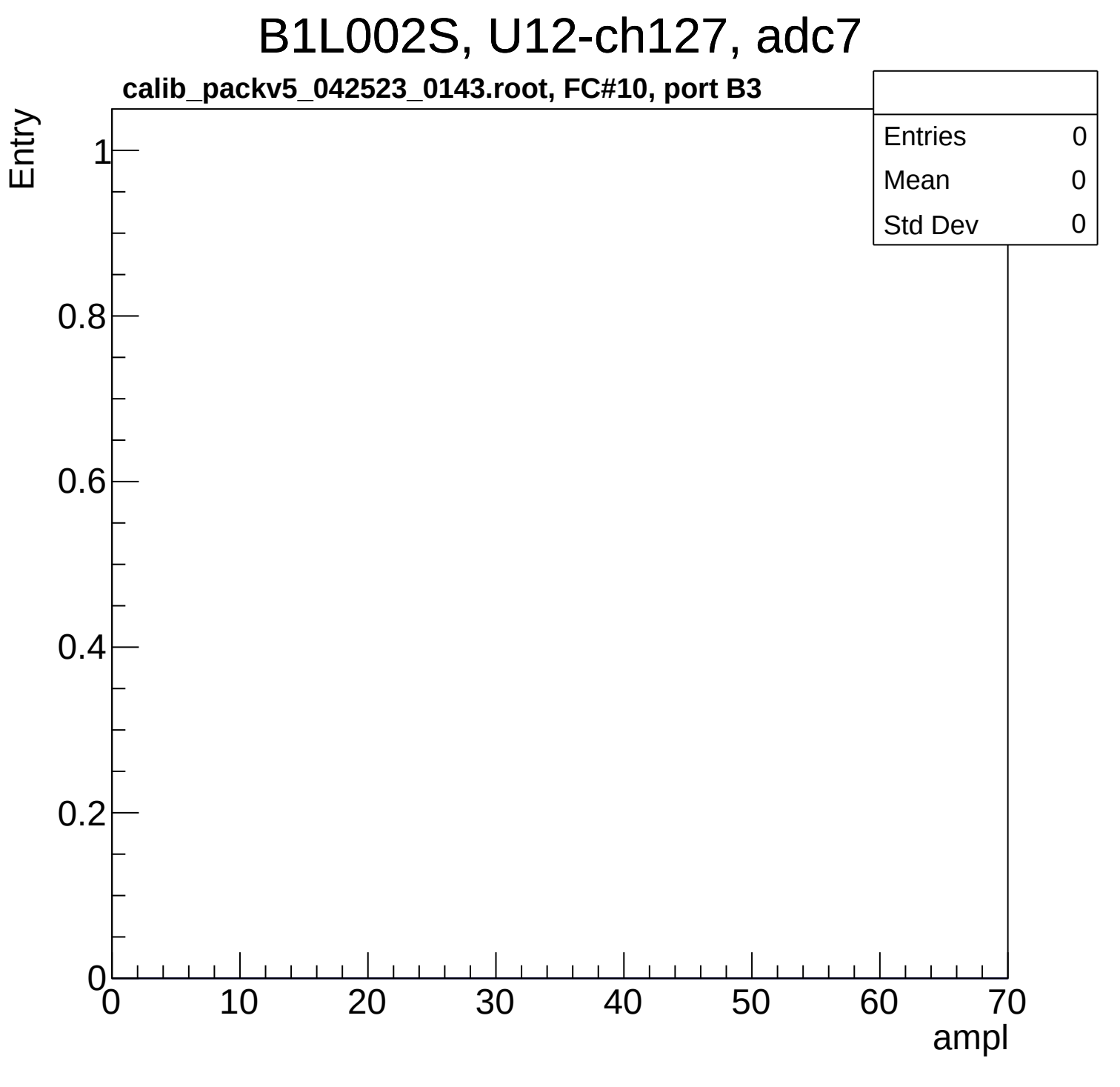

B1L002S, U12-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.54
Std Dev 3.342
Gaus mean : 42.4315
Gaus Width: 3.1691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

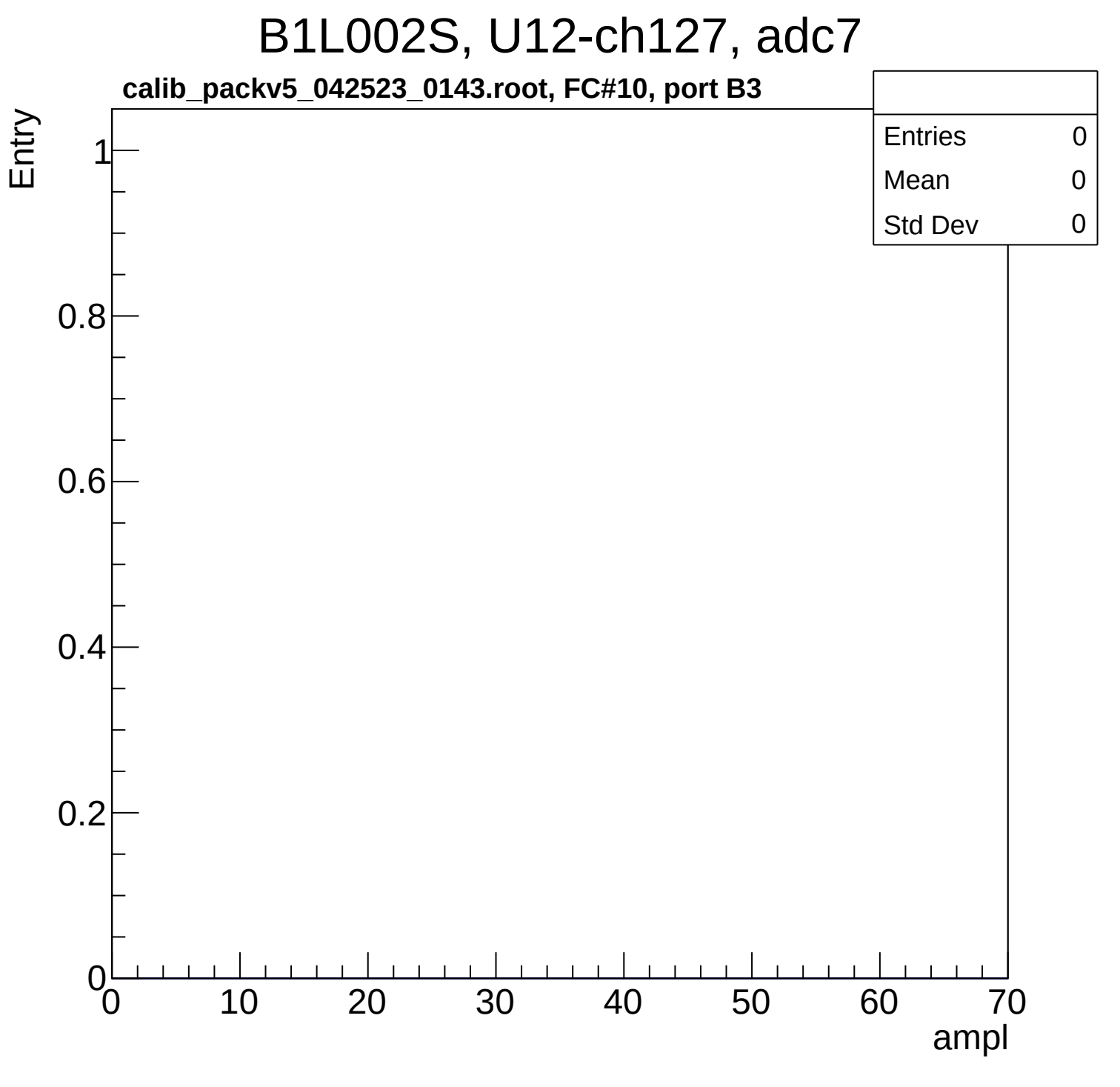

B1L002S, U12-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 48.14
Std Dev 3.556
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U12-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 55.24
Std Dev 3.091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U12-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 59.16
Std Dev 2.719
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U12-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 58.07
Std Dev 15.55
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U12-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl