

B1L003S, U26-ch0

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.38 |
| Std Dev | 11.63 |

Turn on : 27.2235

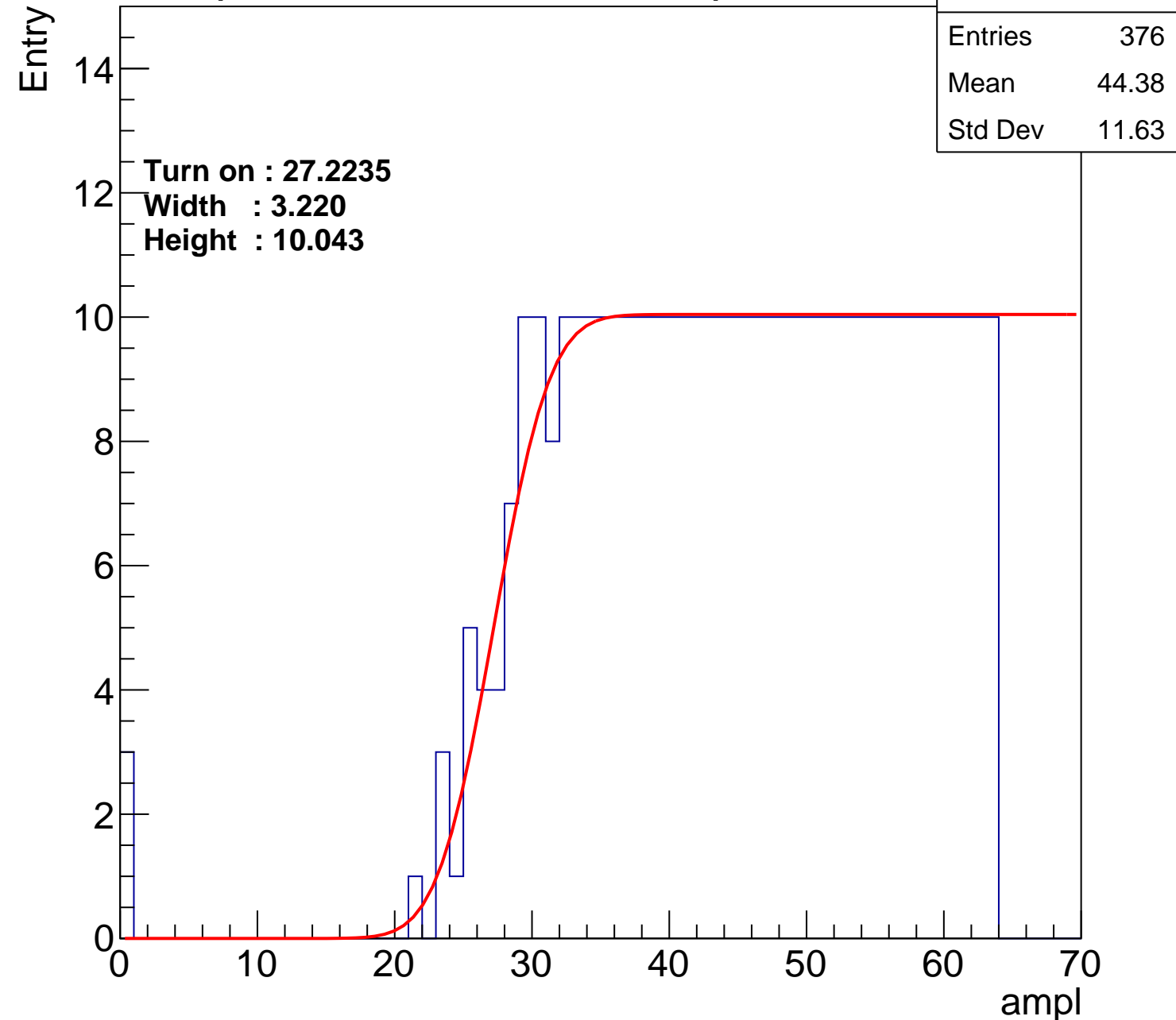
Width : 3.220

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch1

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.02 |
| Std Dev | 12.12 |

Turn on : 26.7583

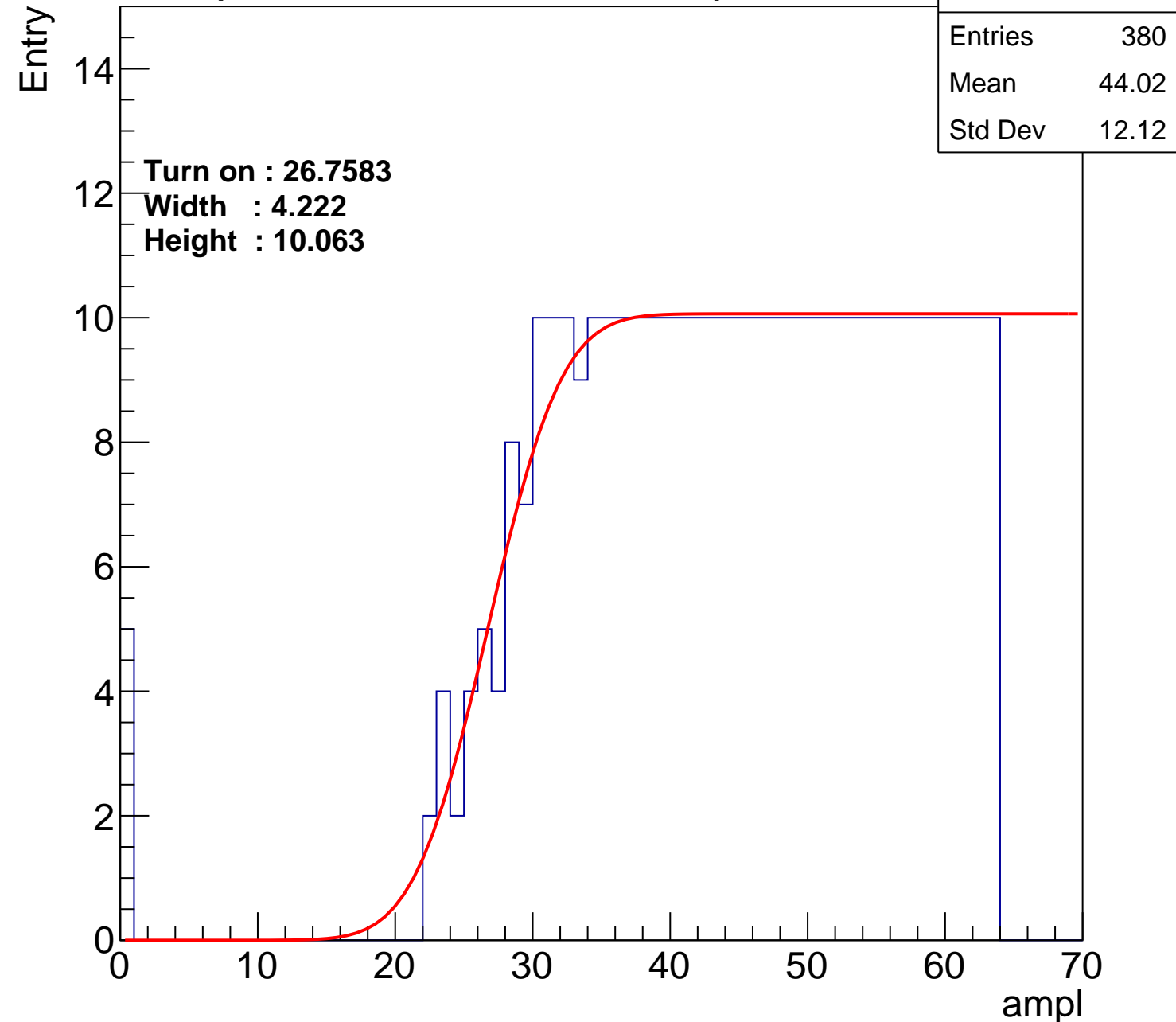
Width : 4.222

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch2

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.55 |
| Std Dev | 11.7 |

Turn on : 24.6085

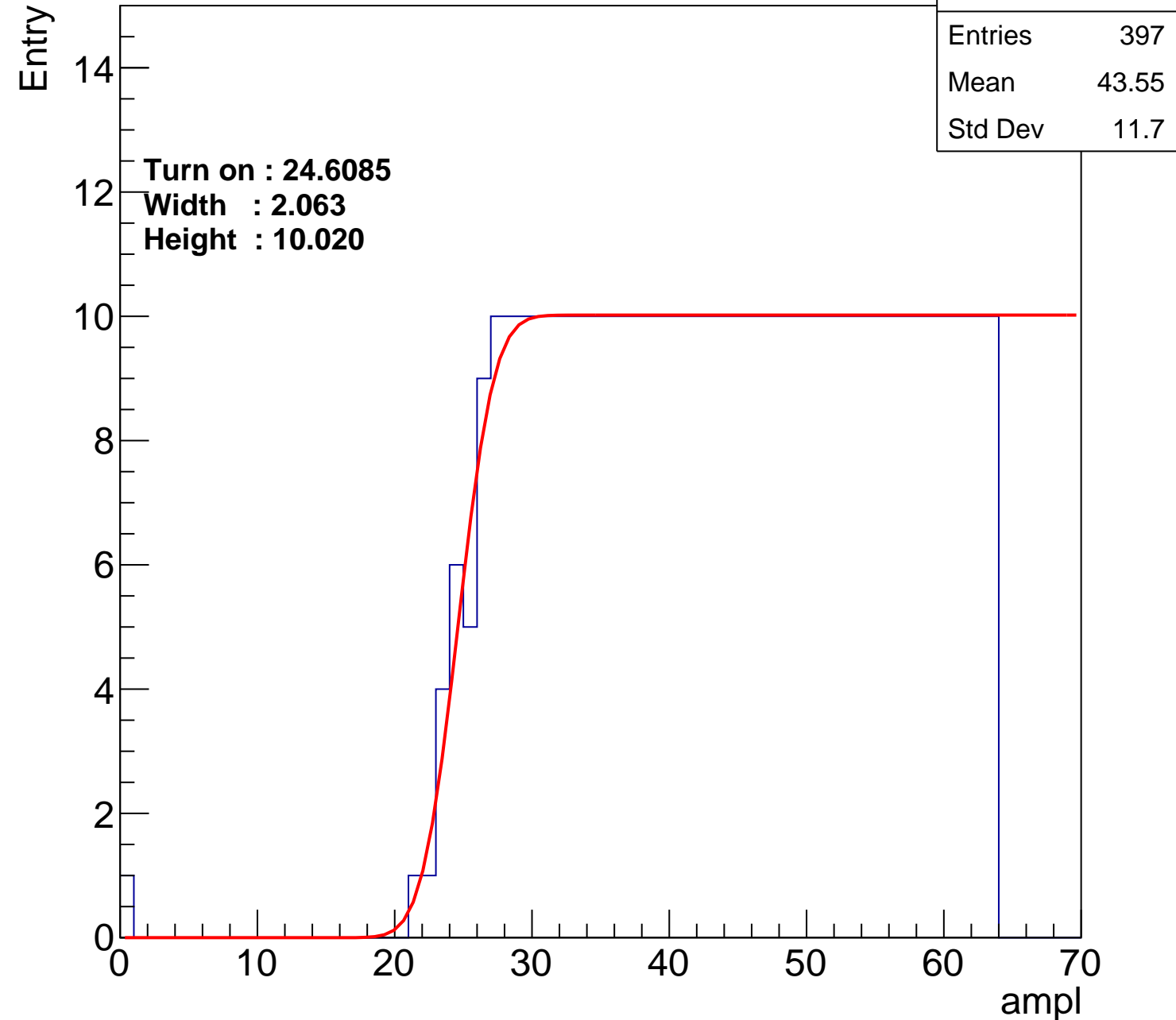
Width : 2.063

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch3

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.79 |
| Std Dev | 11.25 |

Turn on : 27.3074

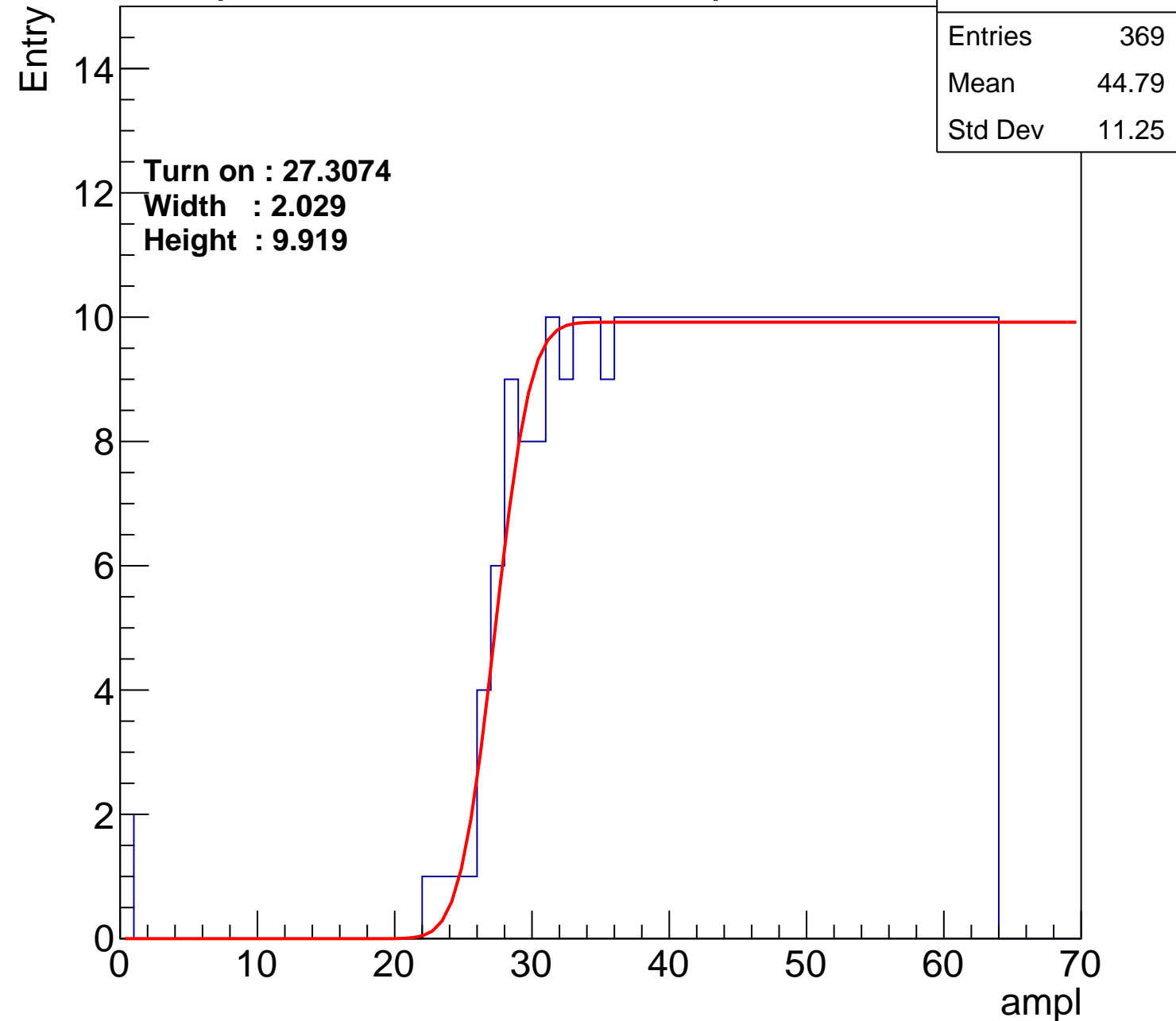
Width : 2.029

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch4

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.53 |
| Std Dev | 11.68 |

Turn on : 27.1217

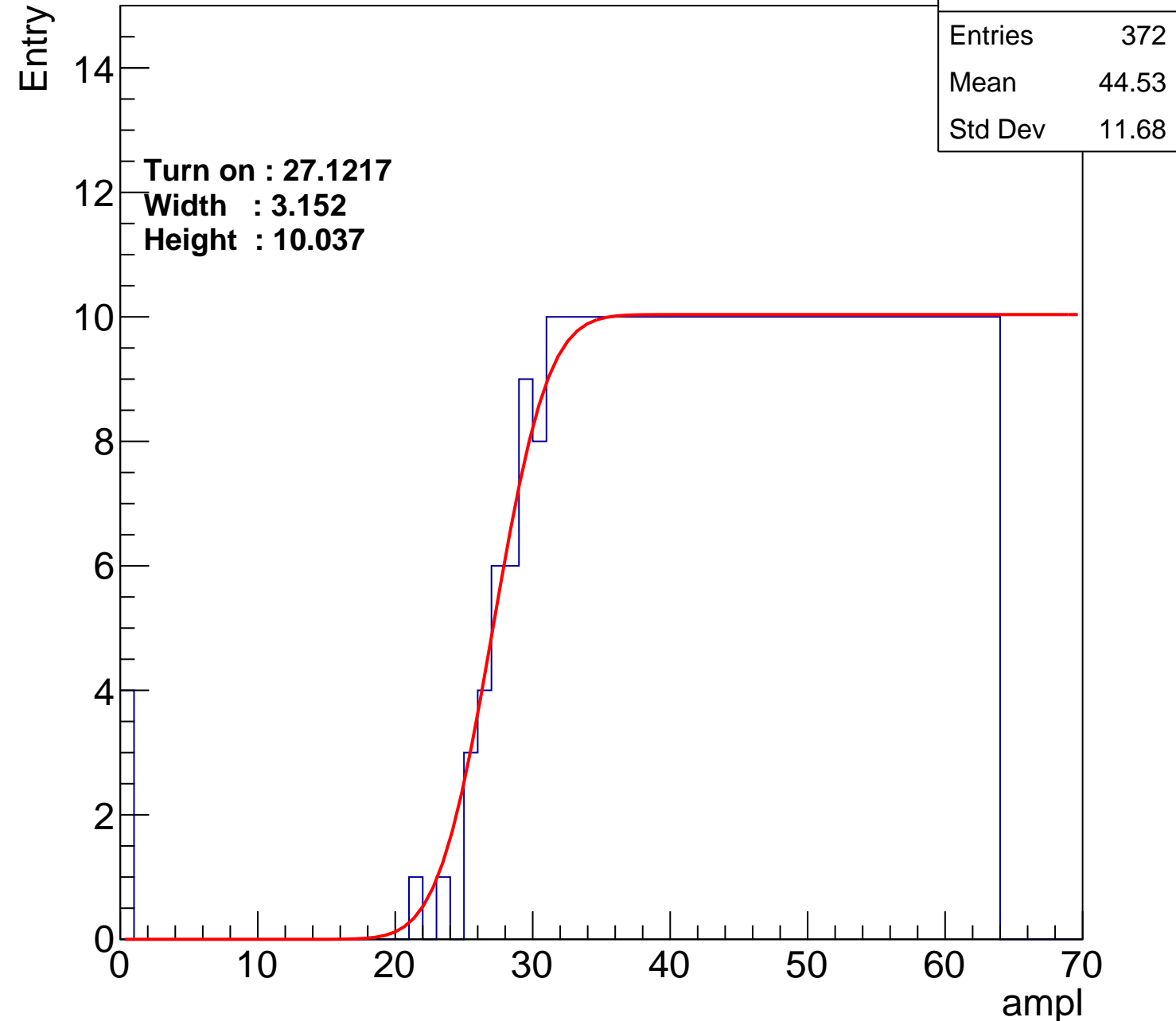
Width : 3.152

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch5

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.23 |
| Std Dev | 12 |

Turn on : 27.1214

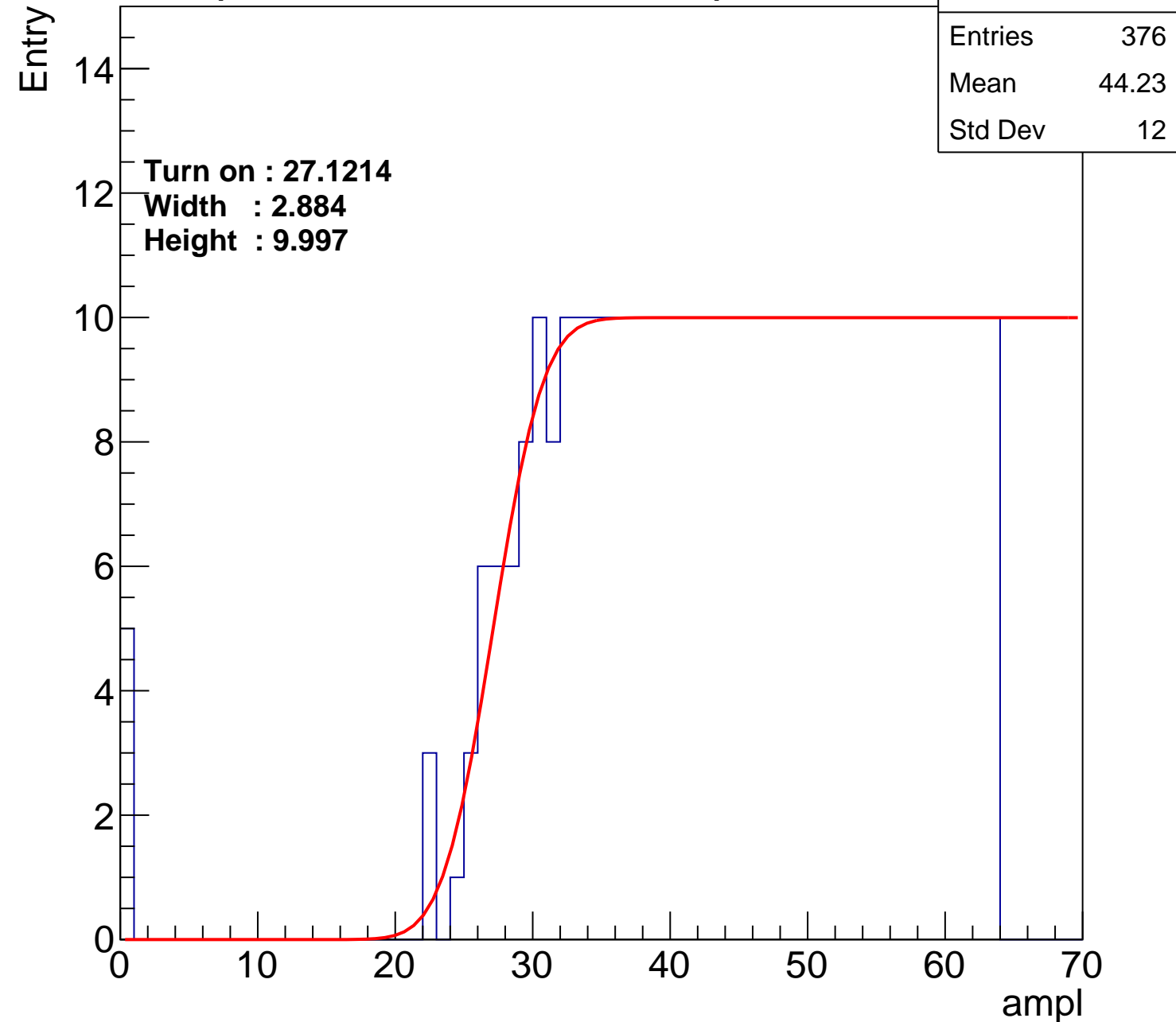
Width : 2.884

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch6

calib_packv5_042523_0143.root, FC#13, port D2

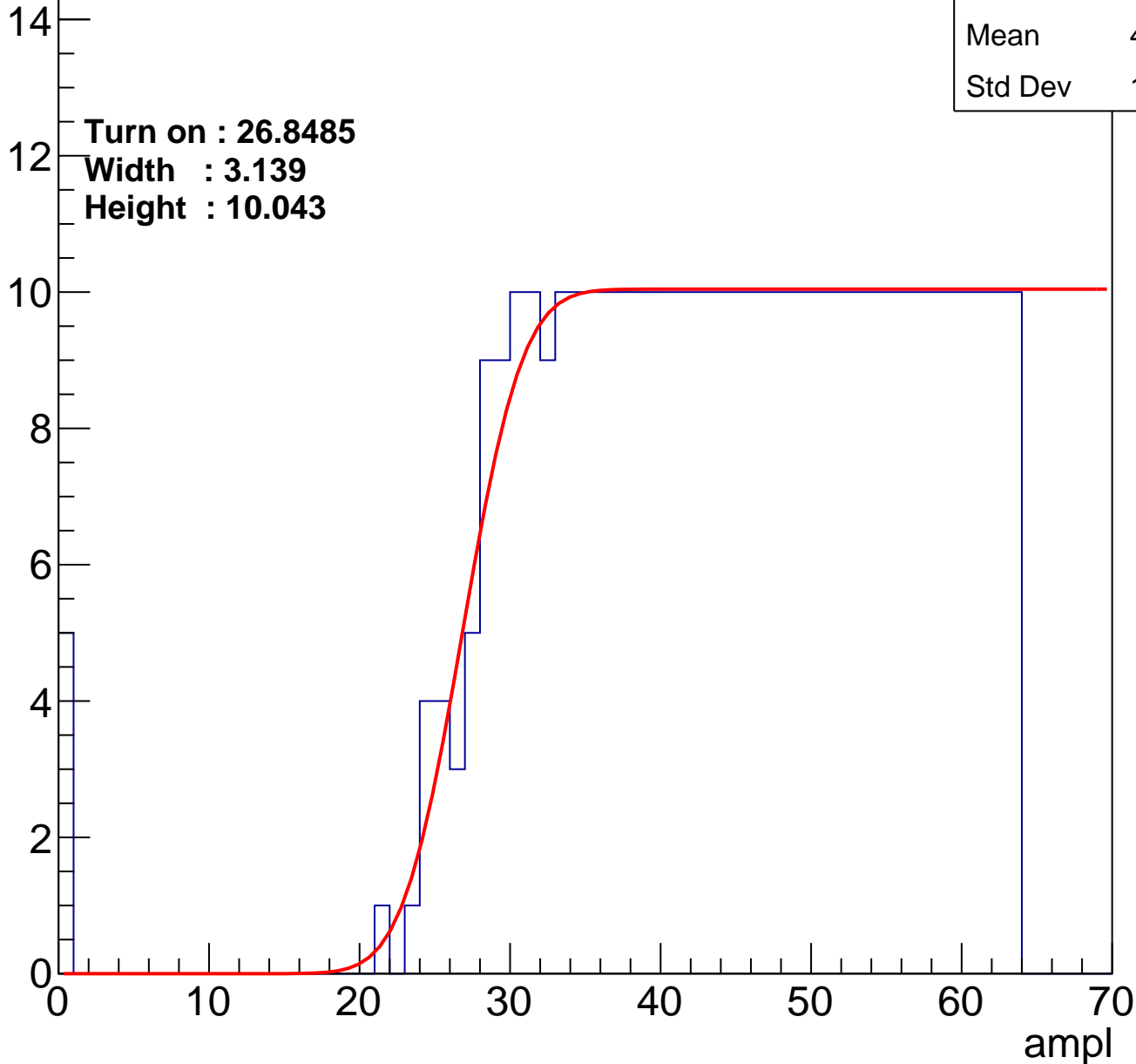
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.06 |
| Std Dev | 12.05 |

Turn on : 26.8485

Width : 3.139

Height : 10.043

Entry



B1L003S, U26-ch7

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.17 |
| Std Dev | 11.57 |

Turn on : 25.7487

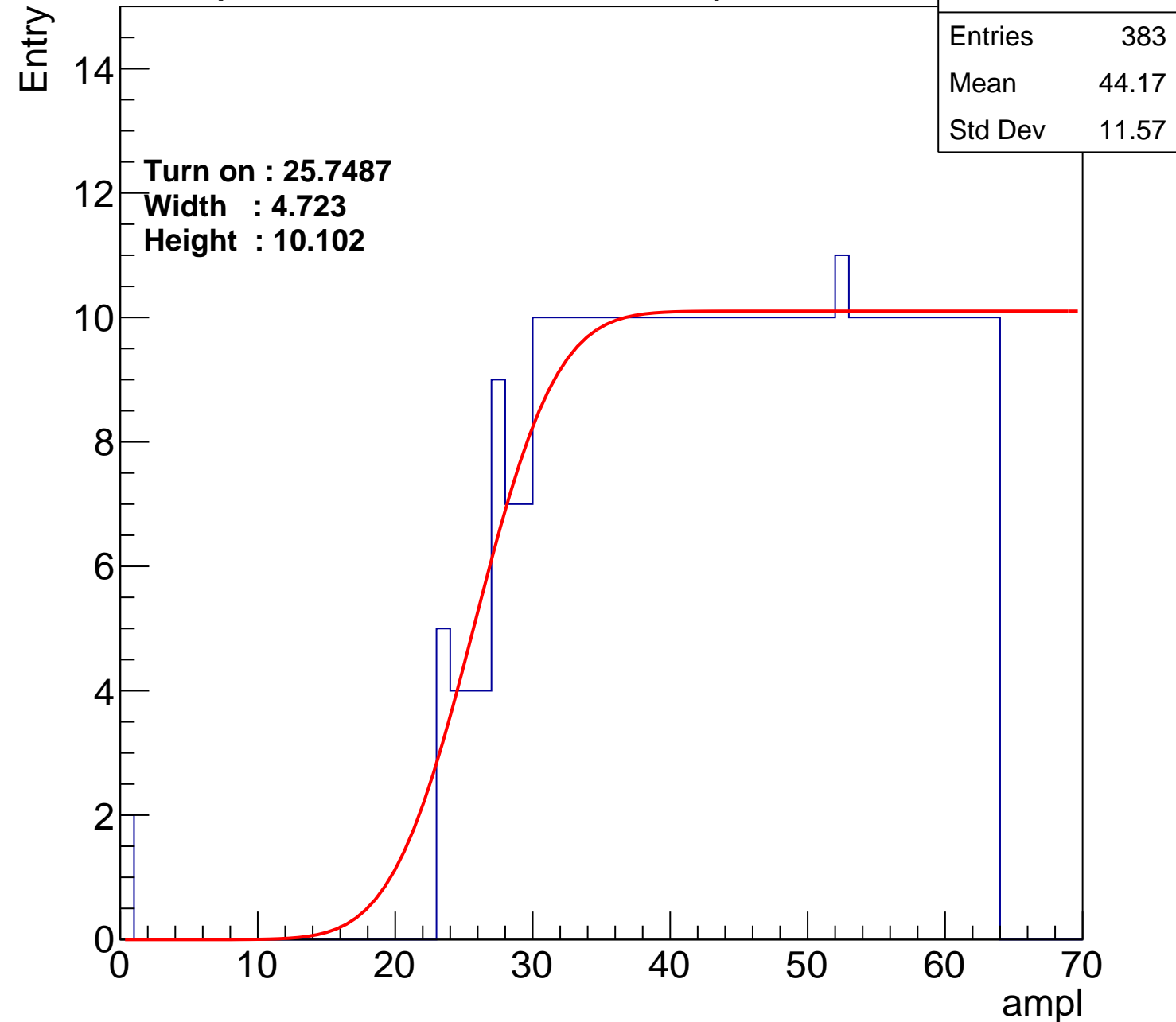
Width : 4.723

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch8

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.16 |
| Std Dev | 10.89 |

Turn on : 28.0090

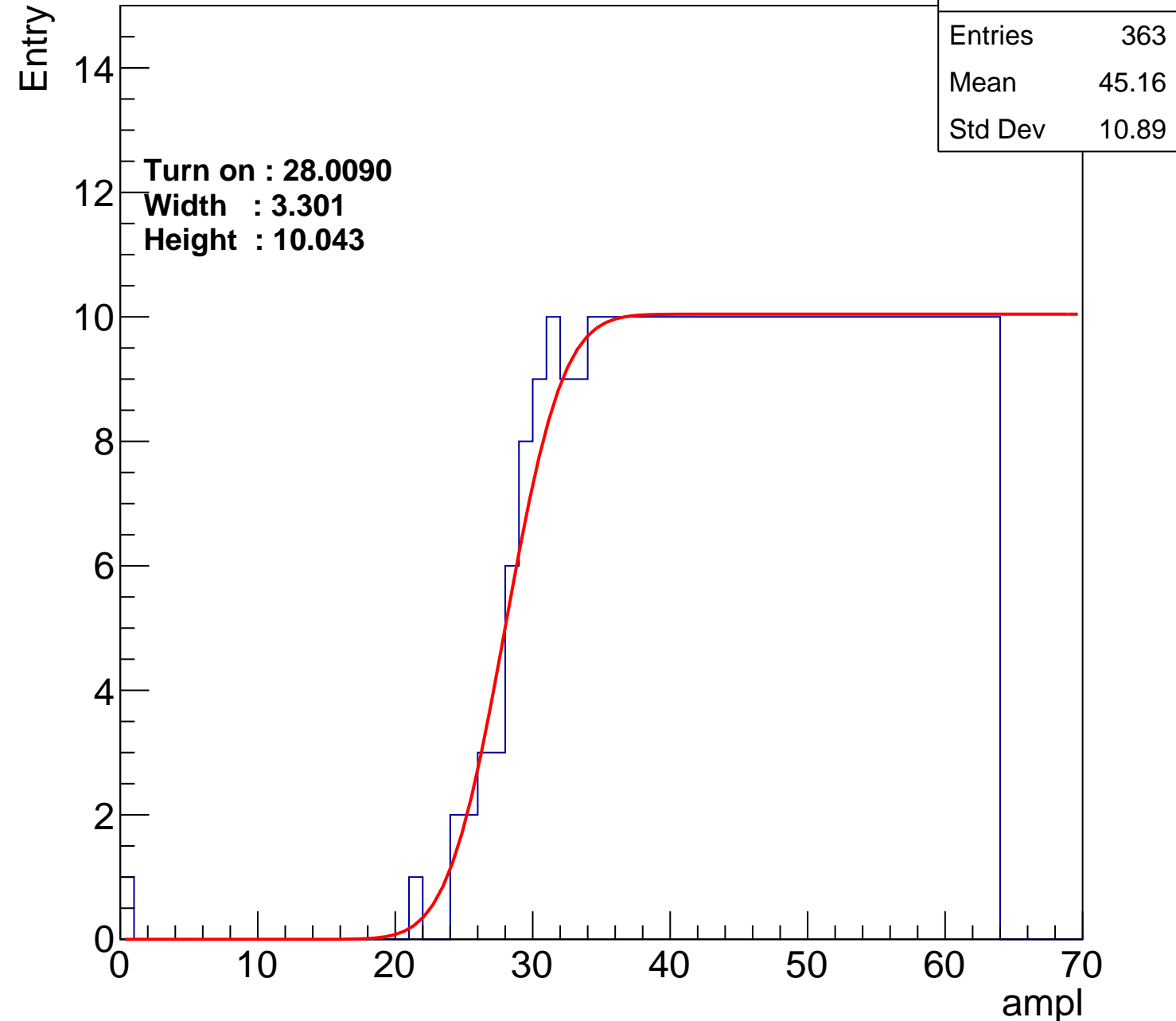
Width : 3.301

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch9

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.55 |
| Std Dev | 12.14 |

Turn on : 25.6176

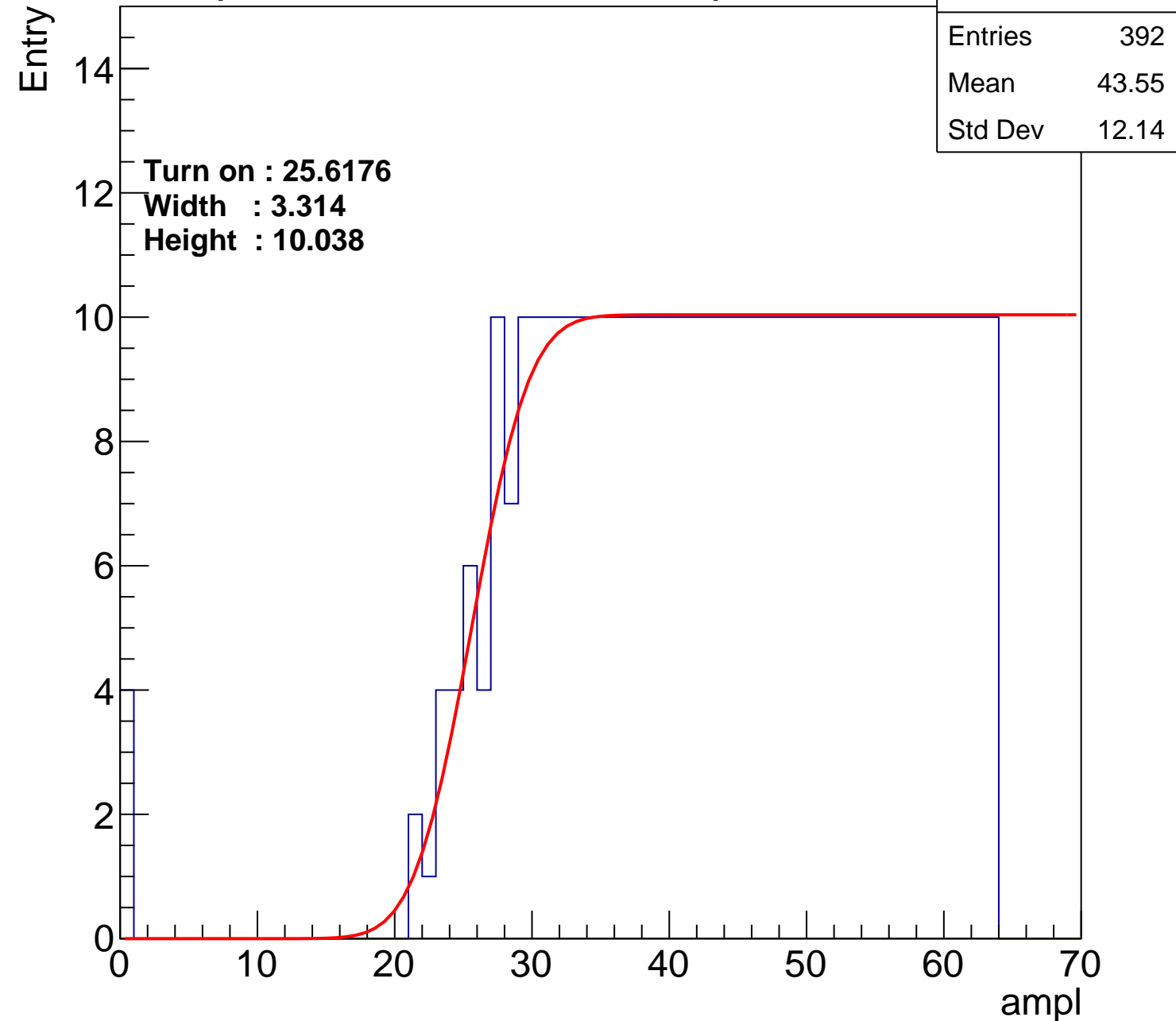
Width : 3.314

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch10

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.19 |
| Std Dev | 11.46 |

Turn on : 26.4110

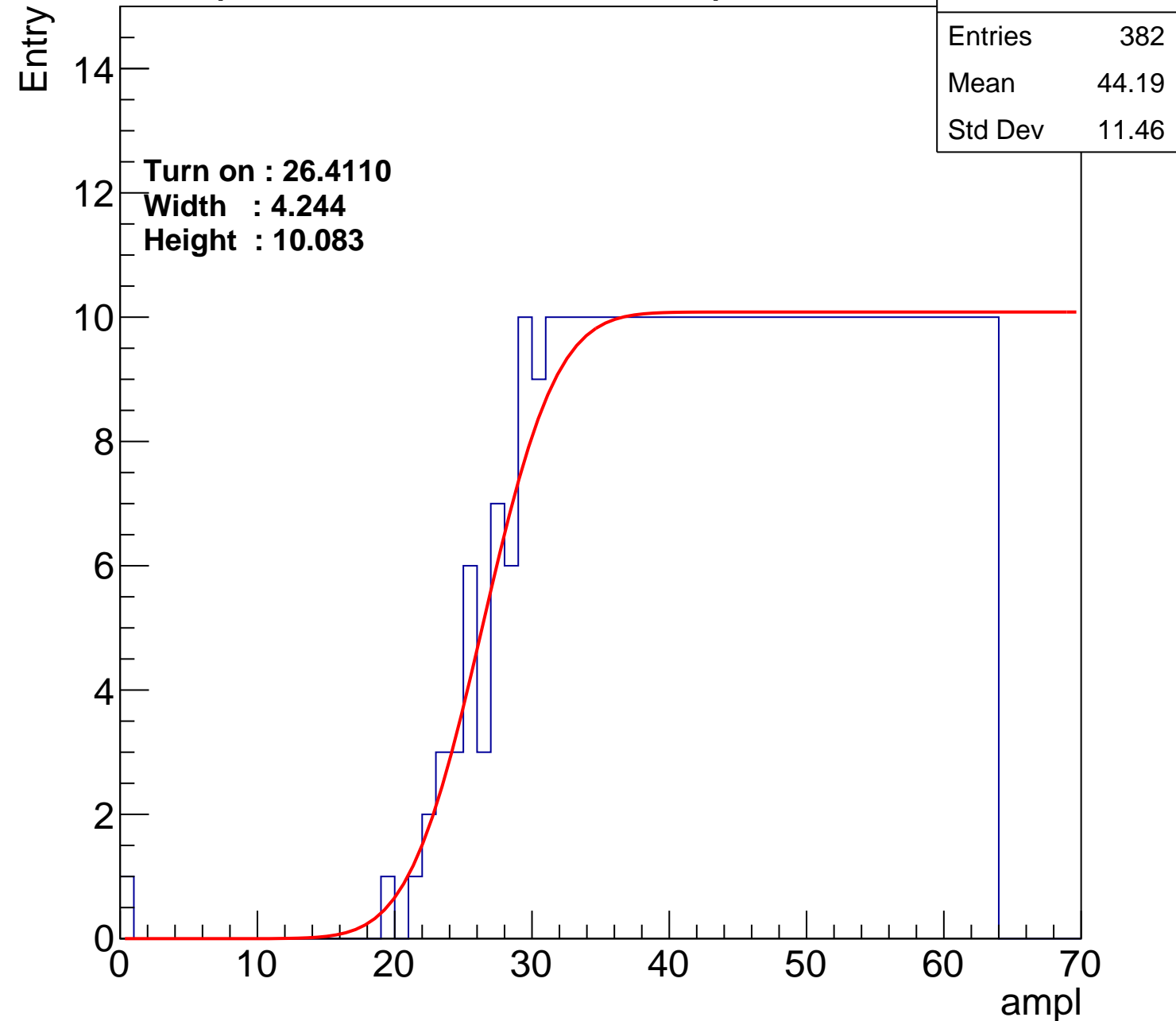
Width : 4.244

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch11

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.67 |
| Std Dev | 12.11 |

Turn on : 25.9591

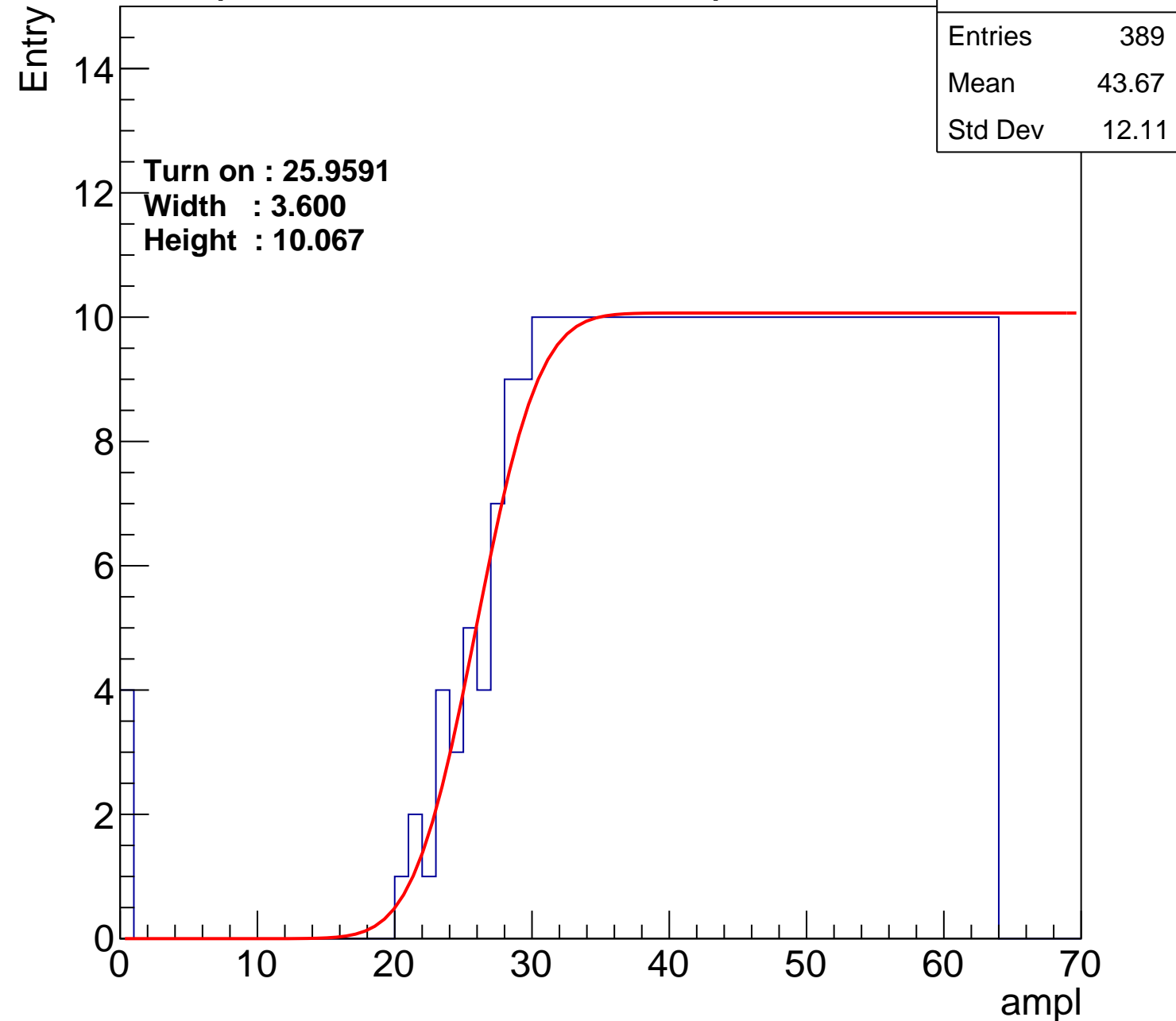
Width : 3.600

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch12

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.47 |
| Std Dev | 11.57 |

Turn on : 26.9320

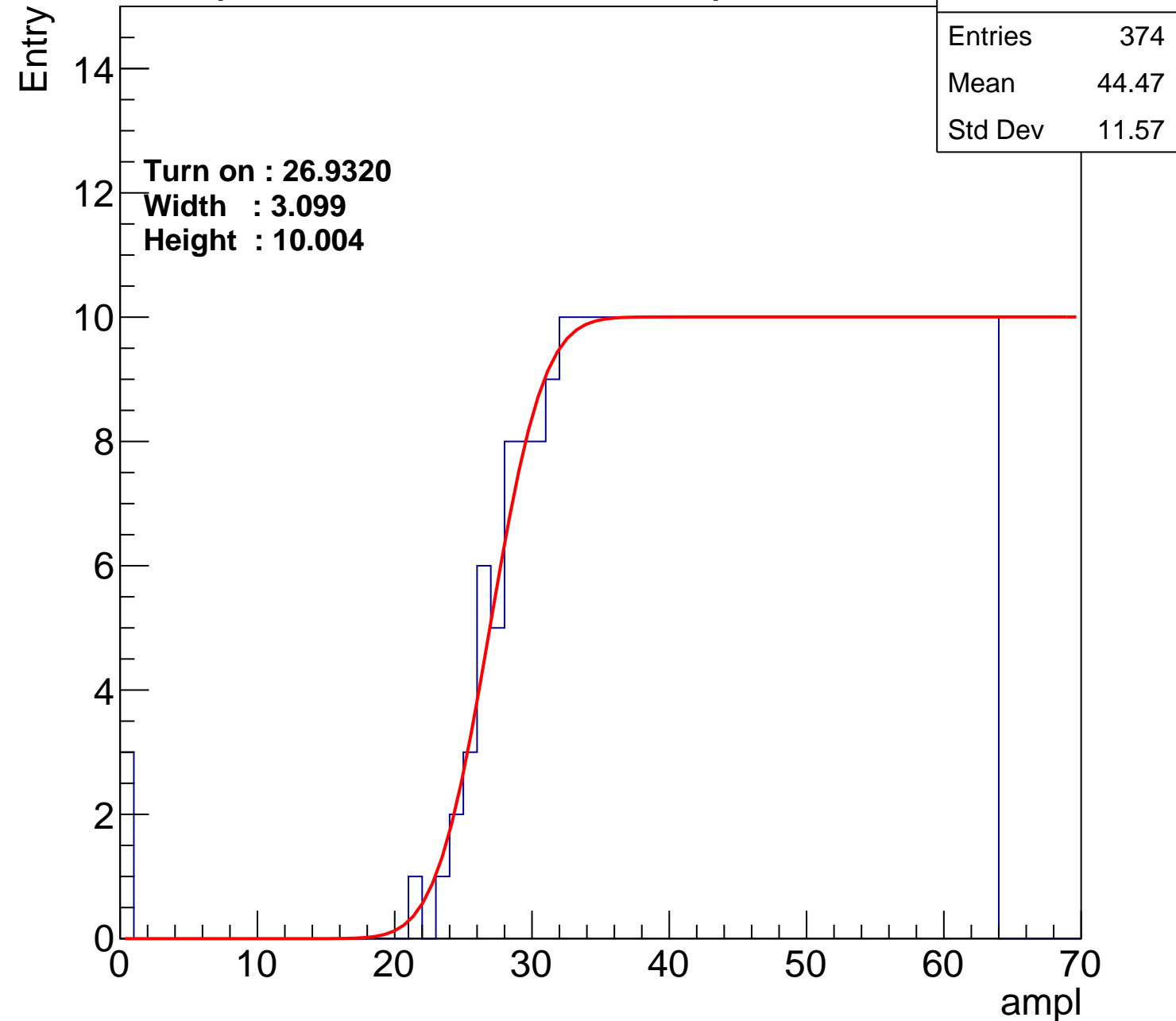
Width : 3.099

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch13

calib_packv5_042523_0143.root, FC#13, port D2

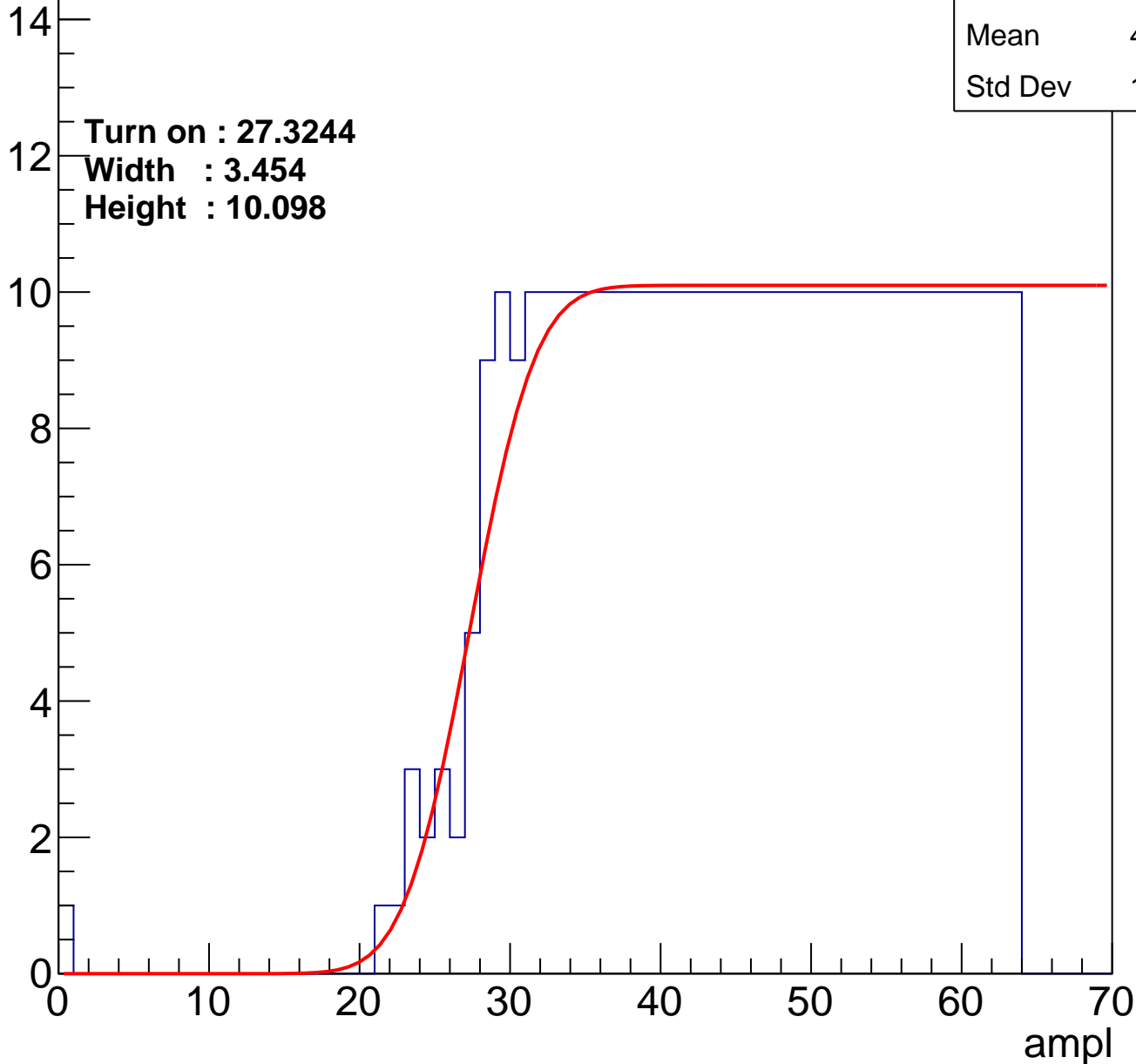
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.53 |
| Std Dev | 11.22 |

Turn on : 27.3244

Width : 3.454

Height : 10.098

Entry



B1L003S, U26-ch14

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.96 |
| Std Dev | 10.95 |

Turn on : 27.3803

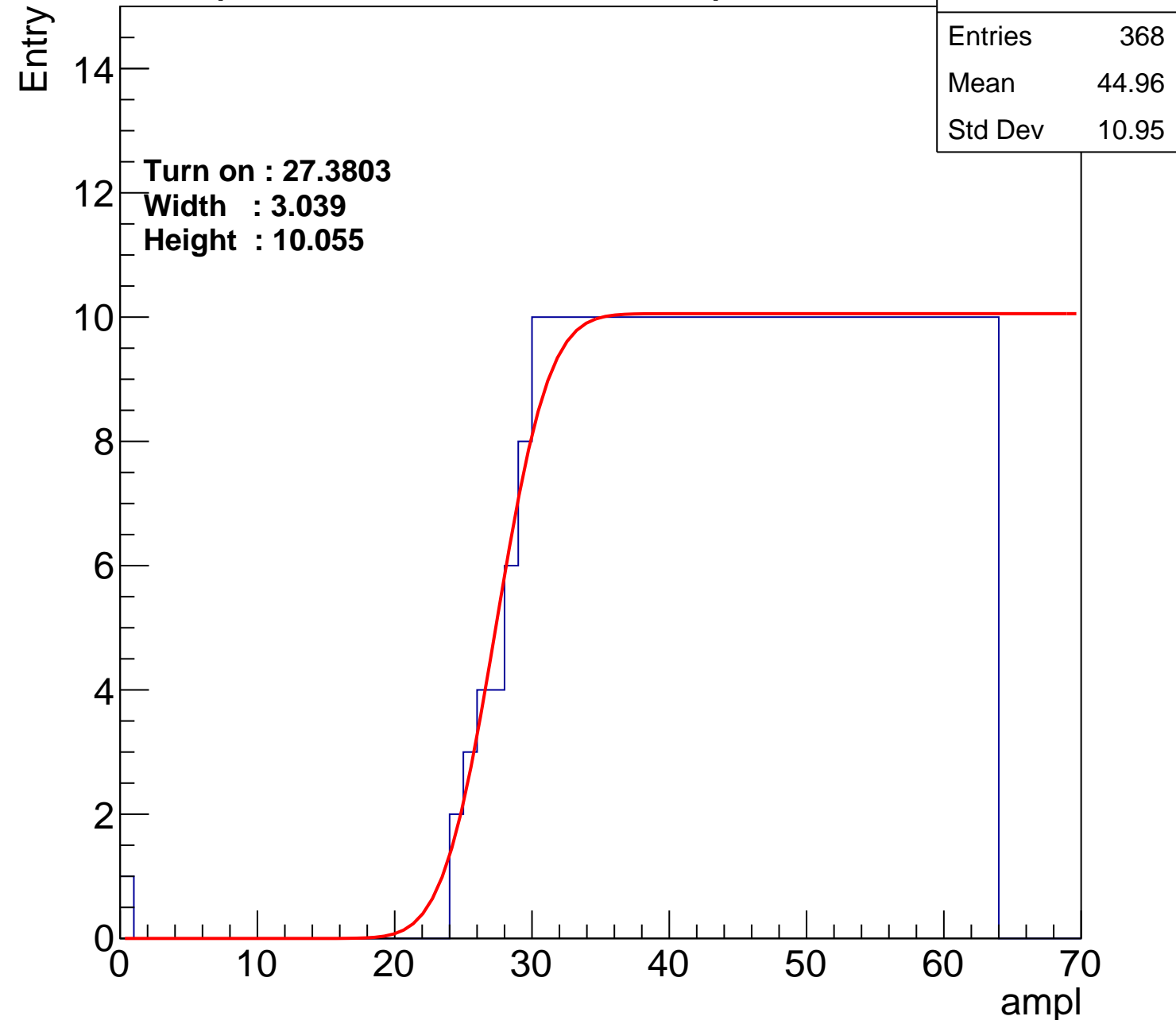
Width : 3.039

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch15

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.63 |
| Std Dev | 11.3 |

Turn on : 27.2056

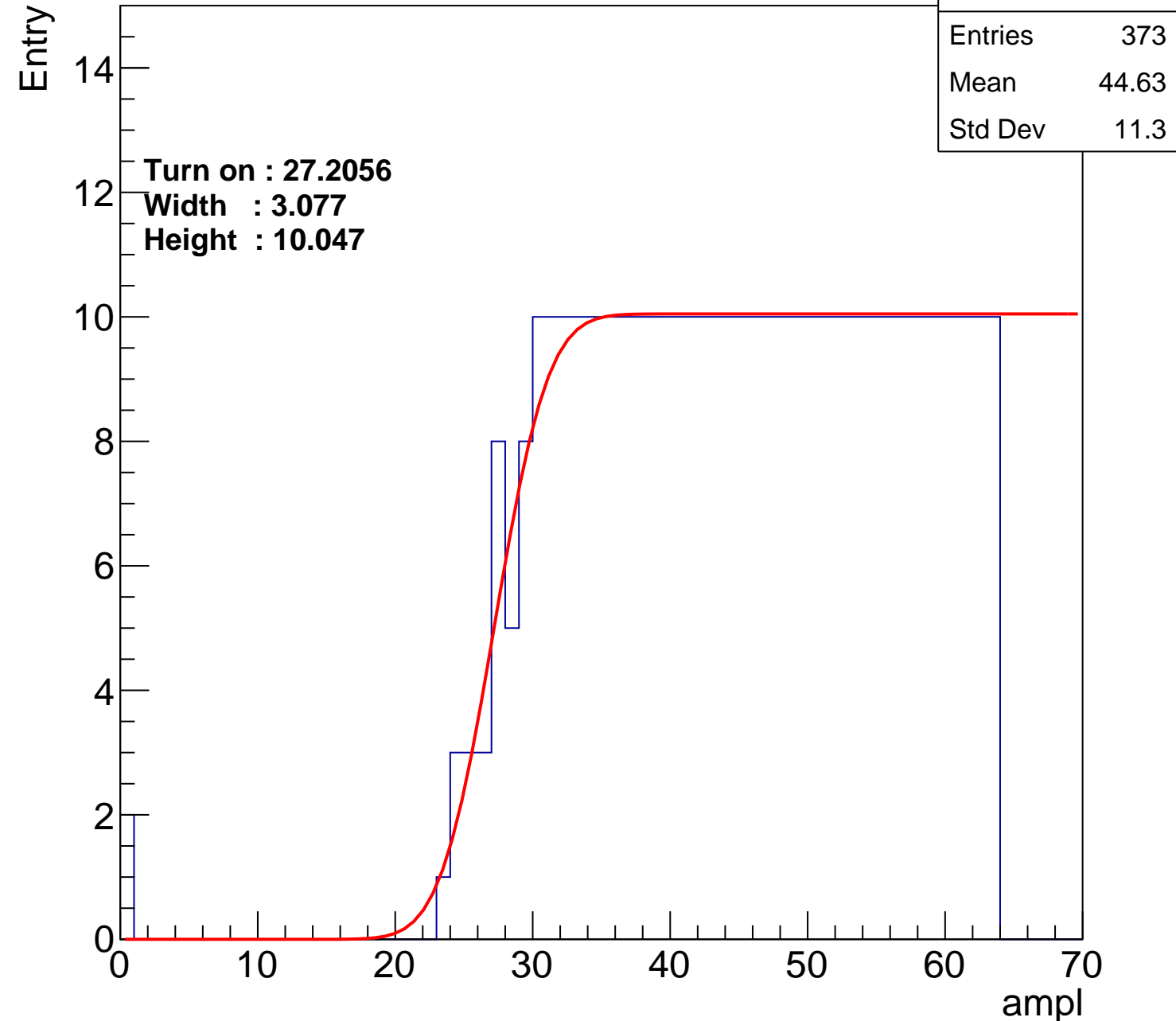
Width : 3.077

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch16

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.35 |
| Std Dev | 11.9 |

Turn on : 27.2894

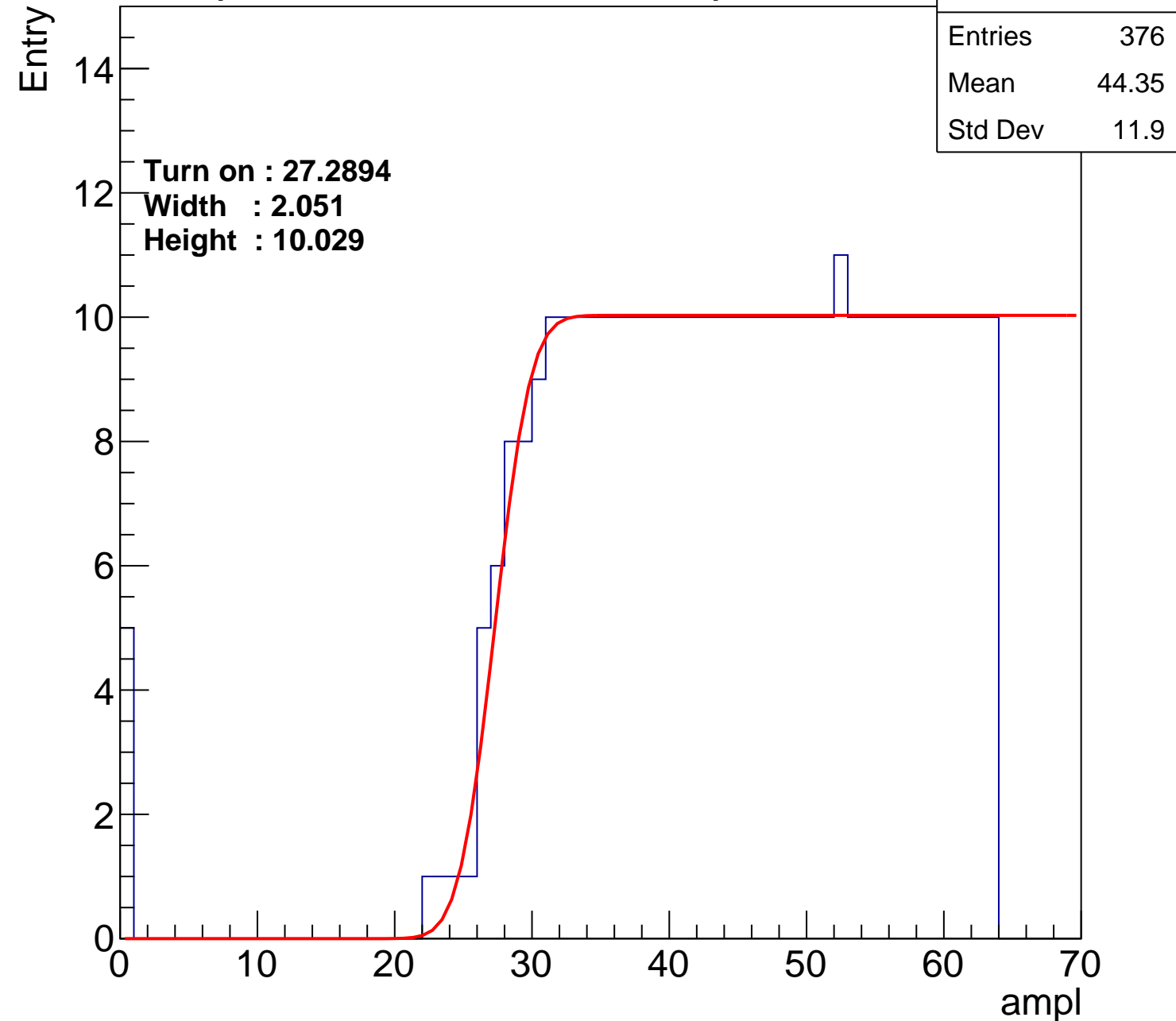
Width : 2.051

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch17

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.54 |
| Std Dev | 12.38 |

Turn on : 25.7766

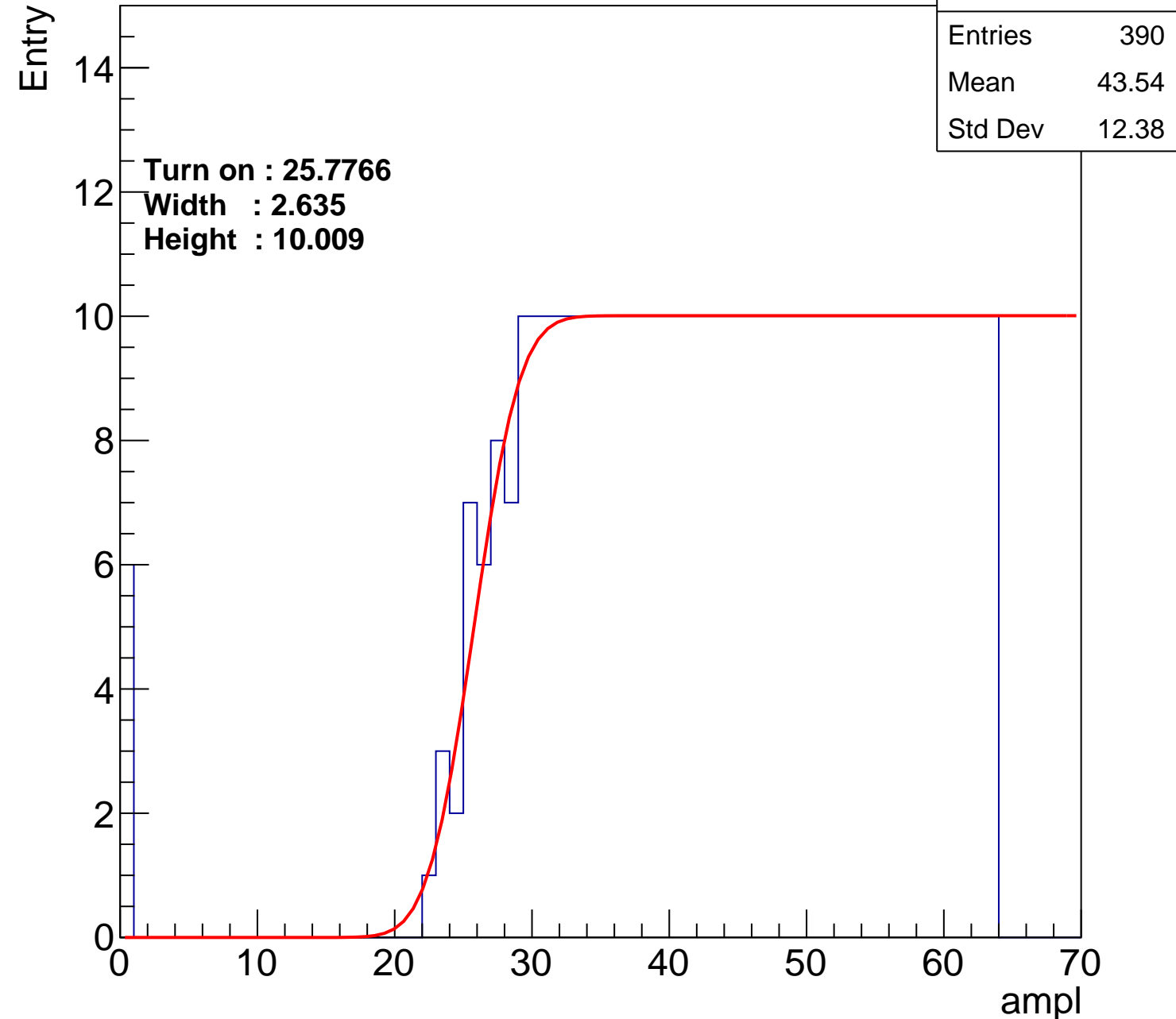
Width : 2.635

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch18

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.87 |
| Std Dev | 11.18 |

Turn on : 27.4484

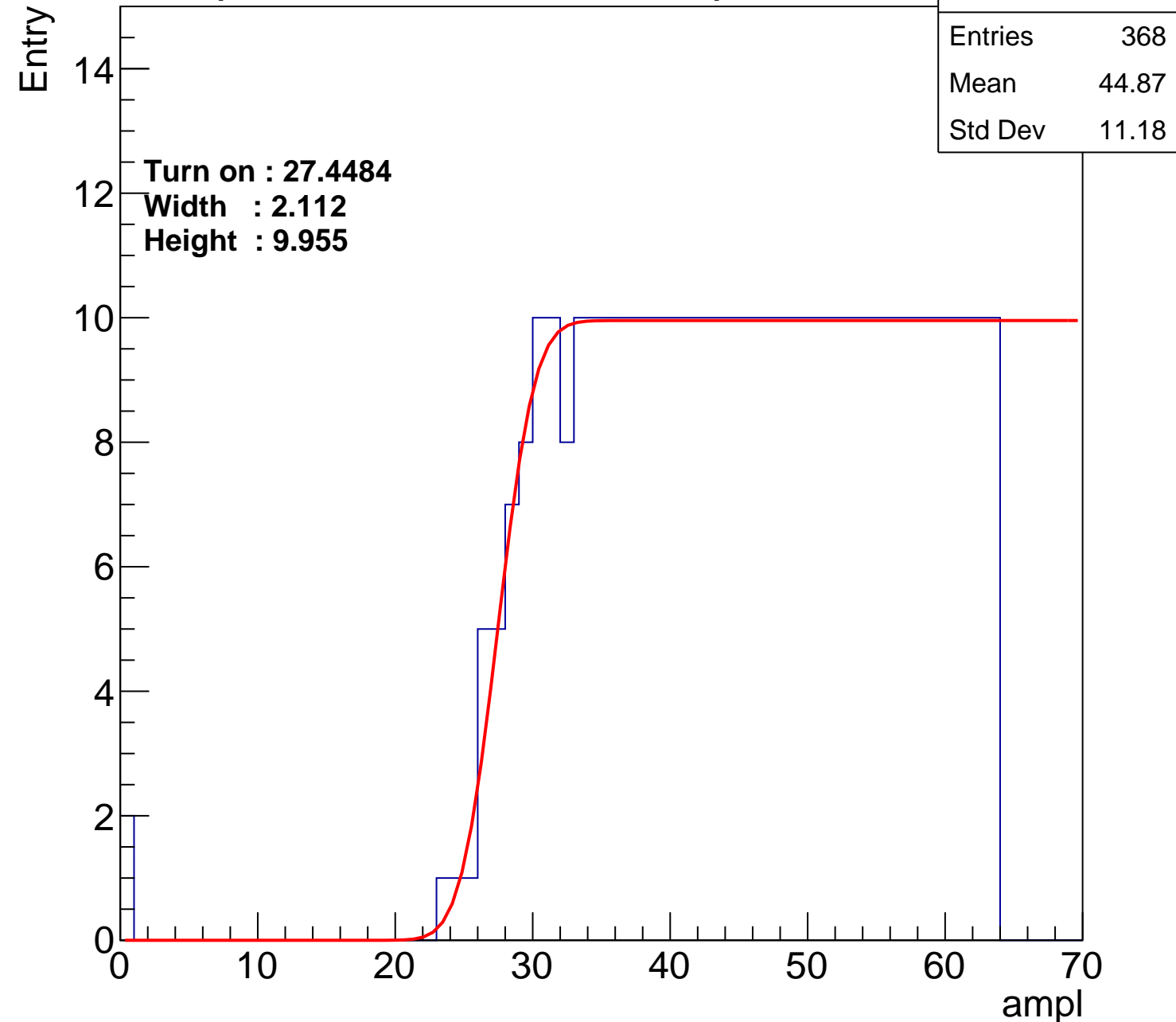
Width : 2.112

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch19

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.09 |
| Std Dev | 11.14 |

Turn on : 27.8208

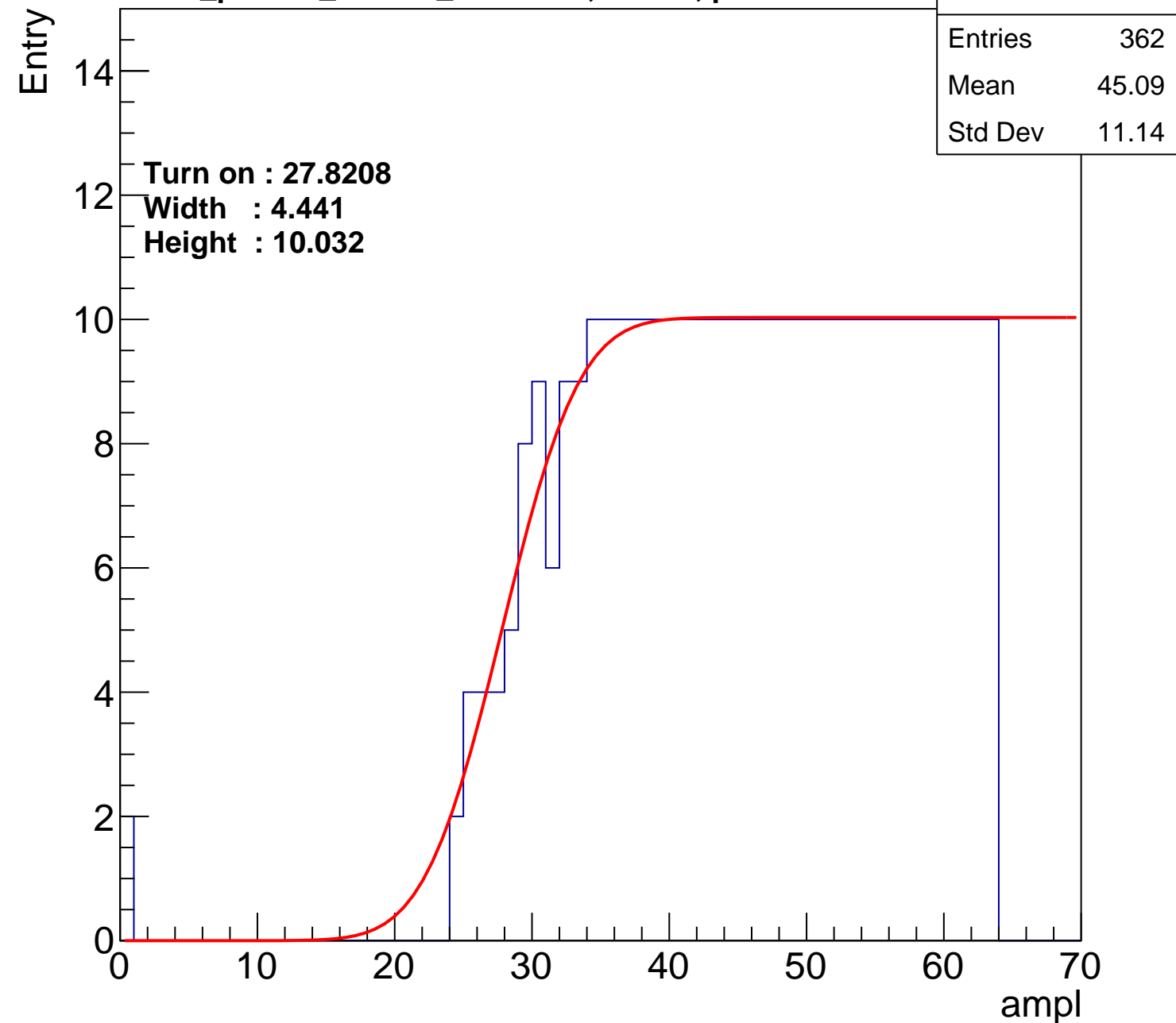
Width : 4.441

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch20

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.41 |
| Std Dev | 11.75 |

Turn on : 27.0818

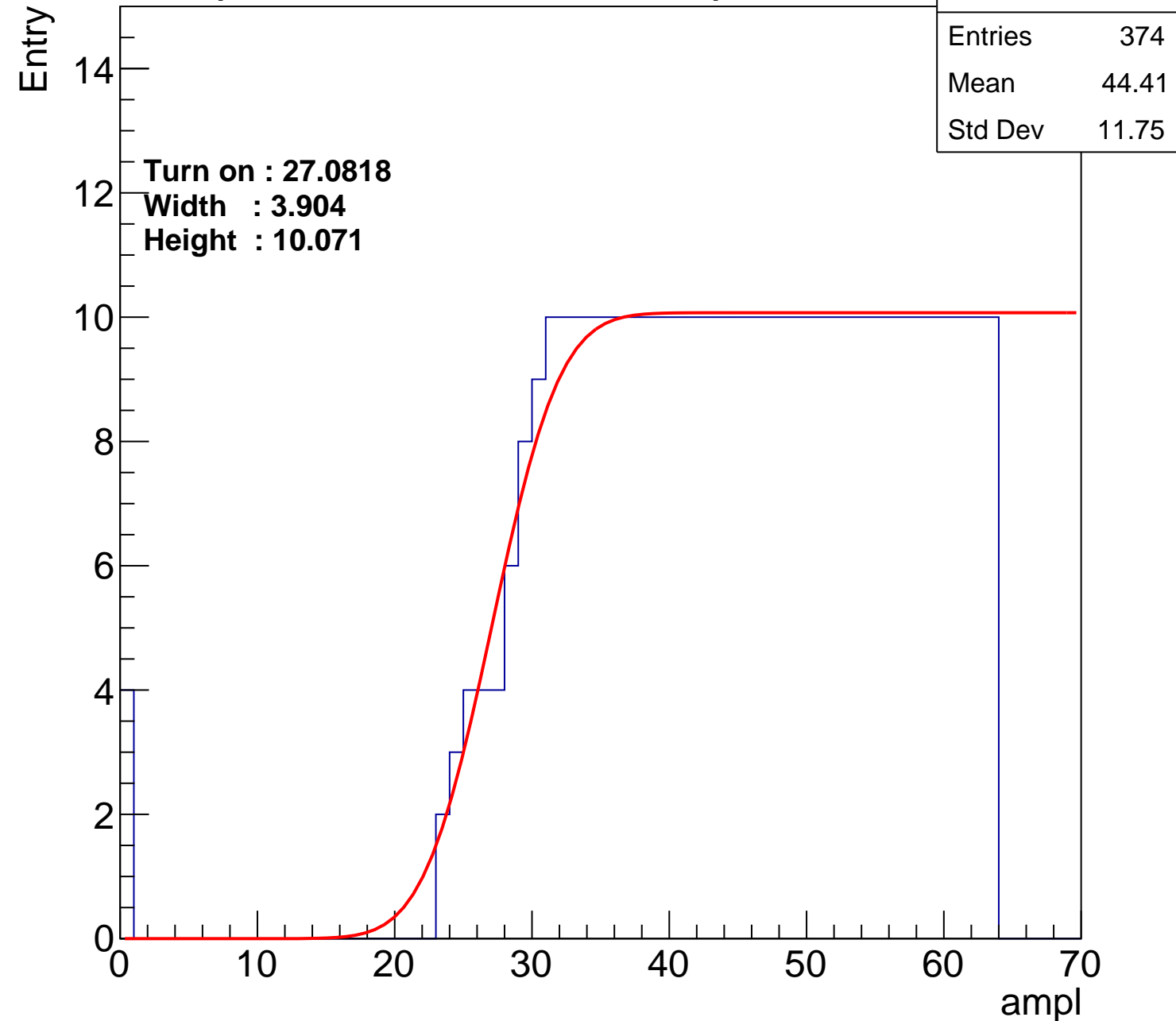
Width : 3.904

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch21

calib_packv5_042523_0143.root, FC#13, port D2

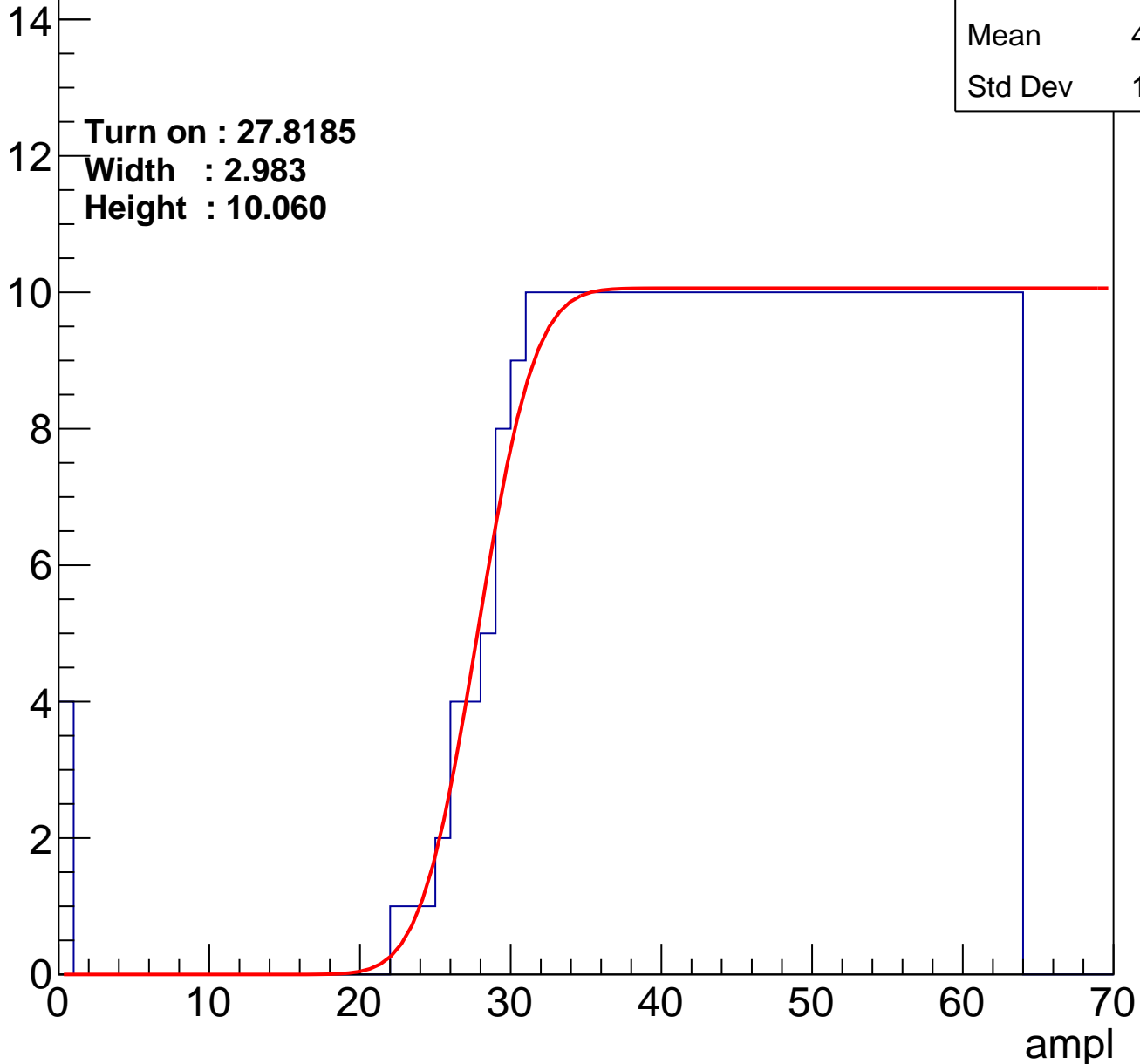
| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.67 |
| Std Dev | 11.62 |

Turn on : 27.8185

Width : 2.983

Height : 10.060

Entry



B1L003S, U26-ch22

calib_packv5_042523_0143.root, FC#13, port D2

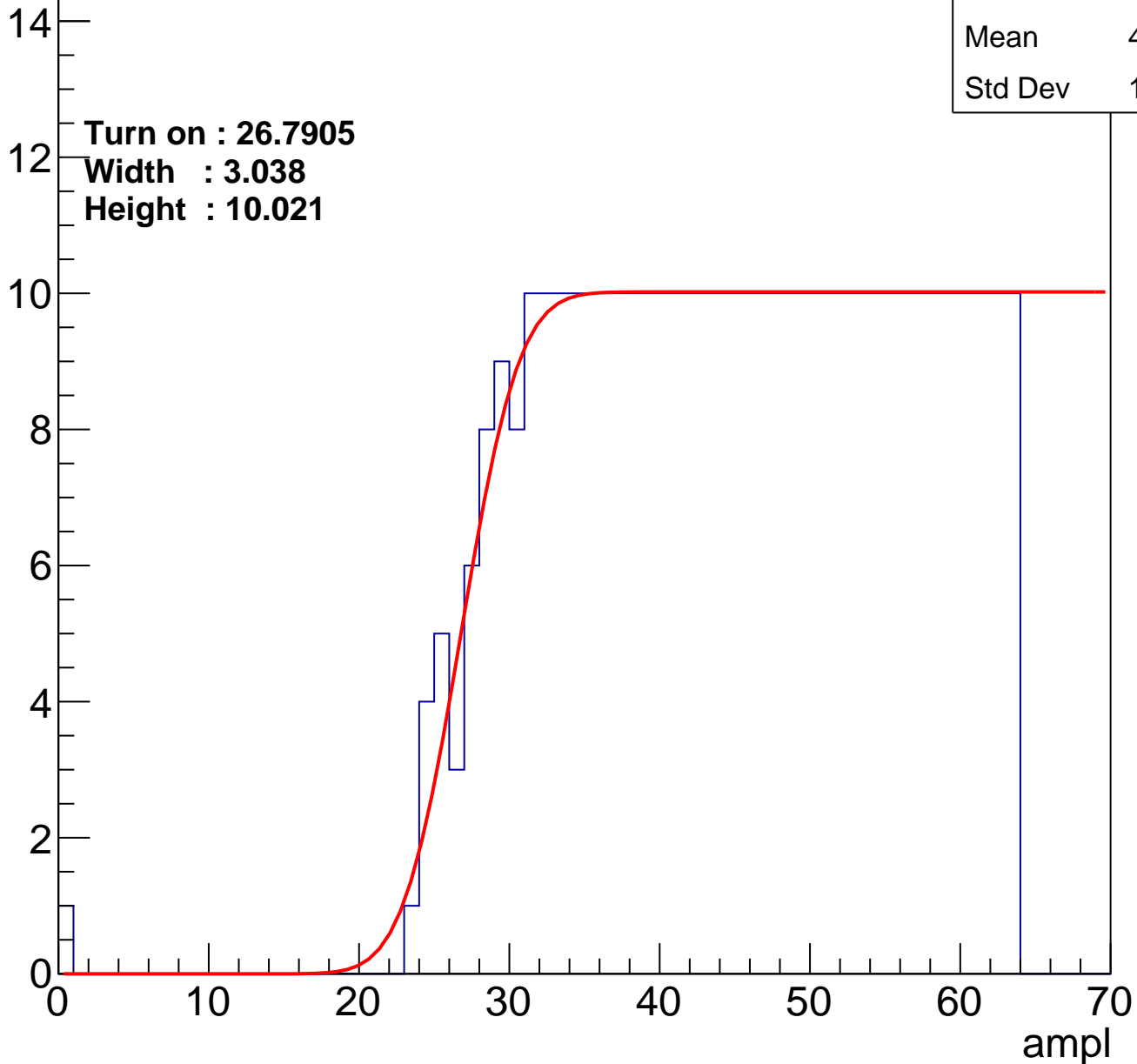
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.58 |
| Std Dev | 11.18 |

Turn on : 26.7905

Width : 3.038

Height : 10.021

Entry



B1L003S, U26-ch23

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.73 |
| Std Dev | 11.75 |

Turn on : 27.7360

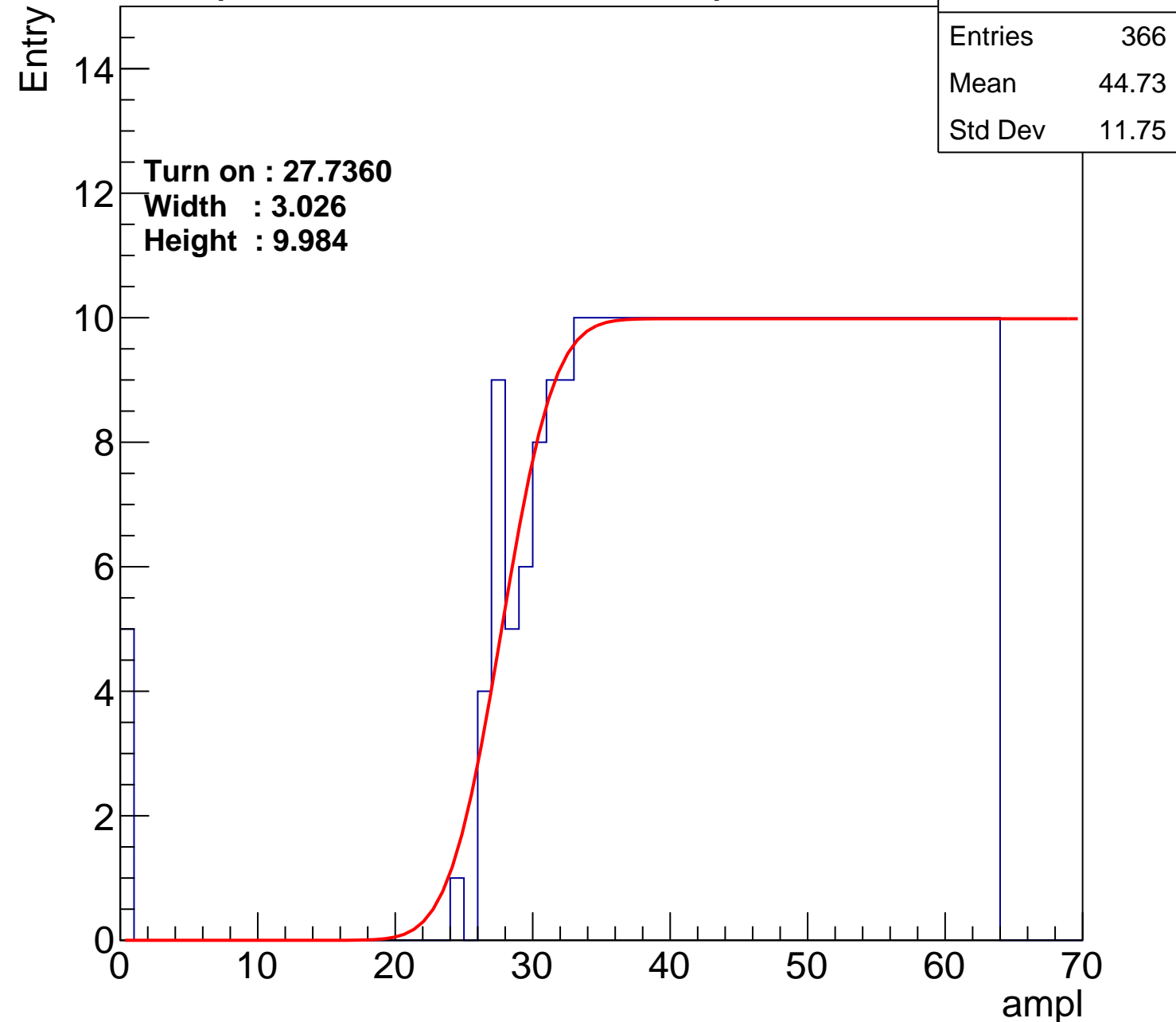
Width : 3.026

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch24

calib_packv5_042523_0143.root, FC#13, port D2

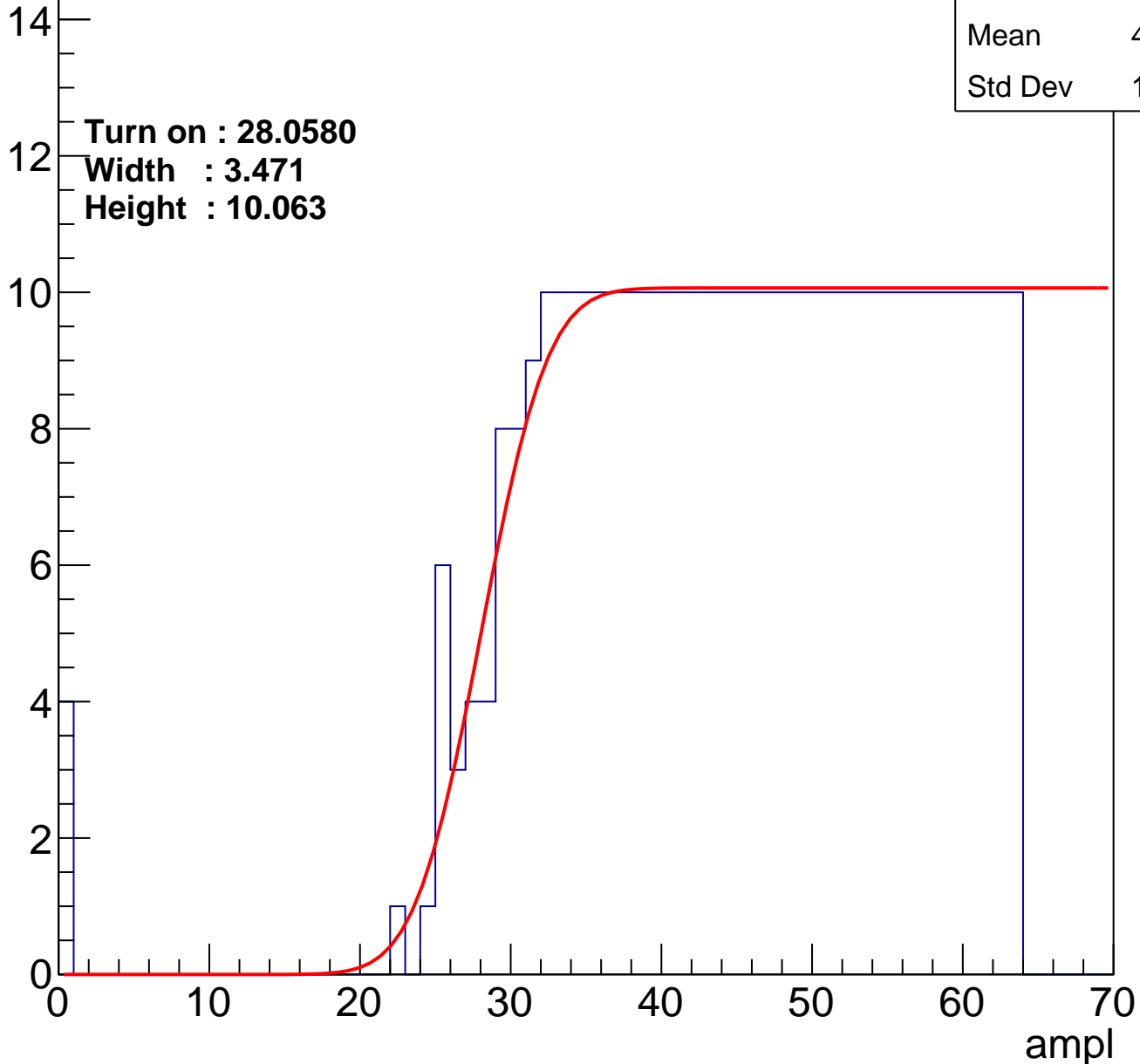
| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.69 |
| Std Dev | 11.64 |

Turn on : 28.0580

Width : 3.471

Height : 10.063

Entry



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.9816
Width : 3.028
Height : 10.064



B1L003S, U26-ch26

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.31 |
| Std Dev | 11.49 |

Turn on : 26.3699

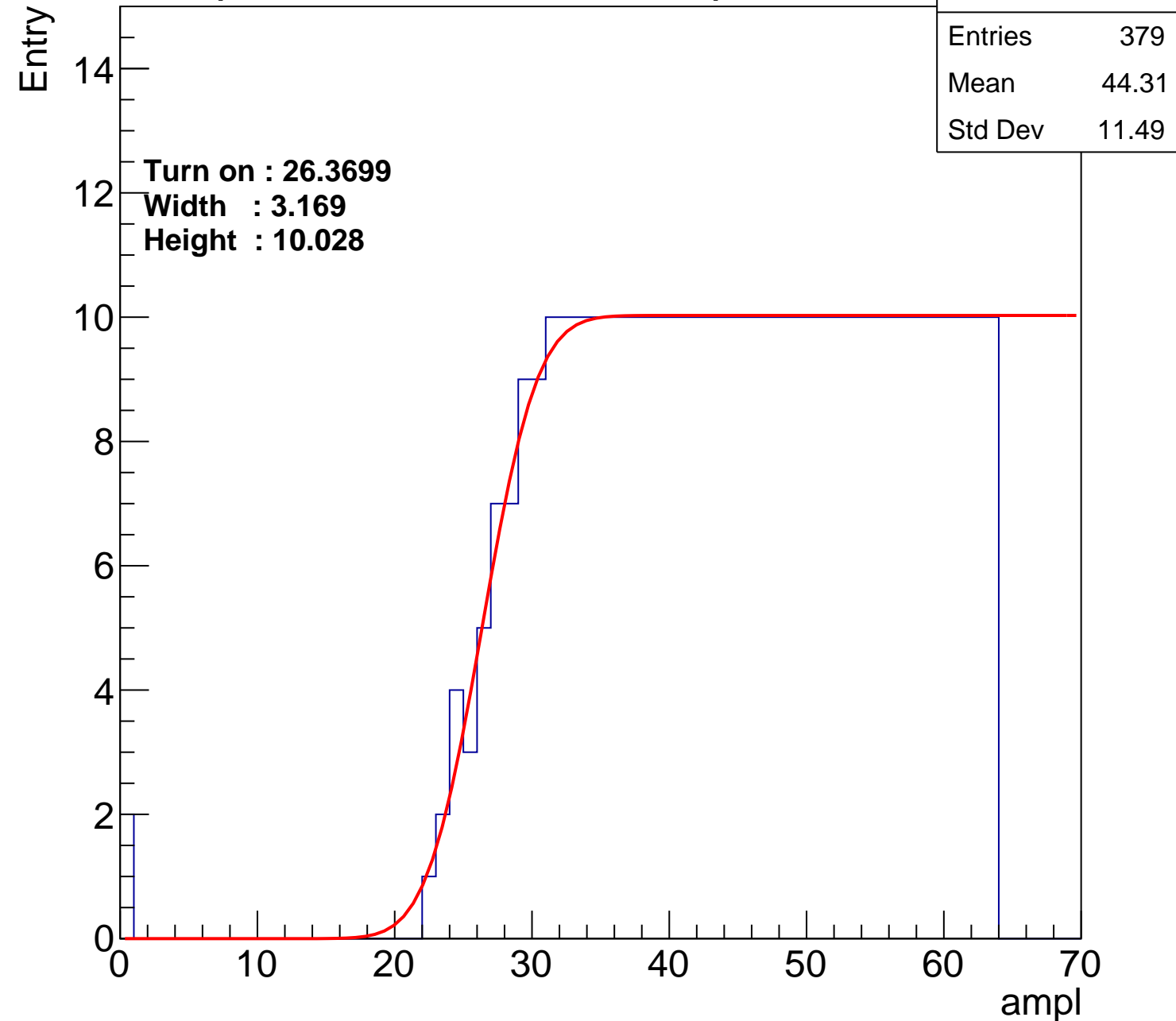
Width : 3.169

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch27

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.46 |
| Std Dev | 11.55 |

Turn on : 26.8584

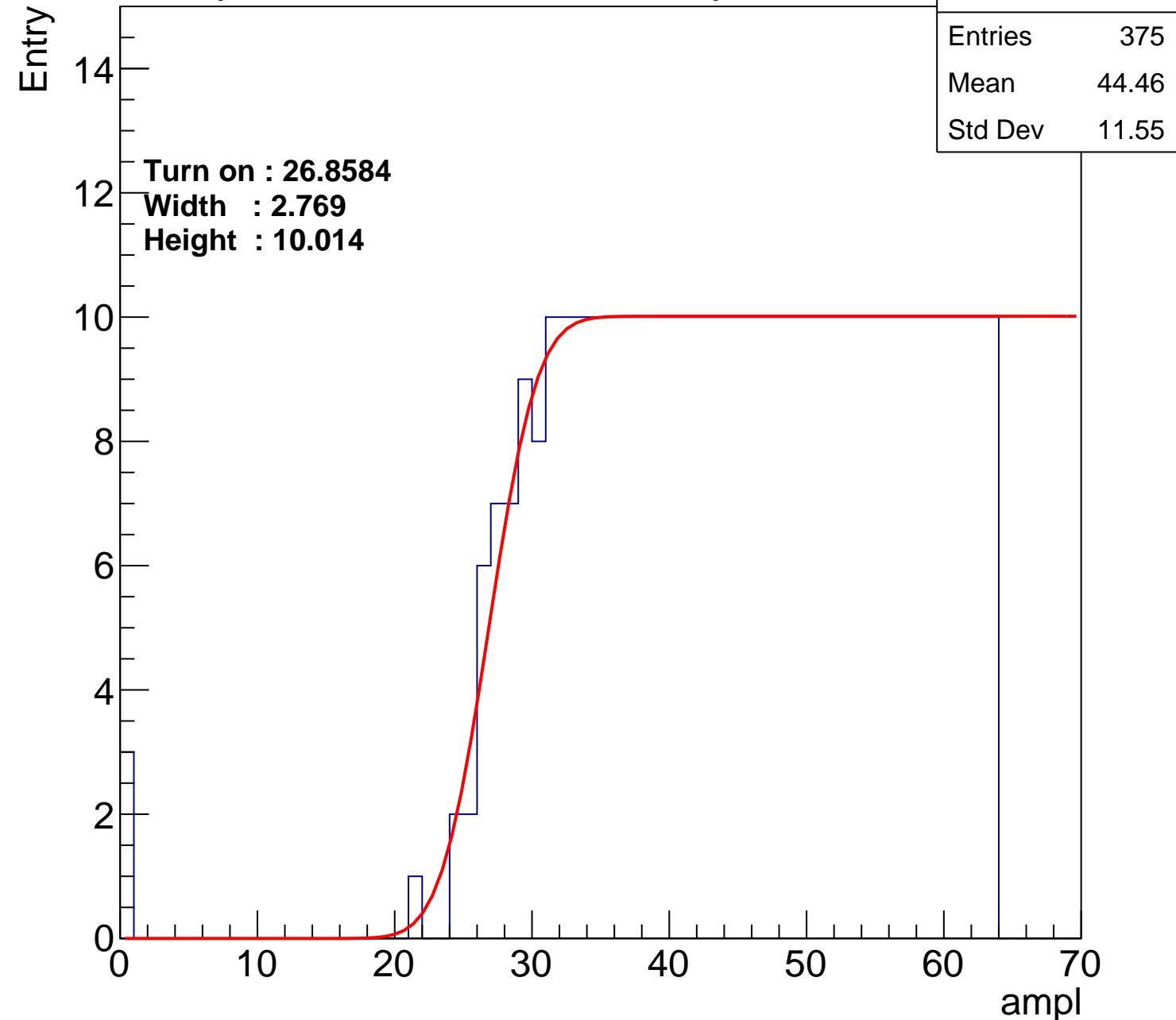
Width : 2.769

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch28

calib_packv5_042523_0143.root, FC#13, port D2

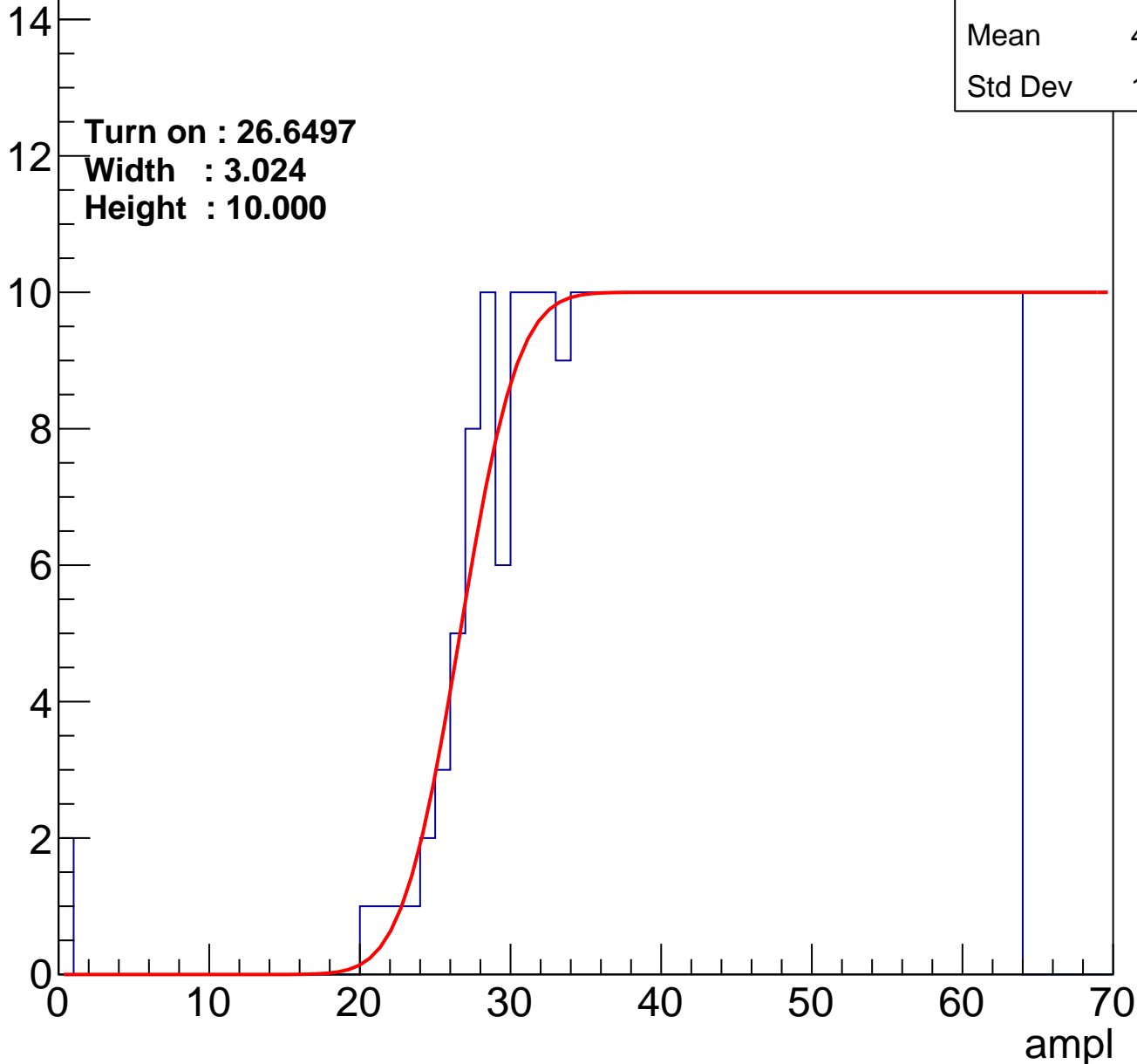
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.29 |
| Std Dev | 11.52 |

Turn on : 26.6497

Width : 3.024

Height : 10.000

Entry



B1L003S, U26-ch29

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.39 |
| Std Dev | 11.6 |

Turn on : 26.9914

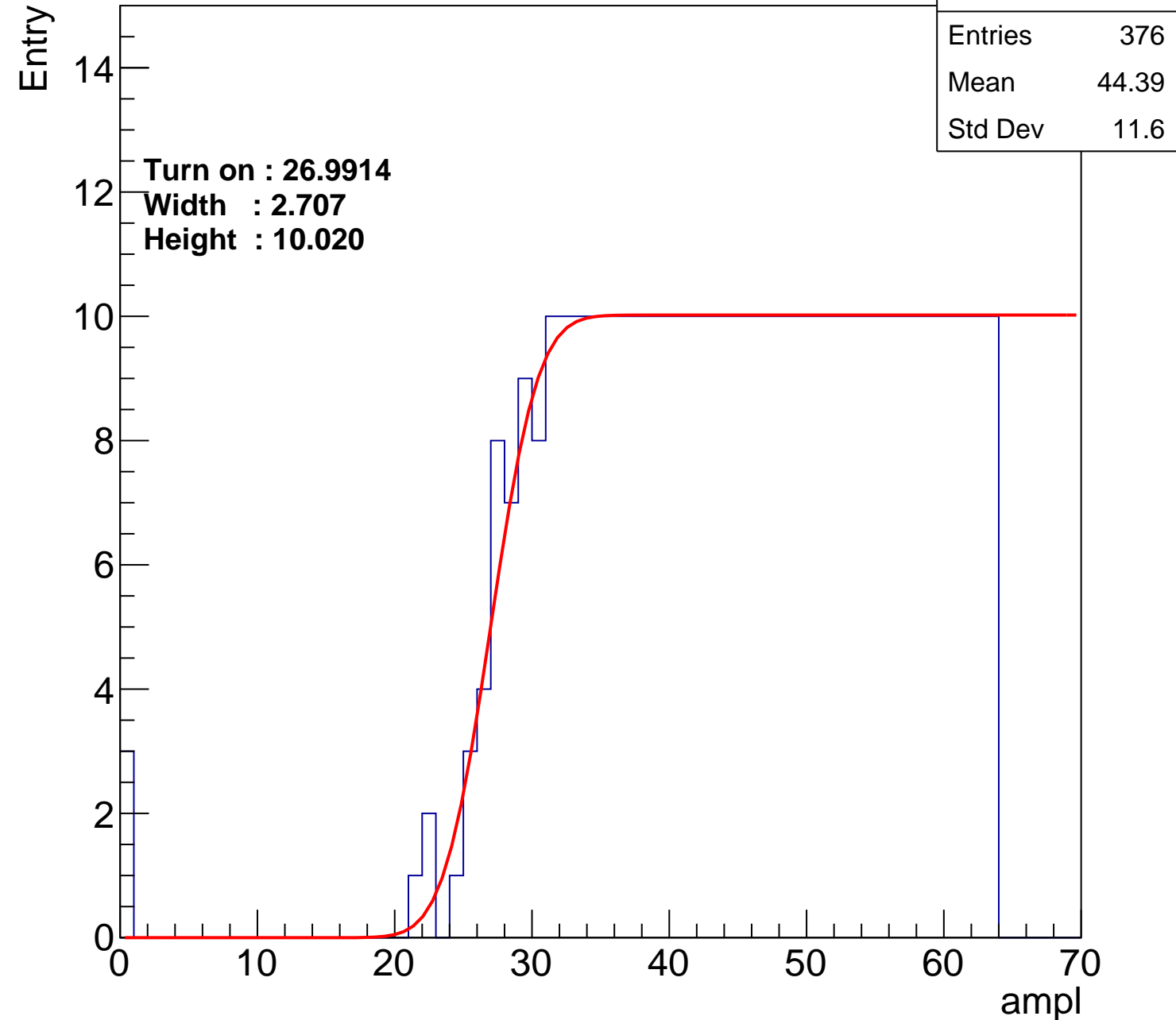
Width : 2.707

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch30

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.19 |
| Std Dev | 11.43 |

Turn on : 25.9983

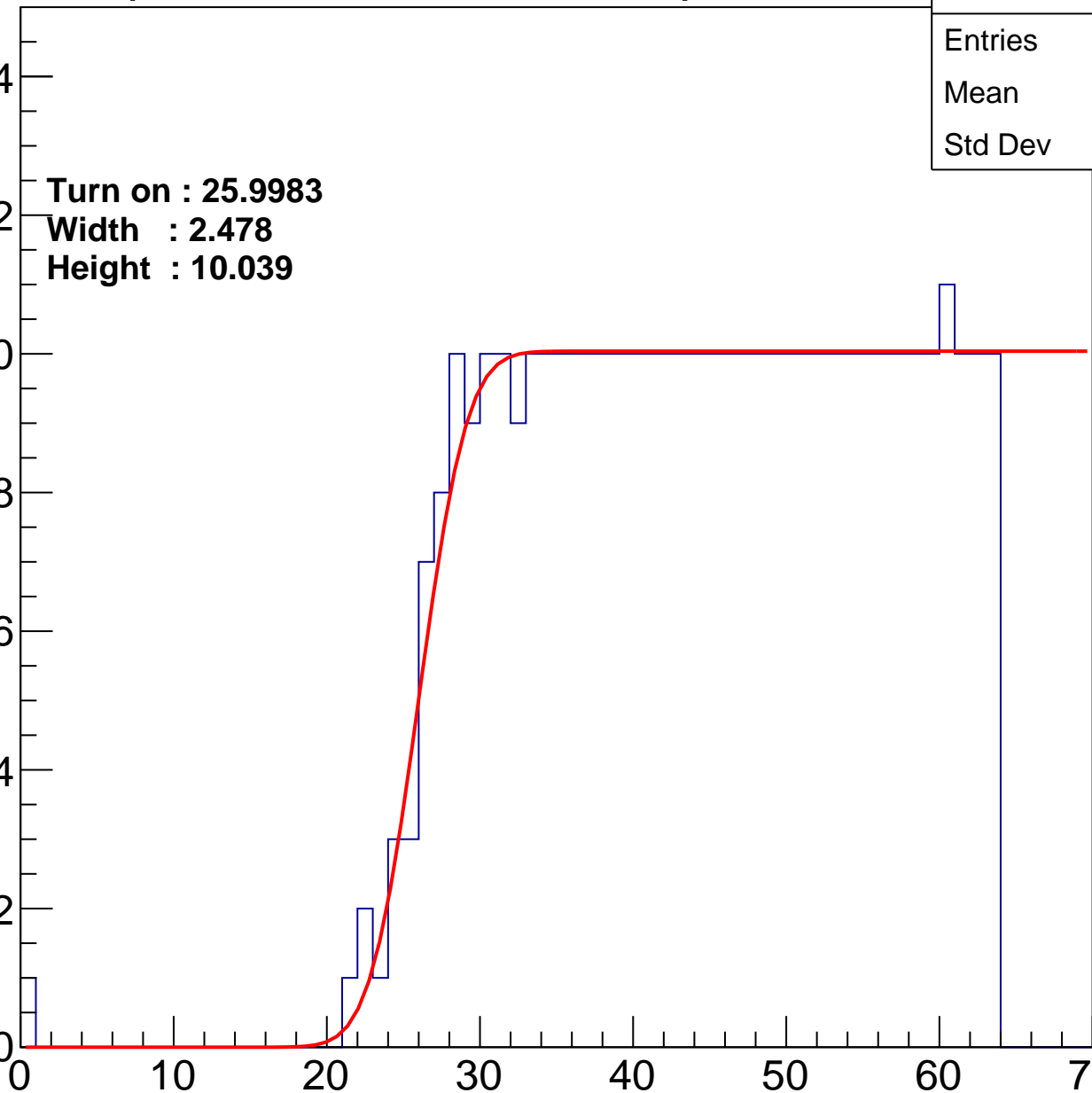
Width : 2.478

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch31

calib_packv5_042523_0143.root, FC#13, port D2

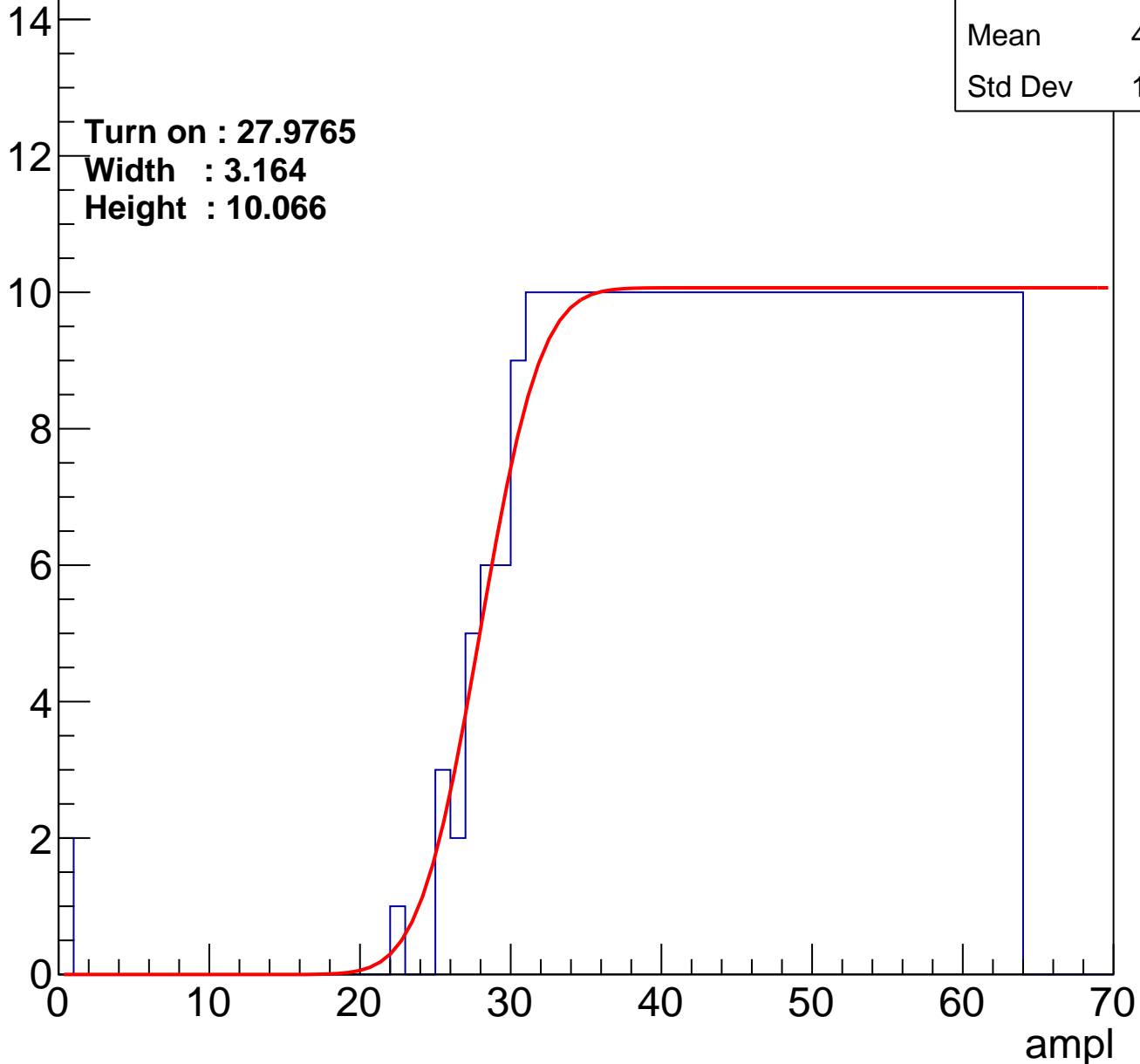
| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.07 |
| Std Dev | 11.08 |

Turn on : 27.9765

Width : 3.164

Height : 10.066

Entry



B1L003S, U26-ch32

calib_packv5_042523_0143.root, FC#13, port D2

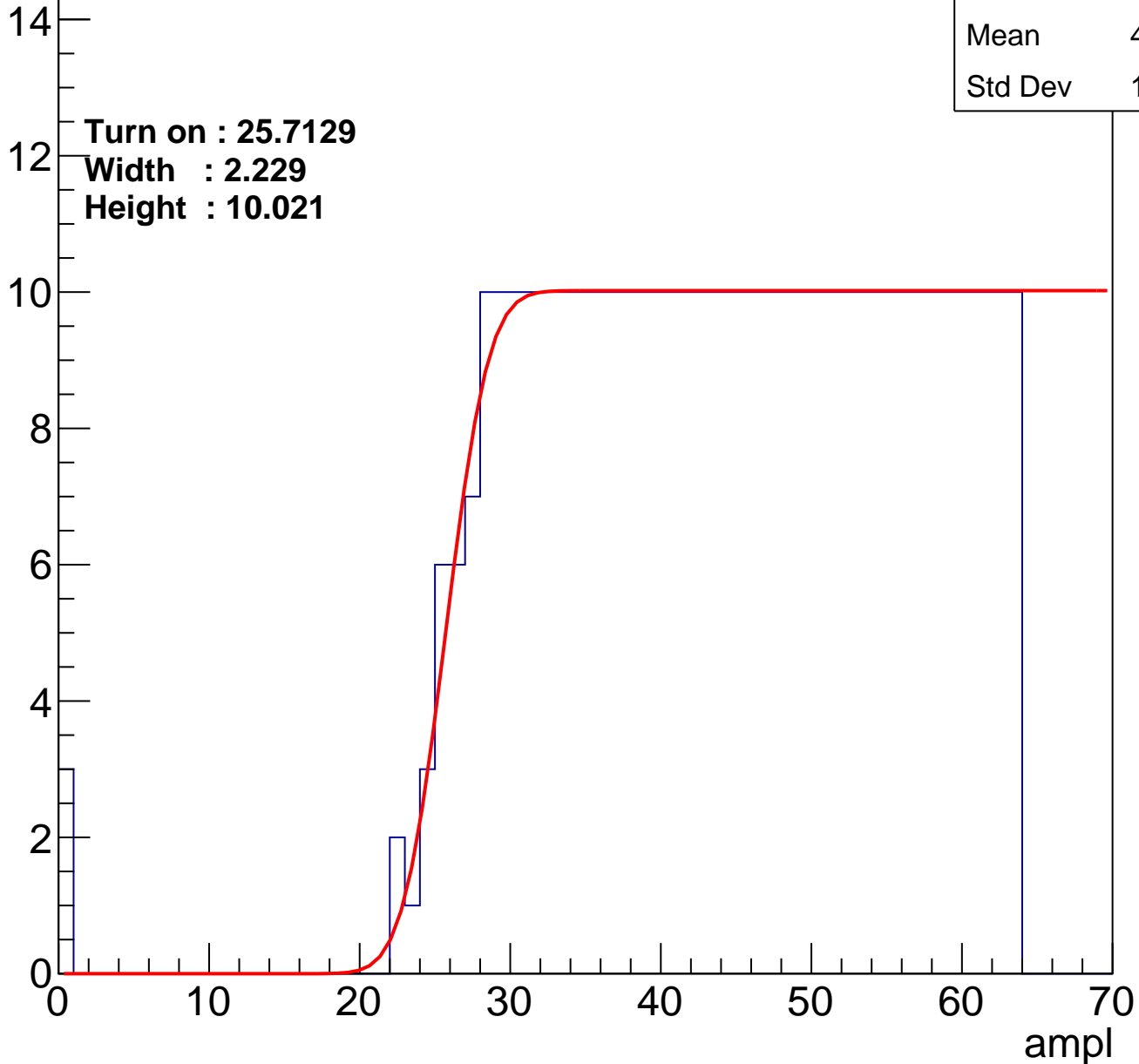
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.85 |
| Std Dev | 11.82 |

Turn on : 25.7129

Width : 2.229

Height : 10.021

Entry



B1L003S, U26-ch33

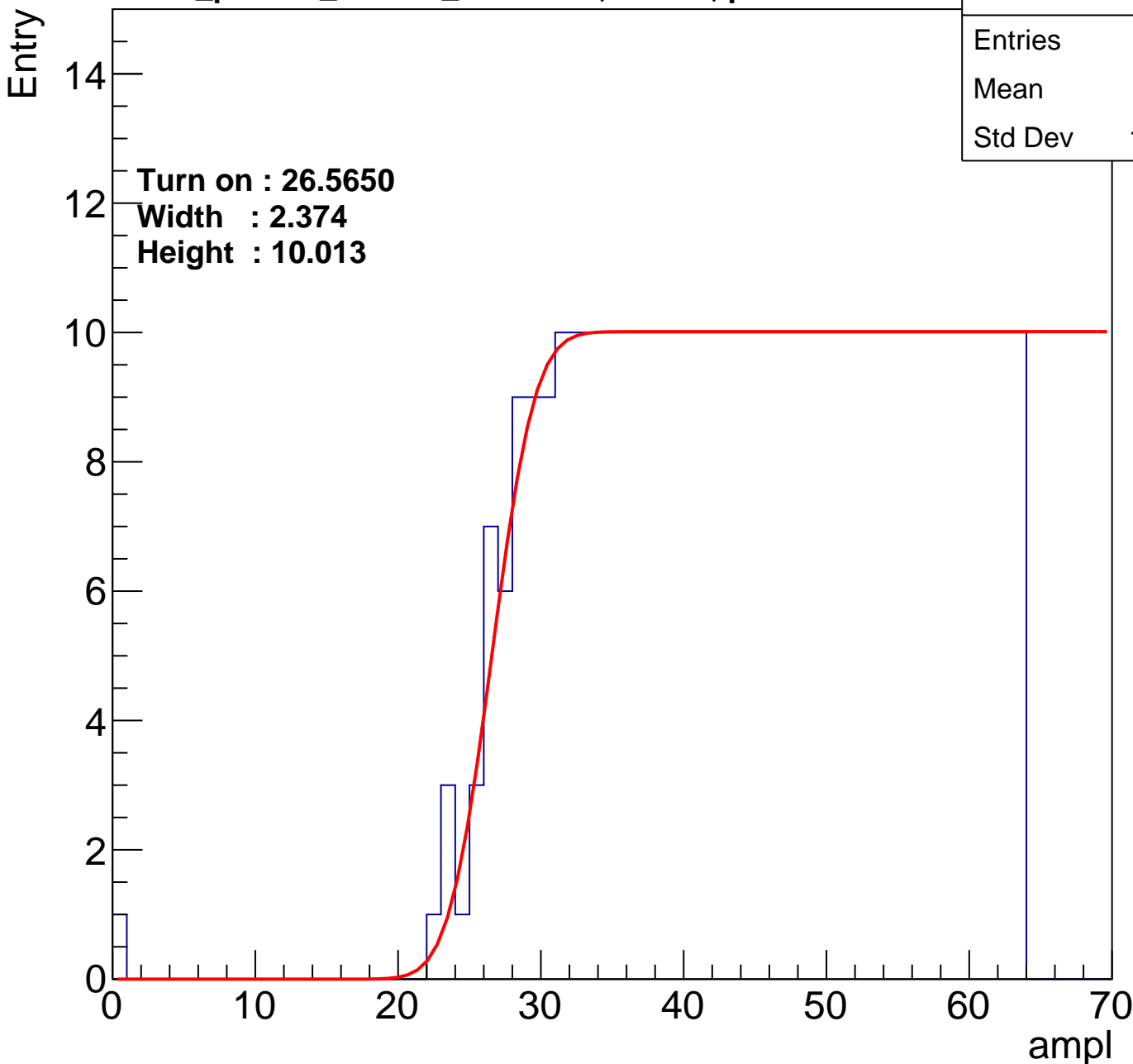
calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.4 |
| Std Dev | 11.27 |

Turn on : 26.5650

Width : 2.374

Height : 10.013



B1L003S, U26-ch34

calib_packv5_042523_0143.root, FC#13, port D2

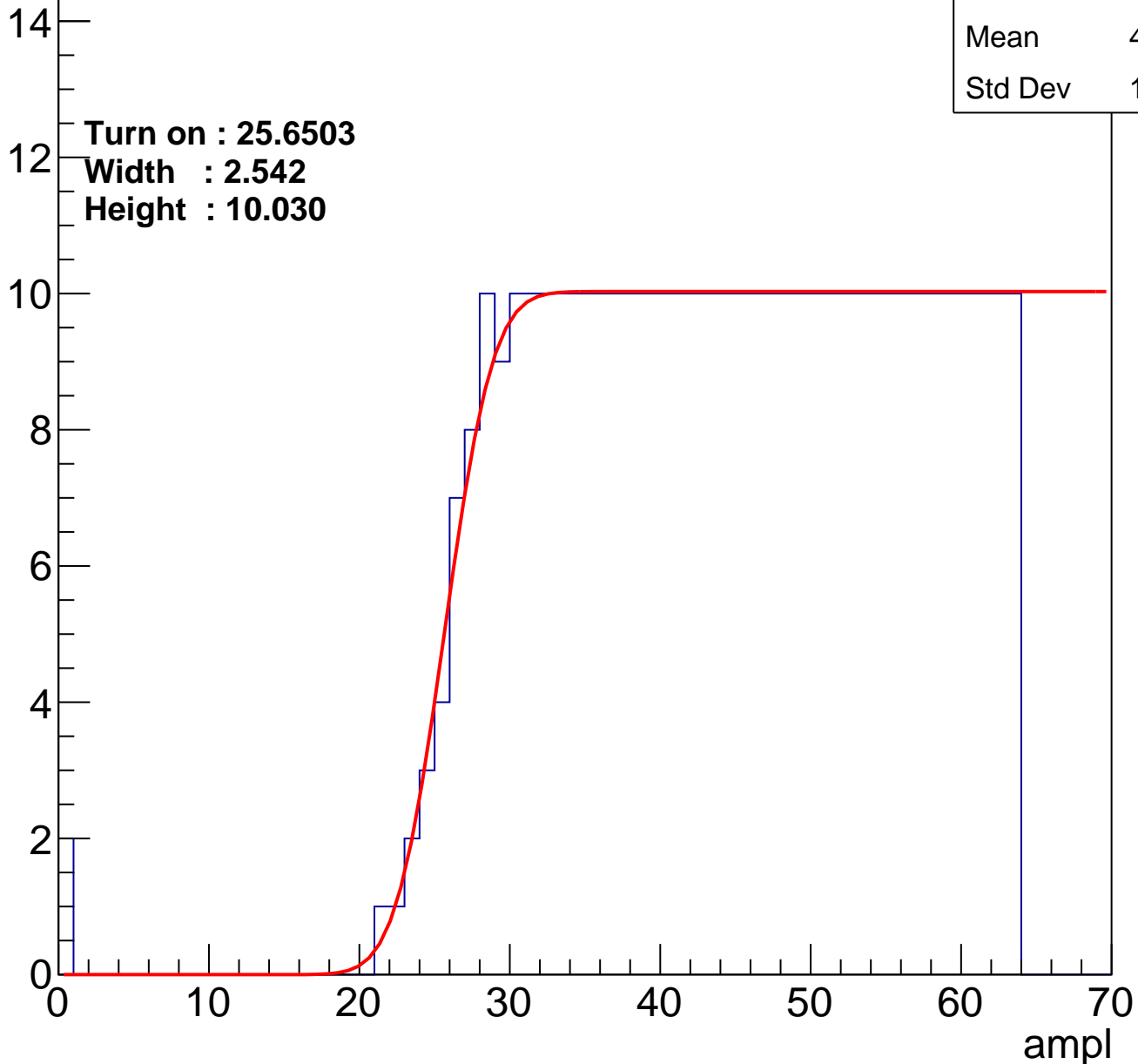
| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.95 |
| Std Dev | 11.64 |

Turn on : 25.6503

Width : 2.542

Height : 10.030

Entry



B1L003S, U26-ch35

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.21 |
| Std Dev | 11.36 |

Turn on : 28.7980

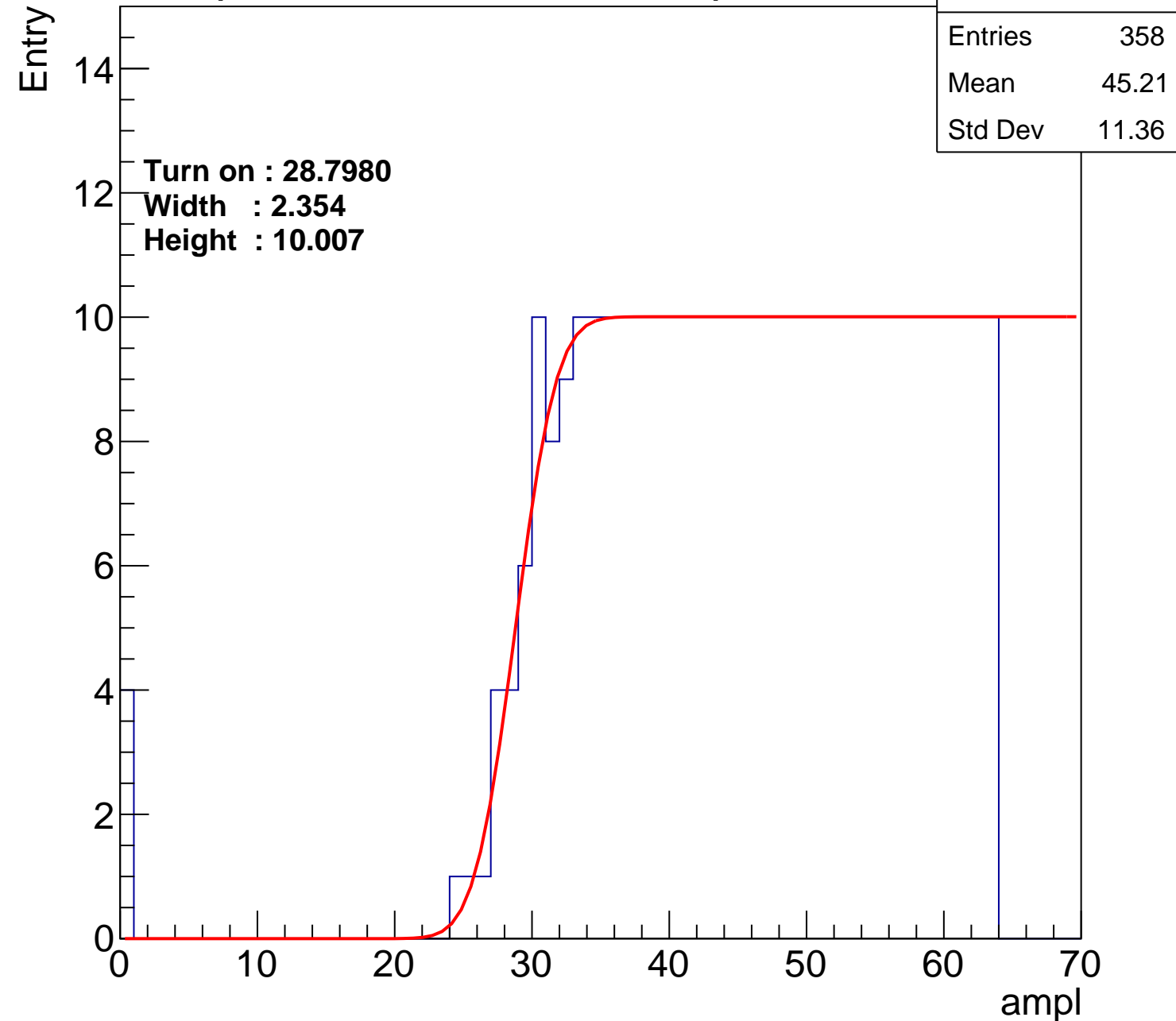
Width : 2.354

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch36

calib_packv5_042523_0143.root, FC#13, port D2

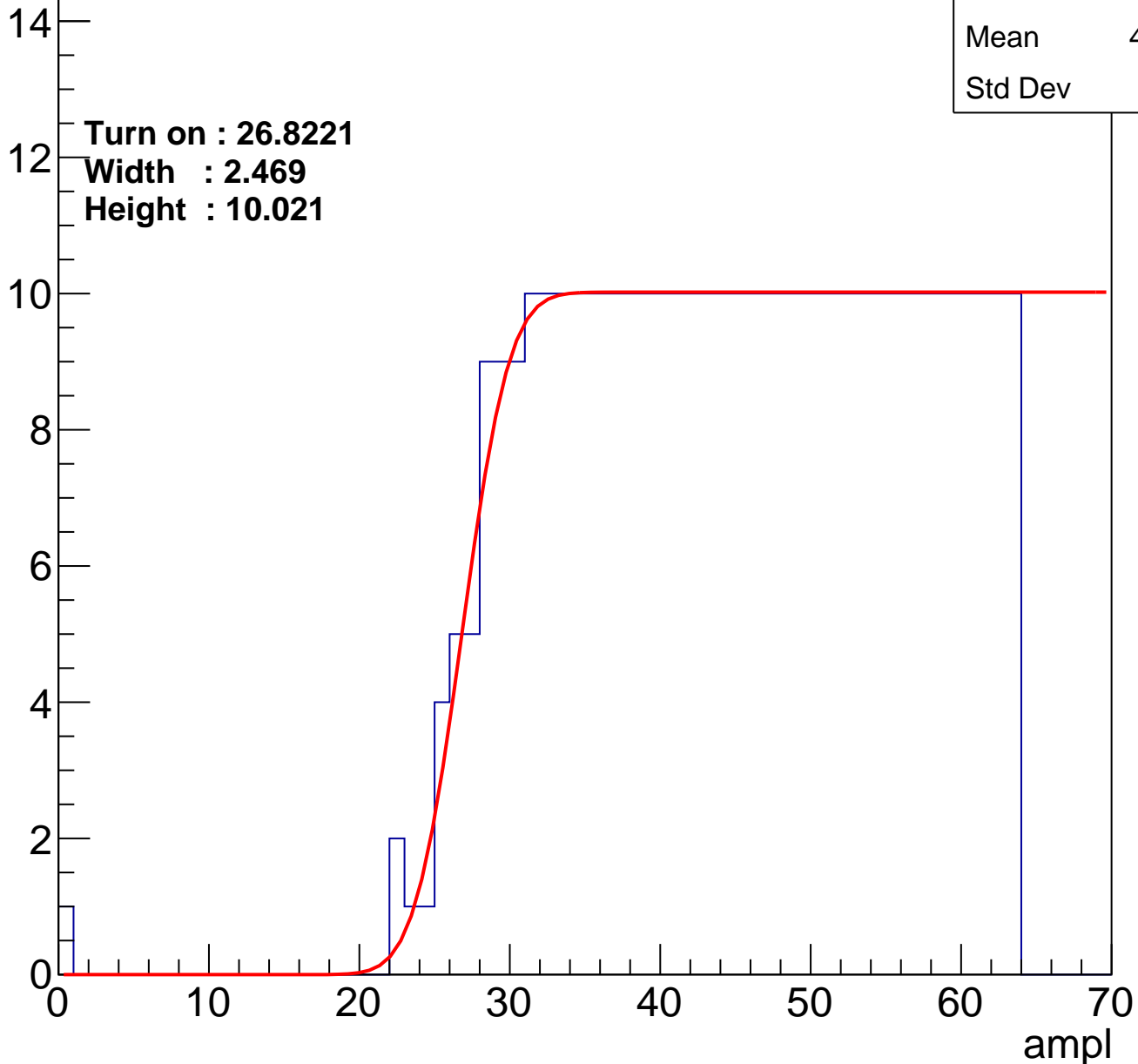
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.55 |
| Std Dev | 11.2 |

Turn on : 26.8221

Width : 2.469

Height : 10.021

Entry



B1L003S, U26-ch37

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.34 |
| Std Dev | 11.77 |

Turn on : 26.9714

Width : 2.993

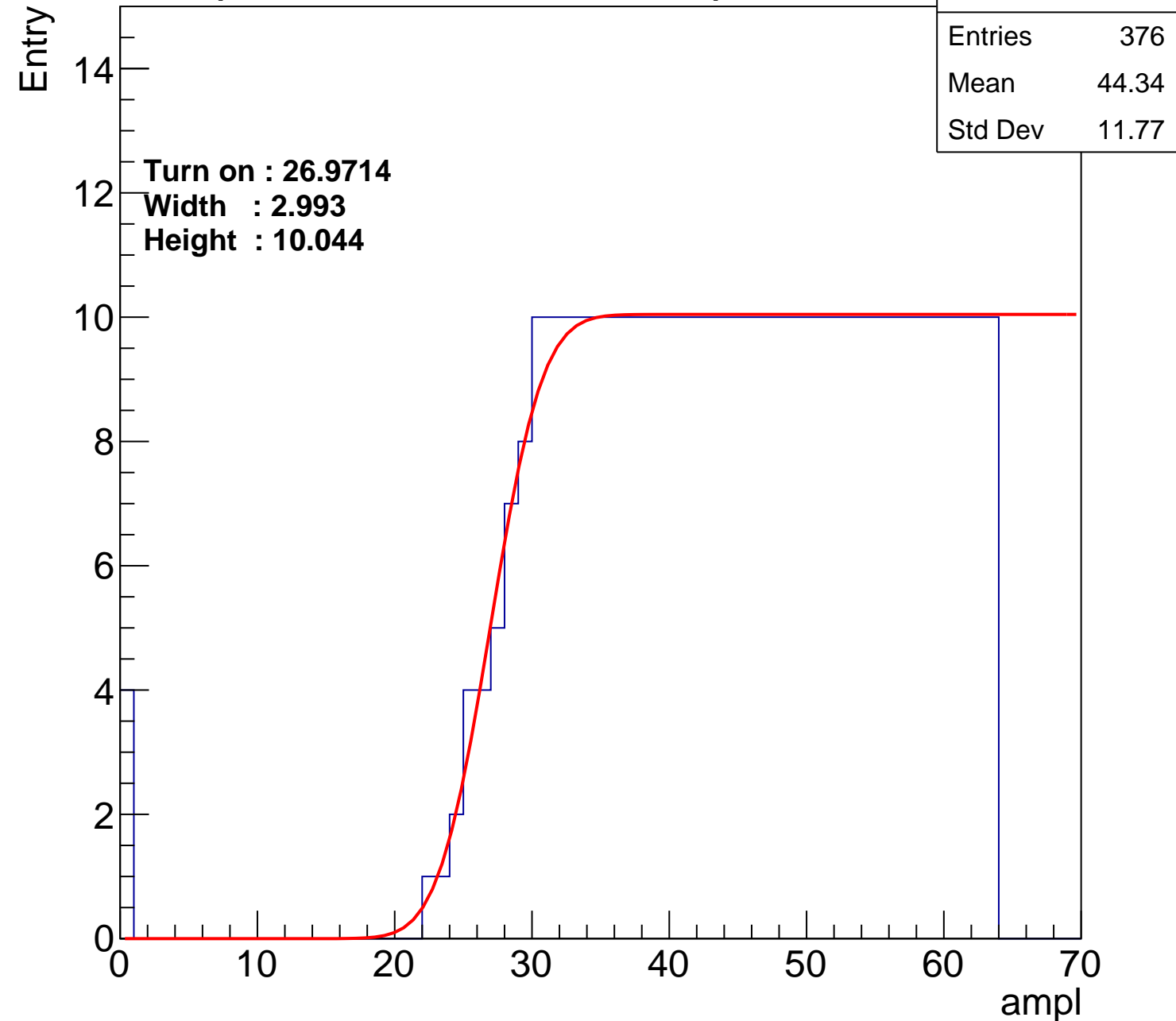
Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U26-ch38

calib_packv5_042523_0143.root, FC#13, port D2

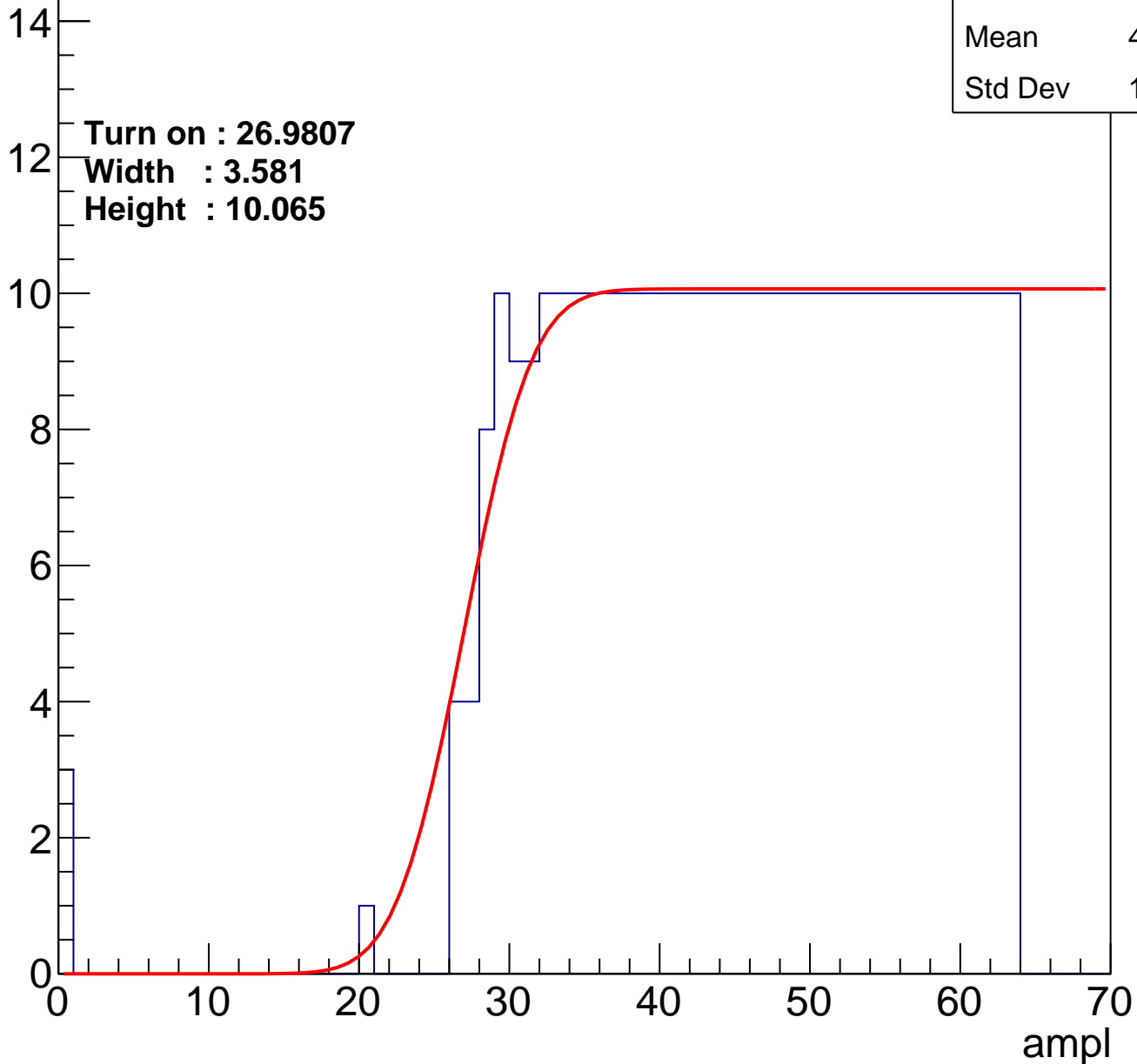
| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.82 |
| Std Dev | 11.35 |

Turn on : 26.9807

Width : 3.581

Height : 10.065

Entry



B1L003S, U26-ch39

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.76 |
| Std Dev | 11.25 |

Turn on : 27.4391

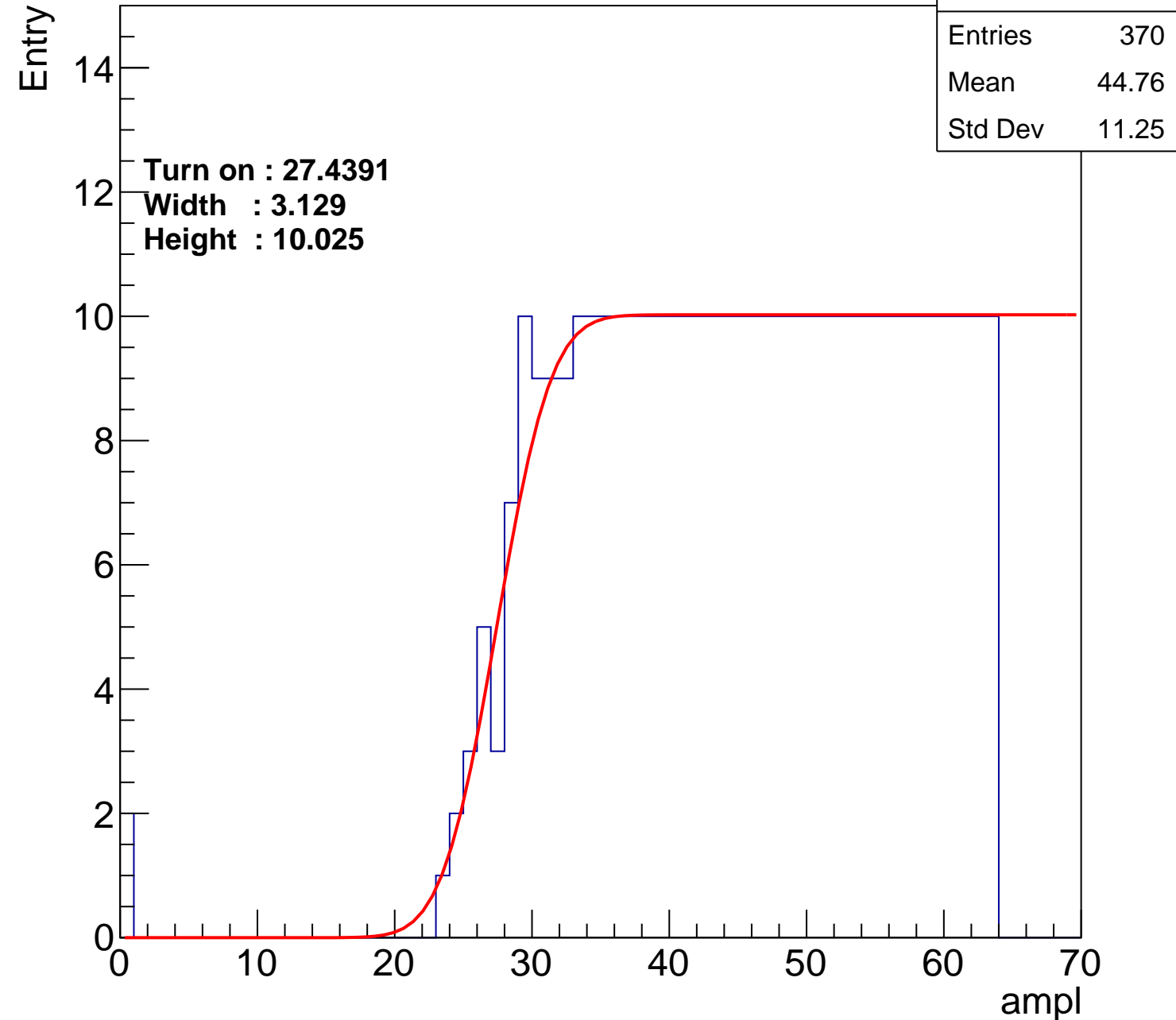
Width : 3.129

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.1627
Width : 2.905
Height : 9.996



B1L003S, U26-ch41

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.92 |
| Std Dev | 11.01 |

Turn on : 27.8627

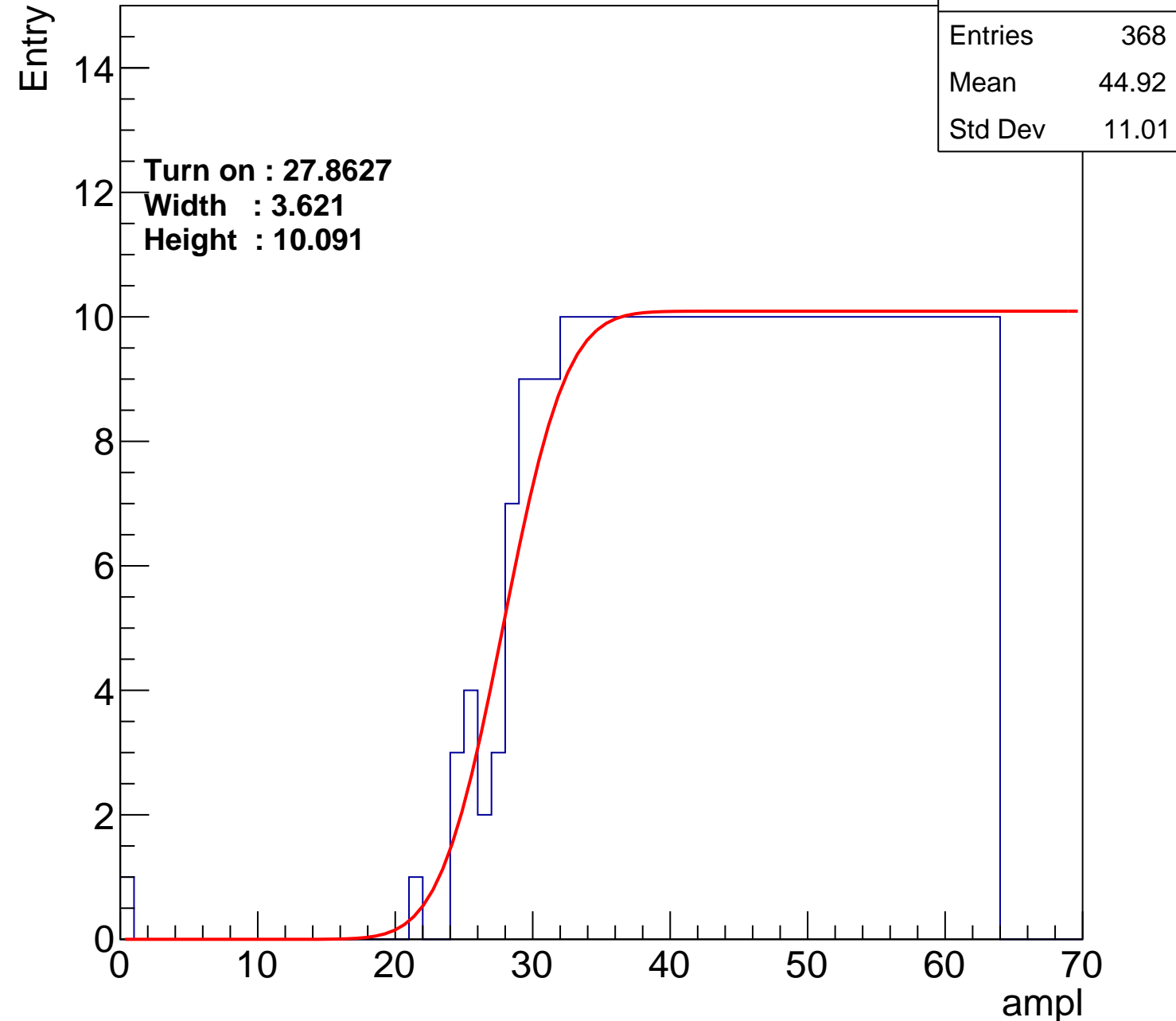
Width : 3.621

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch42

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.13 |
| Std Dev | 11.57 |

Turn on : 26.3252

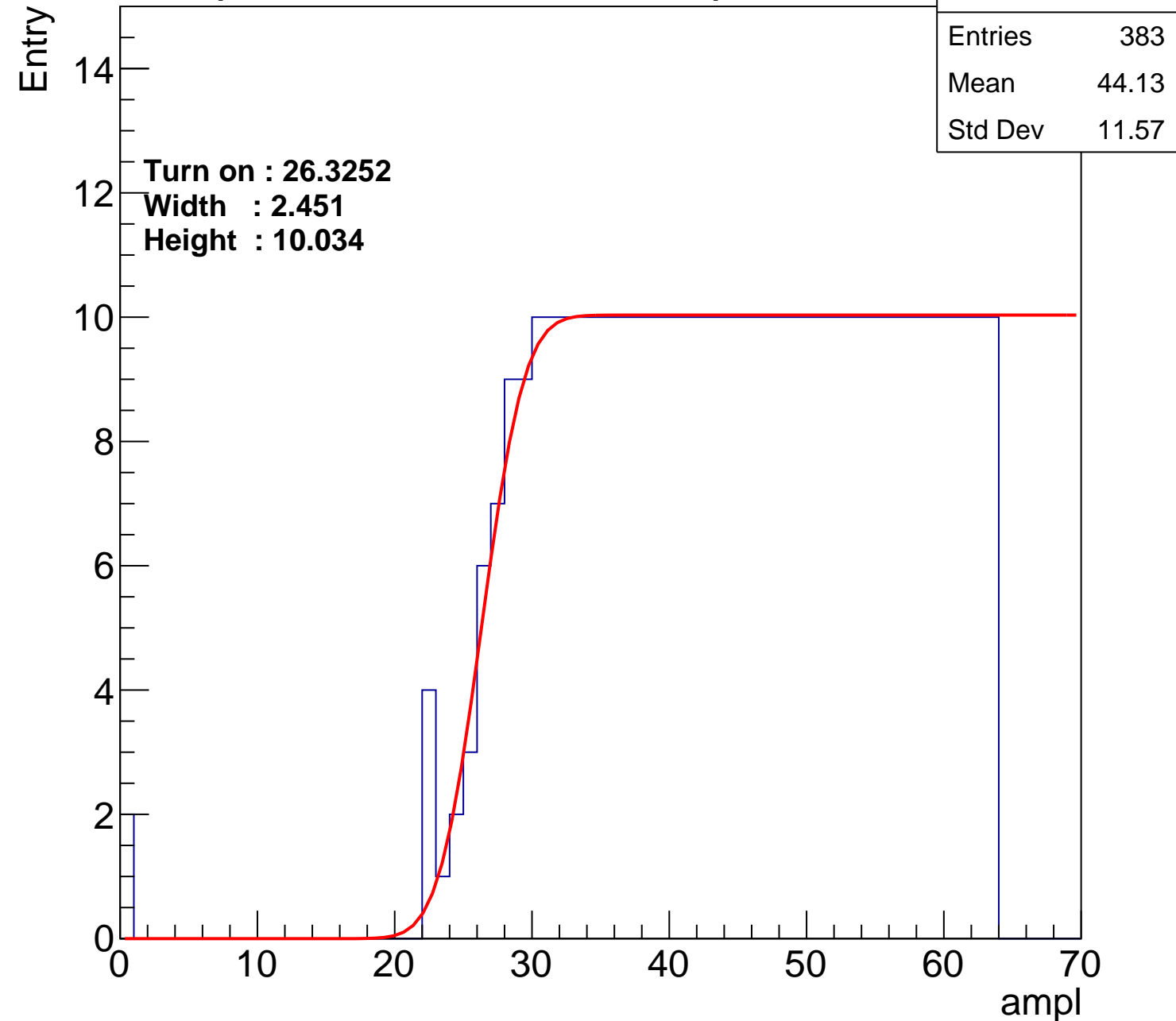
Width : 2.451

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch43

calib_packv5_042523_0143.root, FC#13, port D2

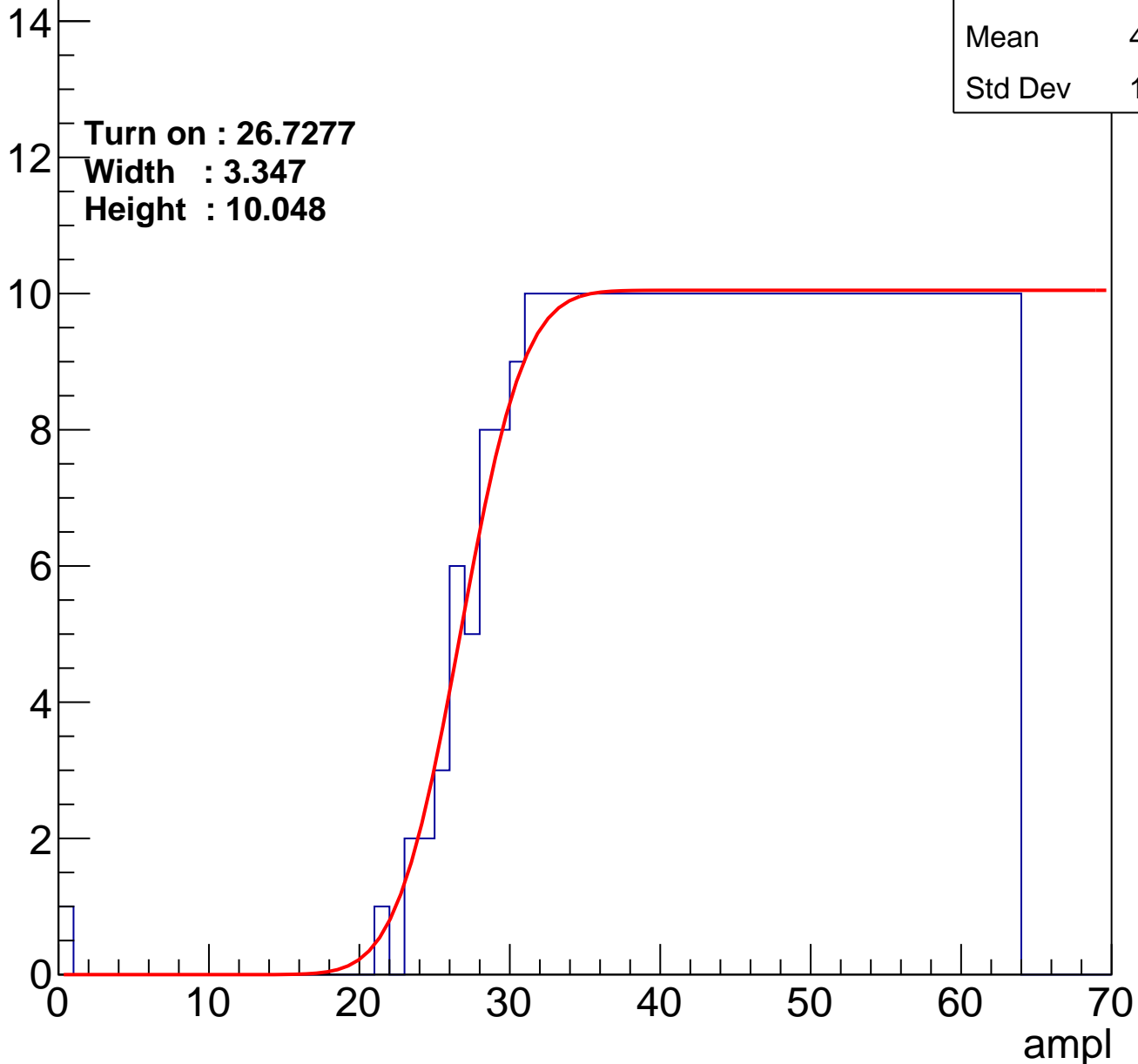
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.58 |
| Std Dev | 11.19 |

Turn on : 26.7277

Width : 3.347

Height : 10.048

Entry



B1L003S, U26-ch44

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|------|
| Entries | 395 |
| Mean | 43.5 |
| Std Dev | 12 |

Turn on : 24.9262

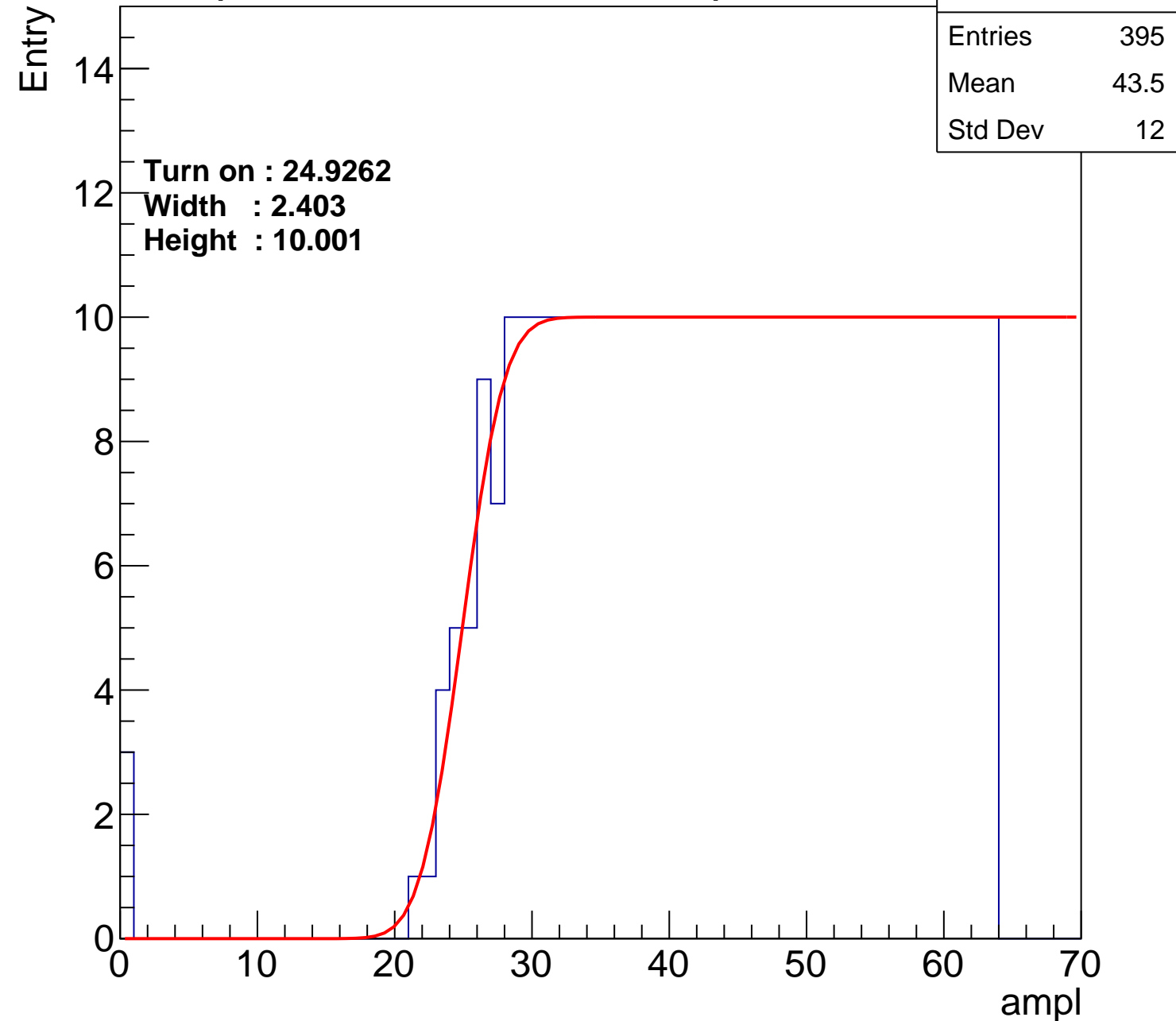
Width : 2.403

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch45

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 44.98 |
| Std Dev | 11.51 |

Turn on : 28.8347

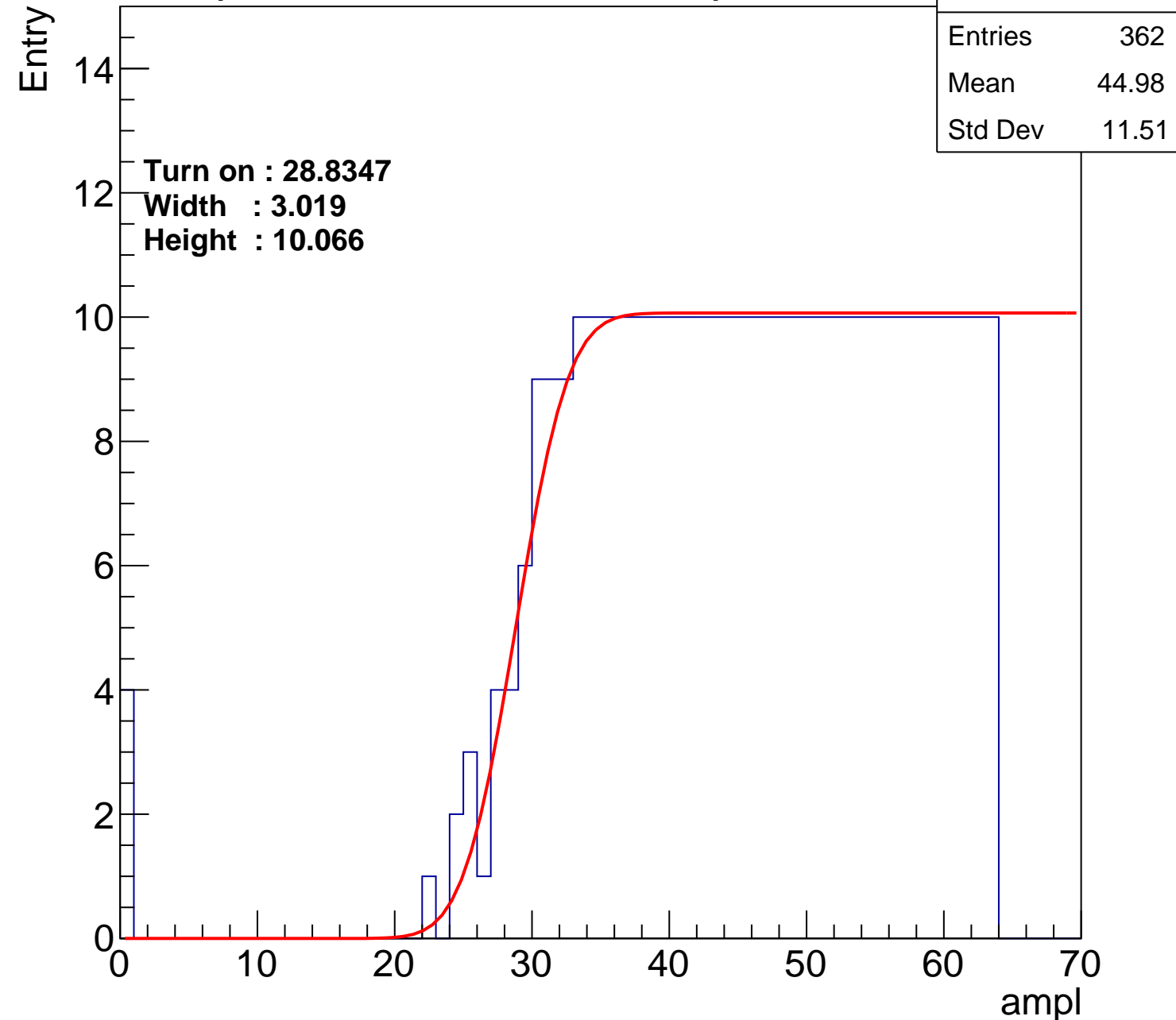
Width : 3.019

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch46

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.96 |
| Std Dev | 11.69 |

Turn on : 26.0083

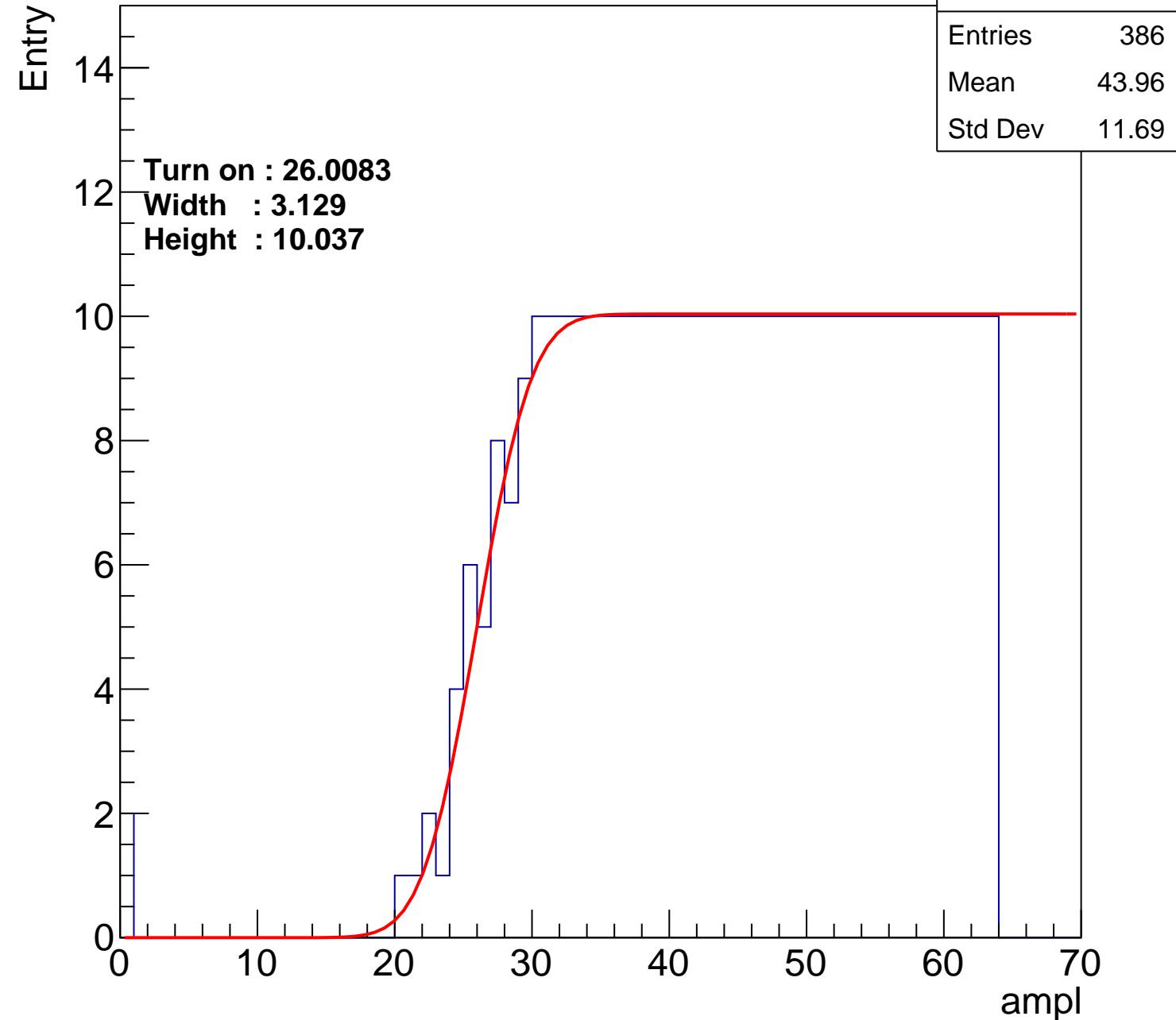
Width : 3.129

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch47

calib_packv5_042523_0143.root, FC#13, port D2

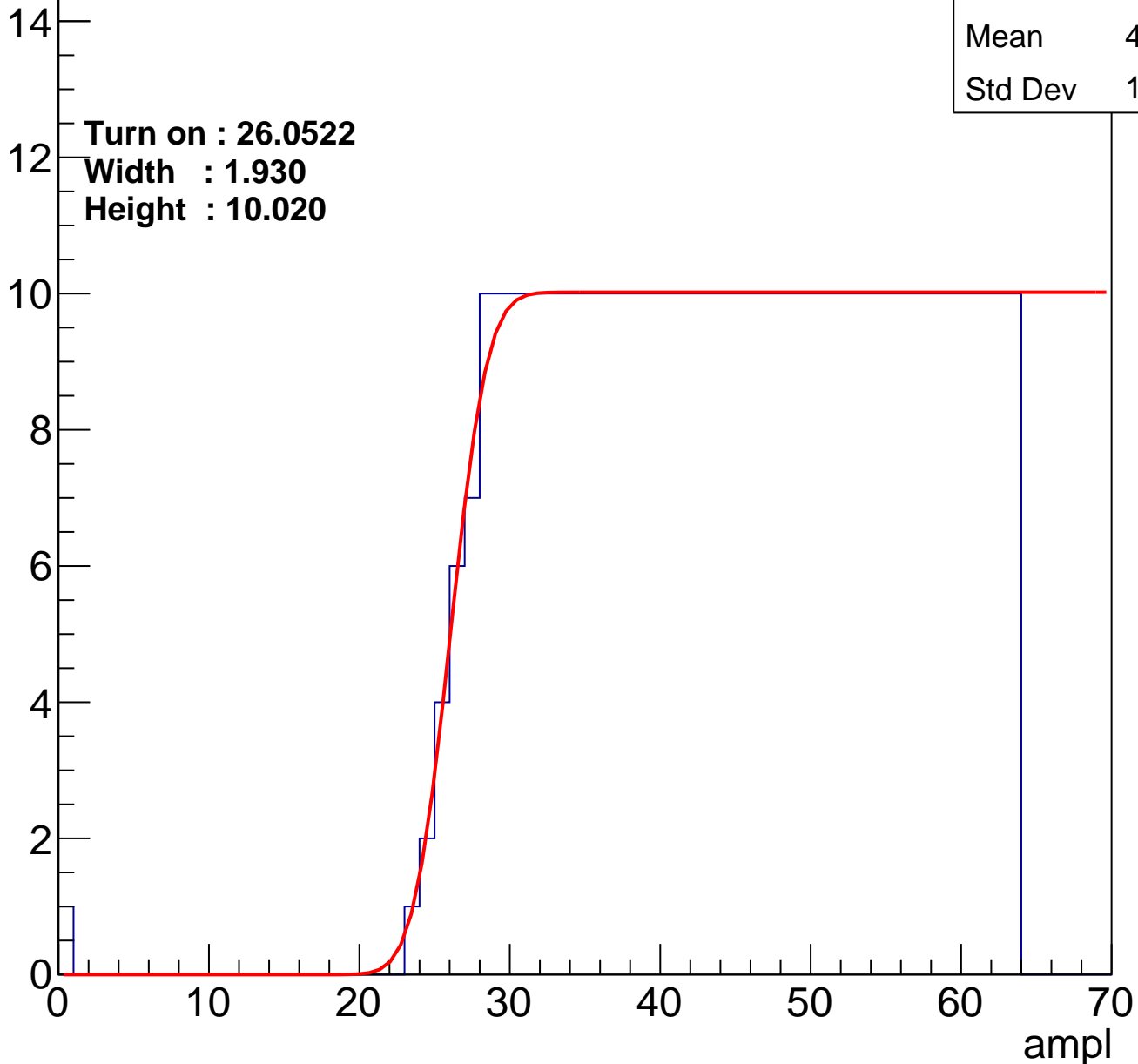
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.35 |
| Std Dev | 11.25 |

Turn on : 26.0522

Width : 1.930

Height : 10.020

Entry



B1L003S, U26-ch48

calib_packv5_042523_0143.root, FC#13, port D2

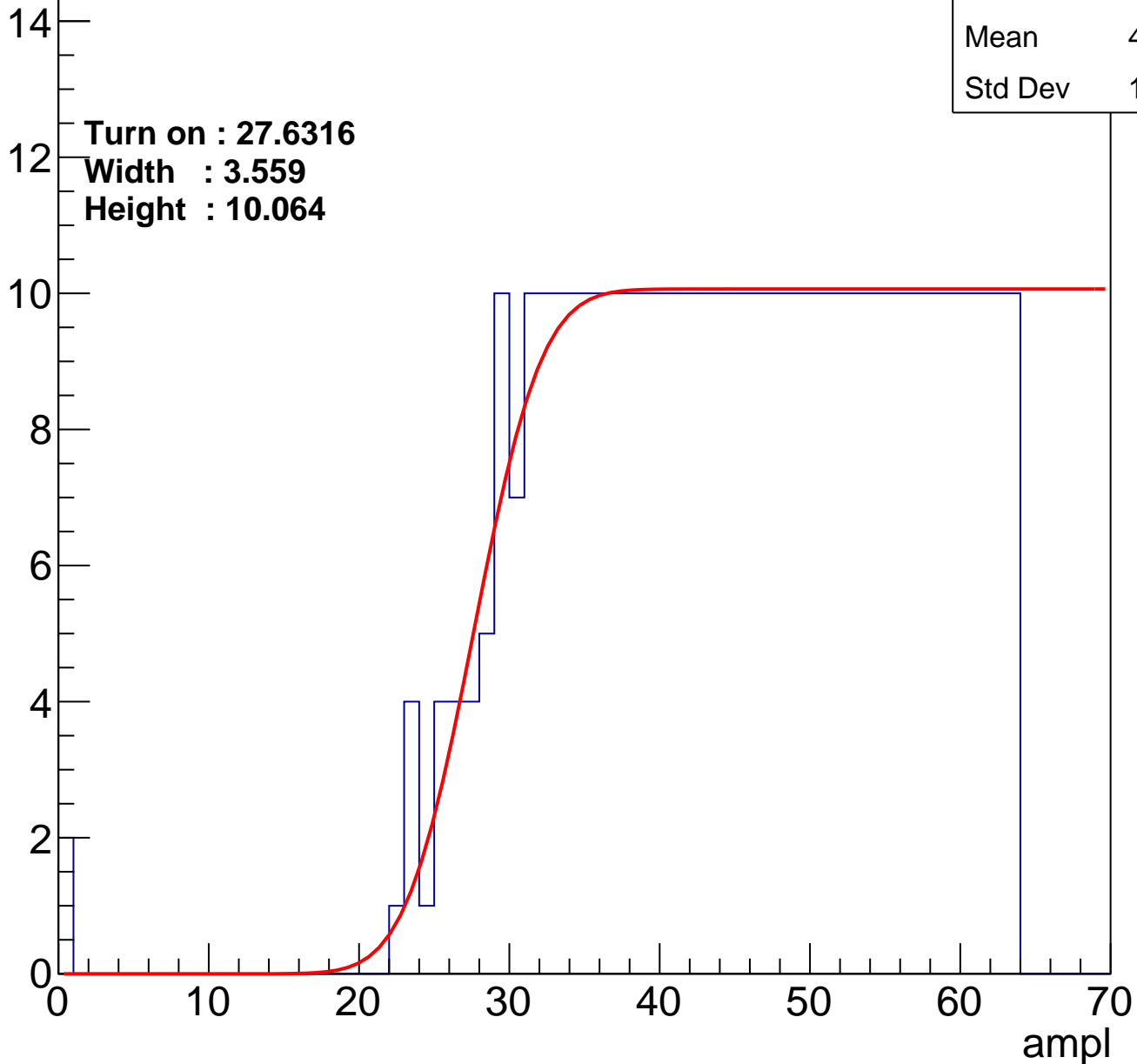
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.62 |
| Std Dev | 11.37 |

Turn on : 27.6316

Width : 3.559

Height : 10.064

Entry



B1L003S, U26-ch49

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.17 |
| Std Dev | 12.1 |

Turn on : 24.6959

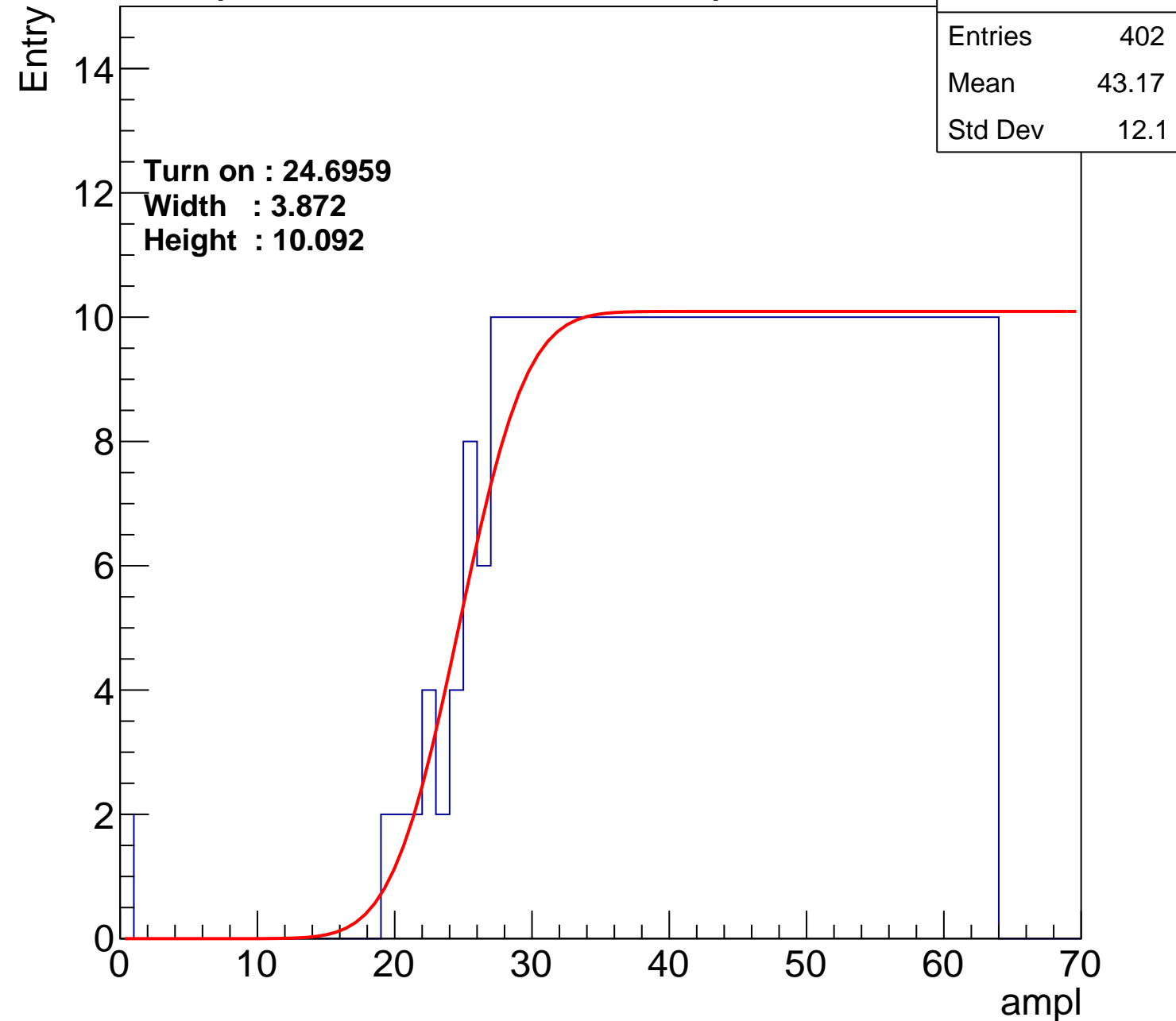
Width : 3.872

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch50

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.7 |
| Std Dev | 11.48 |

Turn on : 27.9457

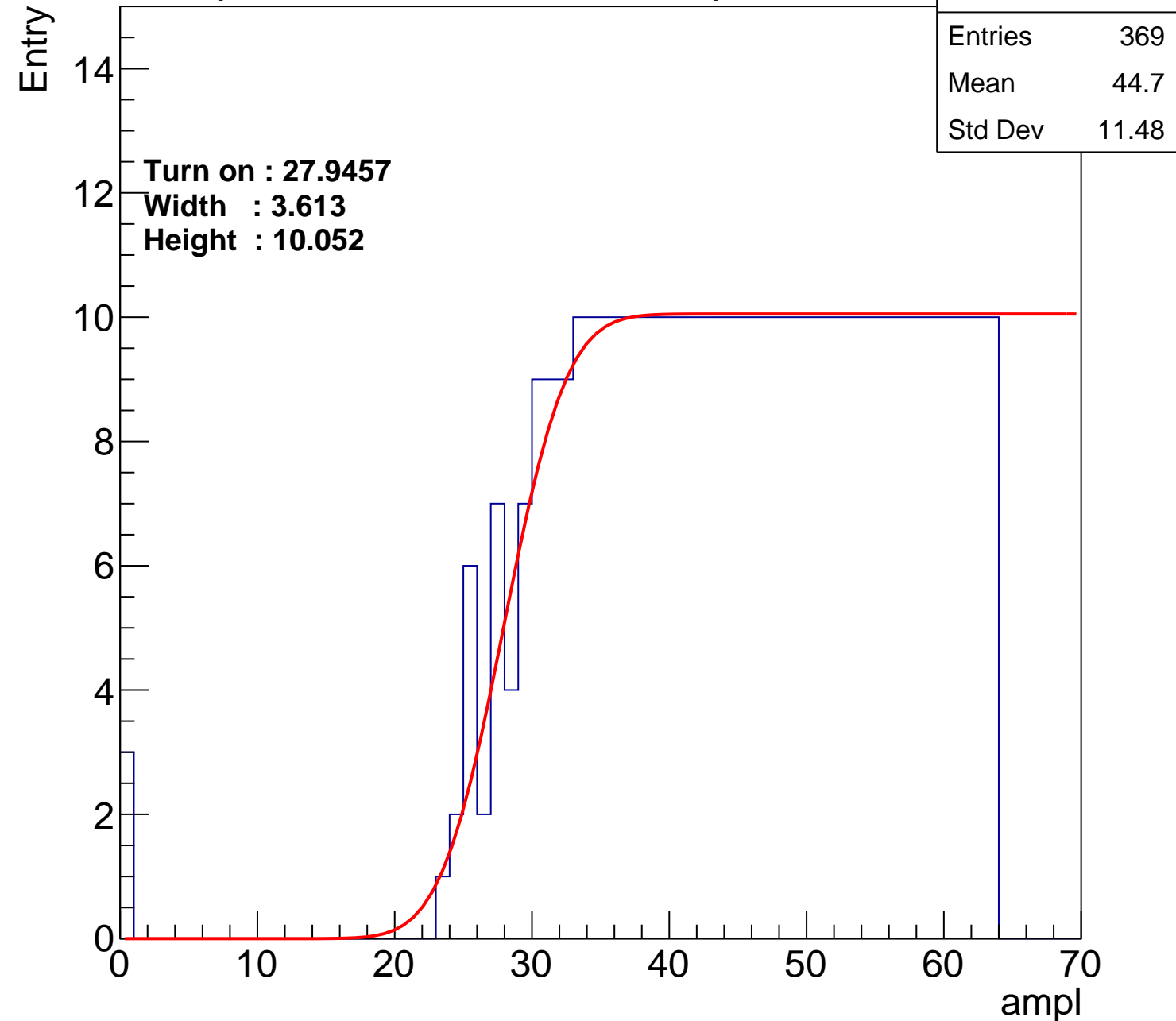
Width : 3.613

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch51

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 349 |
| Mean | 45.75 |
| Std Dev | 10.8 |

Turn on : 29.8374

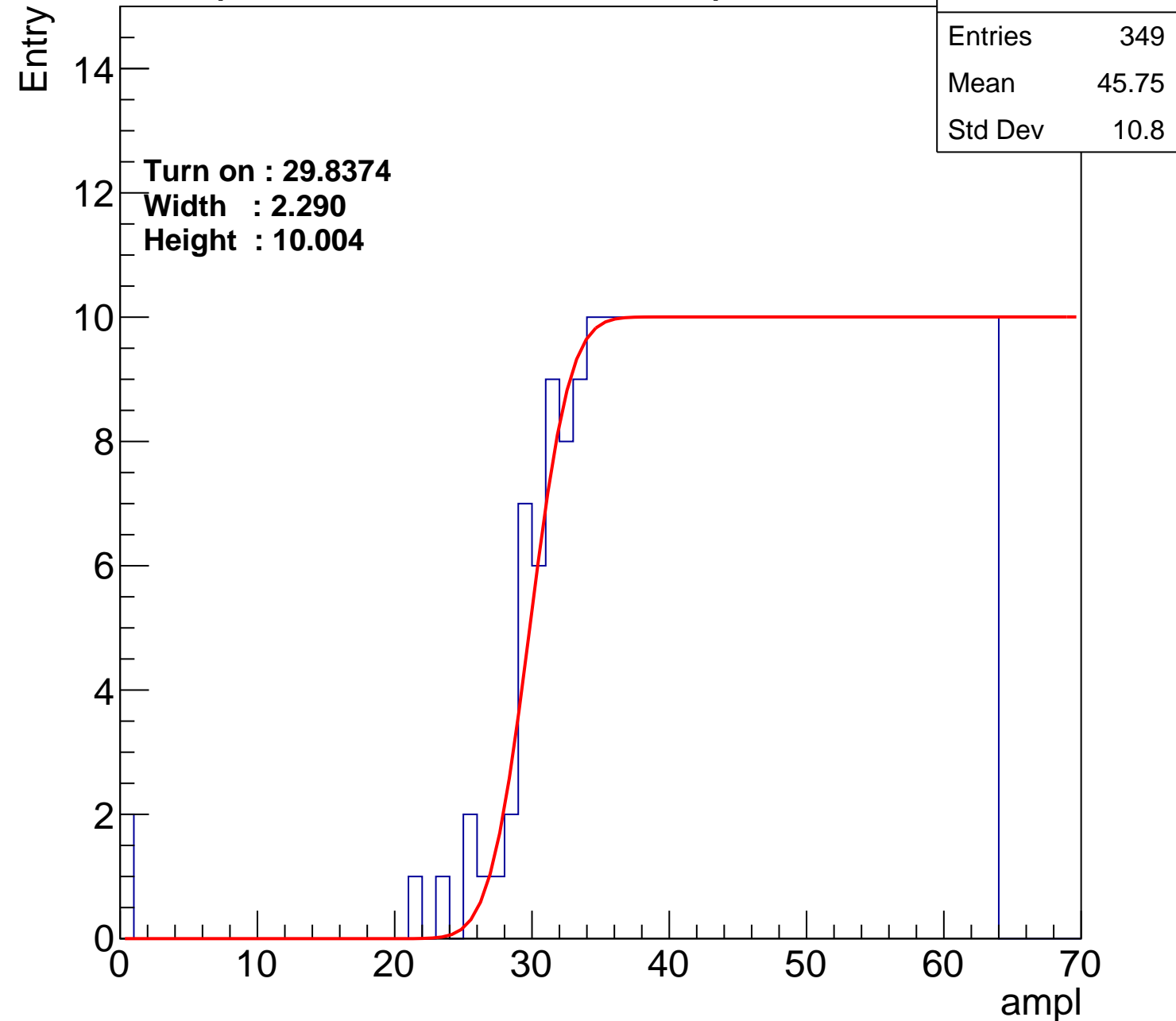
Width : 2.290

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch52

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.02 |
| Std Dev | 11.1 |

Turn on : 27.4929

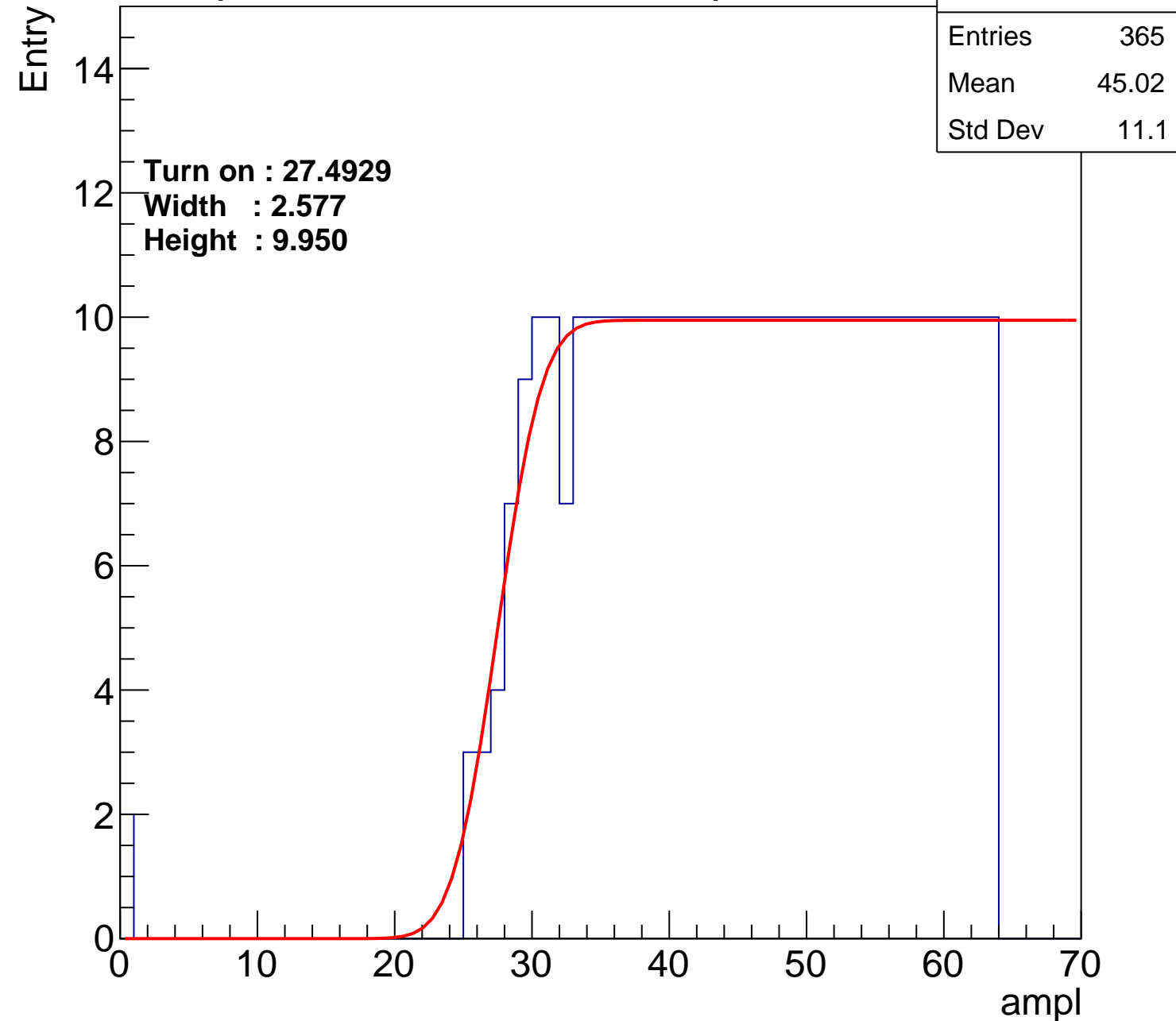
Width : 2.577

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch53

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.46 |
| Std Dev | 11.54 |

Turn on : 27.0393

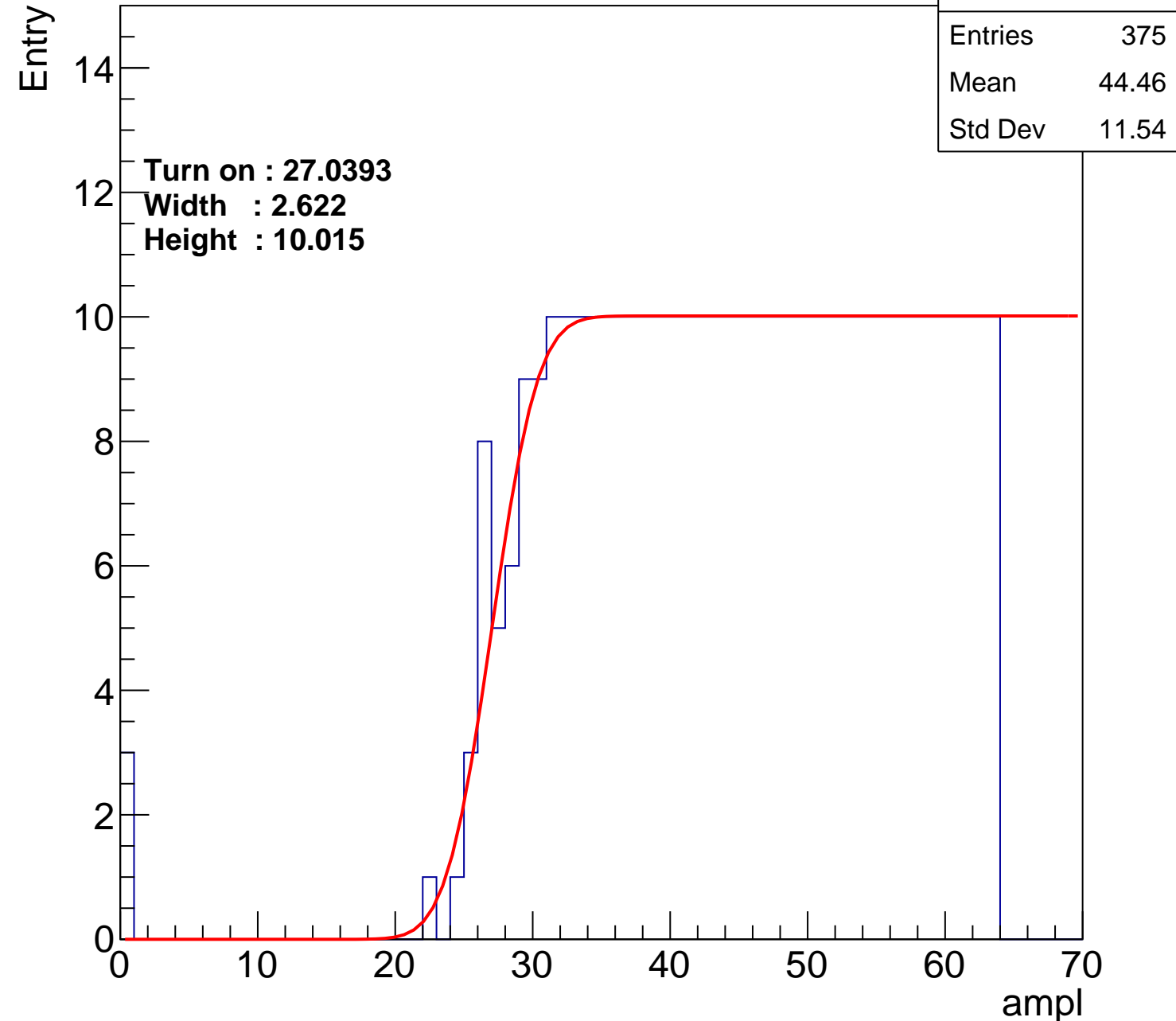
Width : 2.622

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch54

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.72 |
| Std Dev | 11.23 |

Turn on : 27.3835

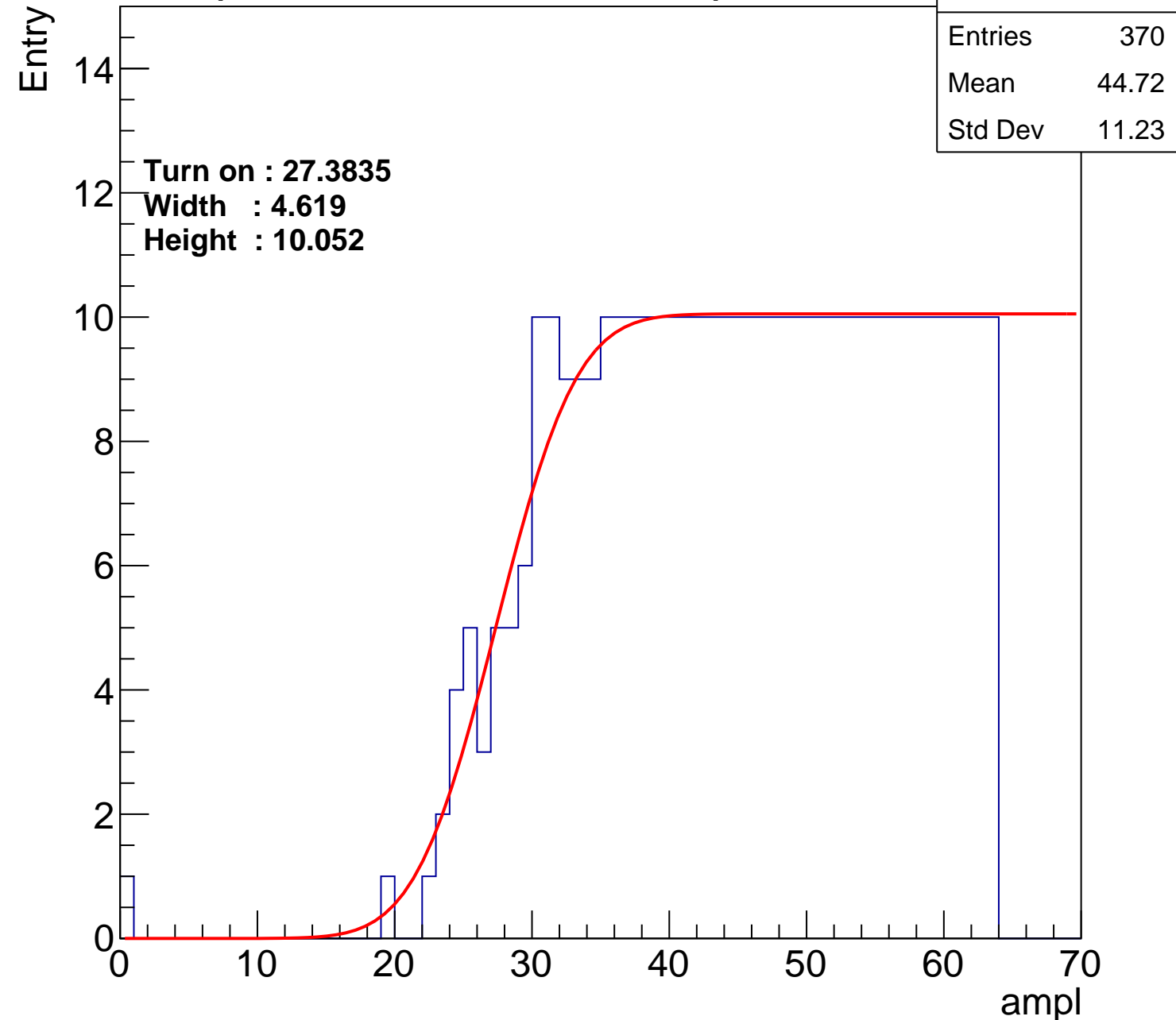
Width : 4.619

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch55

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45 |
| Std Dev | 11.33 |

Turn on : 28.5624

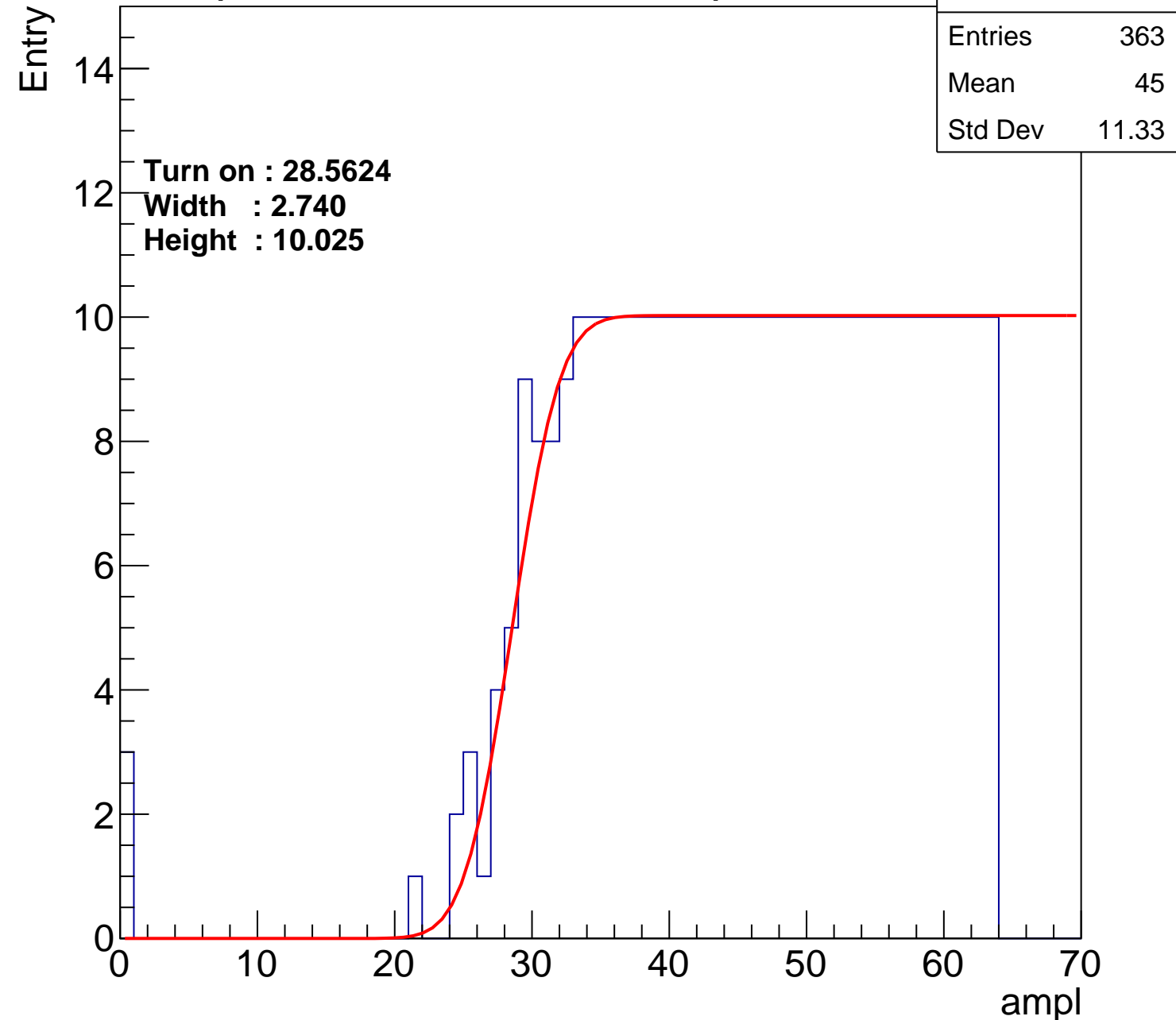
Width : 2.740

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch56

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.62 |
| Std Dev | 11.45 |

Turn on : 26.6157

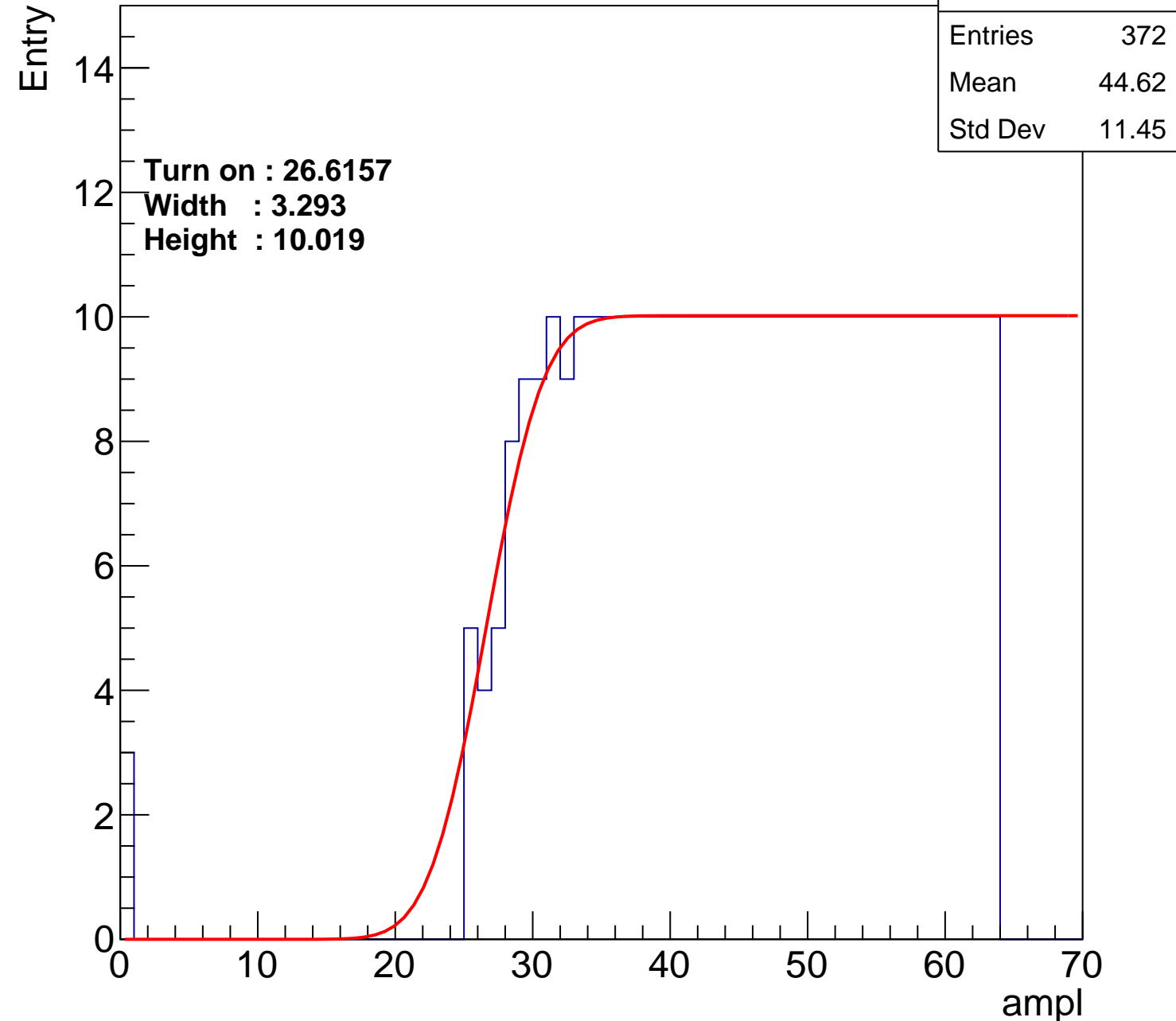
Width : 3.293

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch57

calib_packv5_042523_0143.root, FC#13, port D2

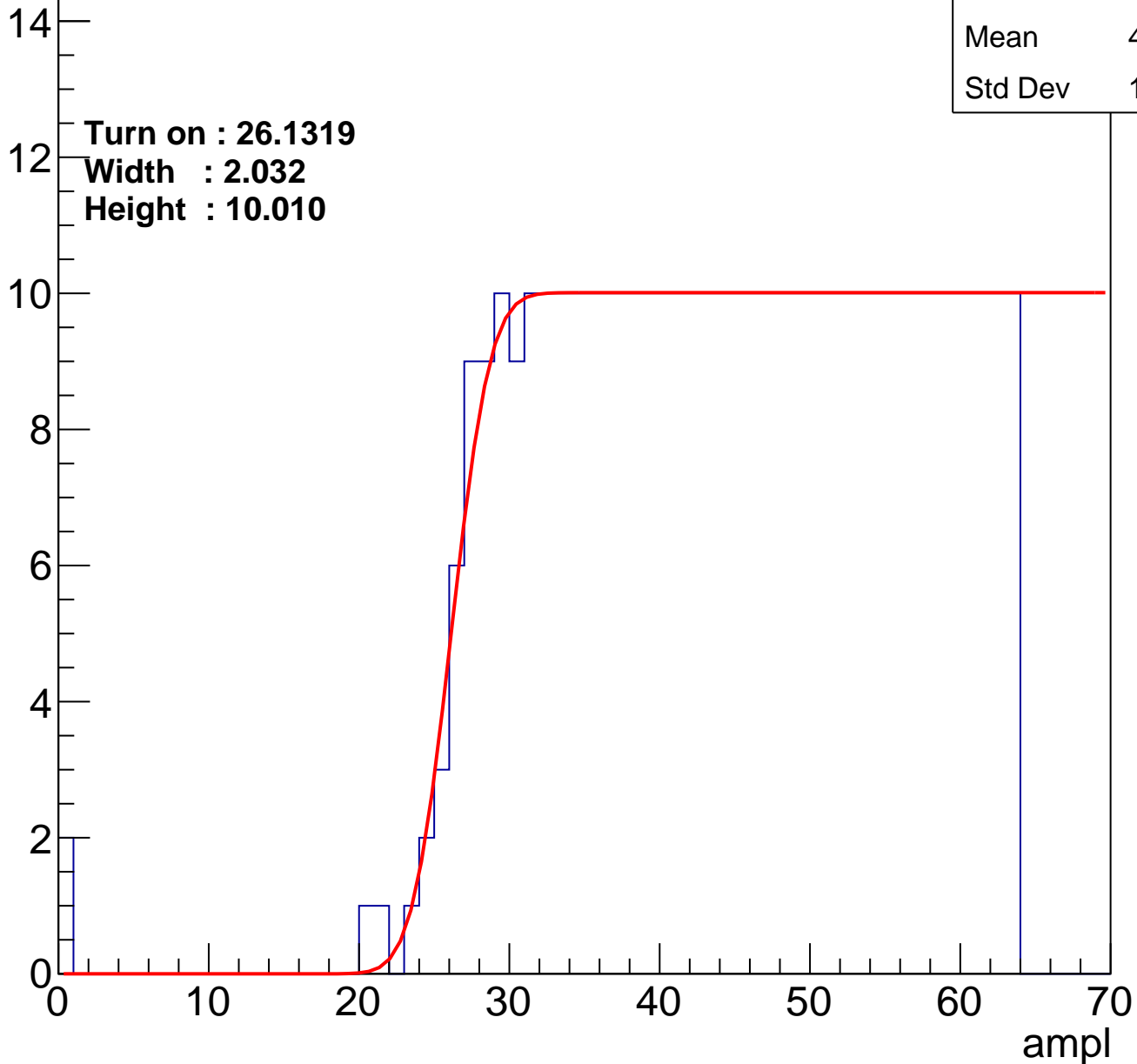
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.15 |
| Std Dev | 11.54 |

Turn on : 26.1319

Width : 2.032

Height : 10.010

Entry



B1L003S, U26-ch58

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.66 |
| Std Dev | 12.08 |

Turn on : 25.3945

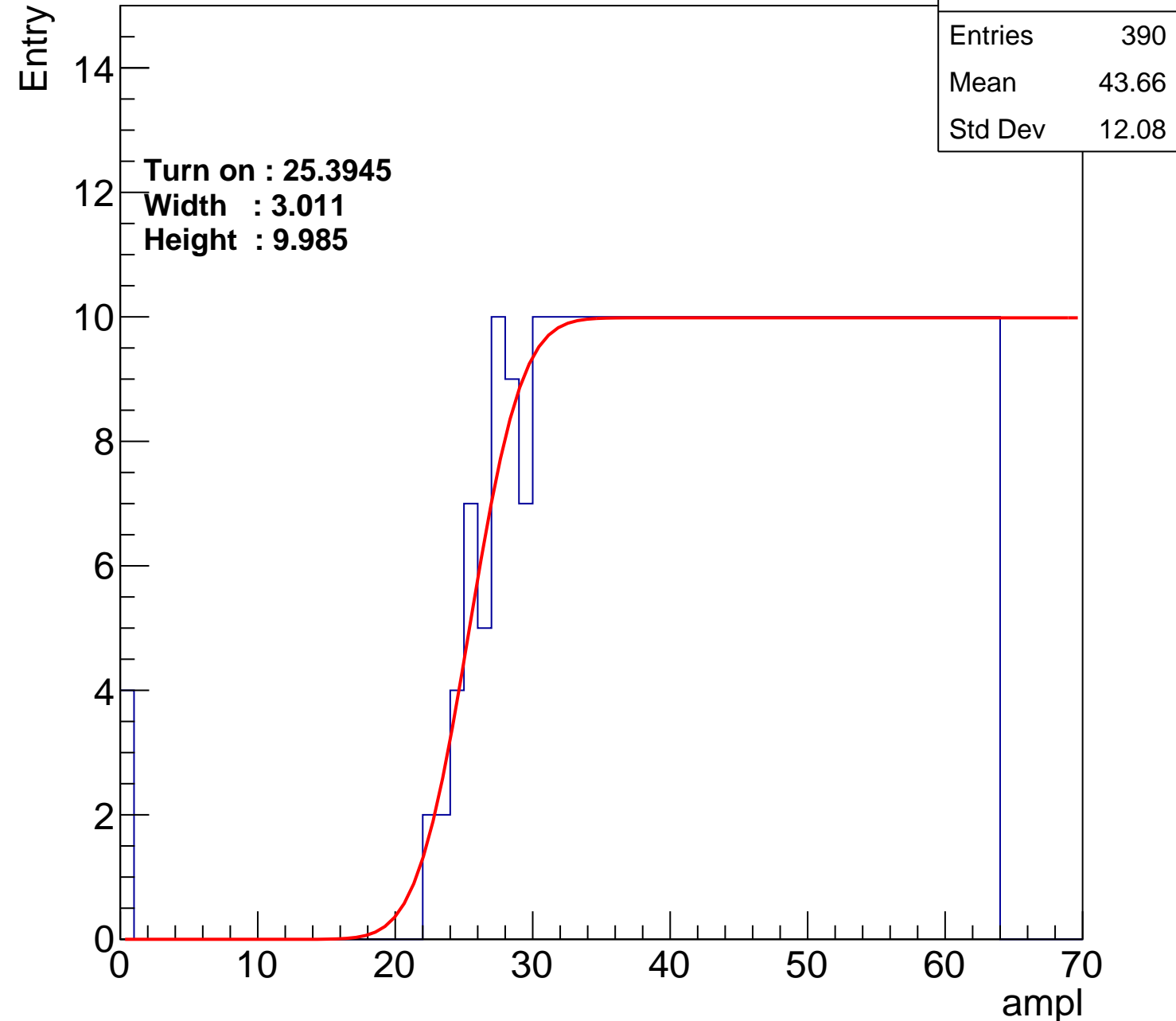
Width : 3.011

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch59

calib_packv5_042523_0143.root, FC#13, port D2

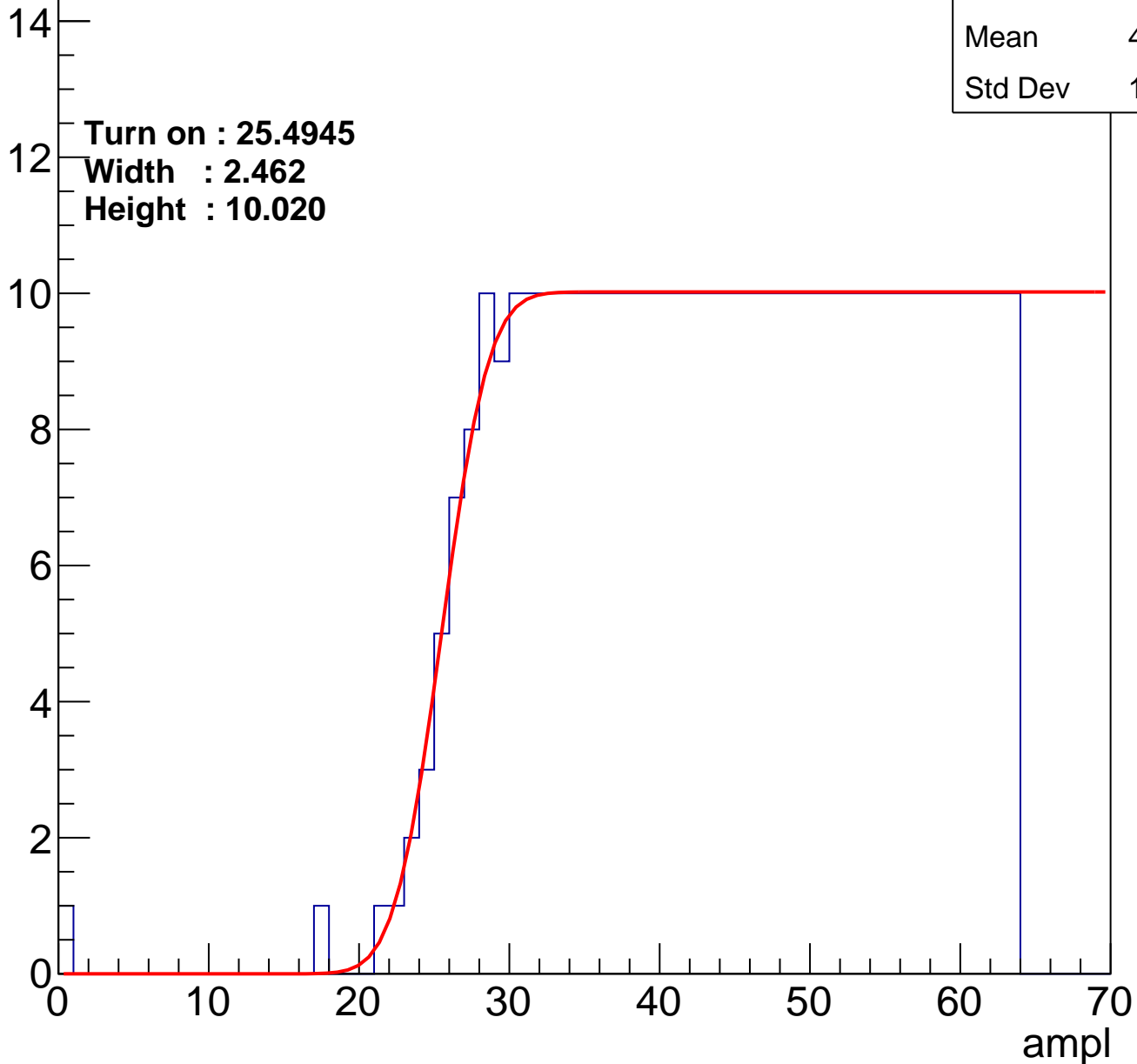
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.95 |
| Std Dev | 11.53 |

Turn on : 25.4945

Width : 2.462

Height : 10.020

Entry



B1L003S, U26-ch60

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.75 |
| Std Dev | 11.4 |

Turn on : 27.7095

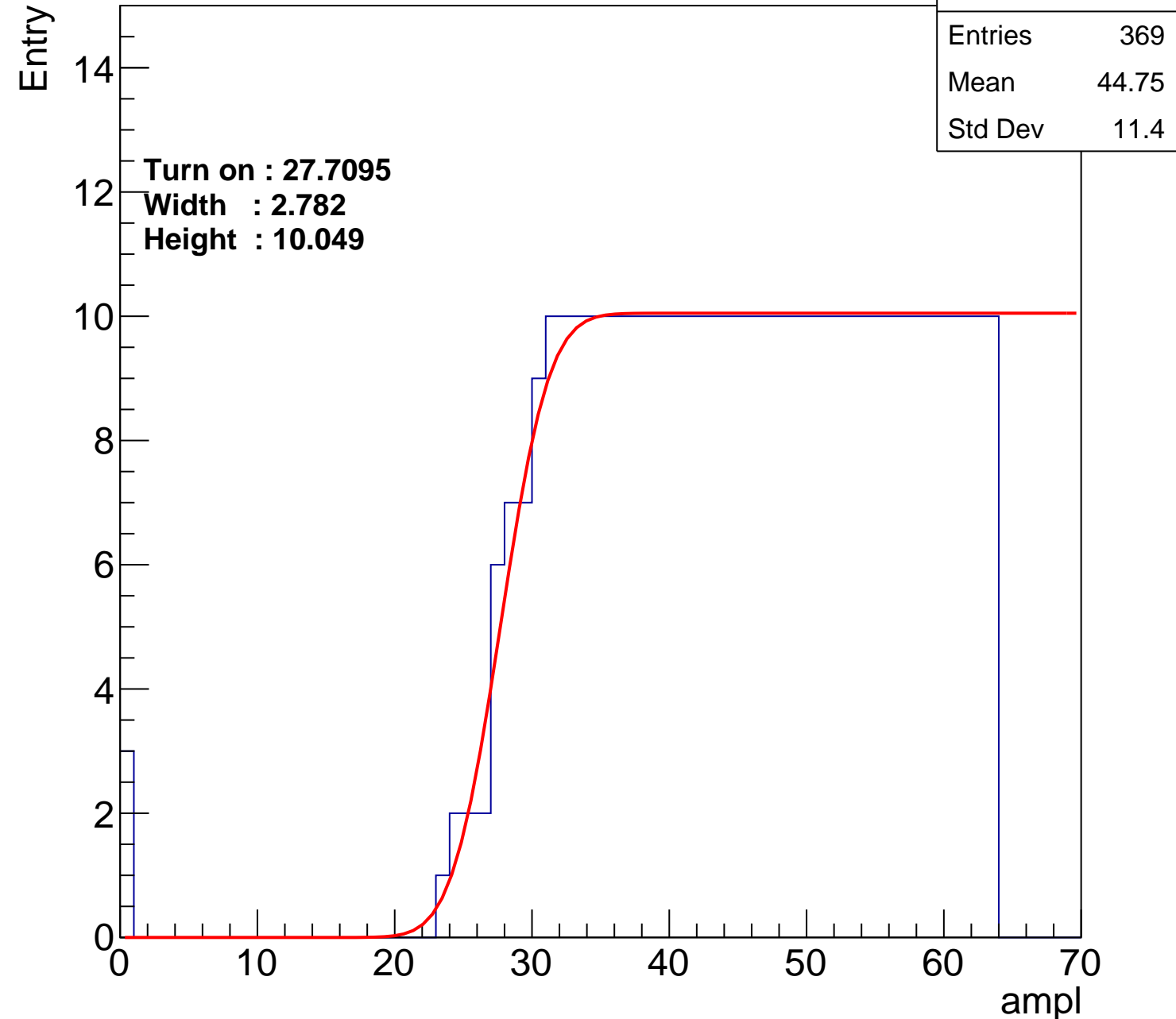
Width : 2.782

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch61

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.22 |
| Std Dev | 10.99 |

Turn on : 28.2330

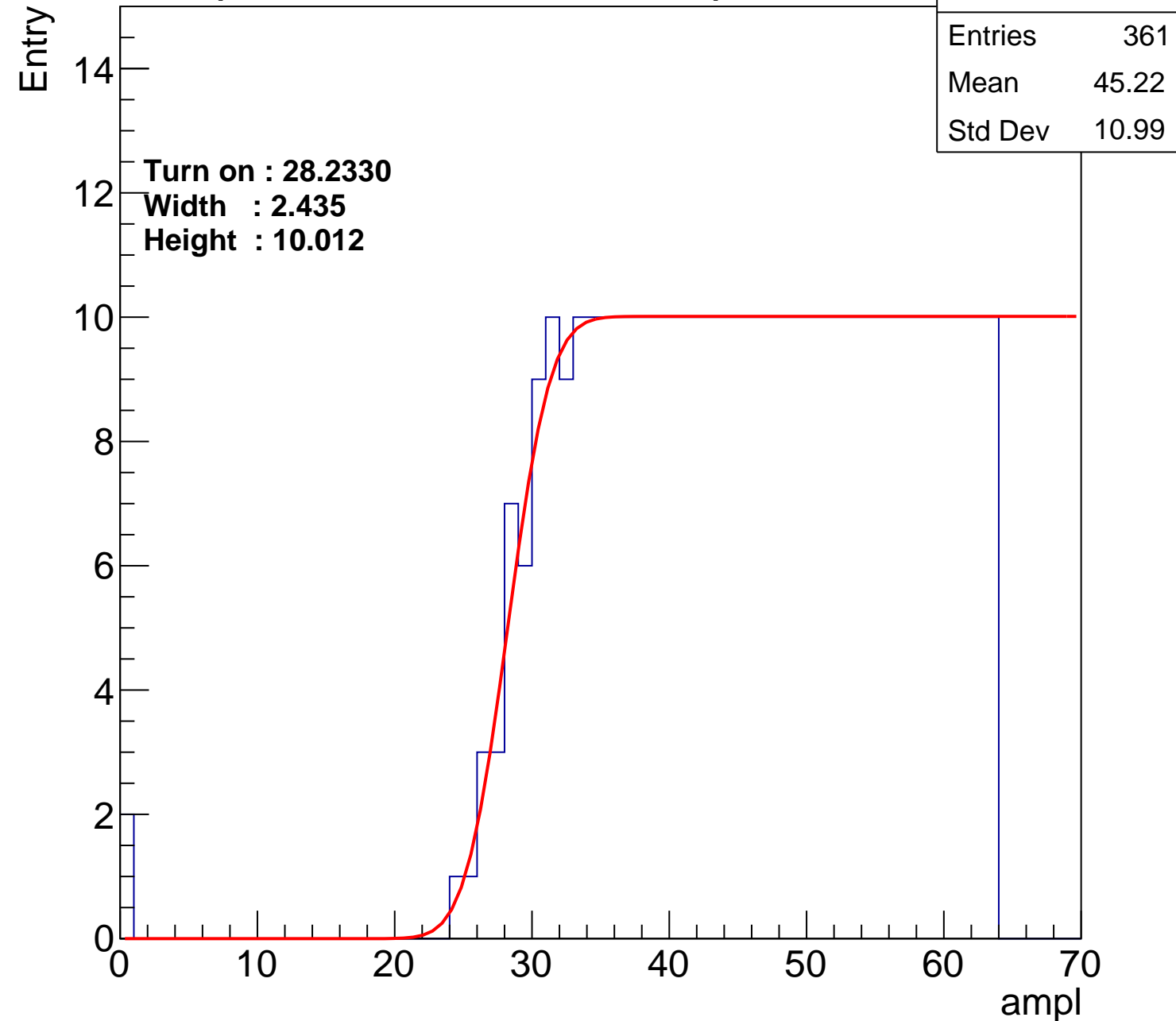
Width : 2.435

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch62

calib_packv5_042523_0143.root, FC#13, port D2

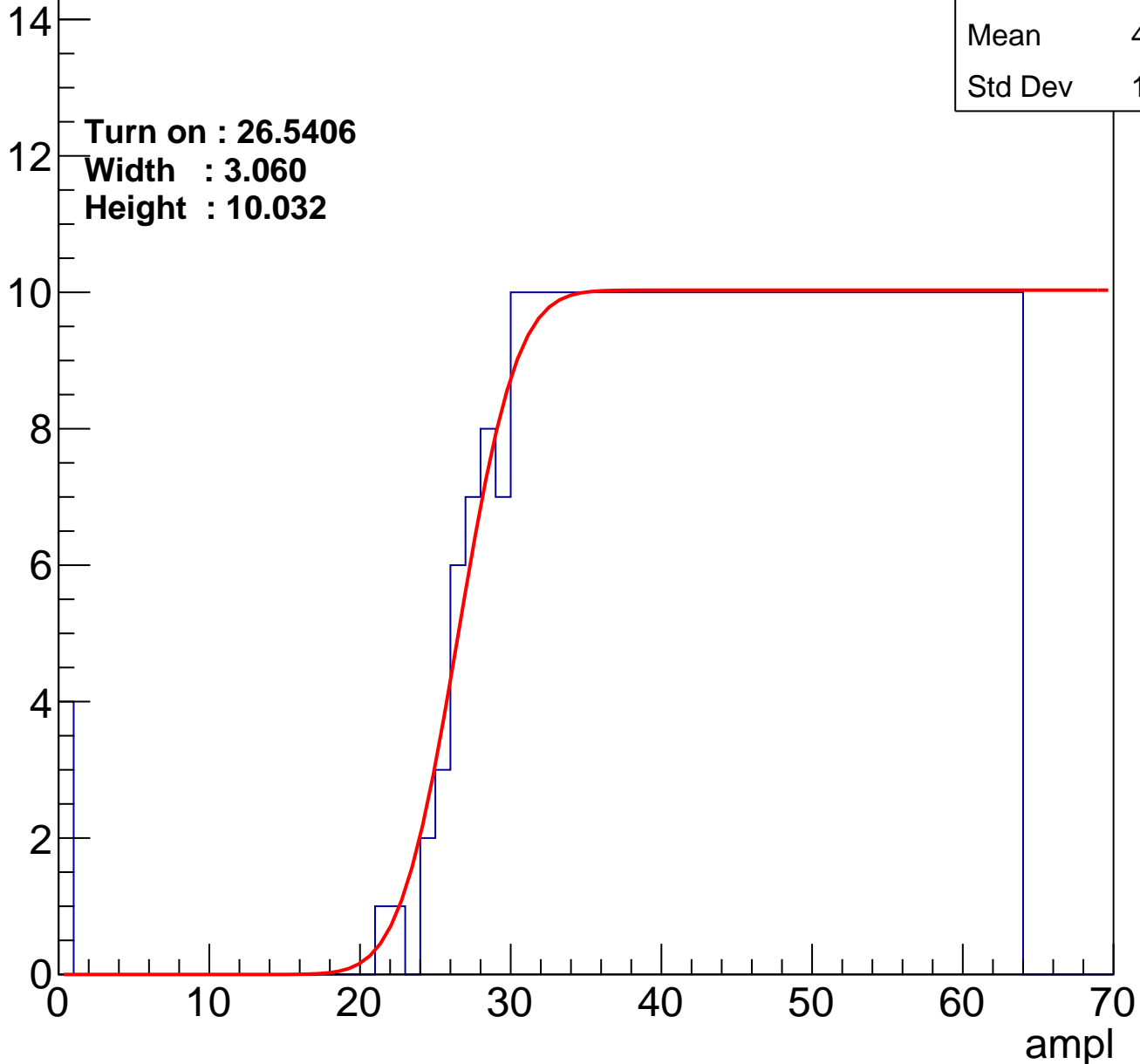
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.19 |
| Std Dev | 11.83 |

Turn on : 26.5406

Width : 3.060

Height : 10.032

Entry



B1L003S, U26-ch63

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.05 |
| Std Dev | 11.76 |

Turn on : 25.4308

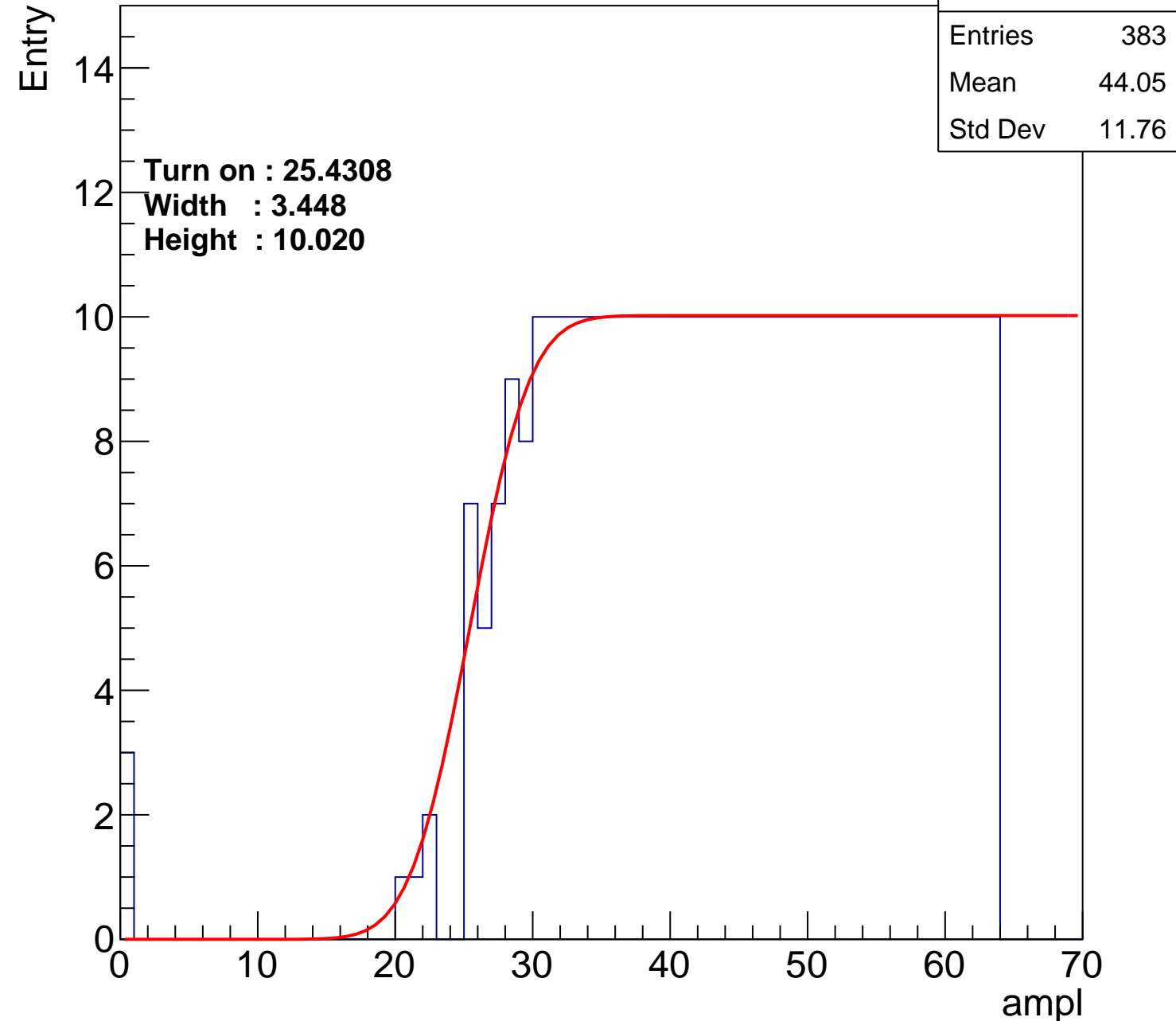
Width : 3.448

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch64

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.31 |
| Std Dev | 12.26 |

Turn on : 25.3307

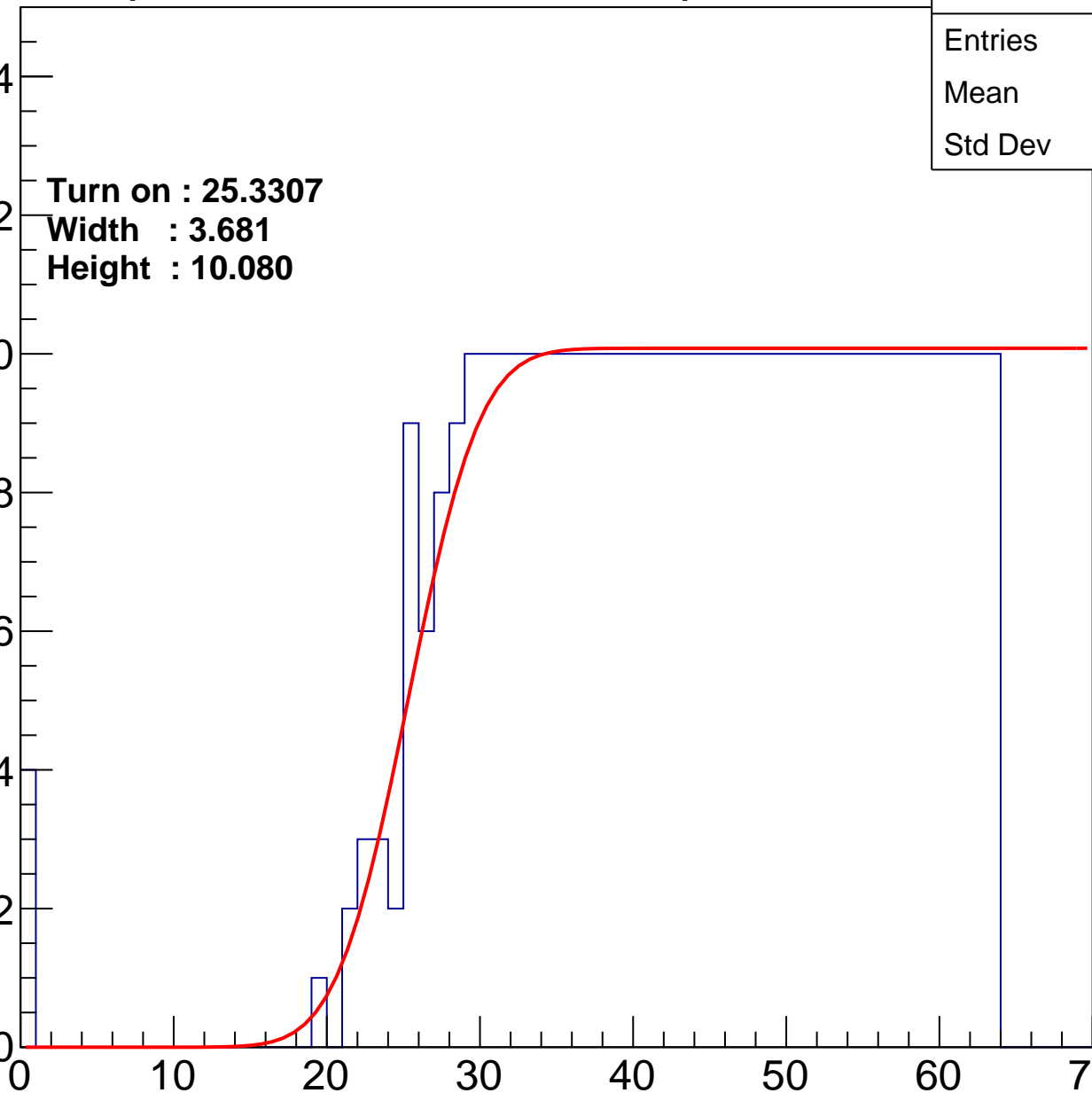
Width : 3.681

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch65

calib_packv5_042523_0143.root, FC#13, port D2

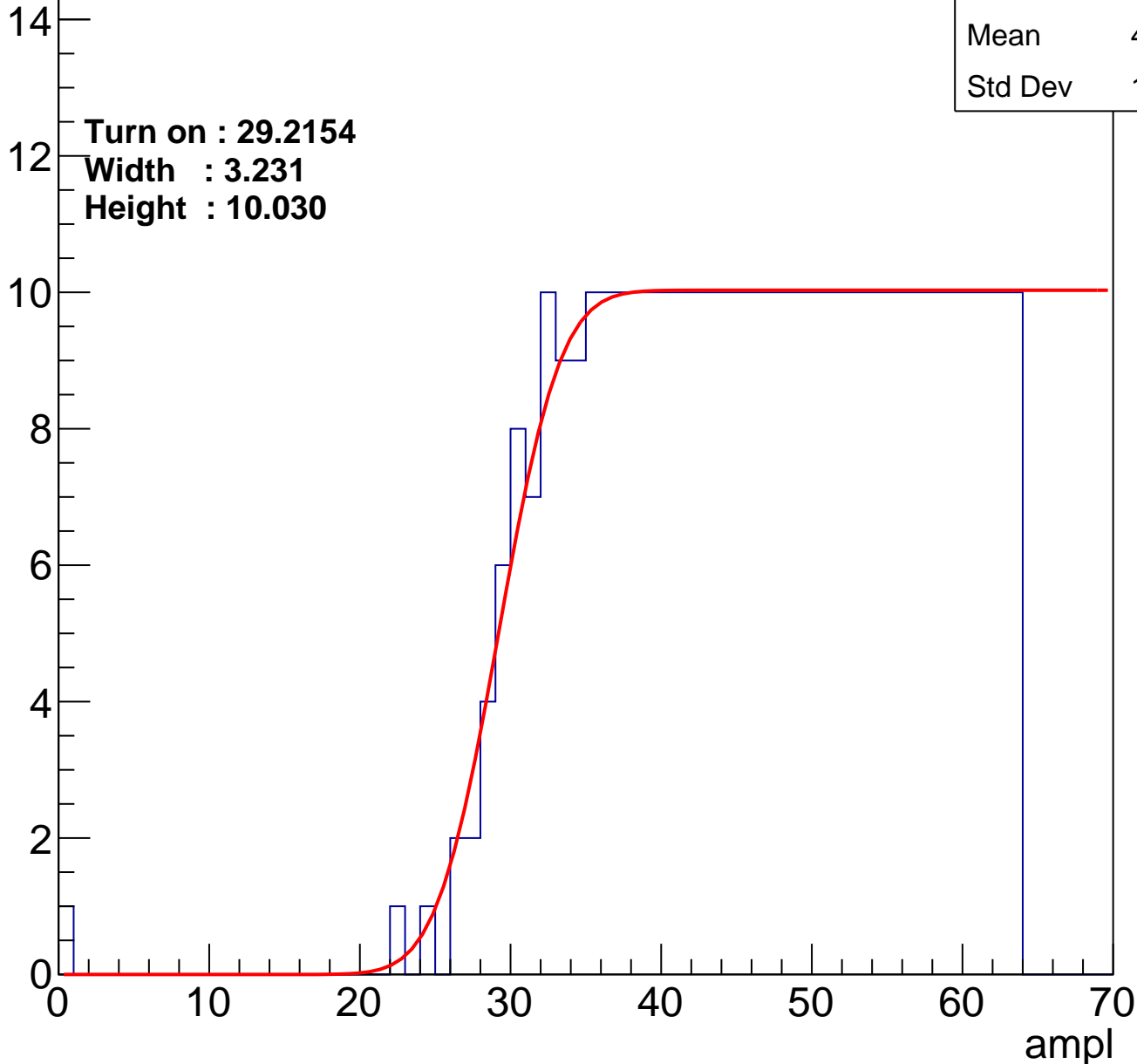
| | |
|---------|-------|
| Entries | 350 |
| Mean | 45.79 |
| Std Dev | 10.56 |

Turn on : 29.2154

Width : 3.231

Height : 10.030

Entry



B1L003S, U26-ch66

calib_packv5_042523_0143.root, FC#13, port D2

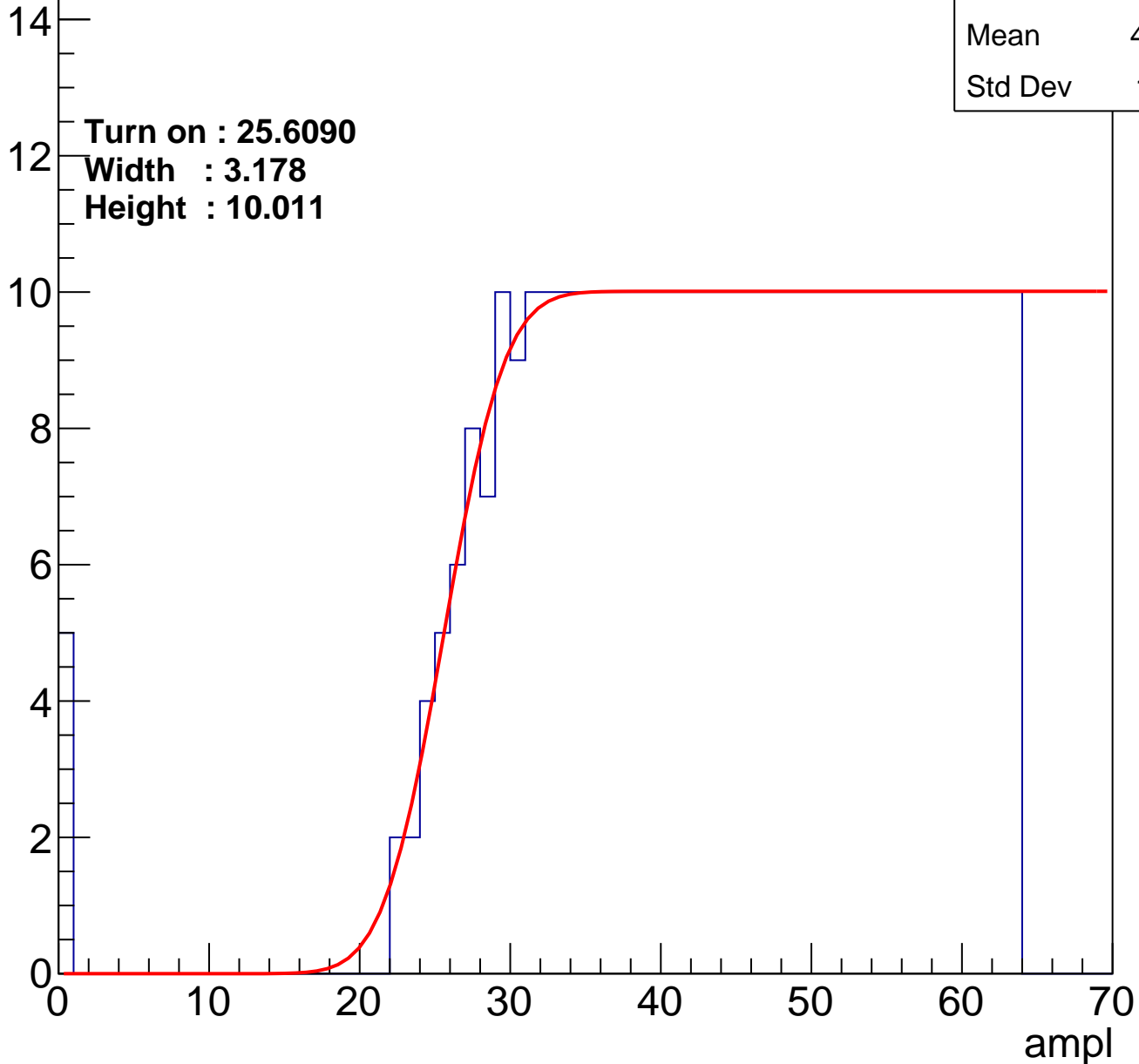
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.68 |
| Std Dev | 12.21 |

Turn on : 25.6090

Width : 3.178

Height : 10.011

Entry



B1L003S, U26-ch67

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.34 |
| Std Dev | 11.5 |

Turn on : 26.7899

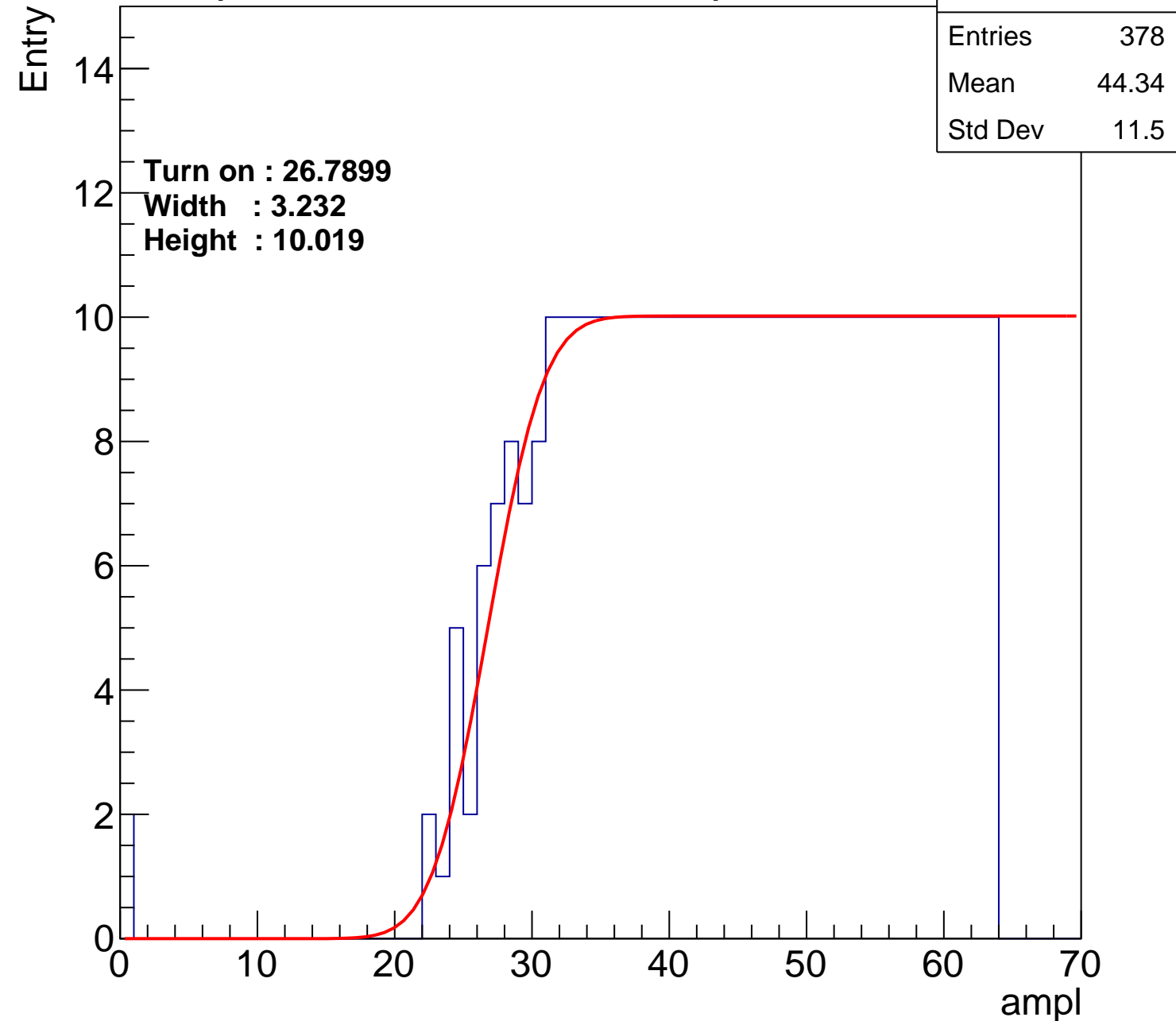
Width : 3.232

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch68

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.79 |
| Std Dev | 11.06 |

Turn on : 27.6815

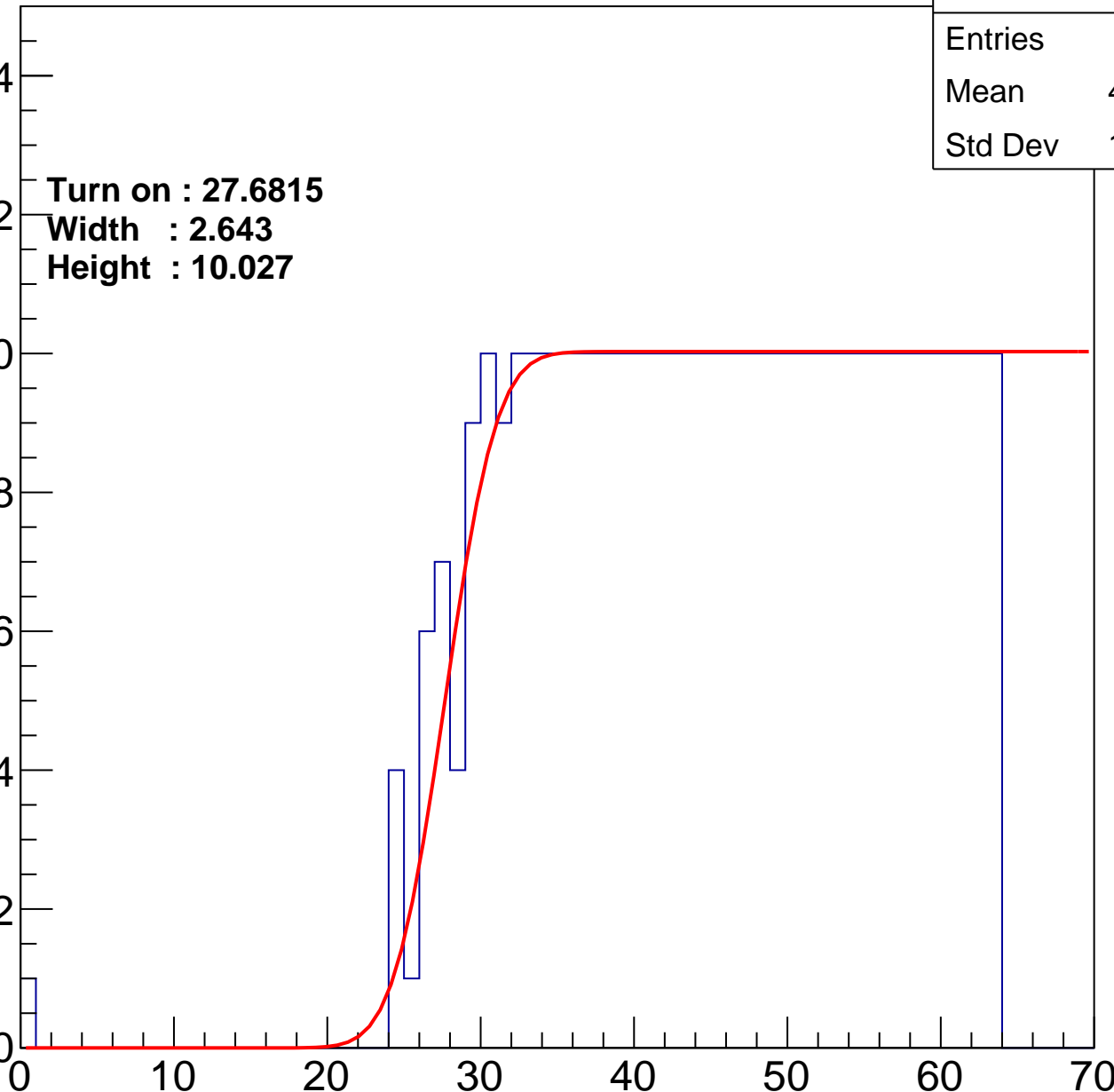
Width : 2.643

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch69

calib_packv5_042523_0143.root, FC#13, port D2

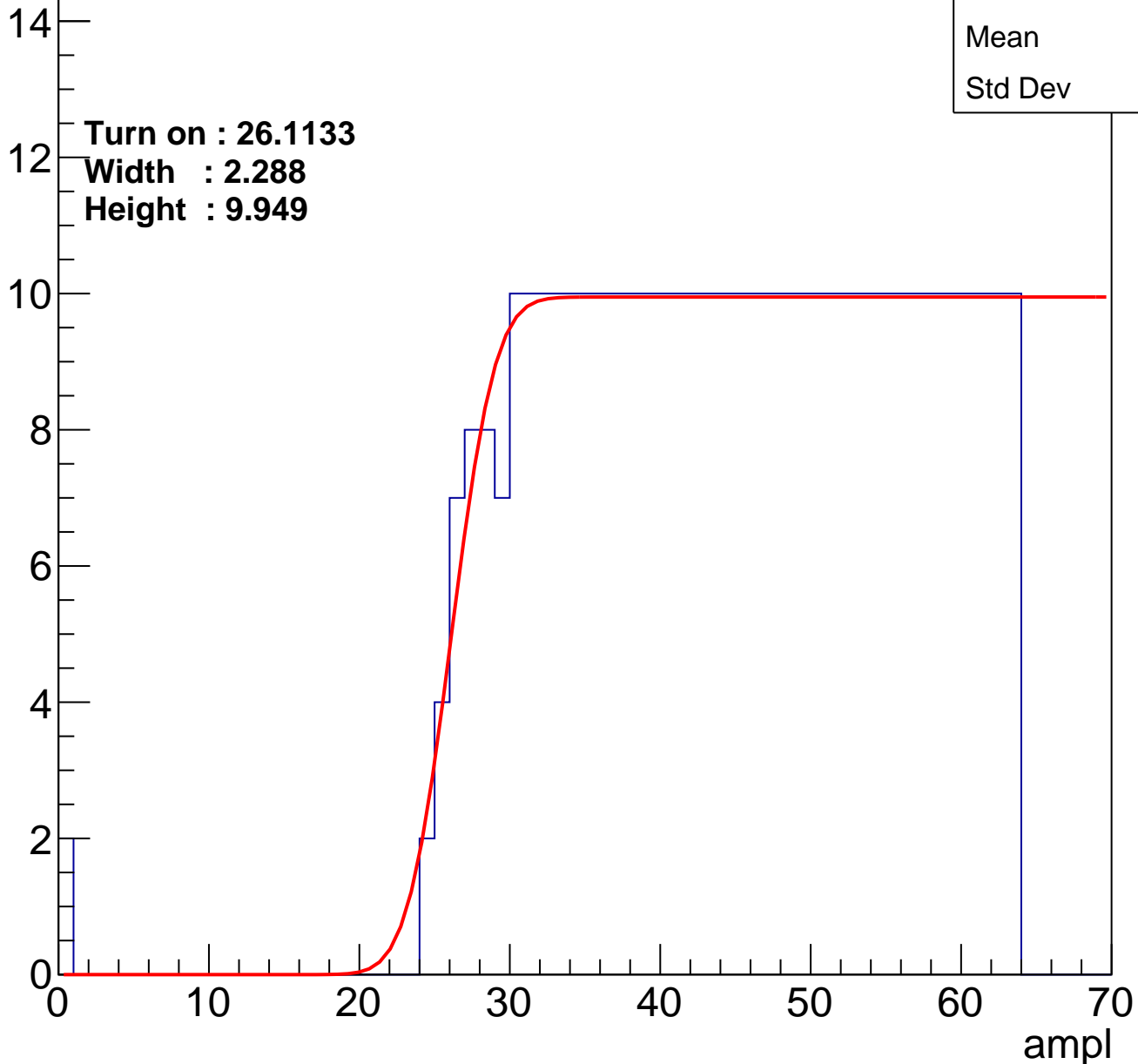
| | |
|---------|------|
| Entries | 378 |
| Mean | 44.4 |
| Std Dev | 11.4 |

Turn on : 26.1133

Width : 2.288

Height : 9.949

Entry



B1L003S, U26-ch70

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.64 |
| Std Dev | 11.7 |

Turn on : 24.8061

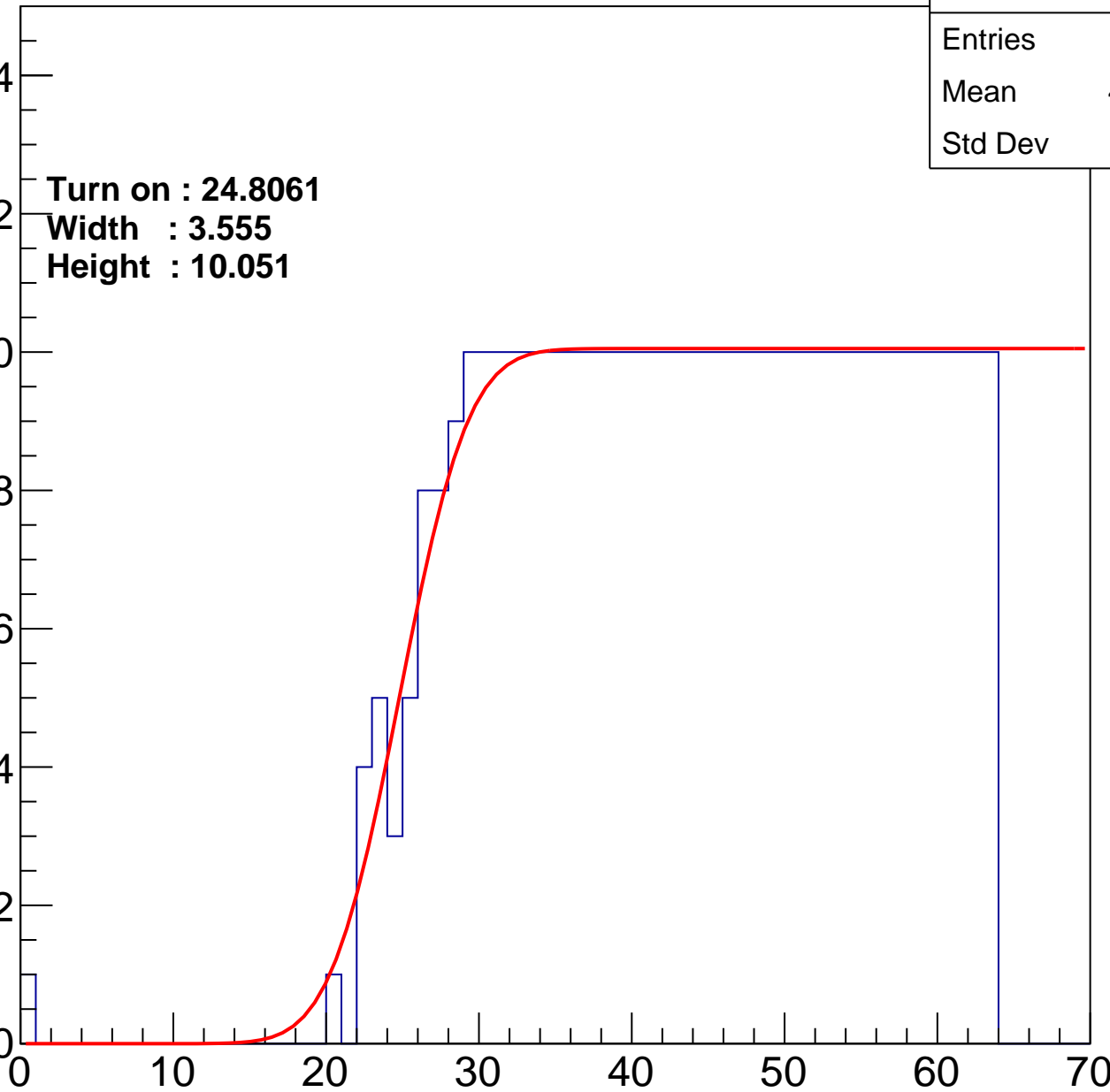
Width : 3.555

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch71

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.14 |
| Std Dev | 12.28 |

Turn on : 27.4444

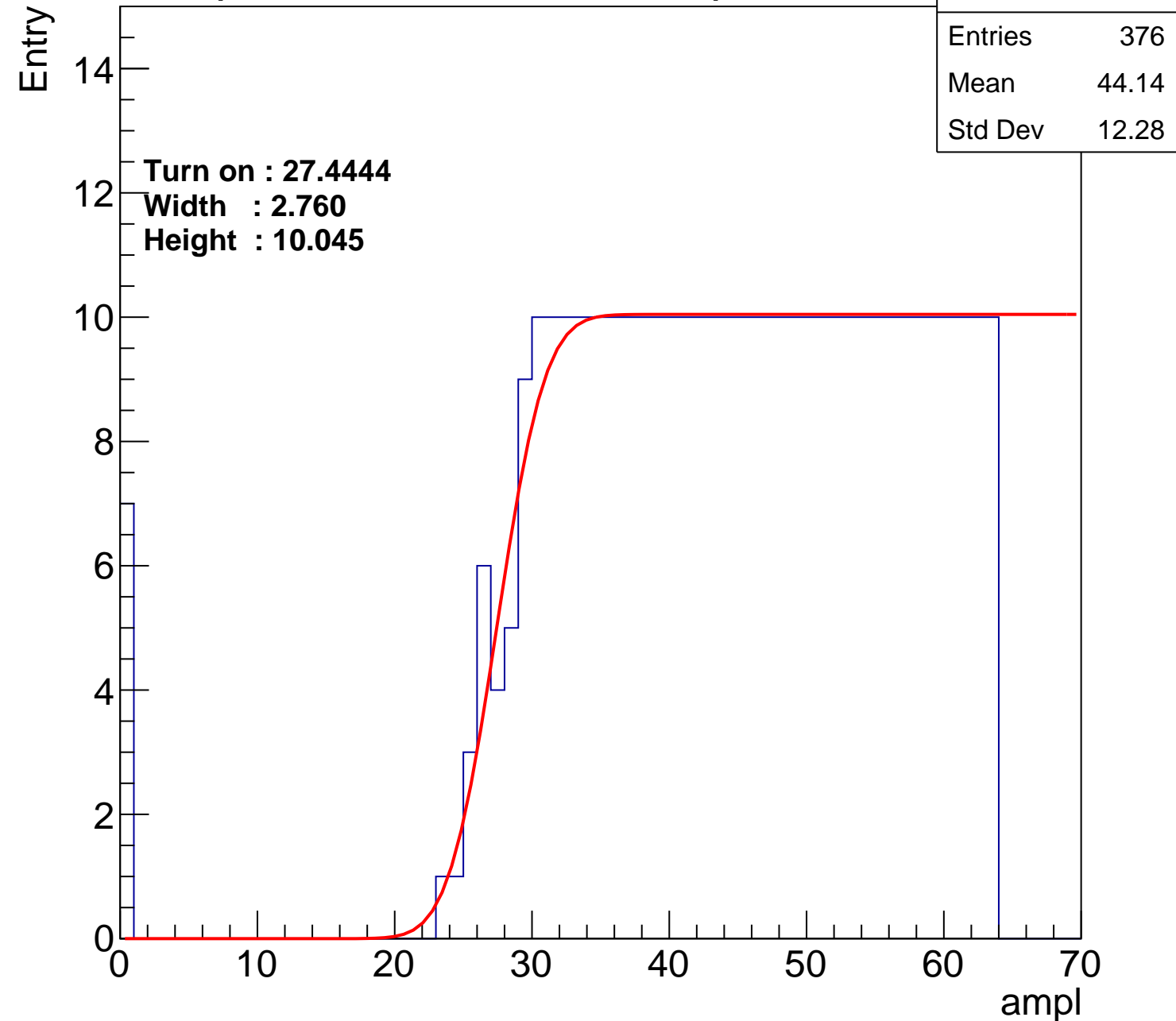
Width : 2.760

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch72

calib_packv5_042523_0143.root, FC#13, port D2

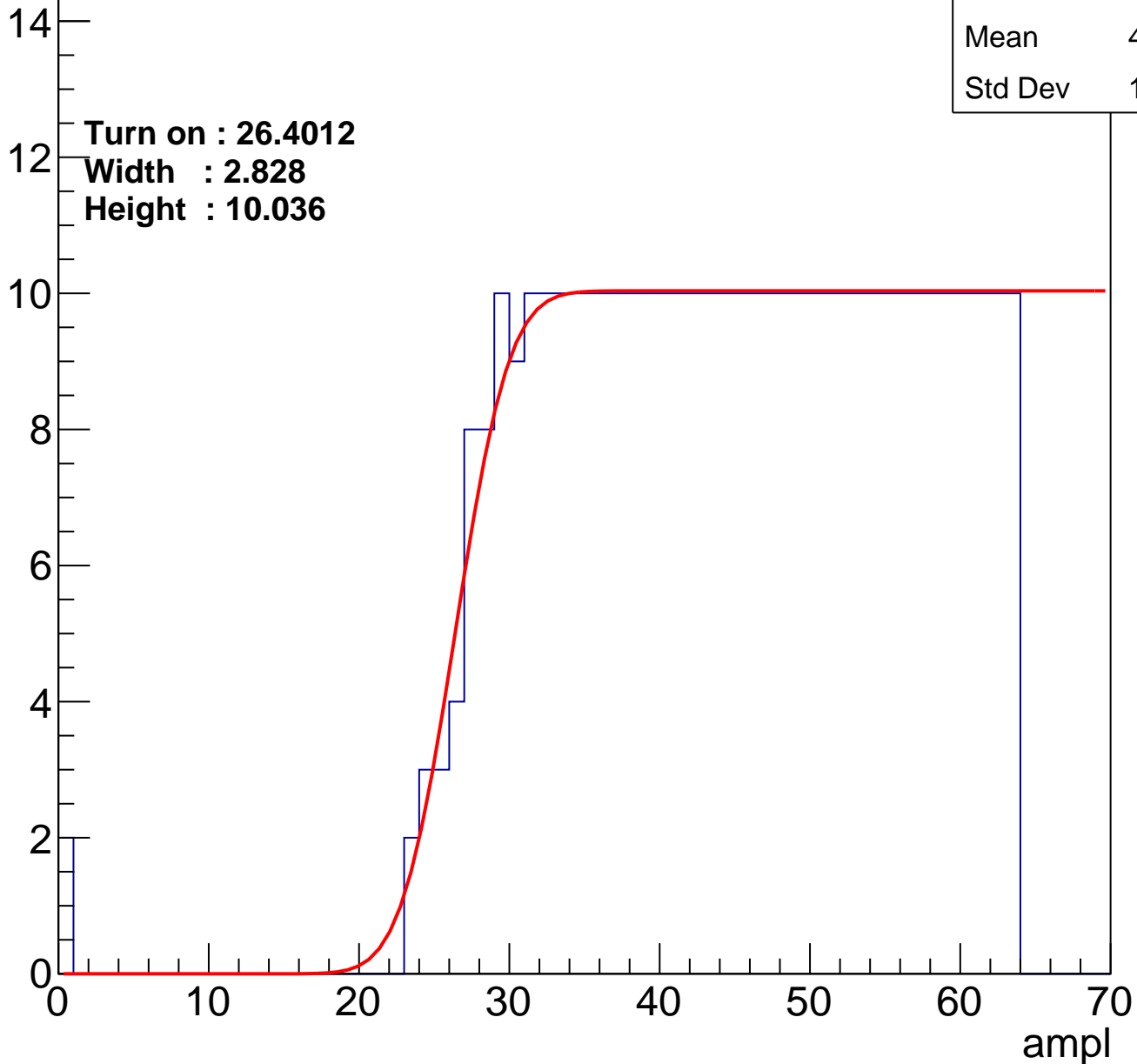
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.35 |
| Std Dev | 11.43 |

Turn on : 26.4012

Width : 2.828

Height : 10.036

Entry



B1L003S, U26-ch73

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.78 |
| Std Dev | 11.75 |

Turn on : 25.4883

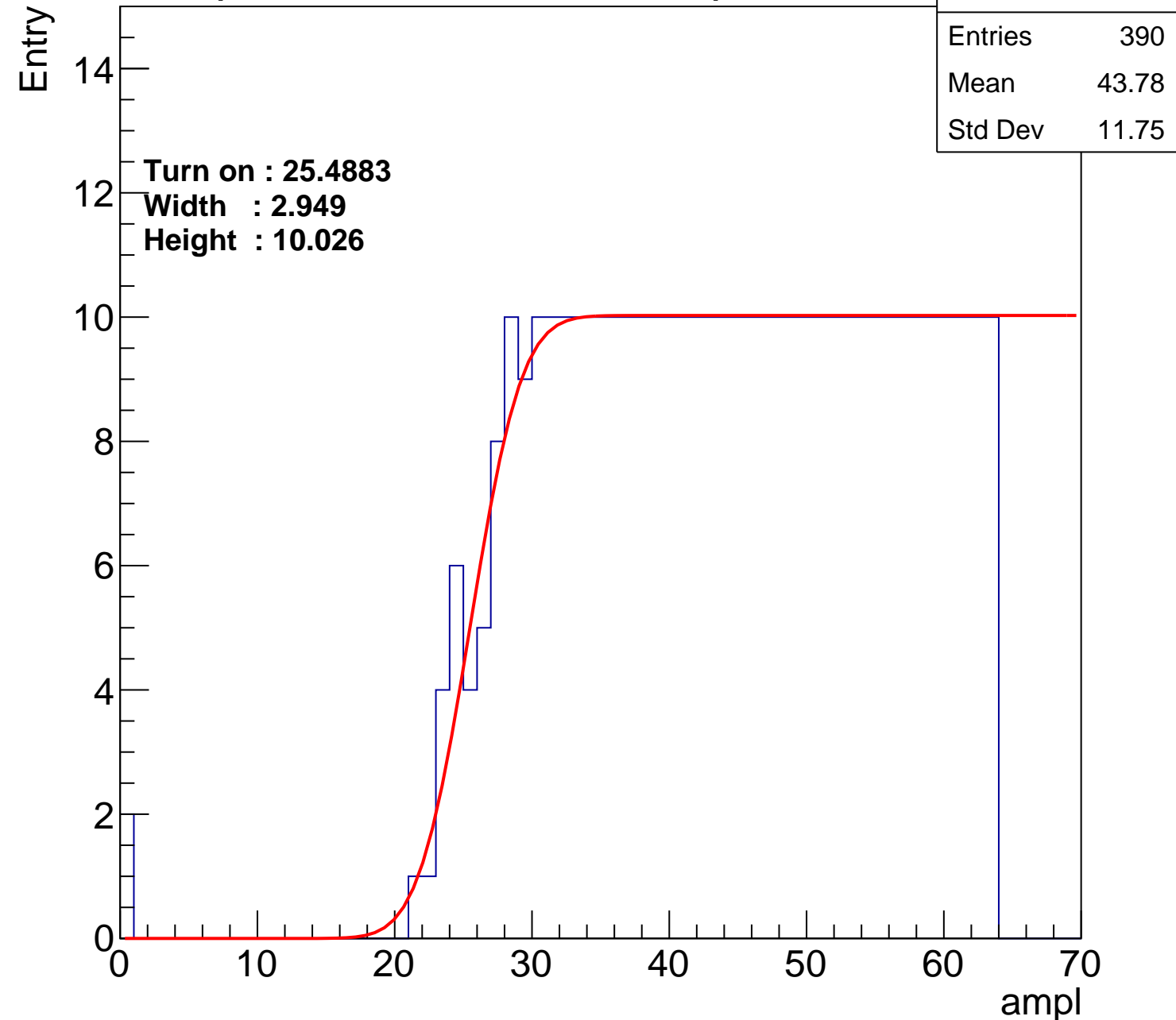
Width : 2.949

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch74

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|------|
| Entries | 387 |
| Mean | 44 |
| Std Dev | 11.5 |

Turn on : 25.3699

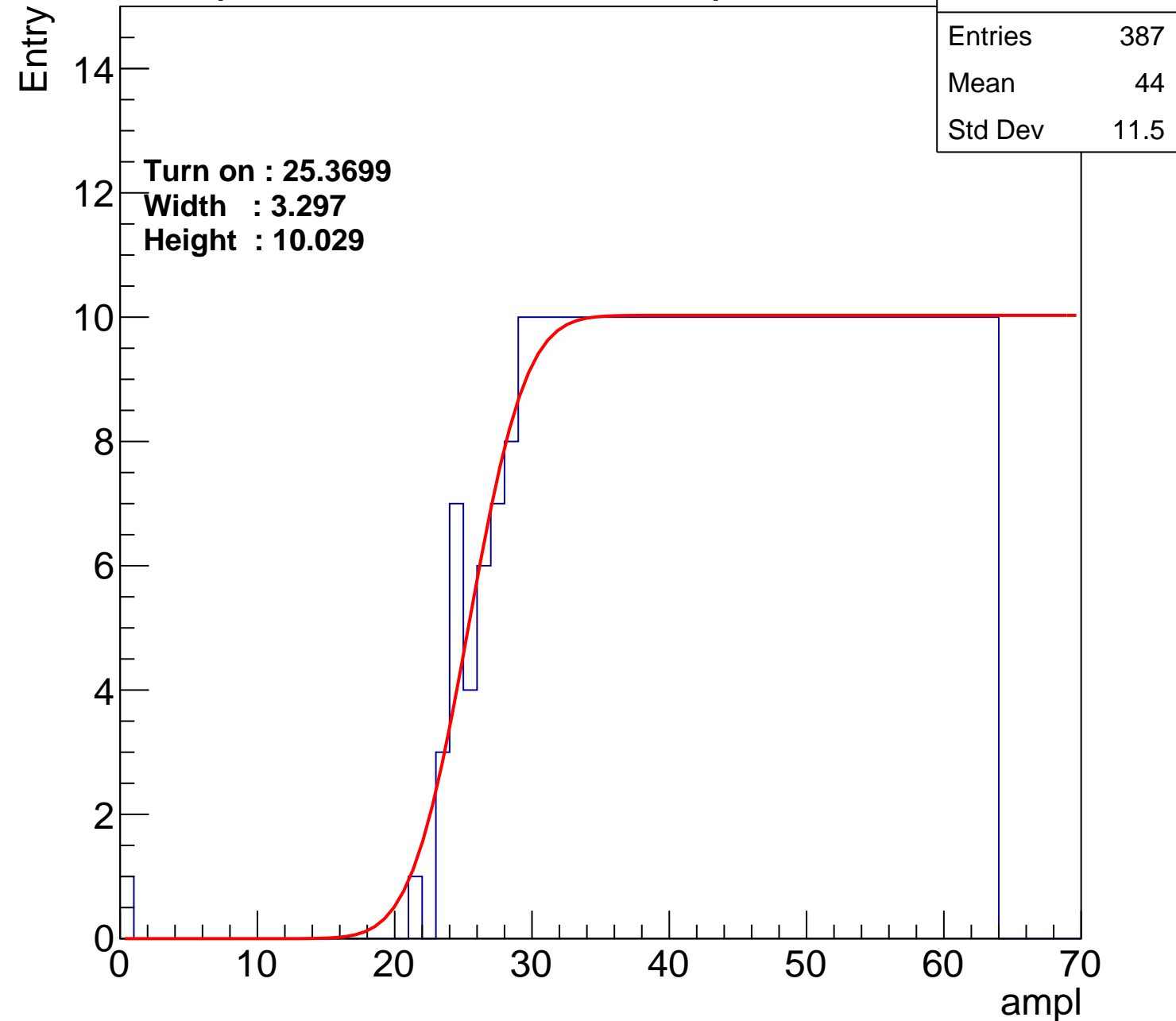
Width : 3.297

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch75

calib_packv5_042523_0143.root, FC#13, port D2

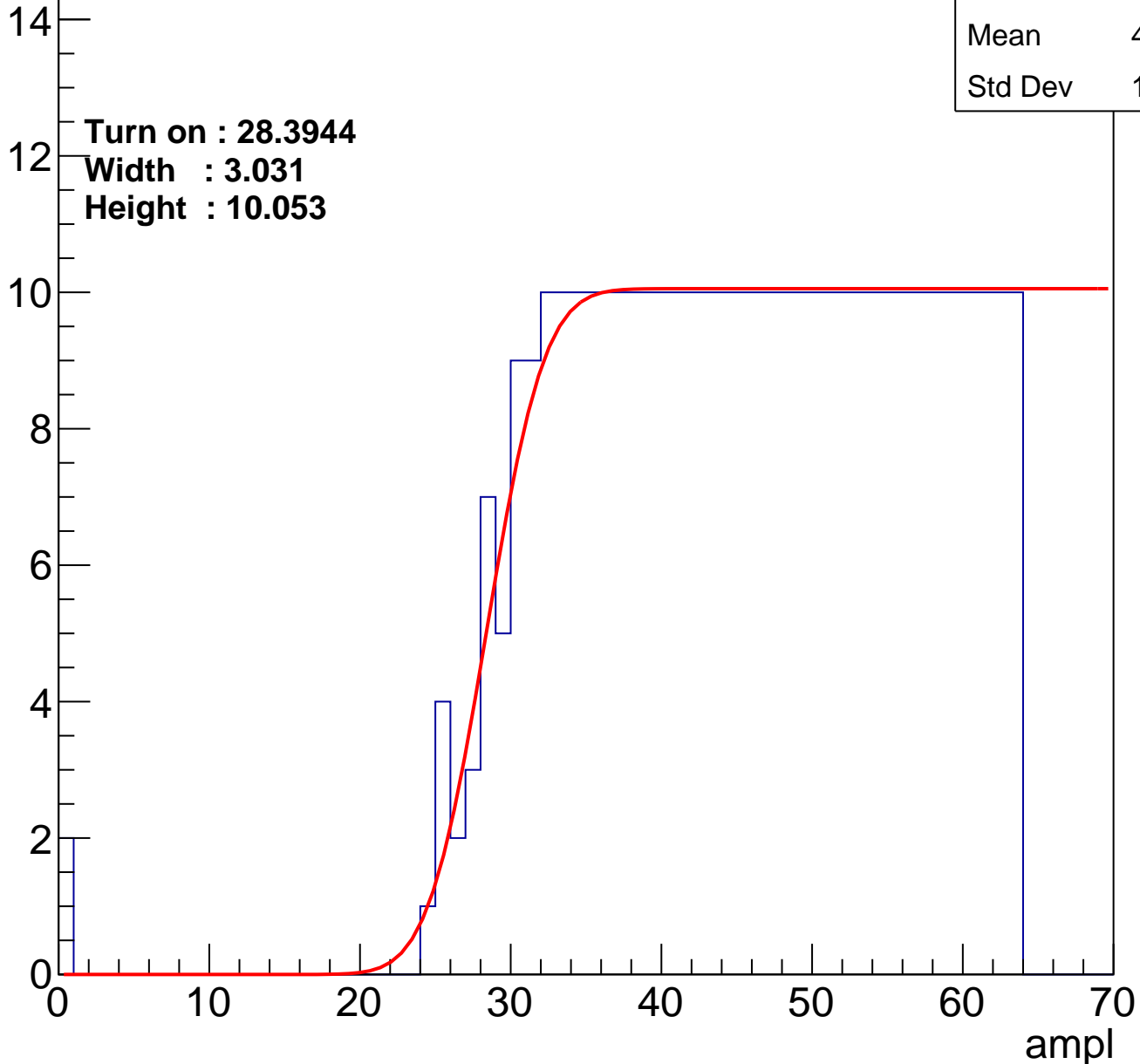
| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.16 |
| Std Dev | 11.04 |

Turn on : 28.3944

Width : 3.031

Height : 10.053

Entry



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.2736
Width : 4.007
Height : 10.046



B1L003S, U26-ch77

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.26 |
| Std Dev | 11.39 |

Turn on : 25.8097

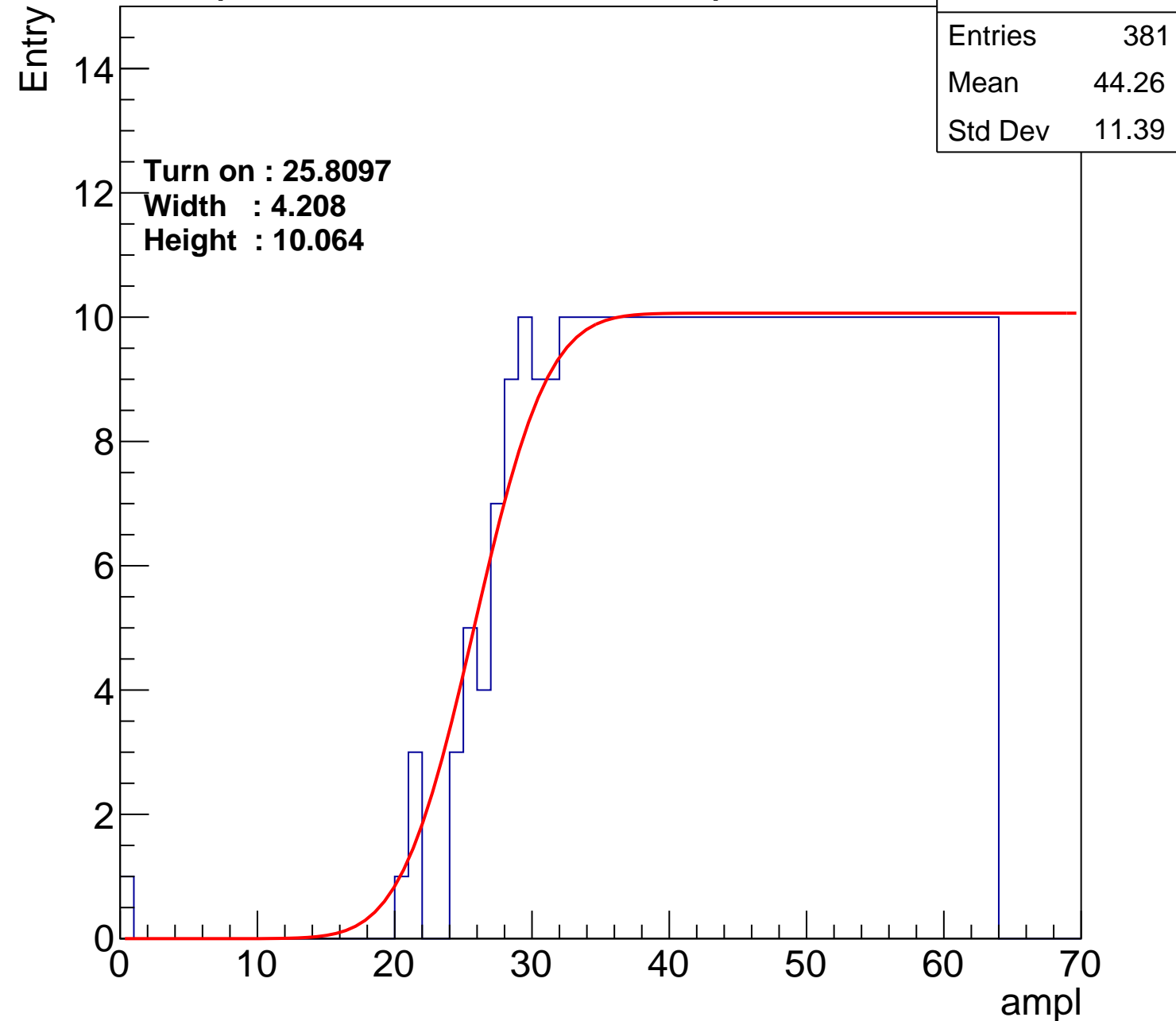
Width : 4.208

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch78

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.13 |
| Std Dev | 11.86 |

Turn on : 26.2983

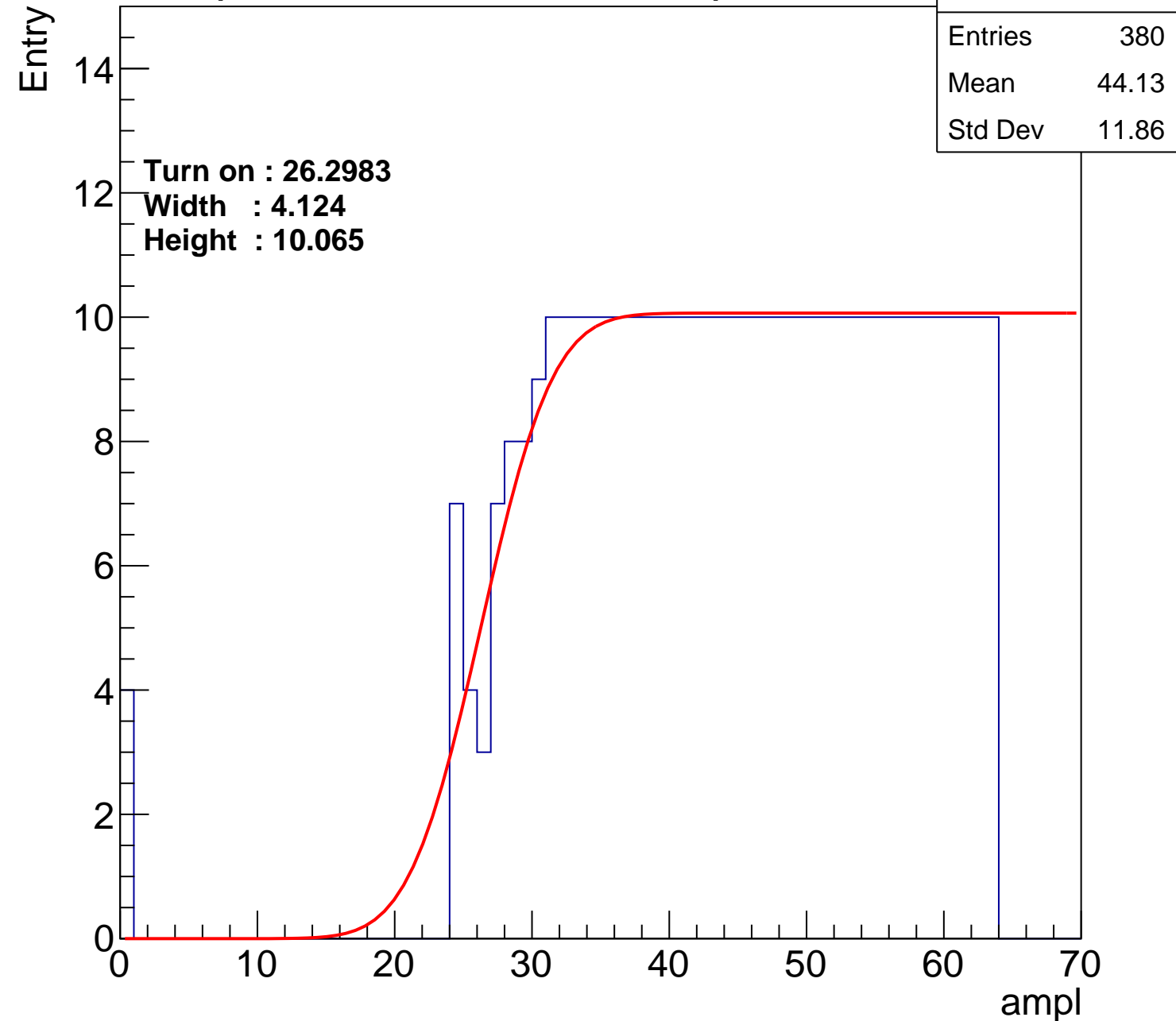
Width : 4.124

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch79

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.43 |
| Std Dev | 11.46 |

Turn on : 27.0097

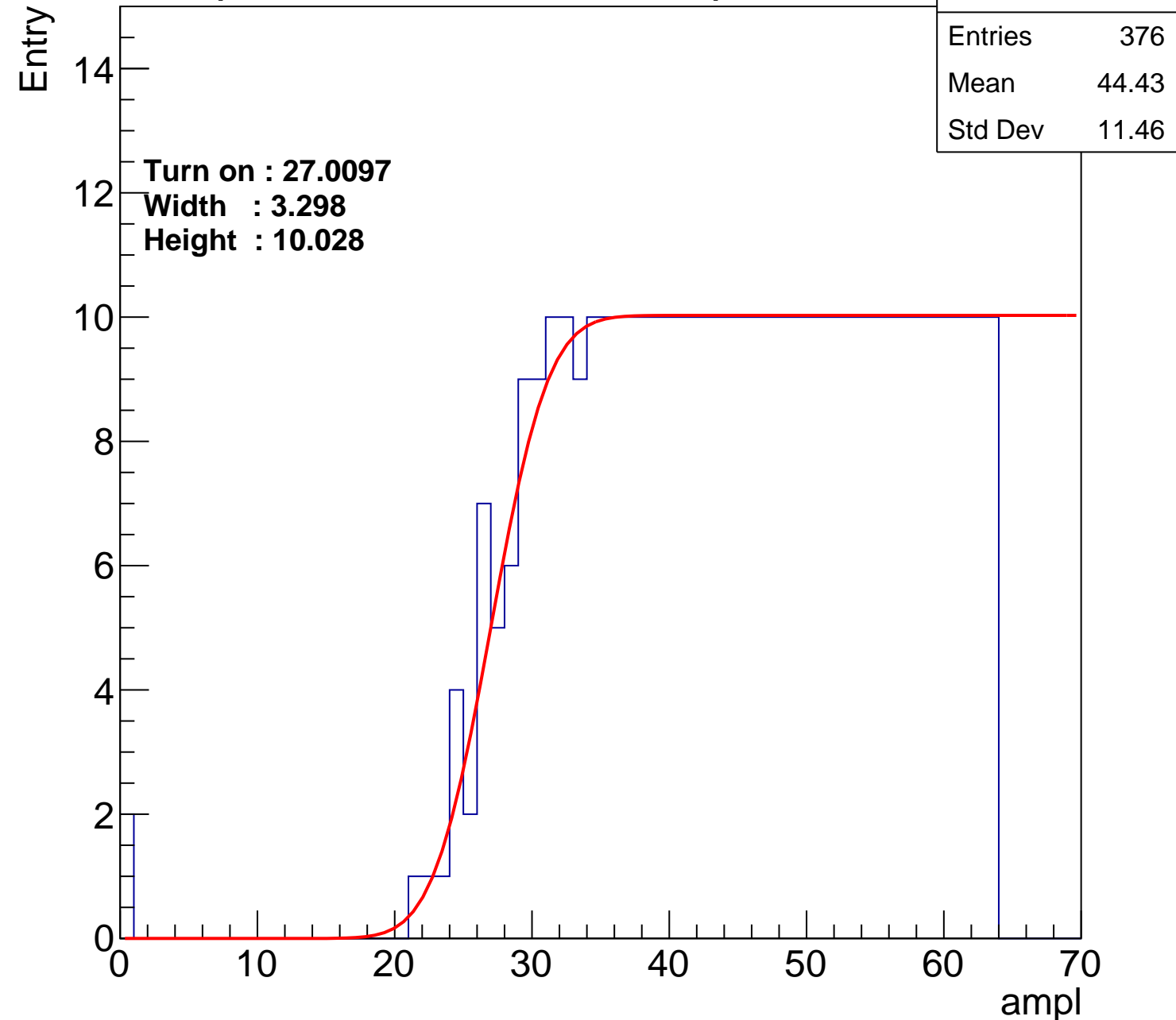
Width : 3.298

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch80

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.74 |
| Std Dev | 11.41 |

Turn on : 27.3723

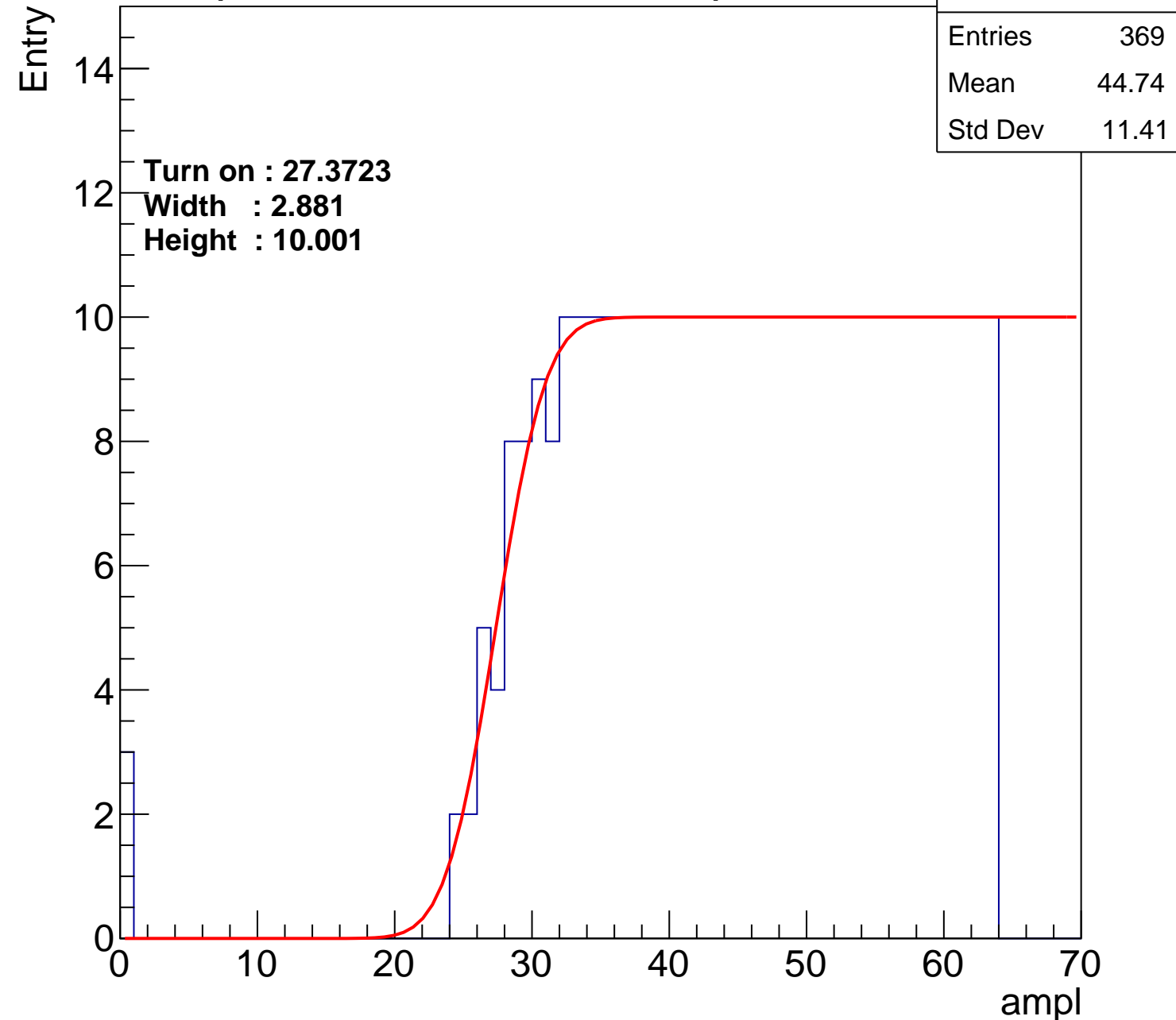
Width : 2.881

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch81

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.59 |
| Std Dev | 11.17 |

Turn on : 26.5933

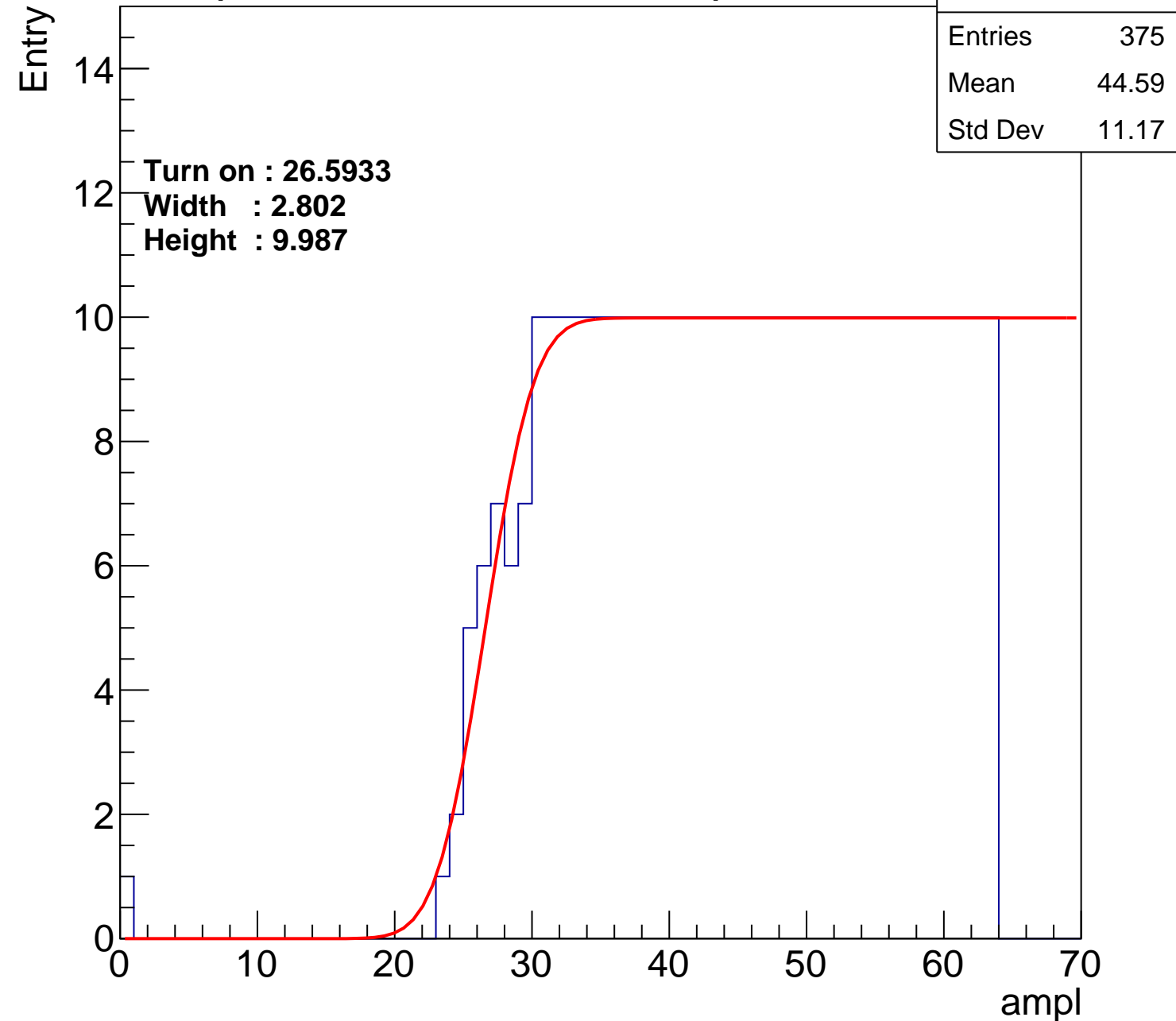
Width : 2.802

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch82

calib_packv5_042523_0143.root, FC#13, port D2

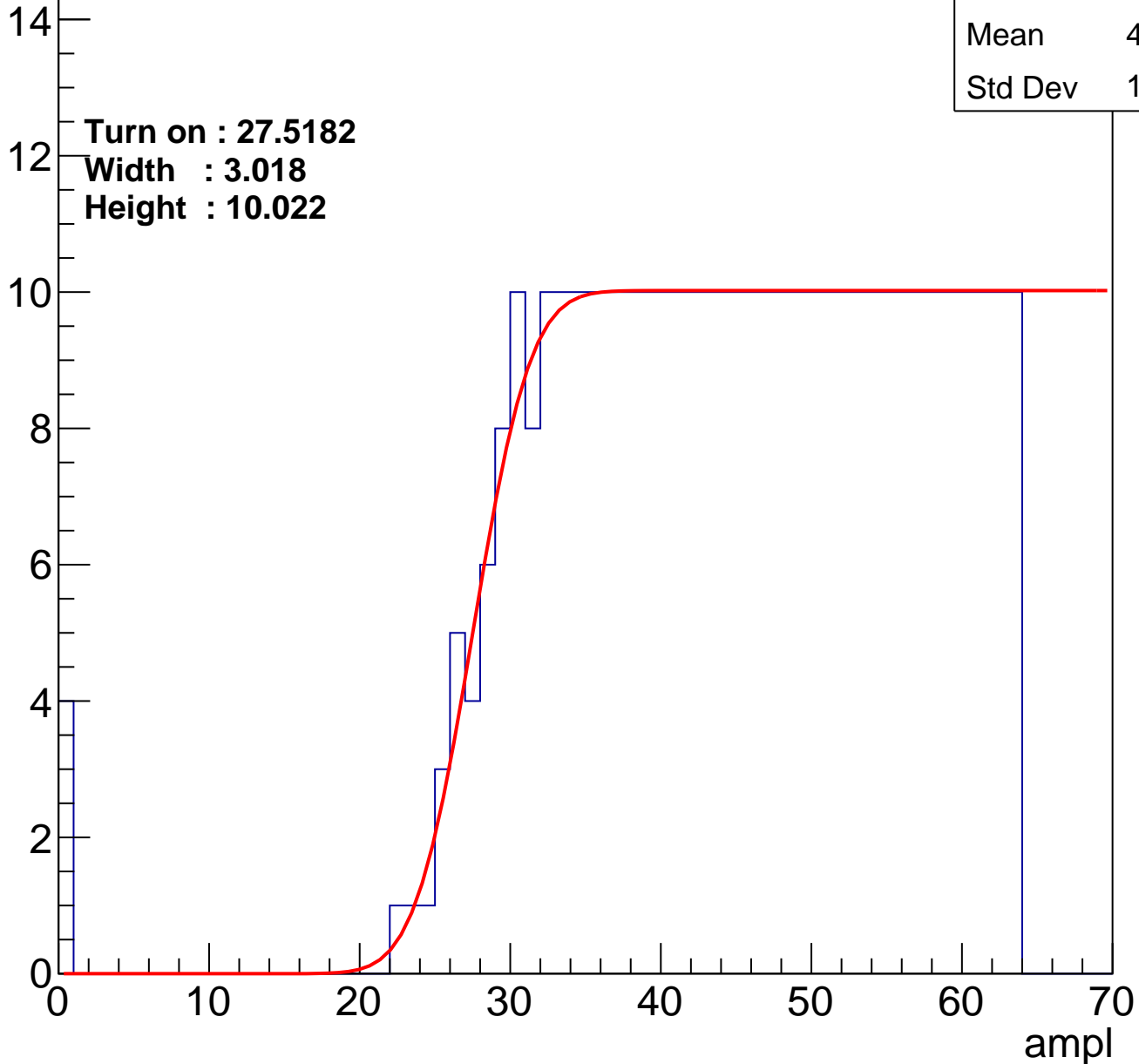
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.56 |
| Std Dev | 11.69 |

Turn on : 27.5182

Width : 3.018

Height : 10.022

Entry



B1L003S, U26-ch83

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.11 |
| Std Dev | 10.93 |

Turn on : 27.7811

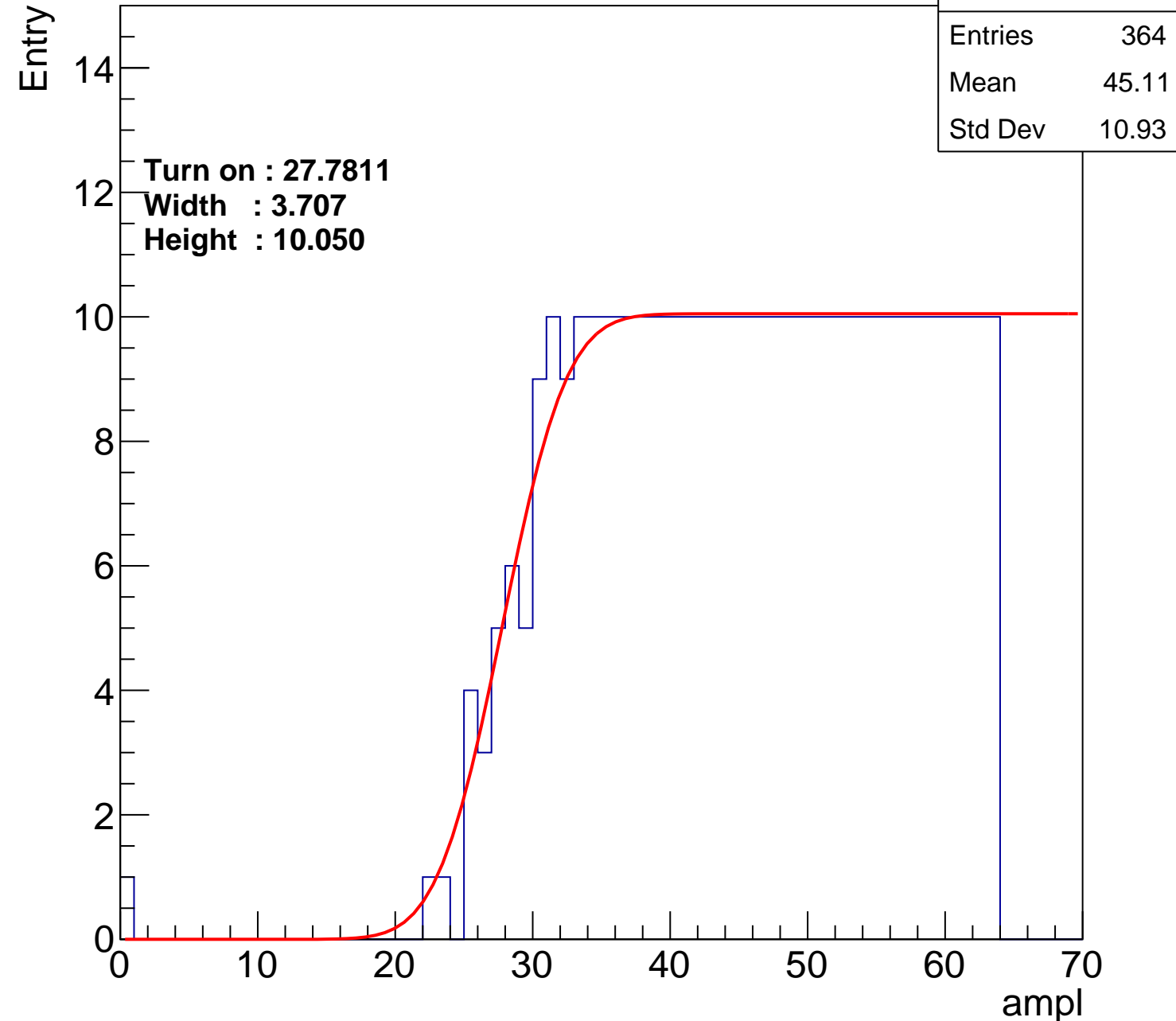
Width : 3.707

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch84

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.1 |
| Std Dev | 10.88 |

Turn on : 27.5257

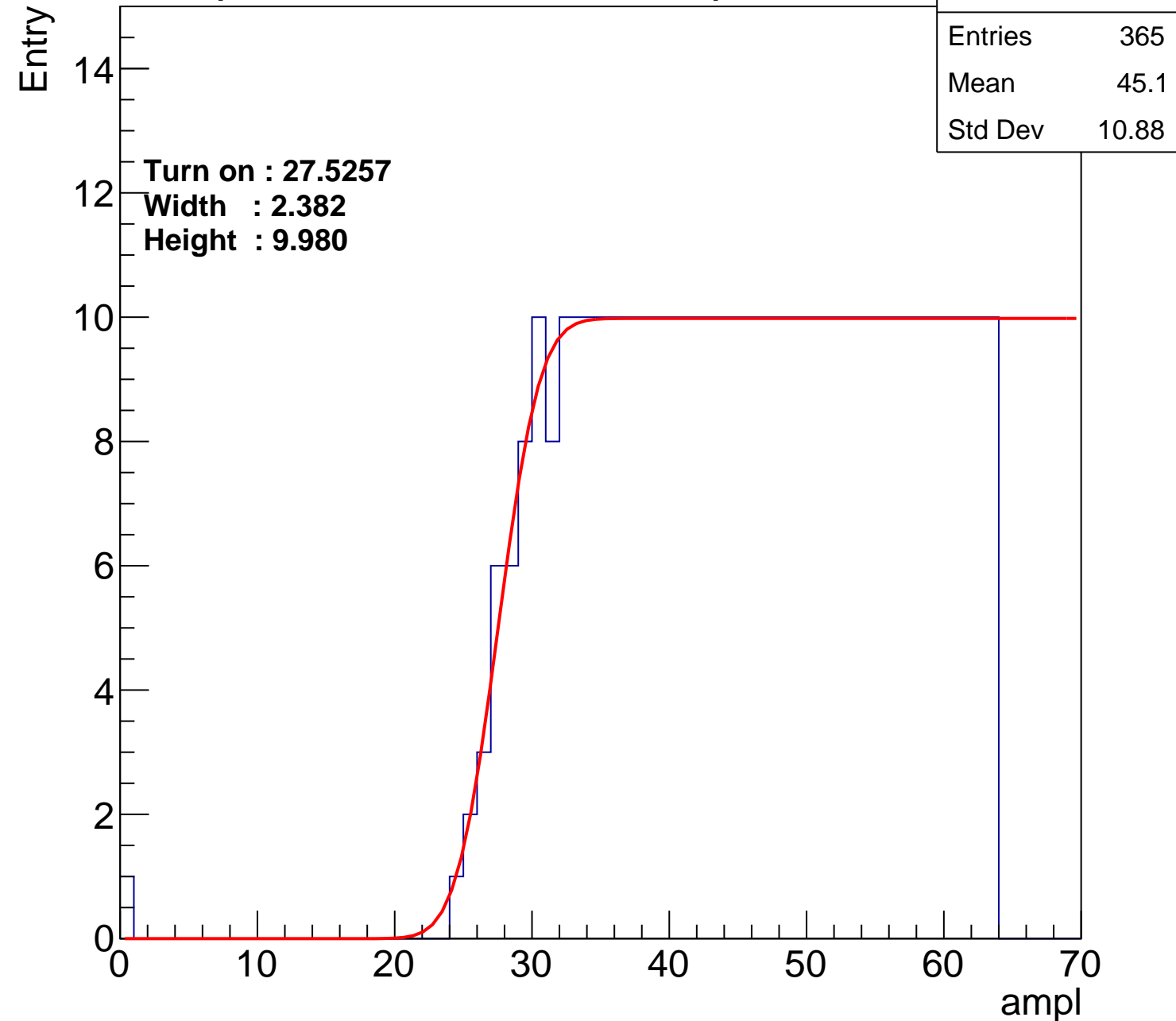
Width : 2.382

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch85

calib_packv5_042523_0143.root, FC#13, port D2

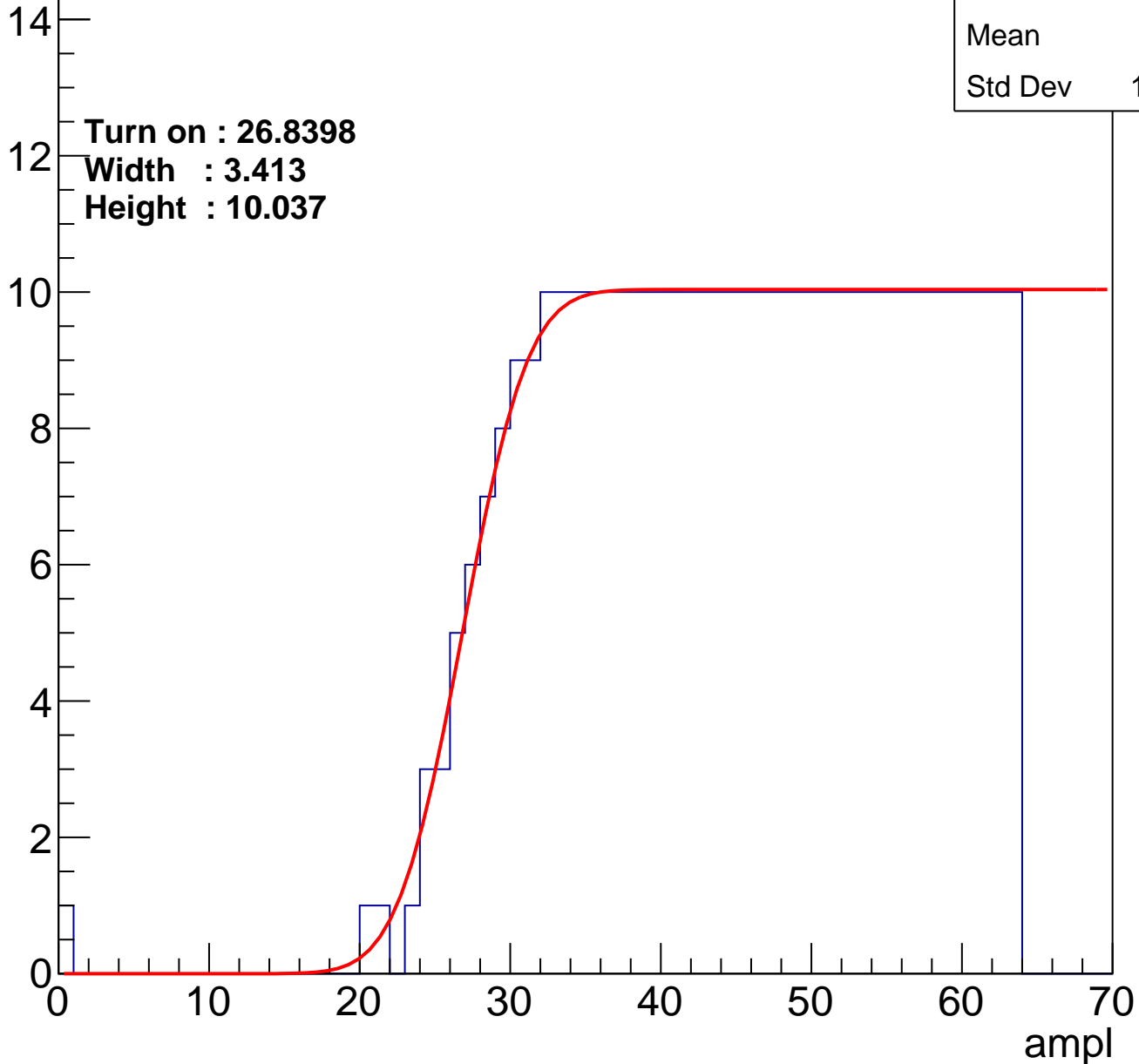
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.6 |
| Std Dev | 11.22 |

Turn on : 26.8398

Width : 3.413

Height : 10.037

Entry



B1L003S, U26-ch86

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.09 |
| Std Dev | 11.59 |

Turn on : 26.0150

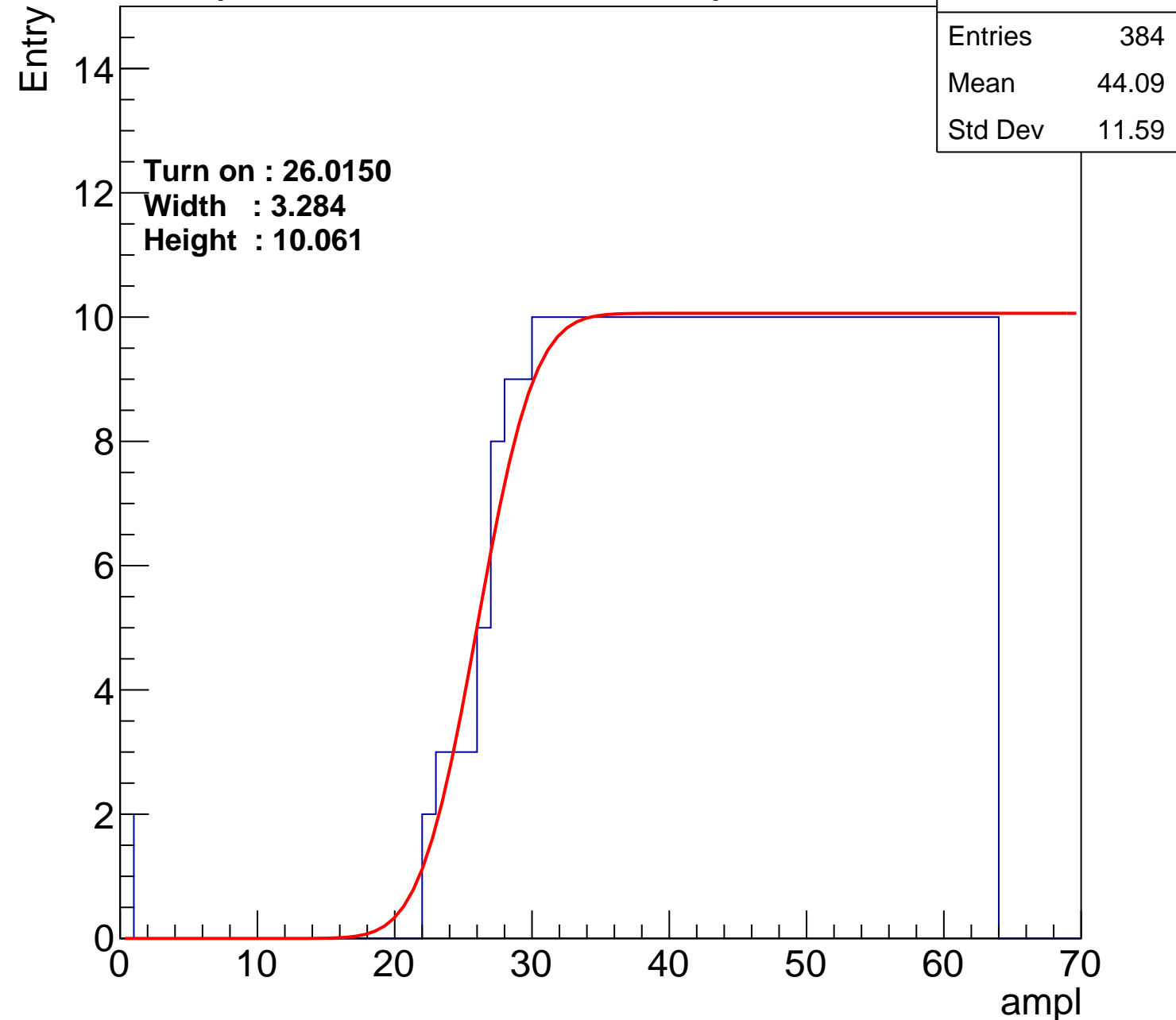
Width : 3.284

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch87

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.78 |
| Std Dev | 12.17 |

Turn on : 26.2746

Width : 3.090

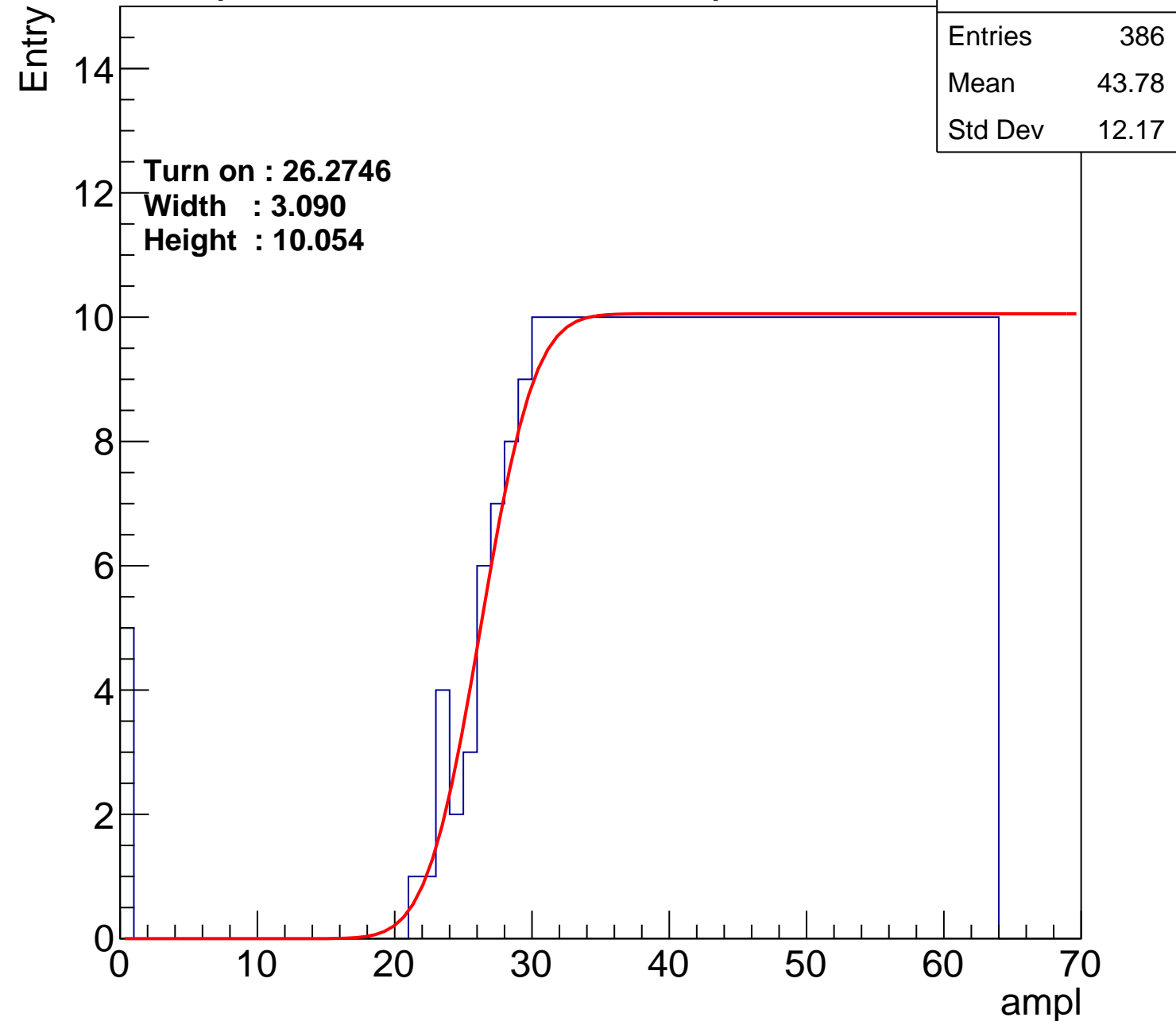
Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U26-ch88

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.94 |
| Std Dev | 11.65 |

Turn on : 25.4506

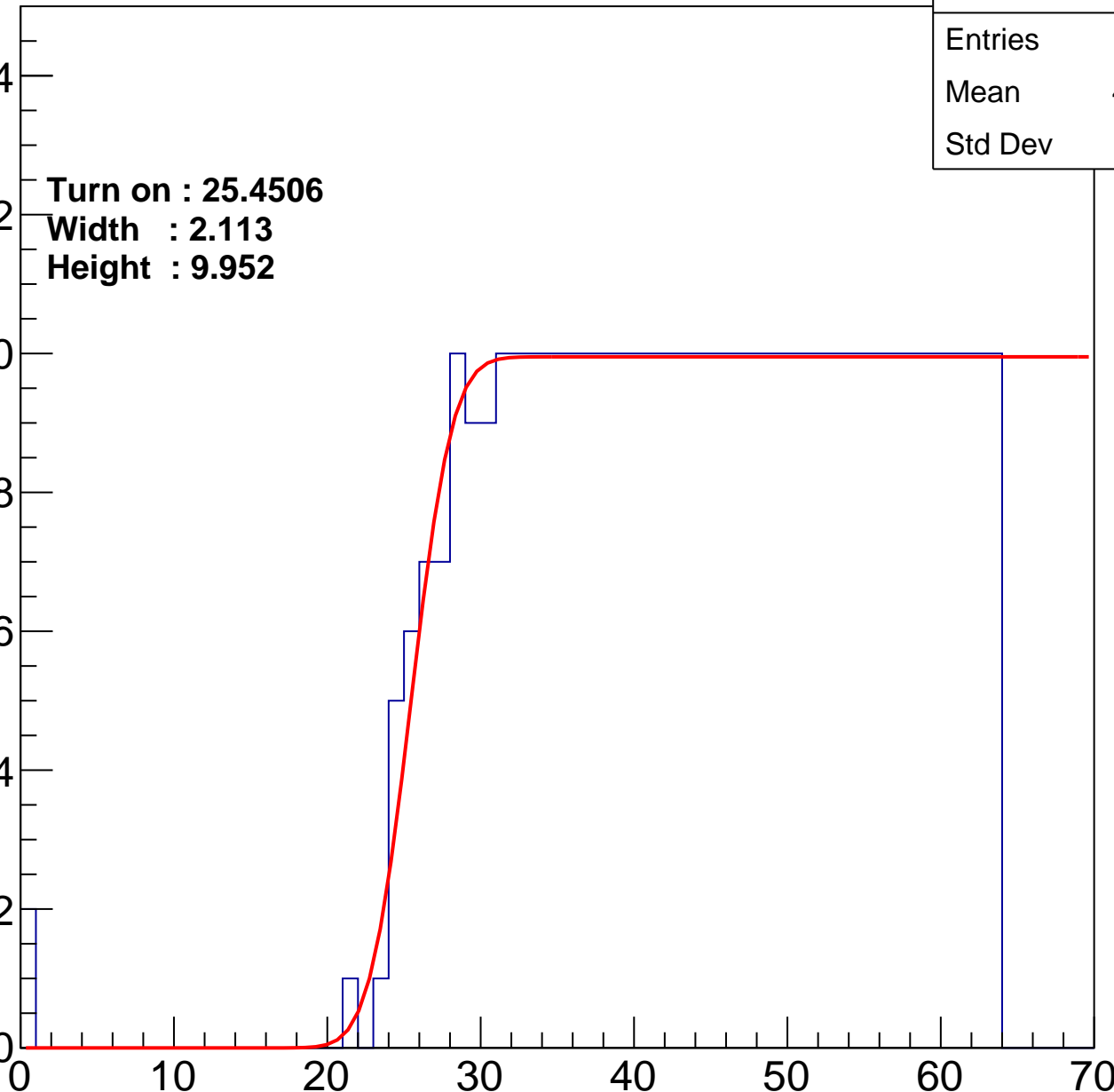
Width : 2.113

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch89

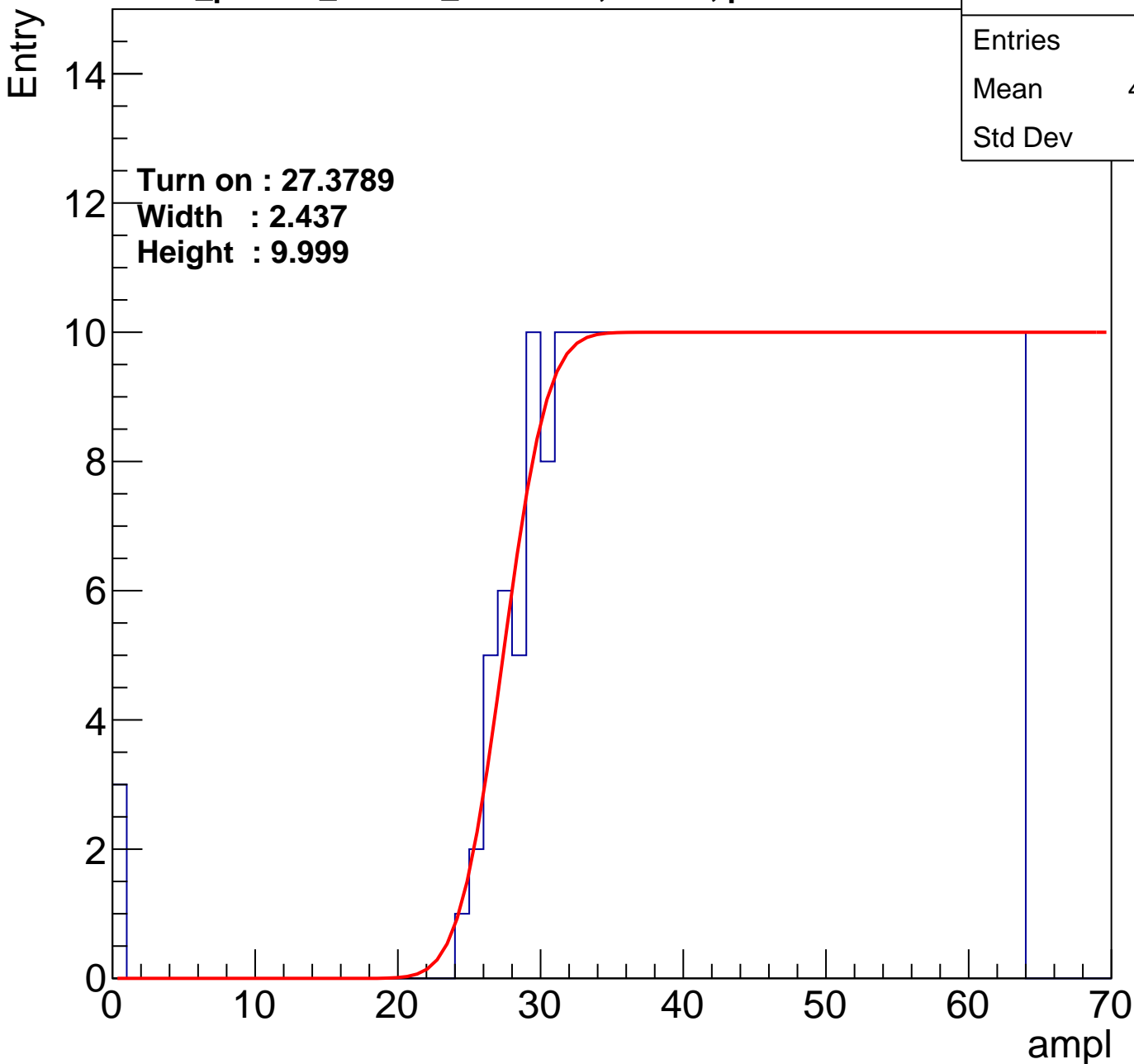
calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.72 |
| Std Dev | 11.4 |

Turn on : 27.3789

Width : 2.437

Height : 9.999



B1L003S, U26-ch90

calib_packv5_042523_0143.root, FC#13, port D2

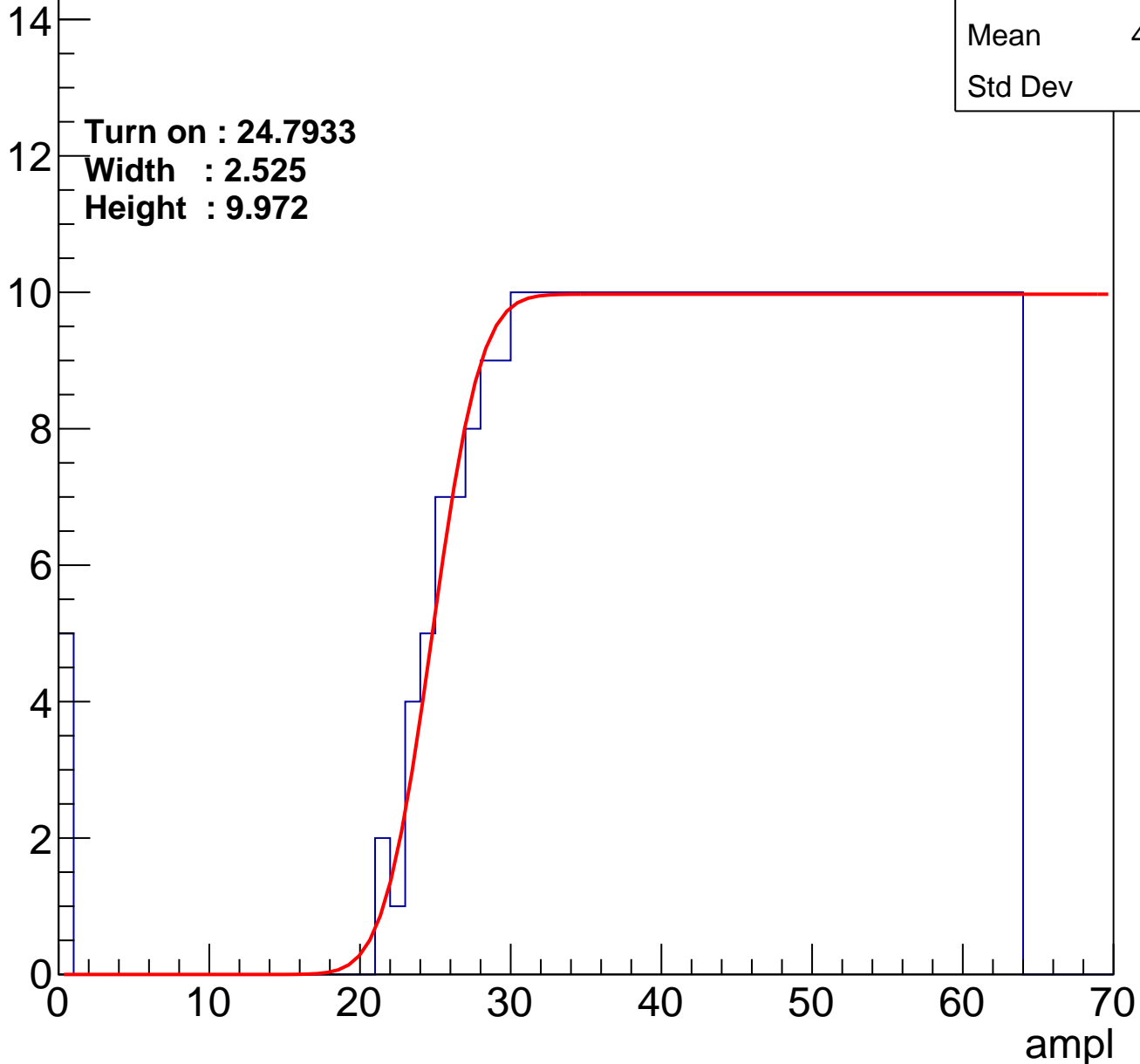
| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.25 |
| Std Dev | 12.4 |

Turn on : 24.7933

Width : 2.525

Height : 9.972

Entry



B1L003S, U26-ch91

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.75 |
| Std Dev | 11.28 |

Turn on : 27.8289

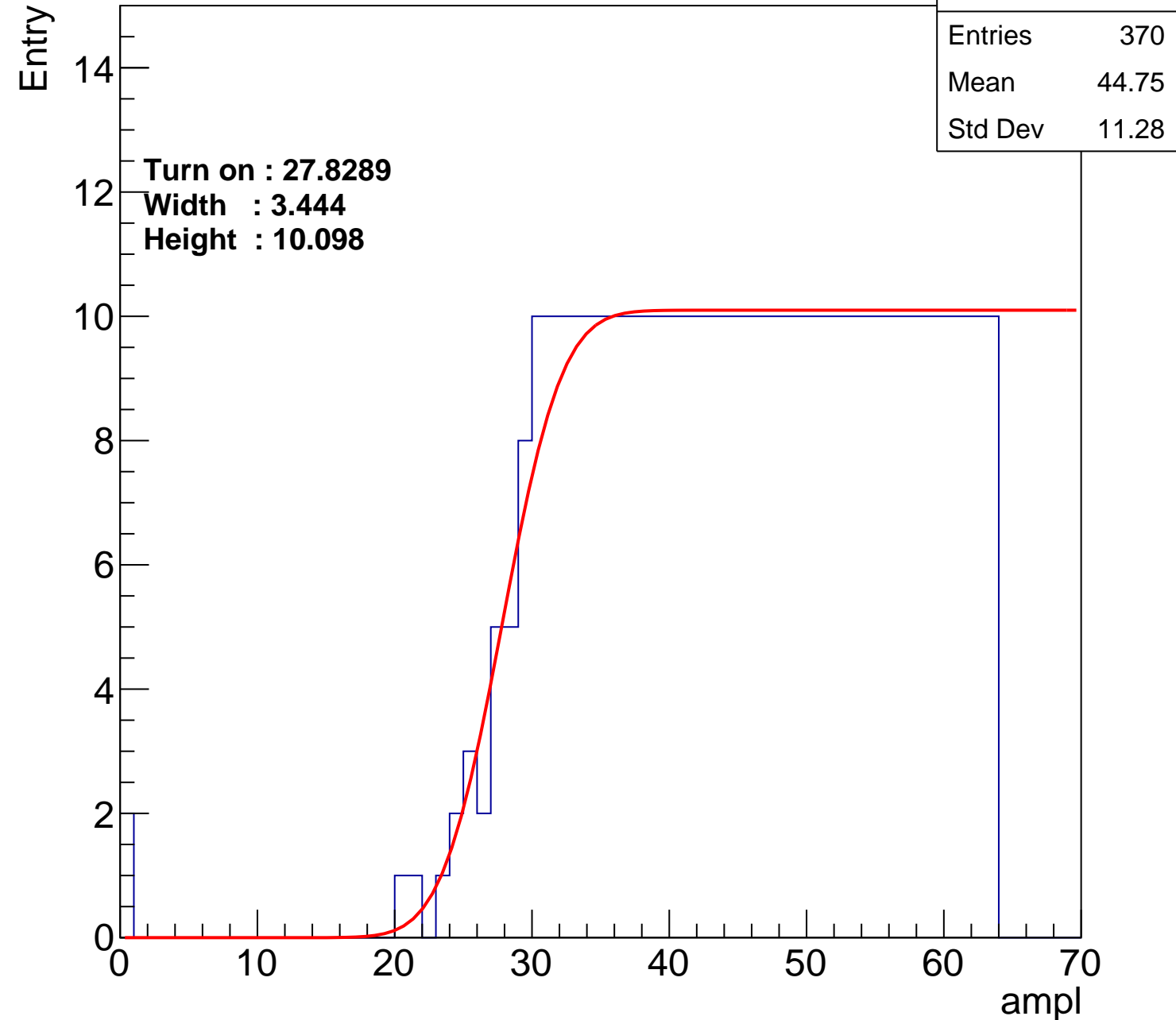
Width : 3.444

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch92

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.67 |
| Std Dev | 11.29 |

Turn on : 27.5649

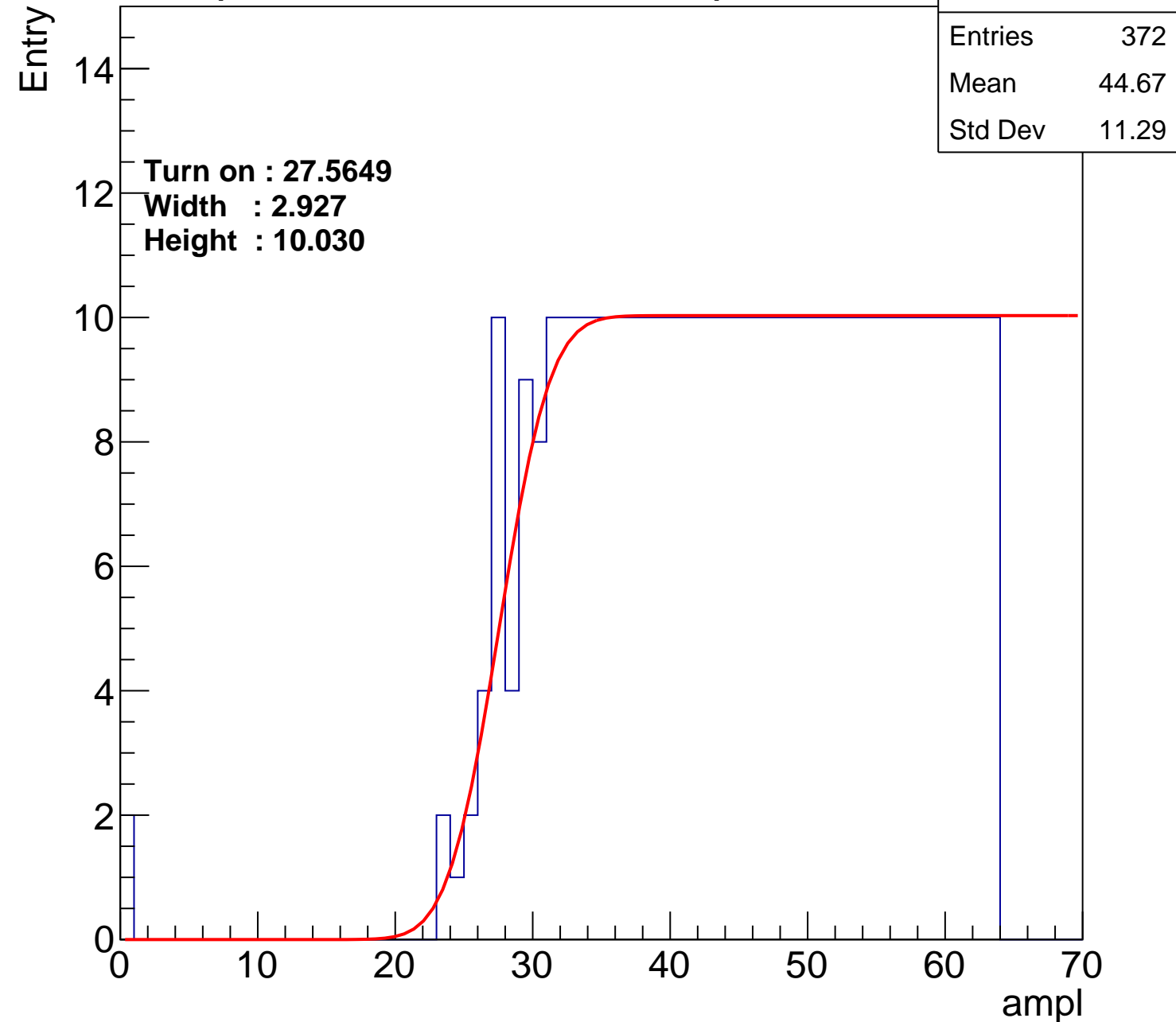
Width : 2.927

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch93

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.49 |
| Std Dev | 11.54 |

Turn on : 27.3077

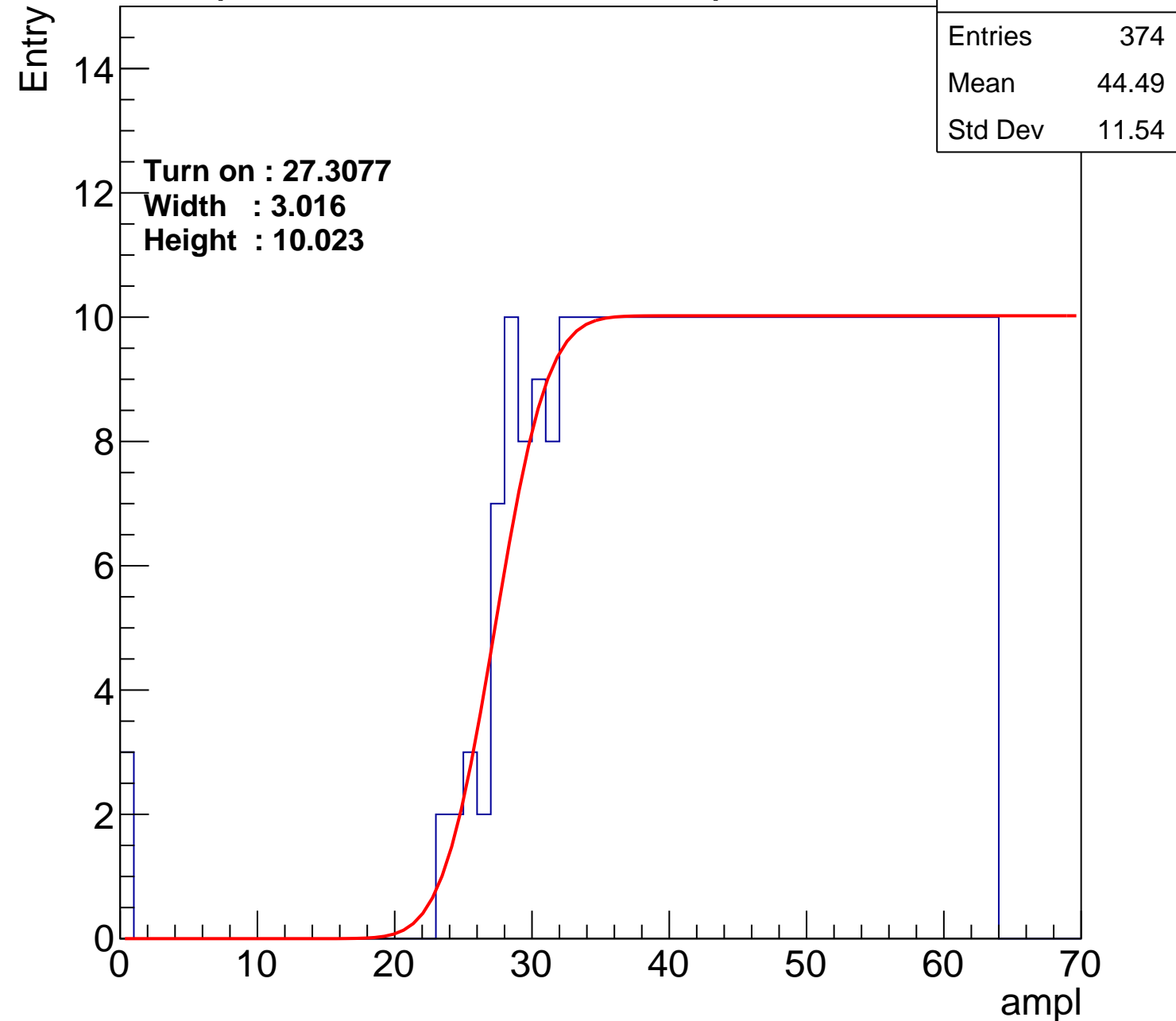
Width : 3.016

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch94

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.05 |
| Std Dev | 11.05 |

Turn on : 27.9993

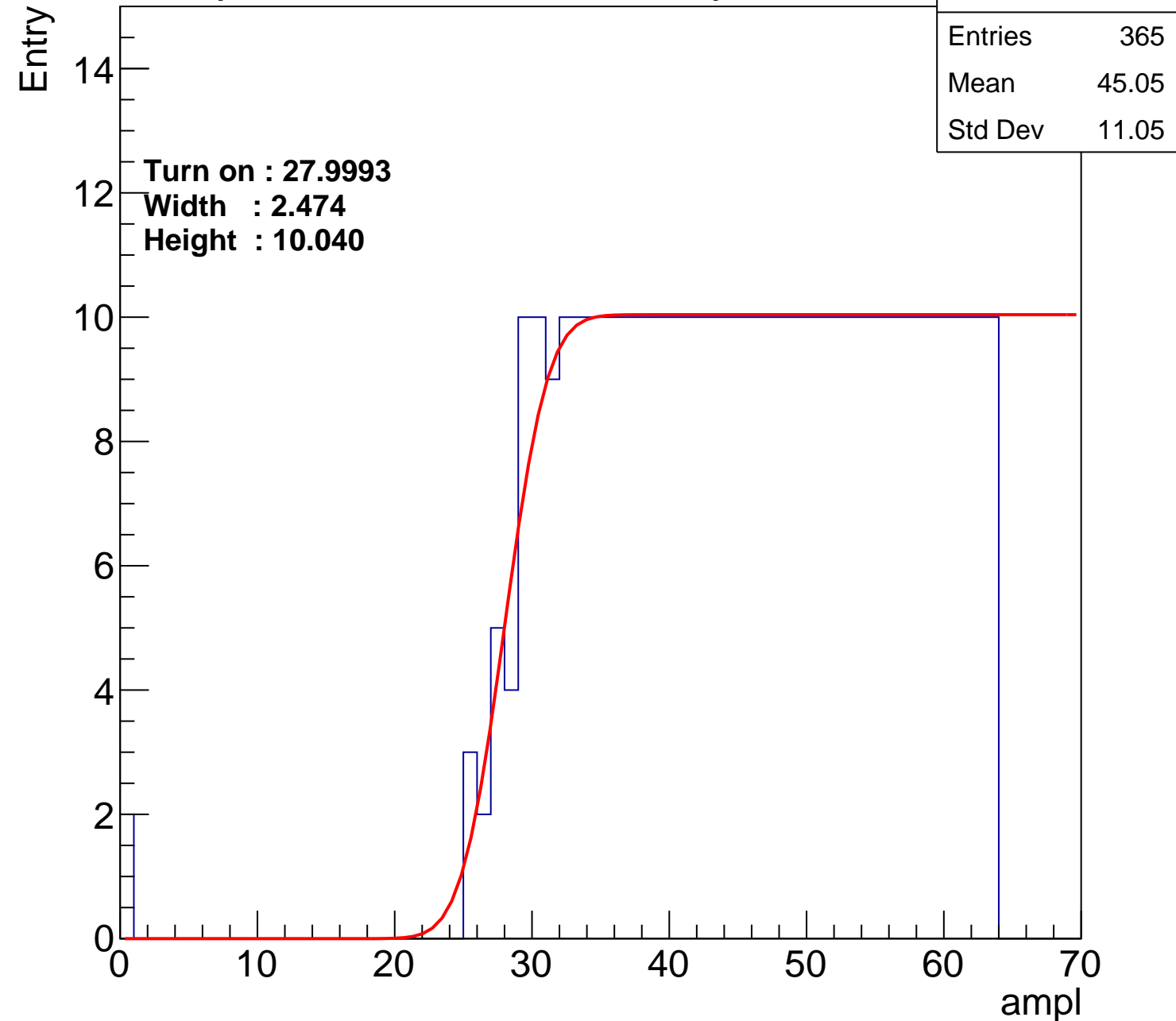
Width : 2.474

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch95

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.36 |
| Std Dev | 10.79 |

Turn on : 28.7213

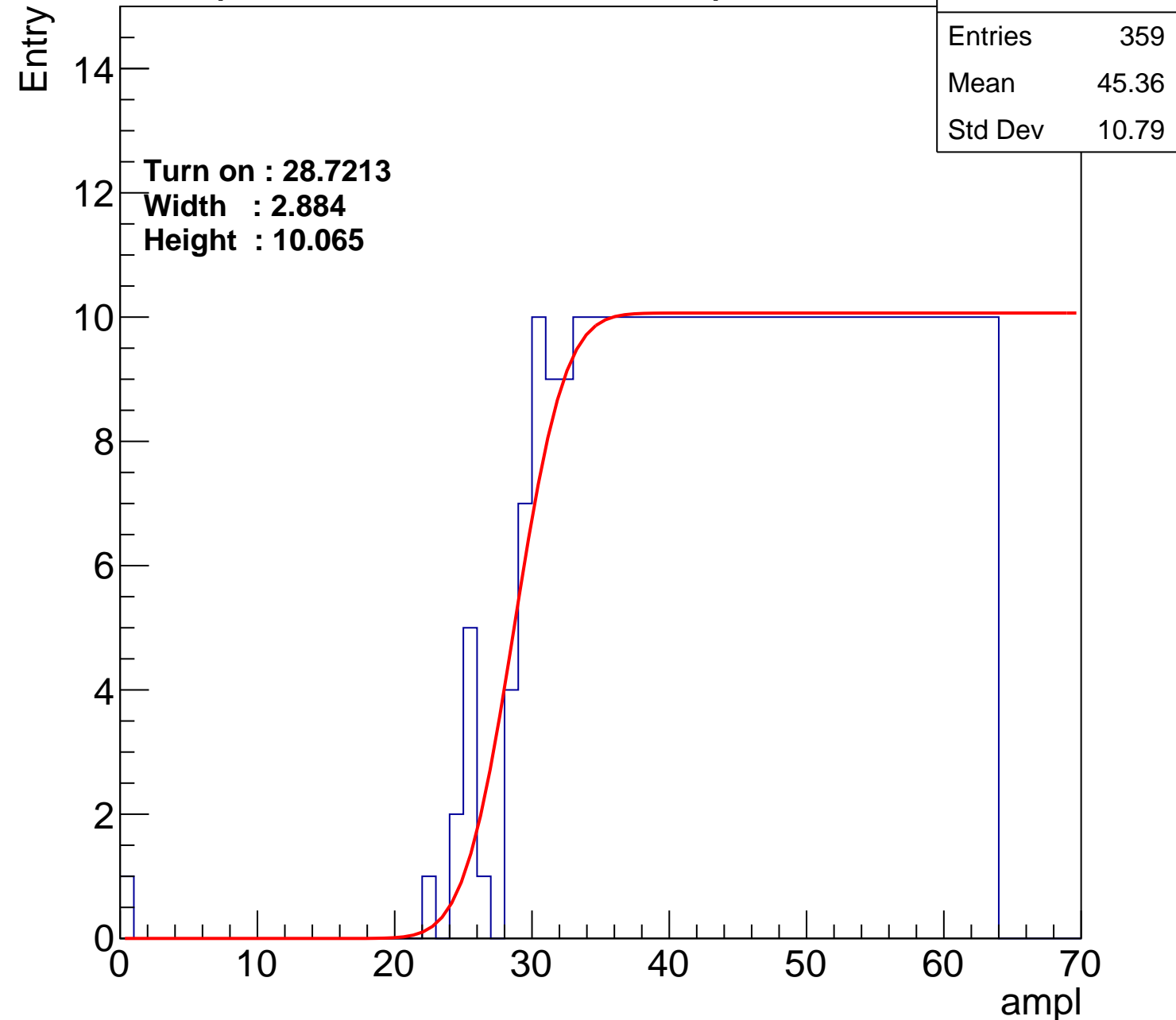
Width : 2.884

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch96

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.7 |
| Std Dev | 11.81 |

Turn on : 25.4076

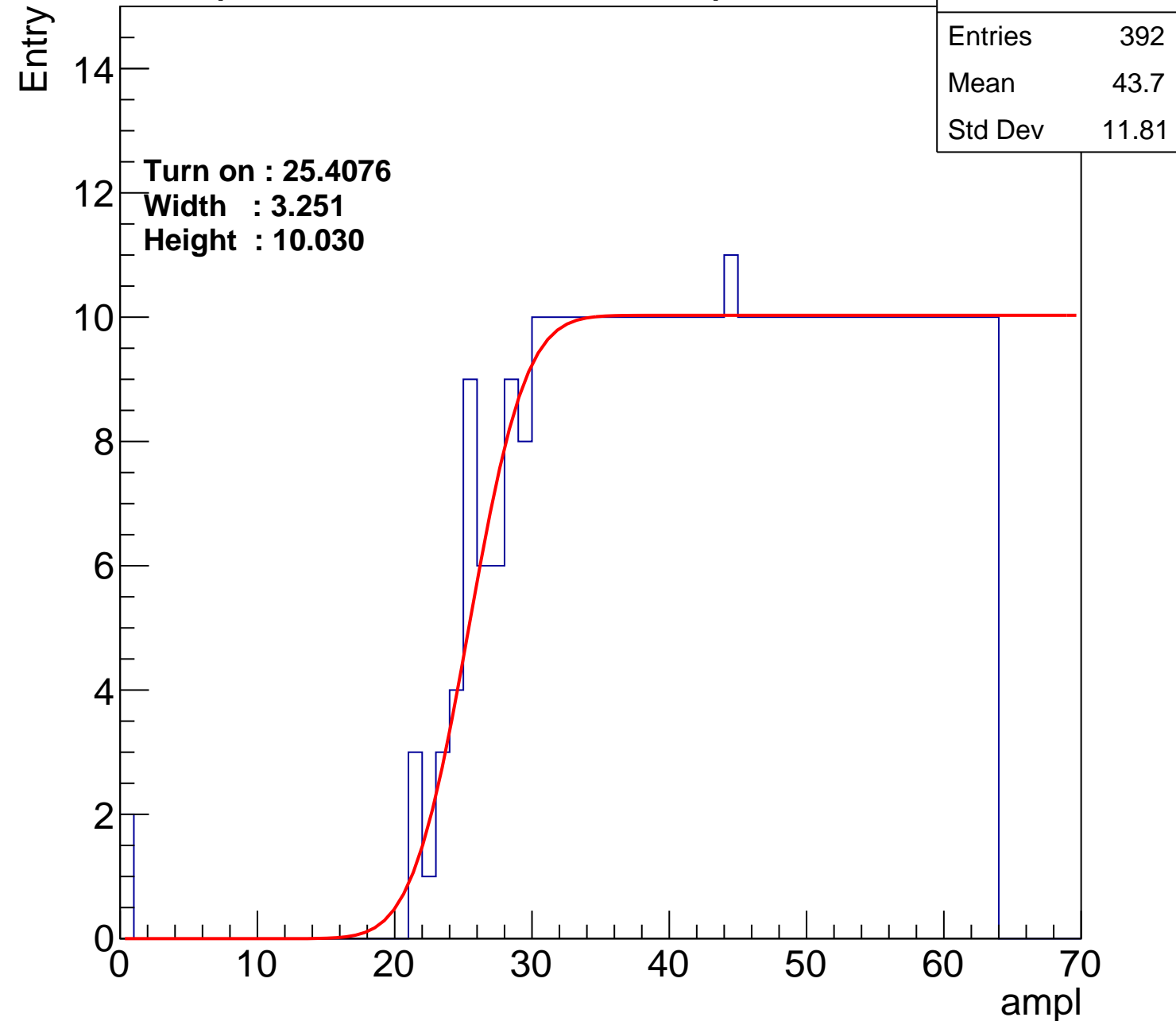
Width : 3.251

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch97

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.13 |
| Std Dev | 11.43 |

Turn on : 25.2030

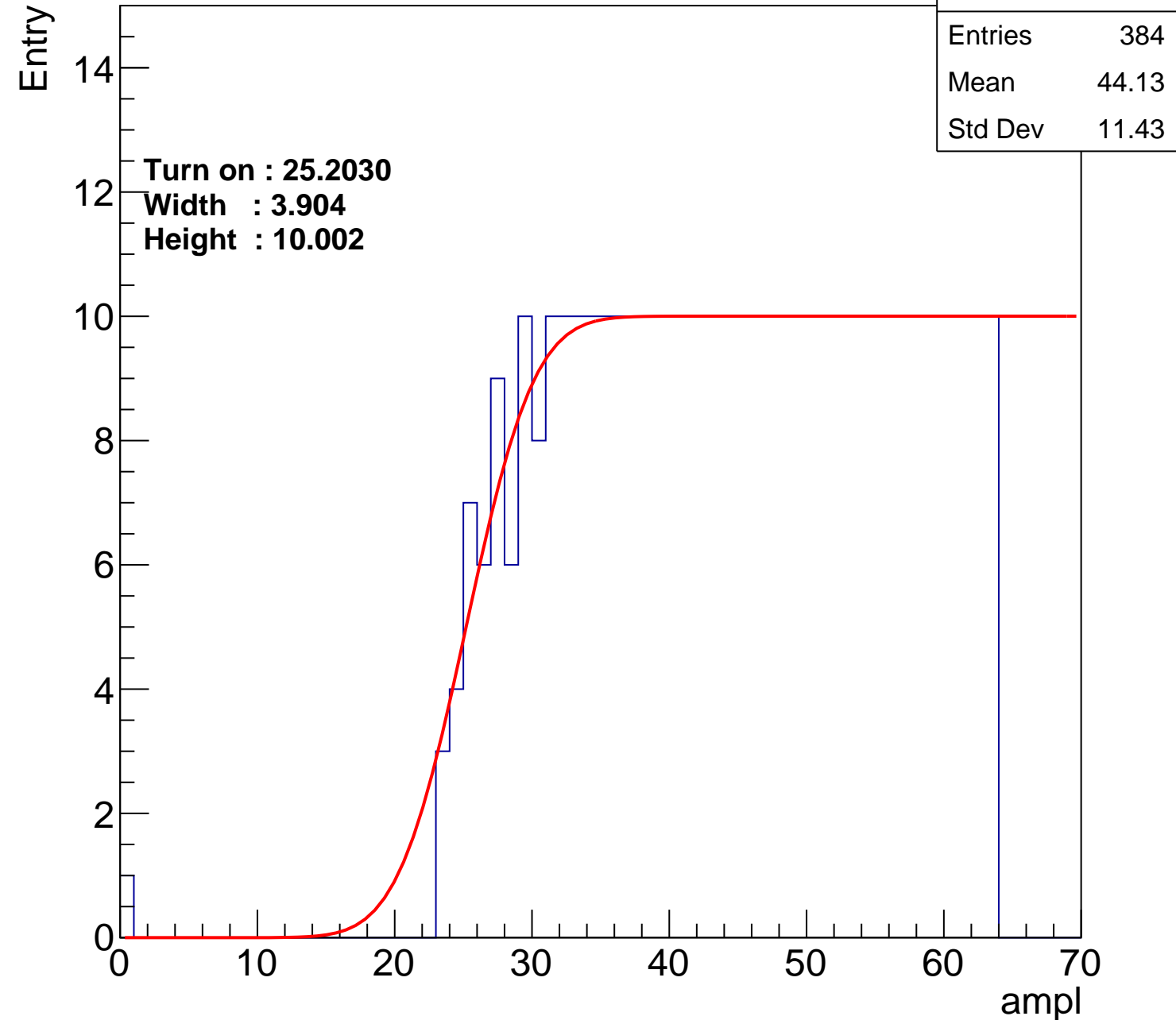
Width : 3.904

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch98

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.39 |
| Std Dev | 11.6 |

Turn on : 26.7754

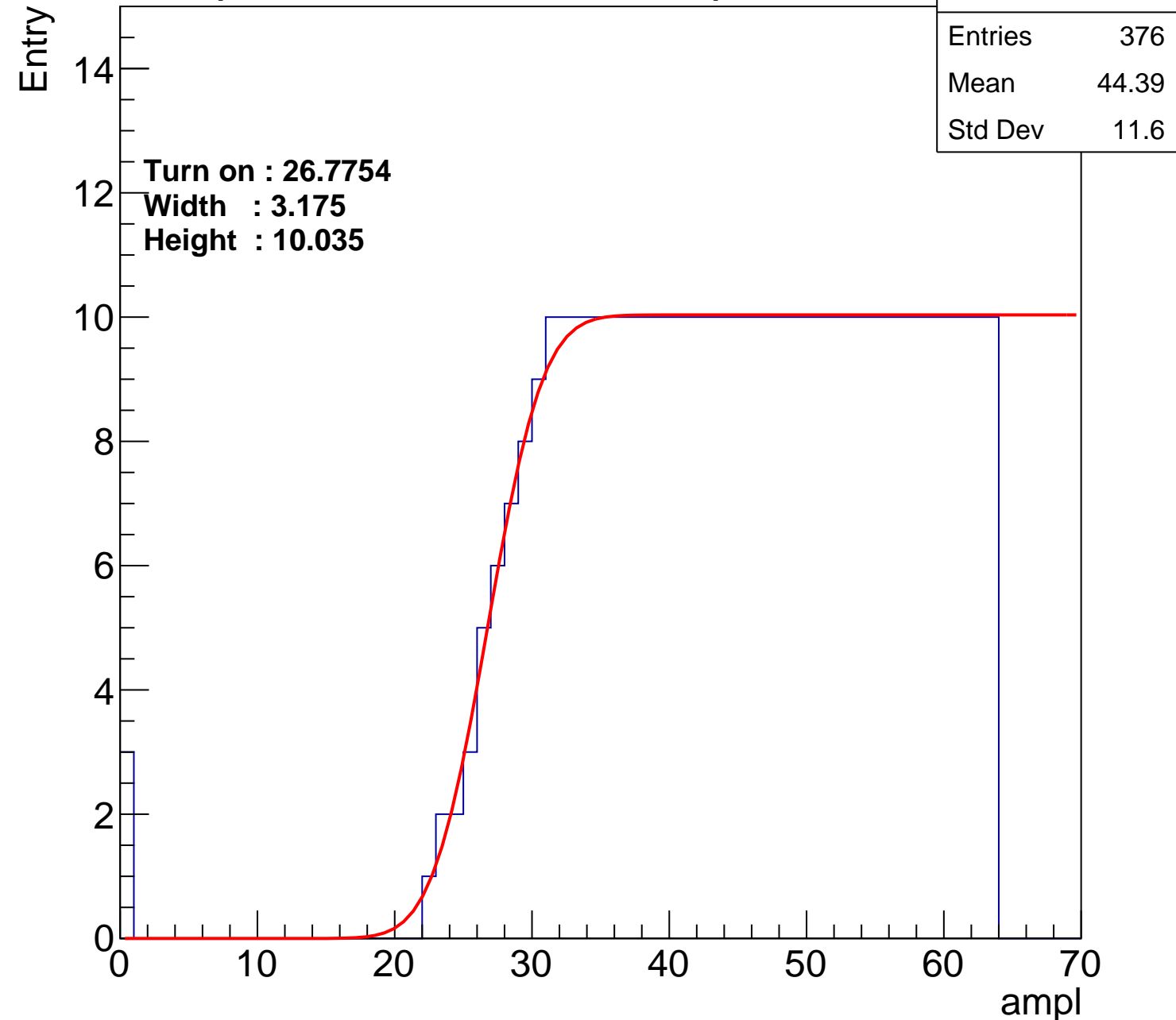
Width : 3.175

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch99

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.76 |
| Std Dev | 11.9 |

Turn on : 25.4780

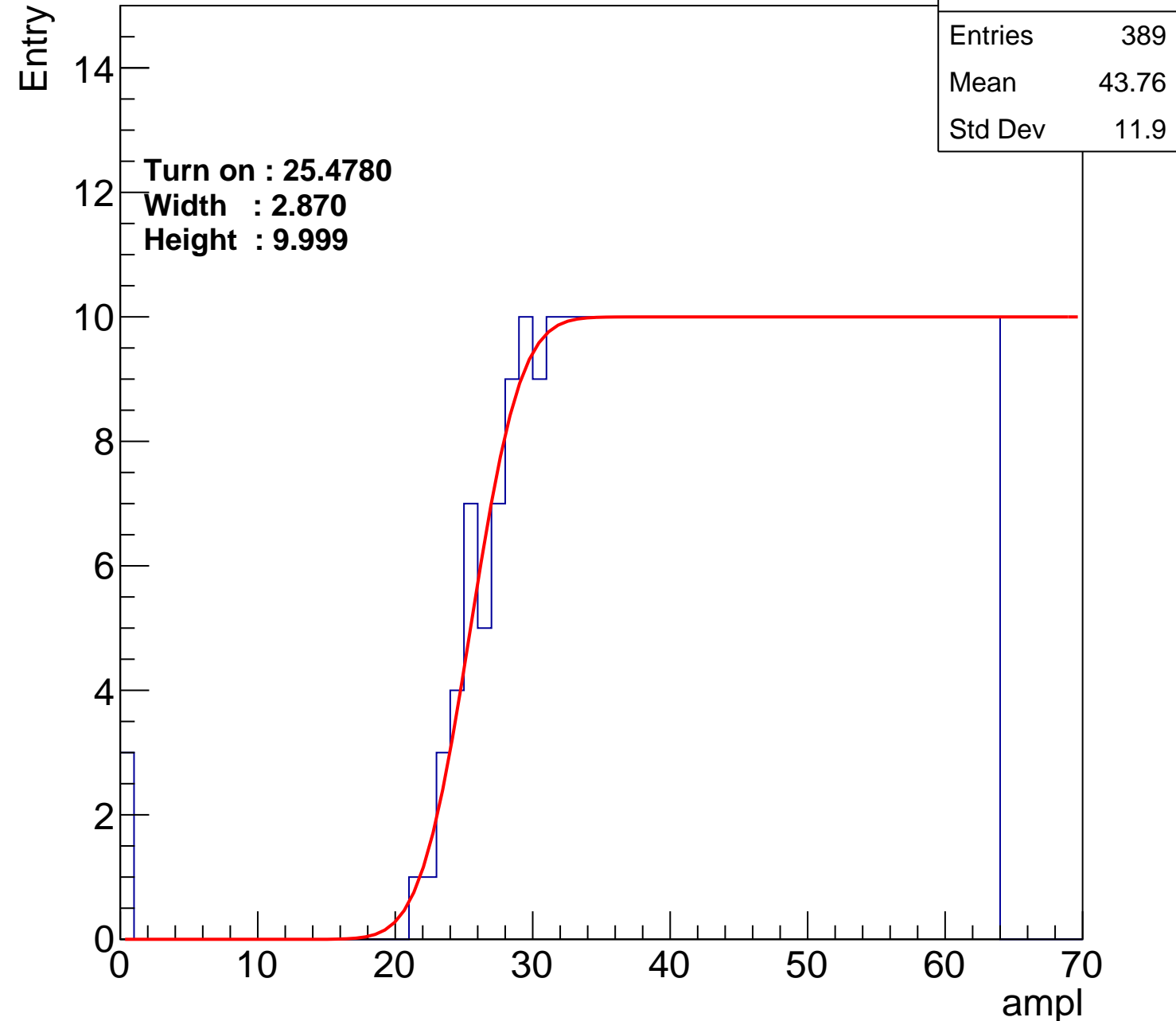
Width : 2.870

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch100

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.01 |
| Std Dev | 11.83 |

Turn on : 25.6317

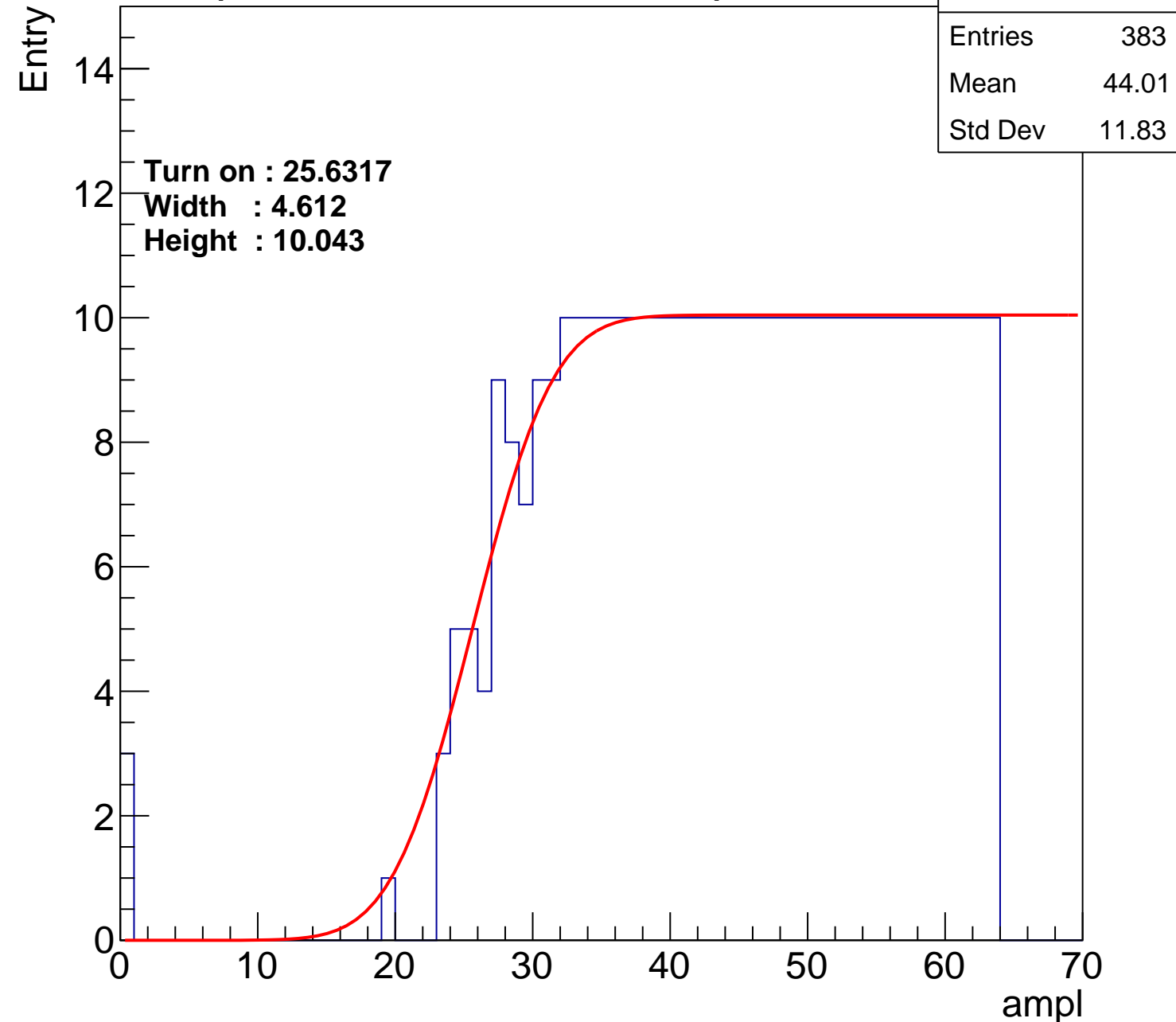
Width : 4.612

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch101

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.2 |
| Std Dev | 11.99 |

Turn on : 27.0150

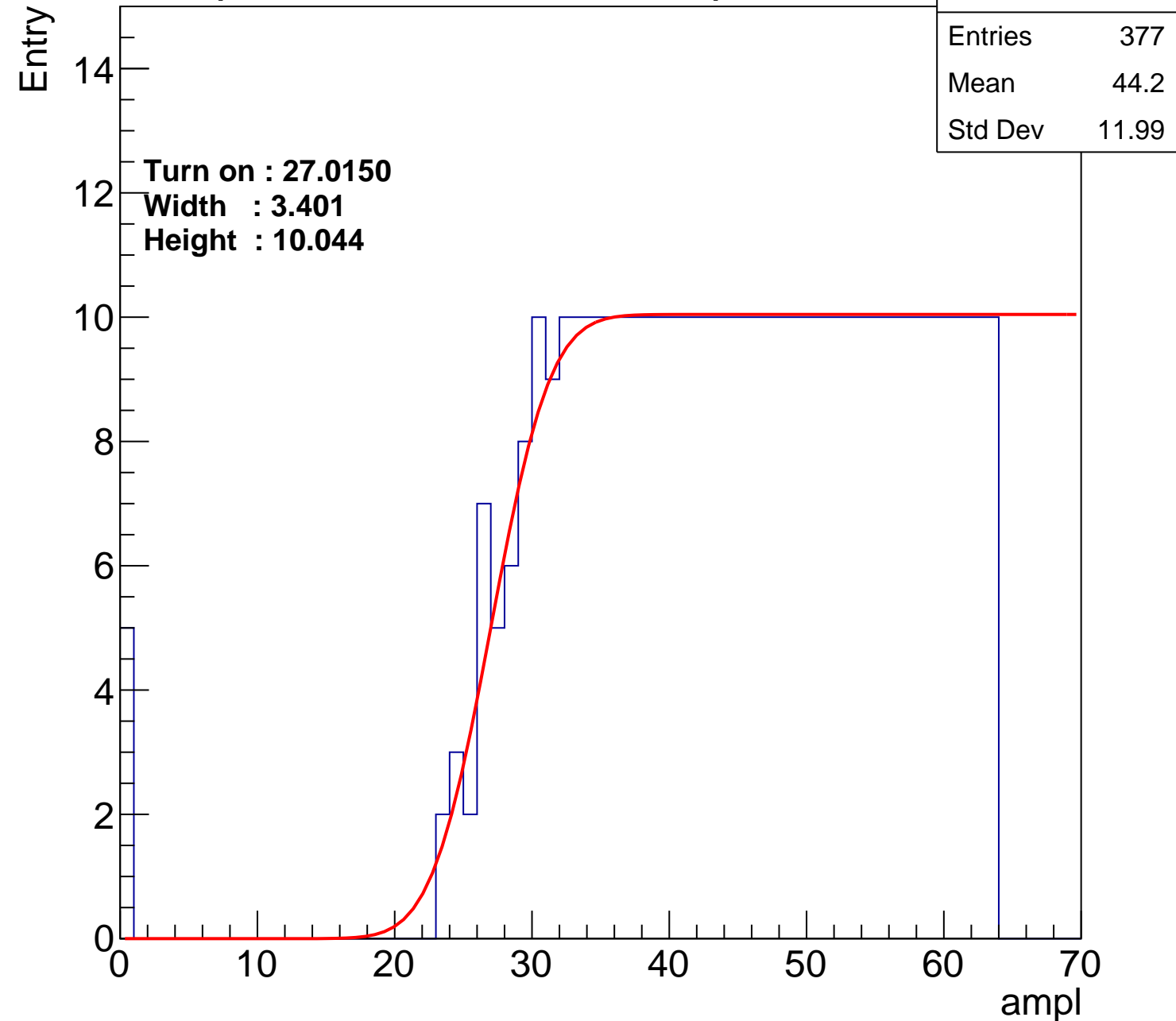
Width : 3.401

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch102

calib_packv5_042523_0143.root, FC#13, port D2

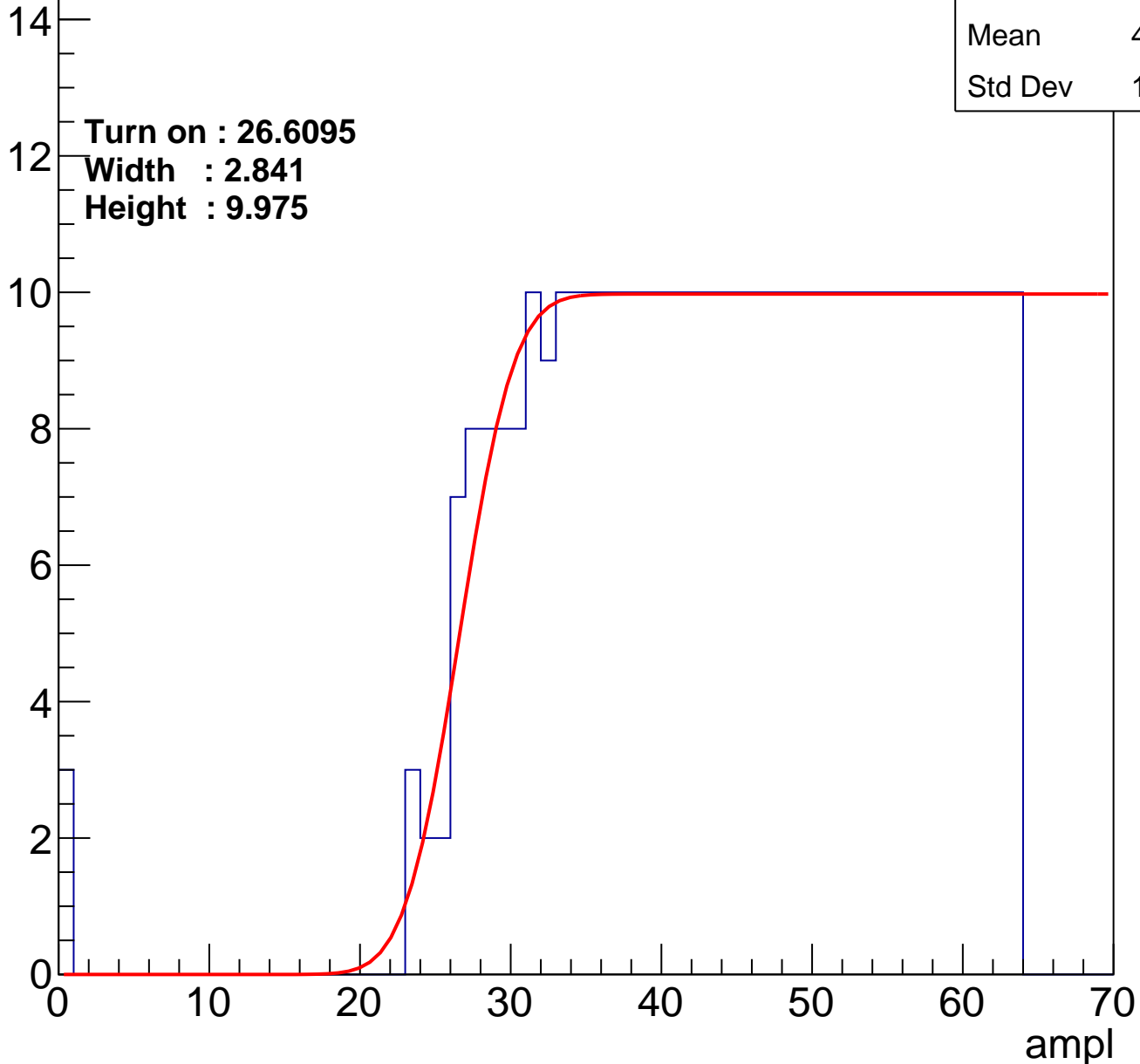
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.28 |
| Std Dev | 11.66 |

Turn on : 26.6095

Width : 2.841

Height : 9.975

Entry



B1L003S, U26-ch103

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.84 |
| Std Dev | 11.24 |

Turn on : 27.9347

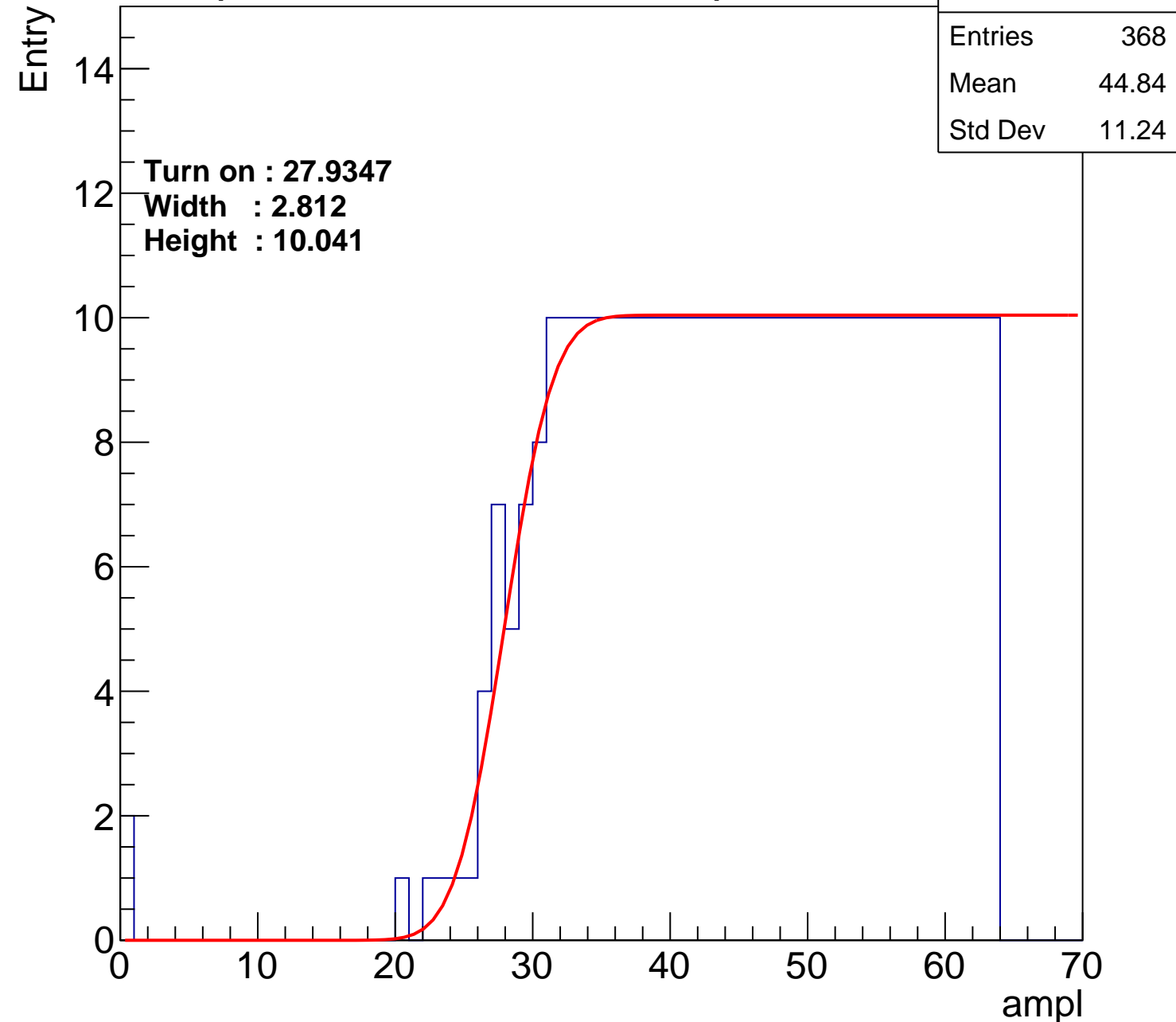
Width : 2.812

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch104

calib_packv5_042523_0143.root, FC#13, port D2

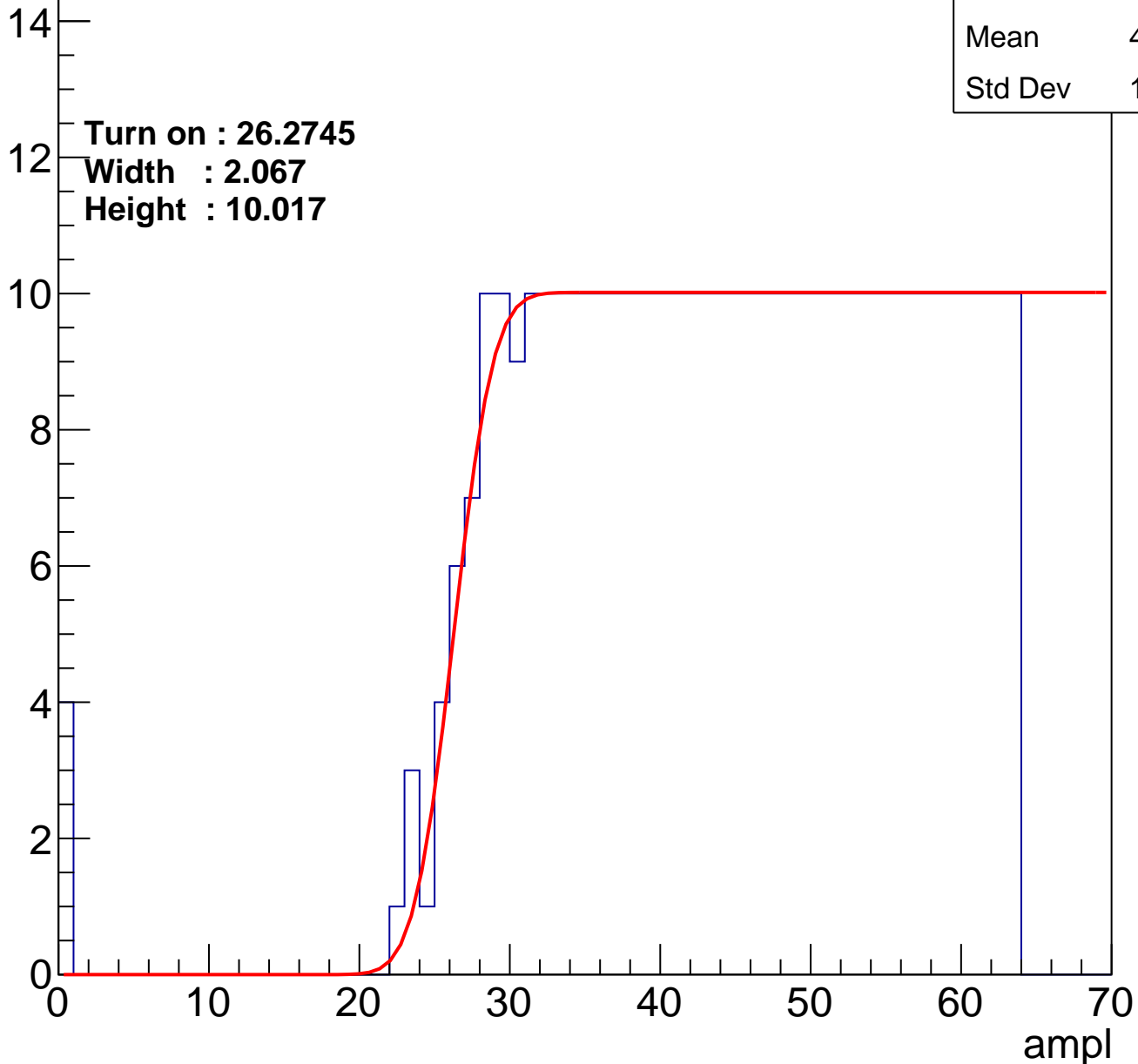
| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.92 |
| Std Dev | 11.93 |

Turn on : 26.2745

Width : 2.067

Height : 10.017

Entry



B1L003S, U26-ch105

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.96 |
| Std Dev | 11.5 |

Turn on : 25.4420

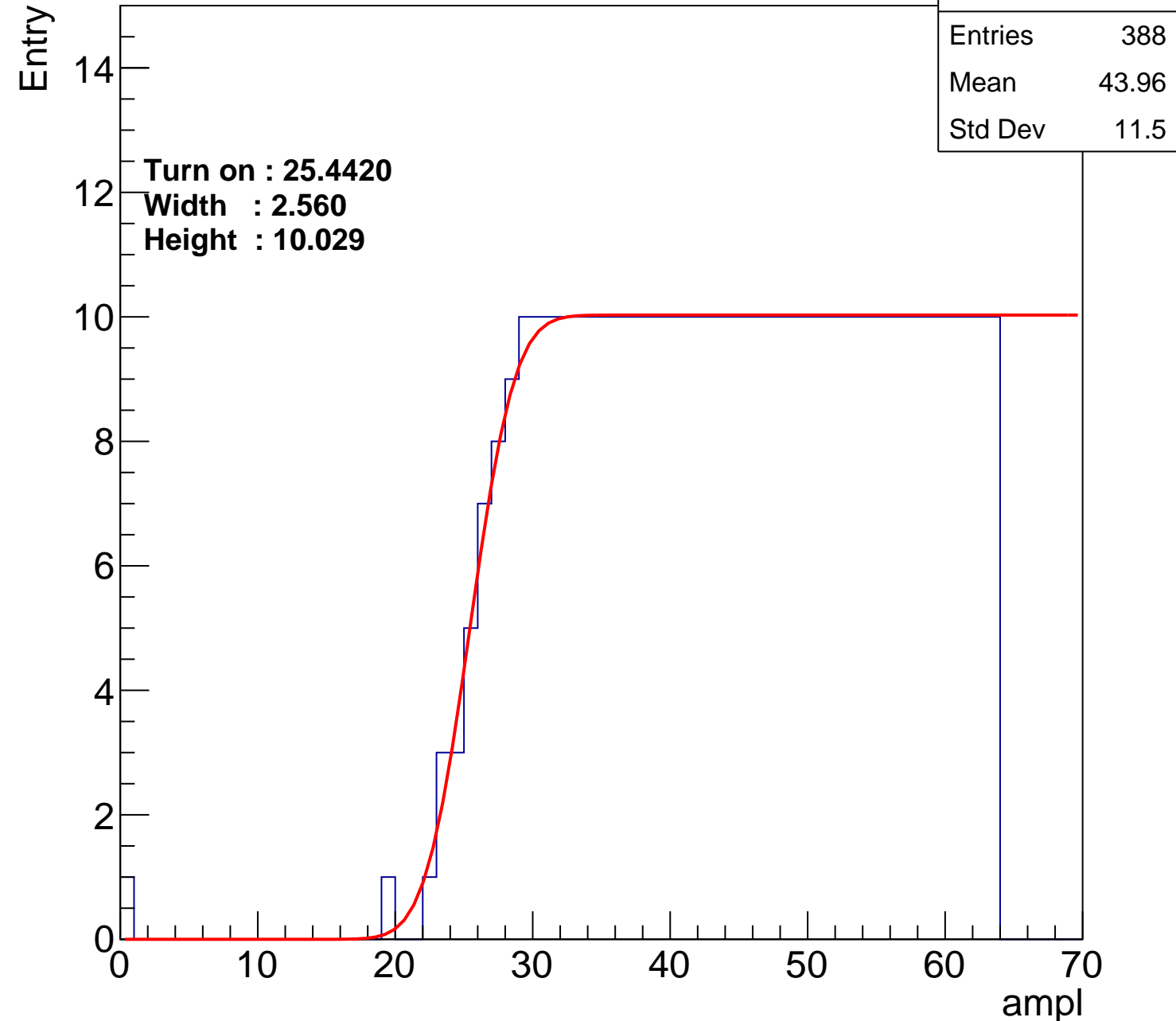
Width : 2.560

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch106

calib_packv5_042523_0143.root, FC#13, port D2

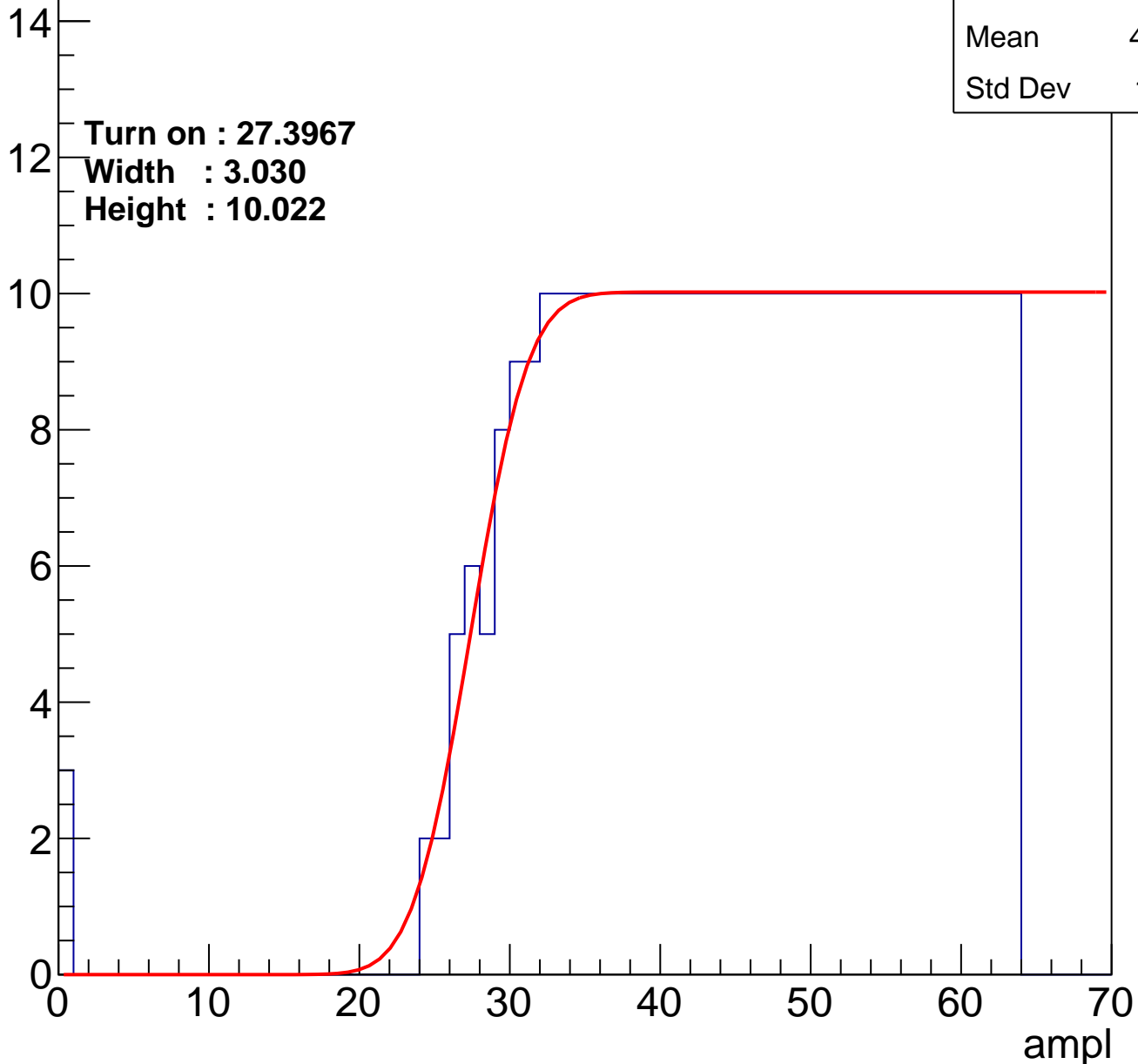
| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.75 |
| Std Dev | 11.41 |

Turn on : 27.3967

Width : 3.030

Height : 10.022

Entry



B1L003S, U26-ch107

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.69 |
| Std Dev | 11.26 |

Turn on : 27.2229

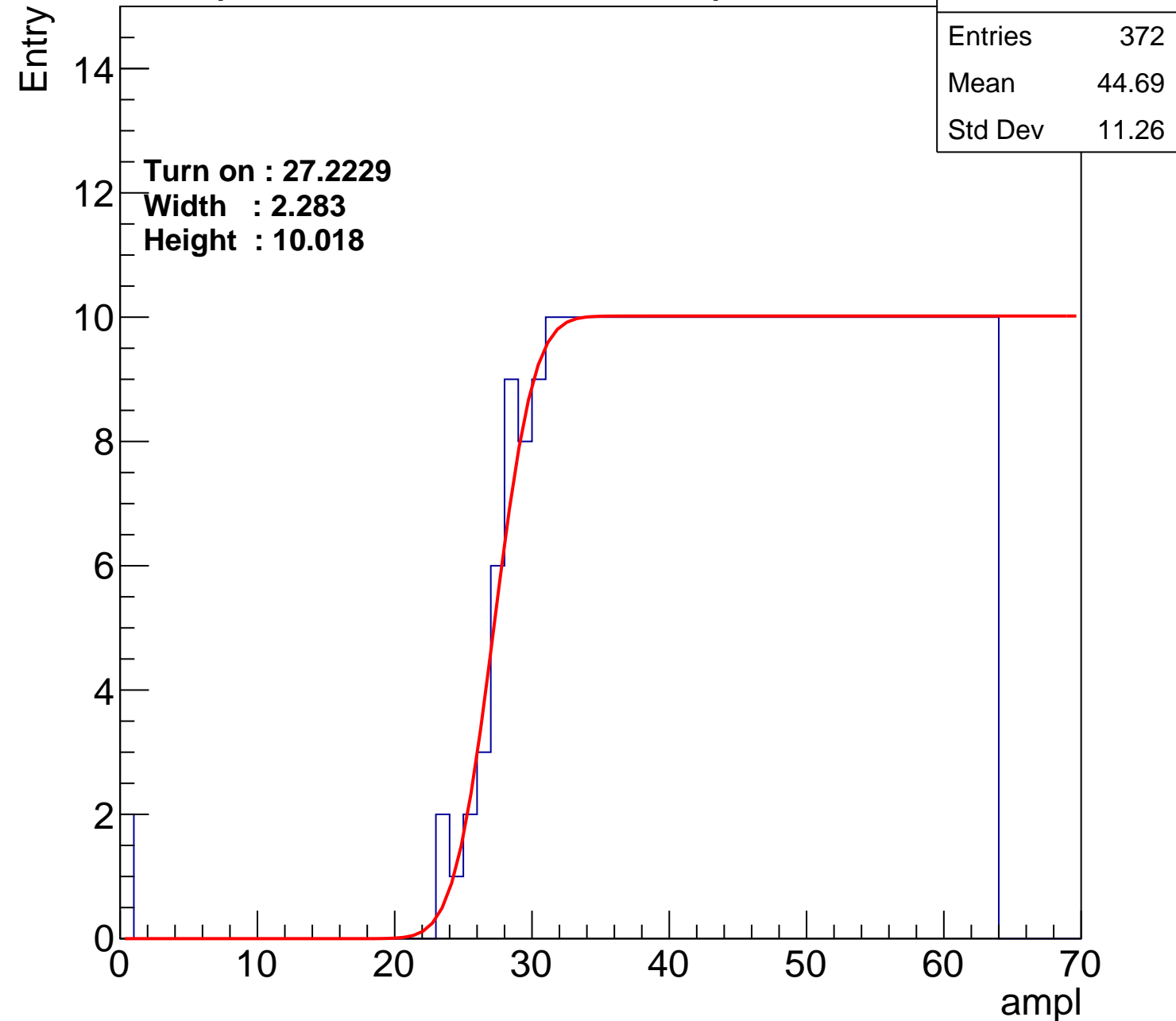
Width : 2.283

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch108

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 345 |
| Mean | 46.01 |
| Std Dev | 10.59 |

Turn on : 29.7391

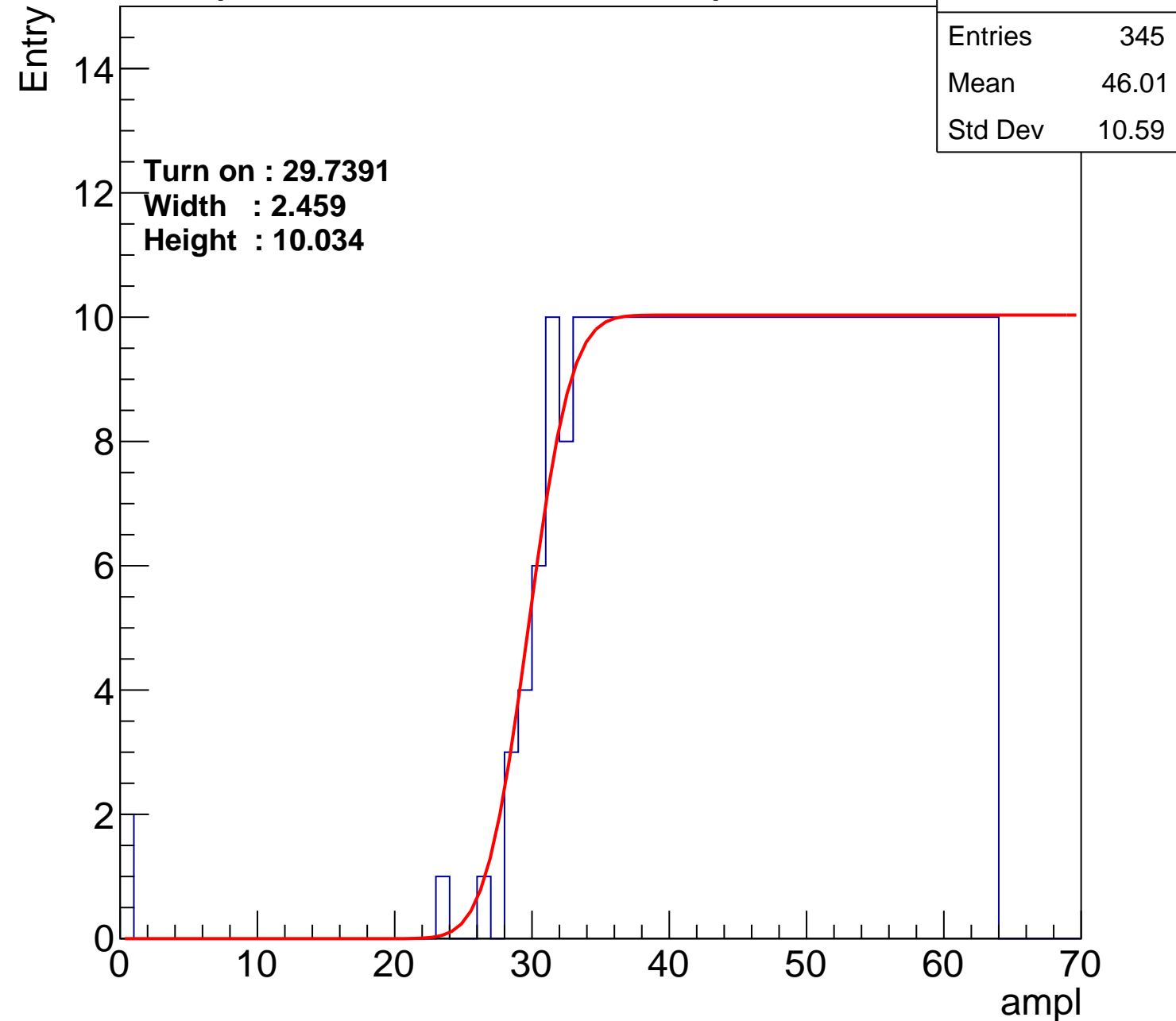
Width : 2.459

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch109

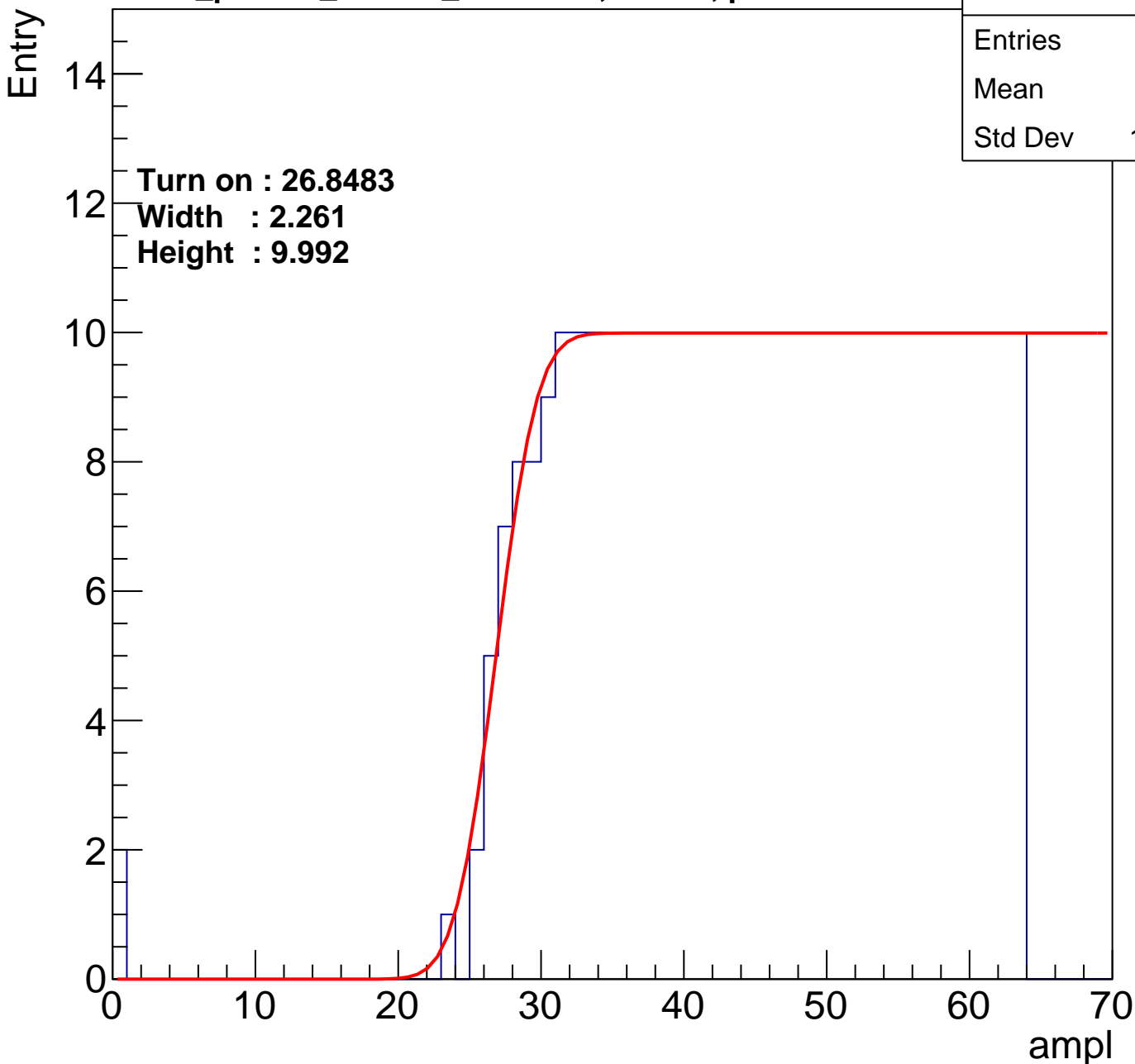
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.8483

Width : 2.261

Height : 9.992

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.7 |
| Std Dev | 11.24 |



B1L003S, U26-ch110

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.75 |
| Std Dev | 11.41 |

Turn on : 27.8669

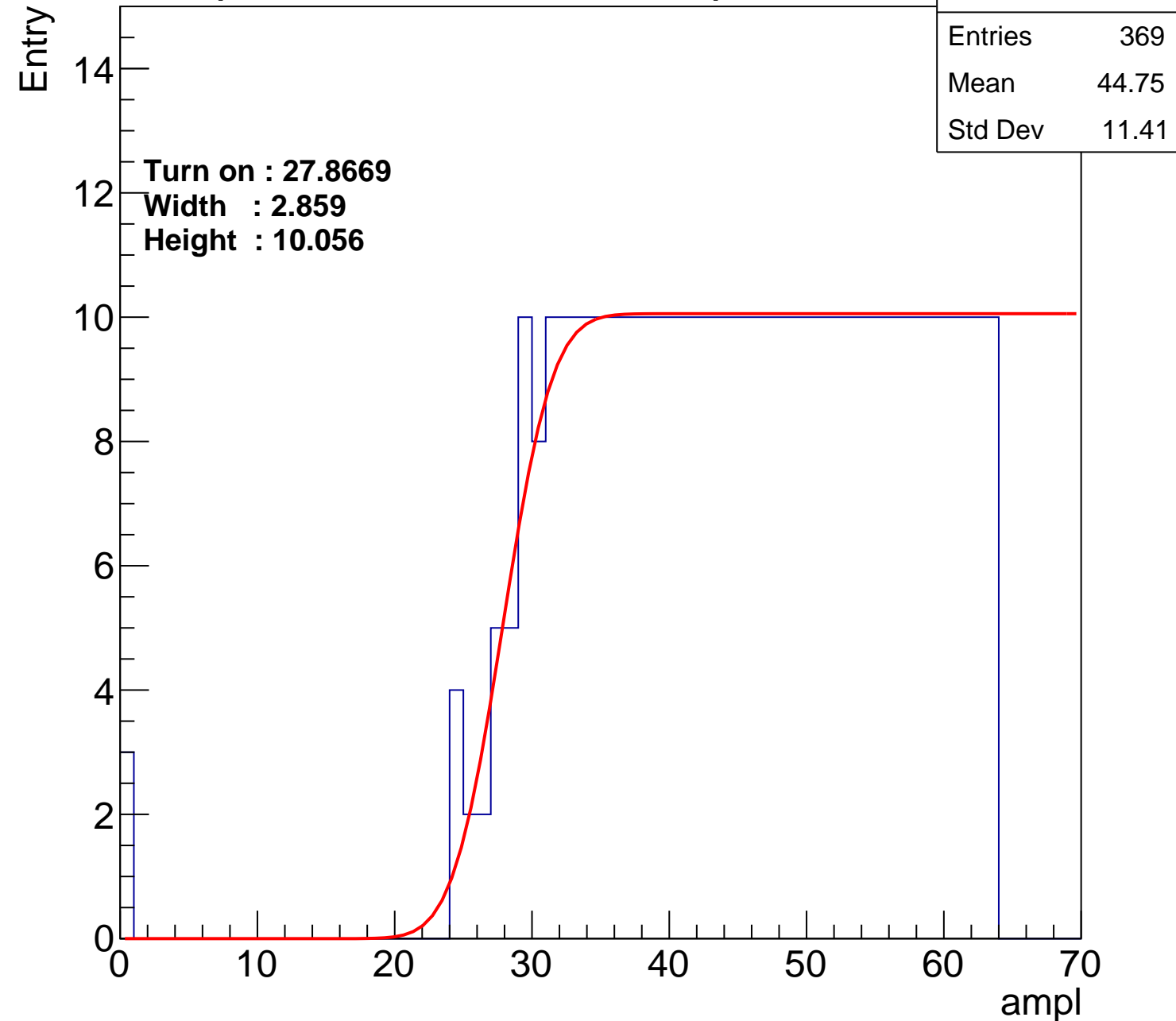
Width : 2.859

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch111

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.45 |
| Std Dev | 11.24 |

Turn on : 26.3291

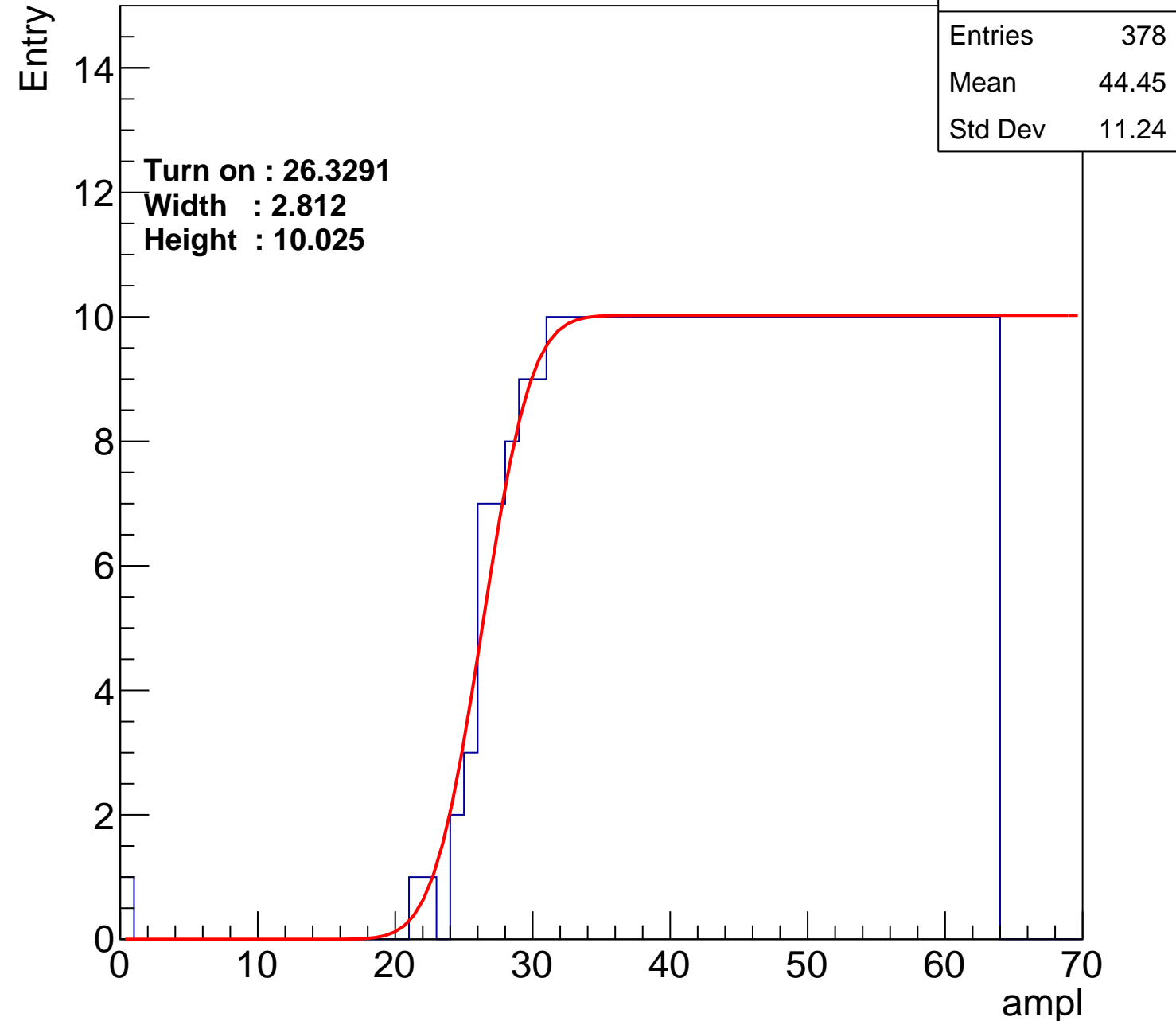
Width : 2.812

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch112

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.05 |
| Std Dev | 10.9 |

Turn on : 27.8596

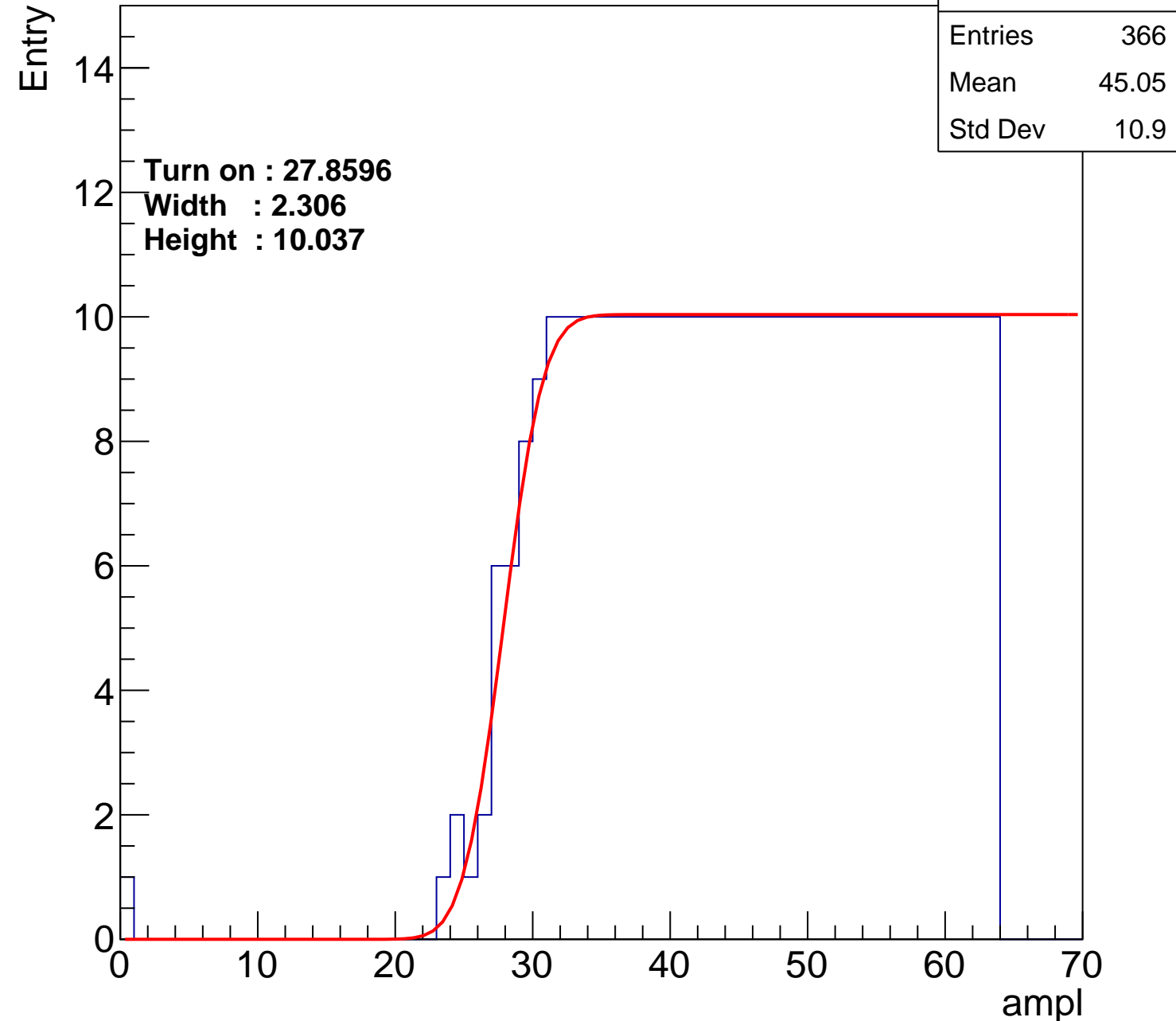
Width : 2.306

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch113

calib_packv5_042523_0143.root, FC#13, port D2

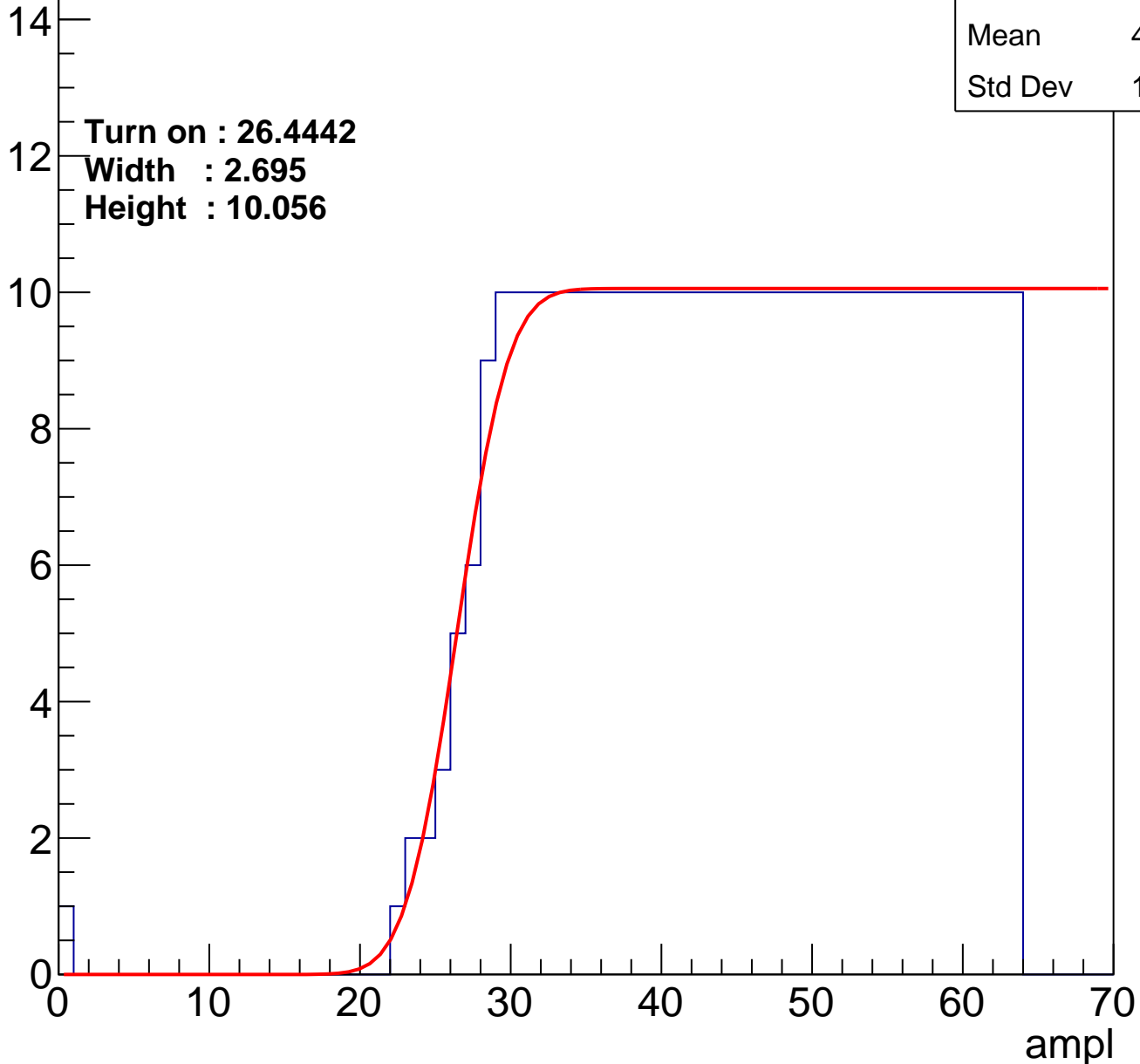
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.42 |
| Std Dev | 11.24 |

Turn on : 26.4442

Width : 2.695

Height : 10.056

Entry



B1L003S, U26-ch114

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.75 |
| Std Dev | 11.07 |

Turn on : 26.8285

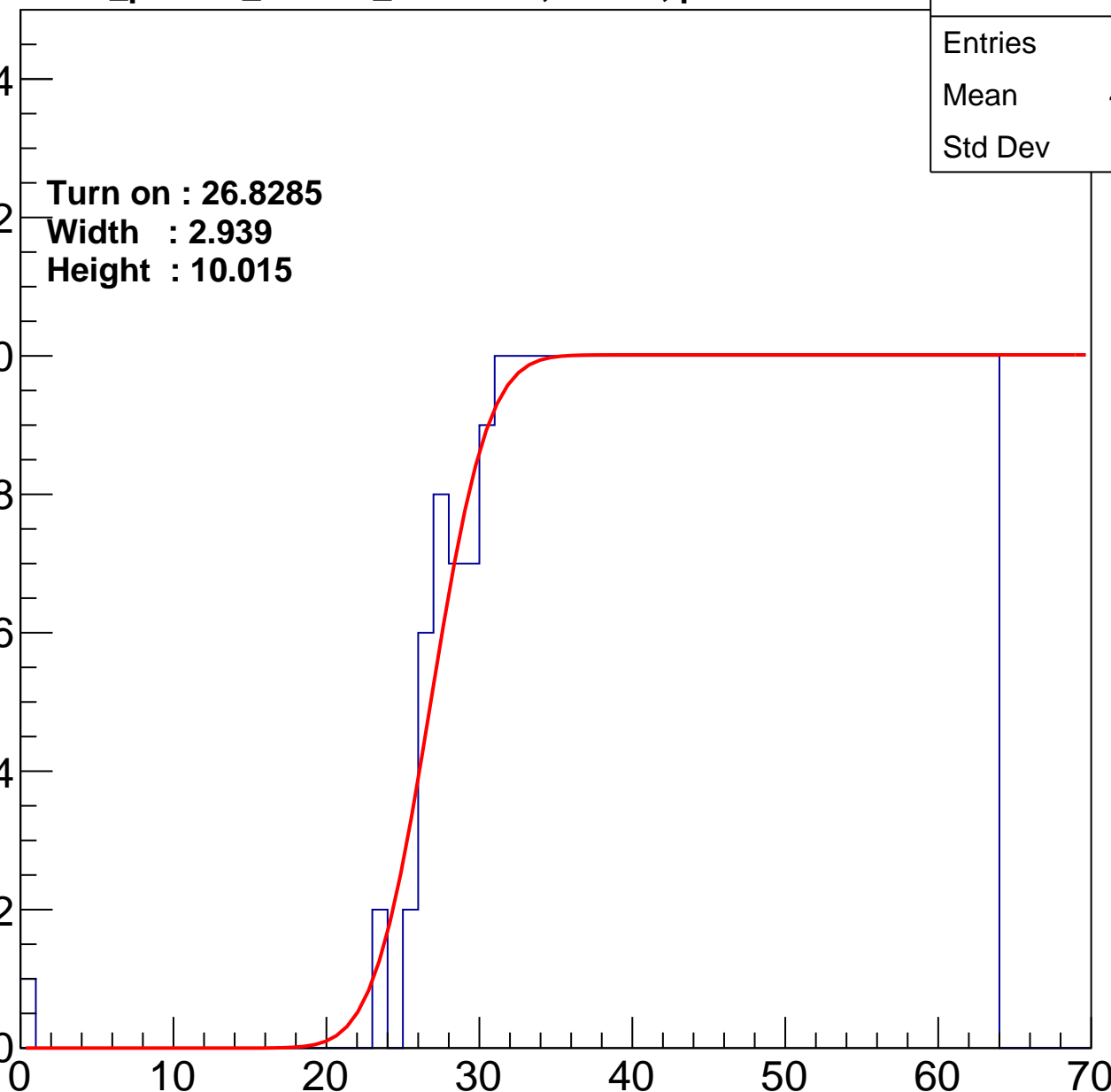
Width : 2.939

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch115

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.49 |
| Std Dev | 11.29 |

Turn on : 27.1205

Width : 4.057

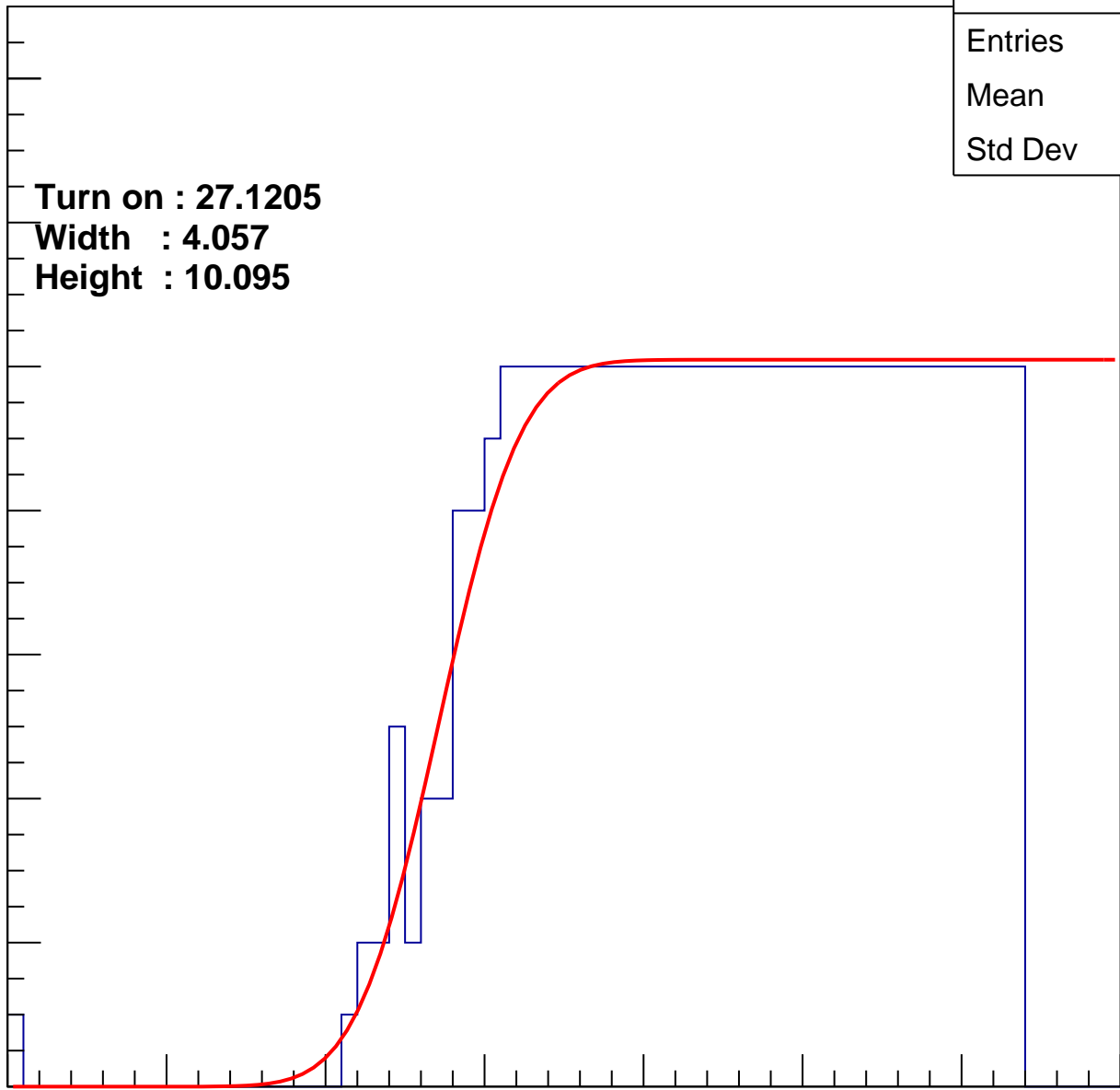
Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U26-ch116

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.37 |
| Std Dev | 11.58 |

Turn on : 26.6640

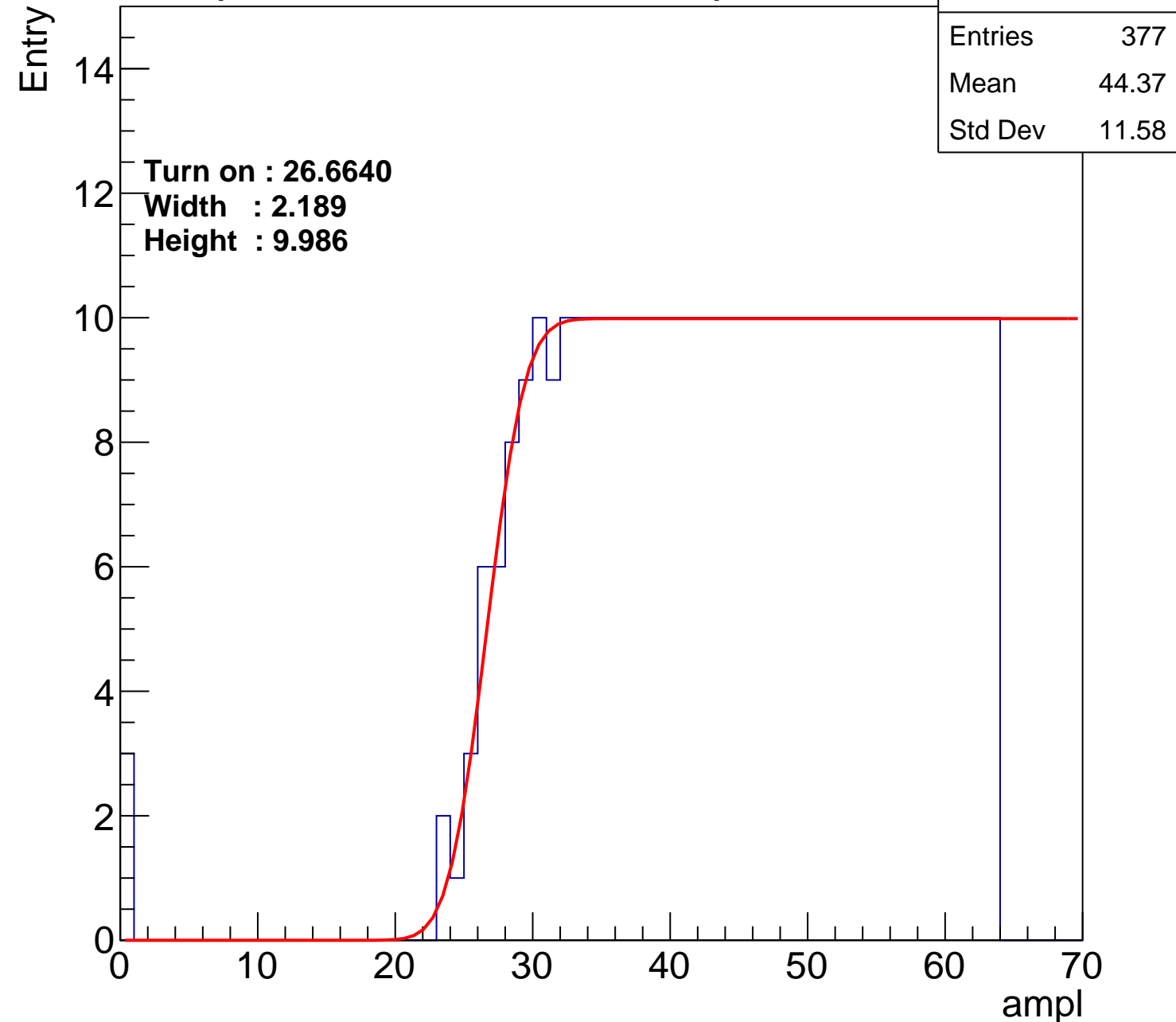
Width : 2.189

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch117

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.07 |
| Std Dev | 11.61 |

Turn on : 26.1195

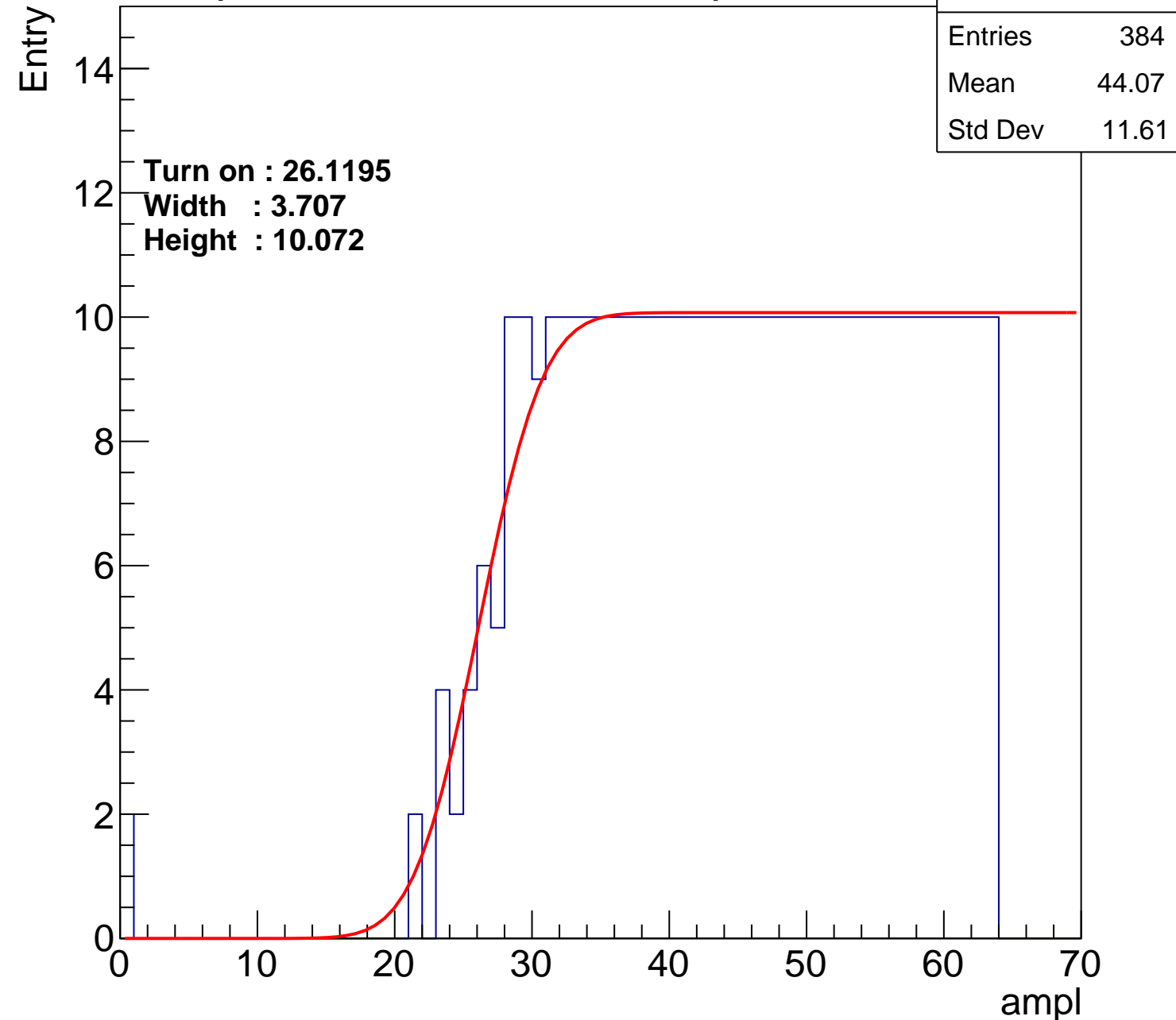
Width : 3.707

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch118

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.13 |
| Std Dev | 11.04 |

Turn on : 28.4348

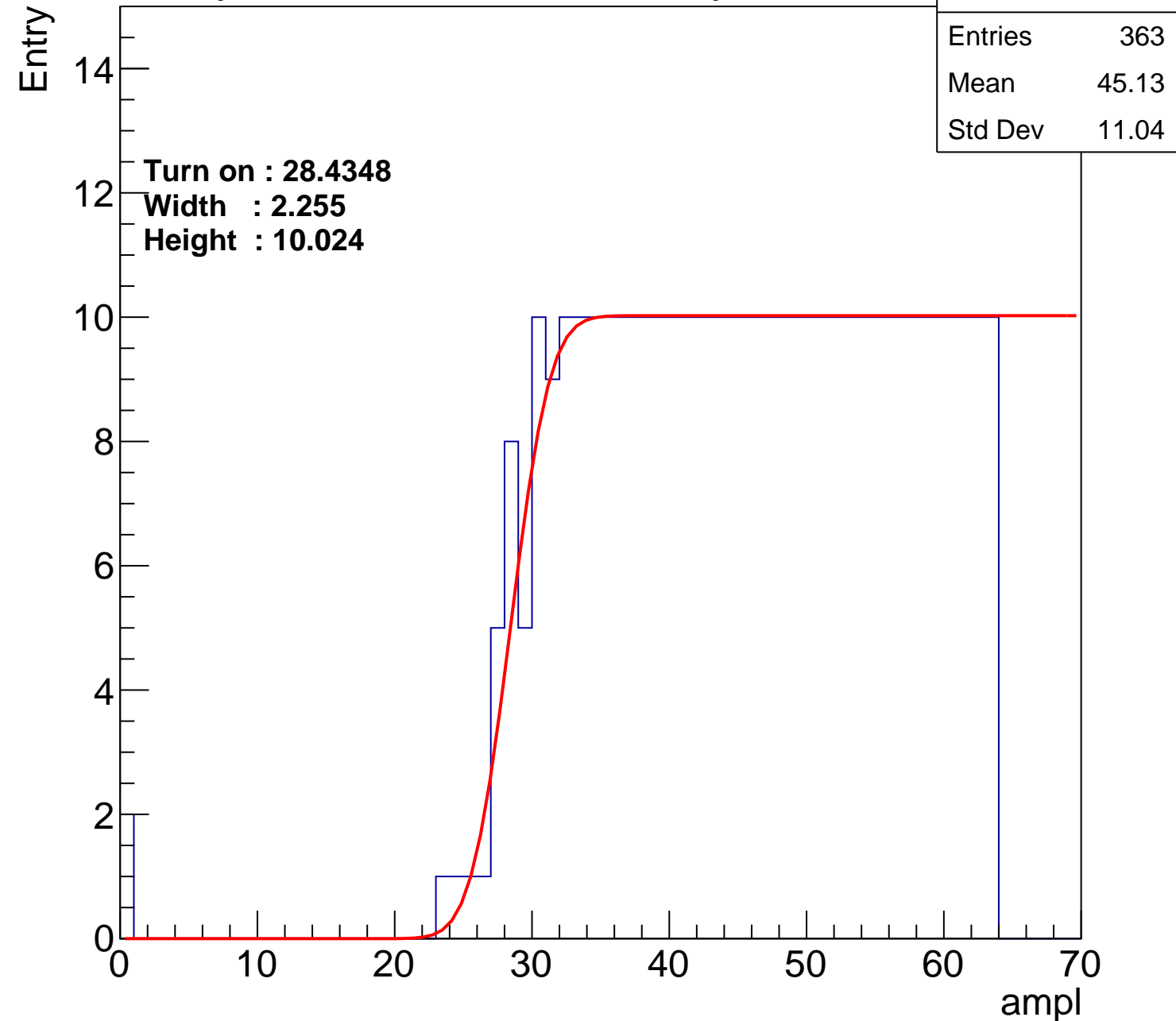
Width : 2.255

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch119

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 351 |
| Mean | 45.72 |
| Std Dev | 10.73 |

Turn on : 28.9811

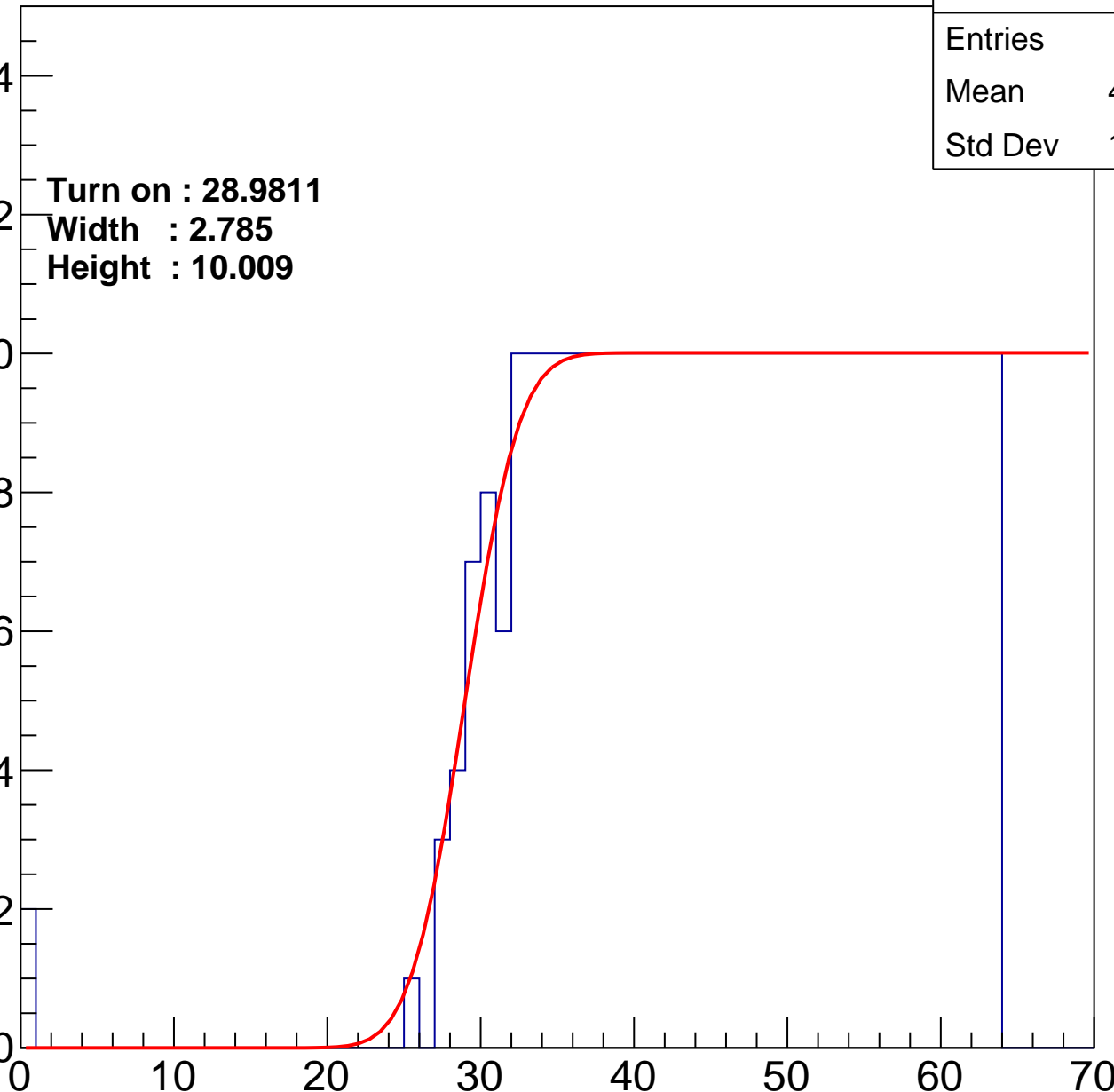
Width : 2.785

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch120

calib_packv5_042523_0143.root, FC#13, port D2

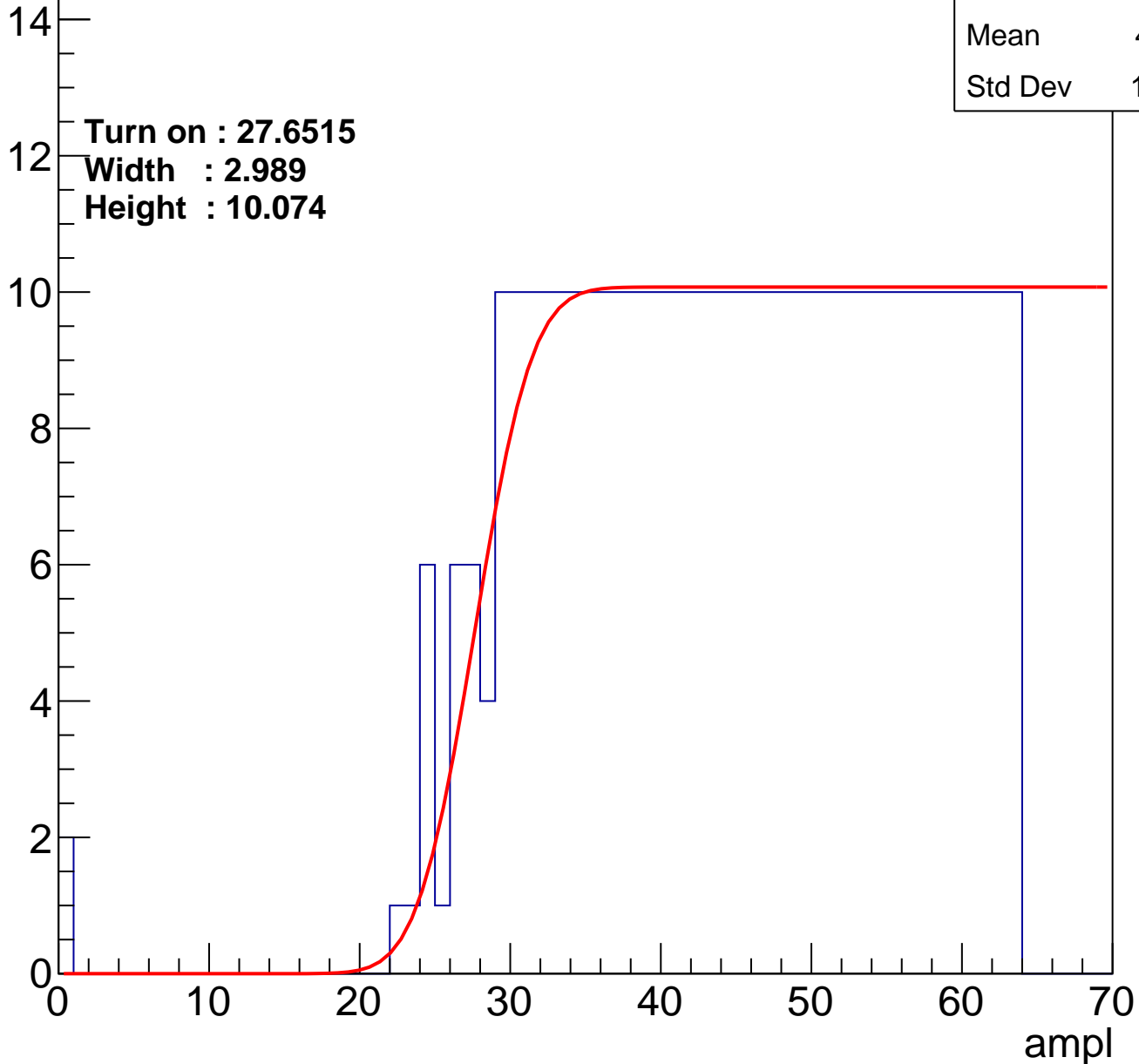
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.41 |
| Std Dev | 11.43 |

Turn on : 27.6515

Width : 2.989

Height : 10.074

Entry



B1L003S, U26-ch121

calib_packv5_042523_0143.root, FC#13, port D2

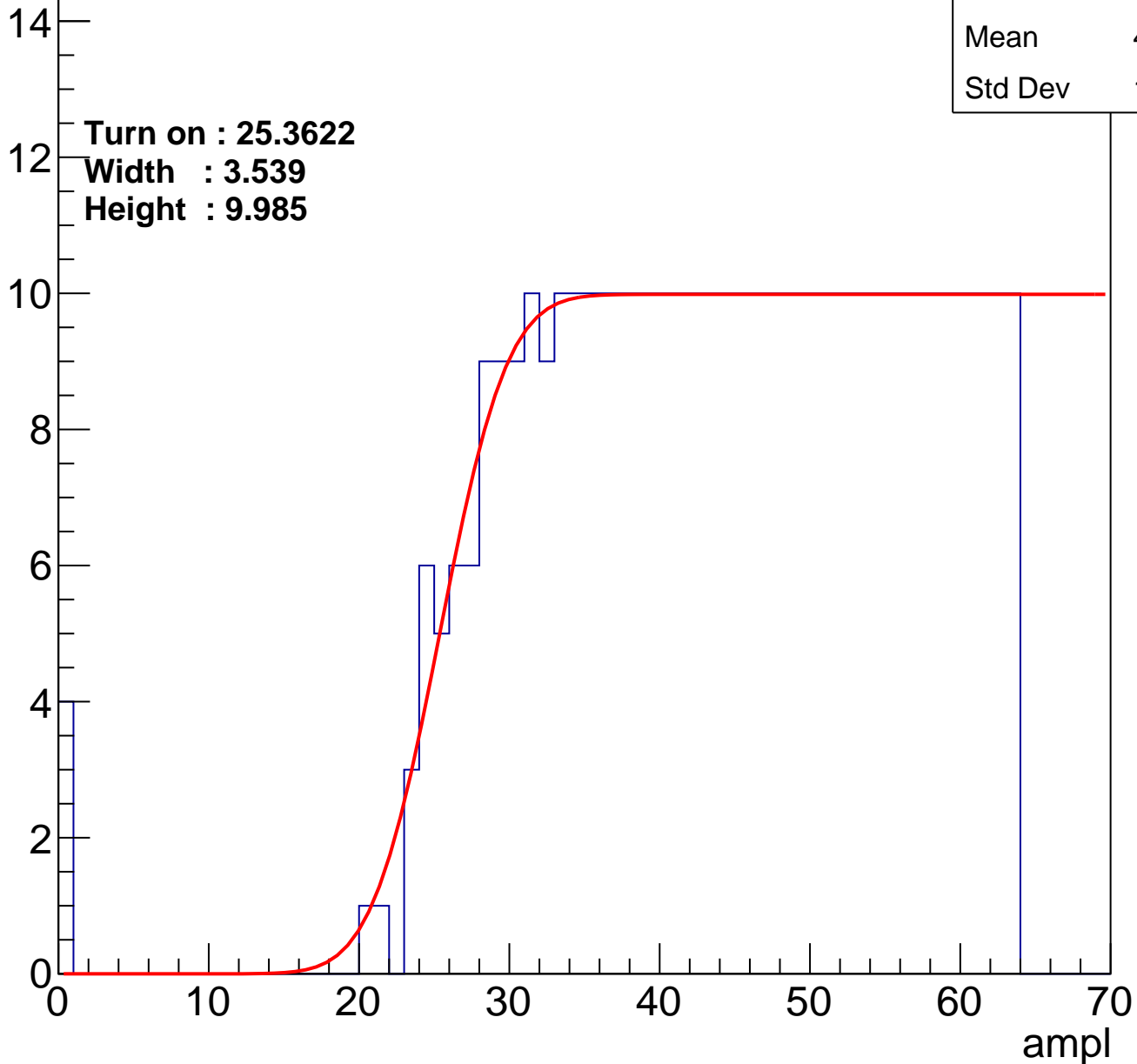
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.71 |
| Std Dev | 12.11 |

Turn on : 25.3622

Width : 3.539

Height : 9.985

Entry



B1L003S, U26-ch122

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.13 |
| Std Dev | 10.89 |

Turn on : 27.7924

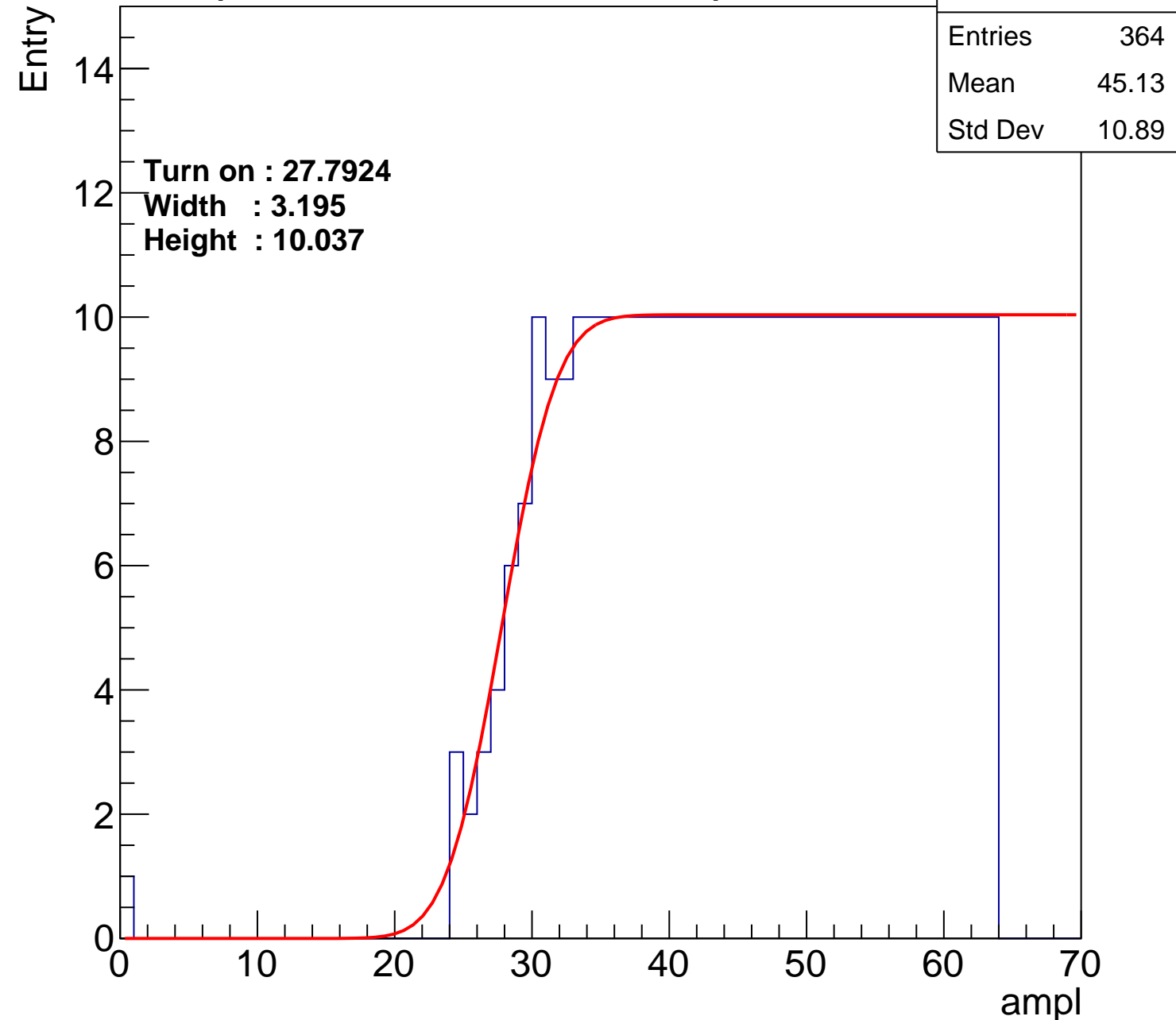
Width : 3.195

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch123

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.62 |
| Std Dev | 11.63 |

Turn on : 27.5292

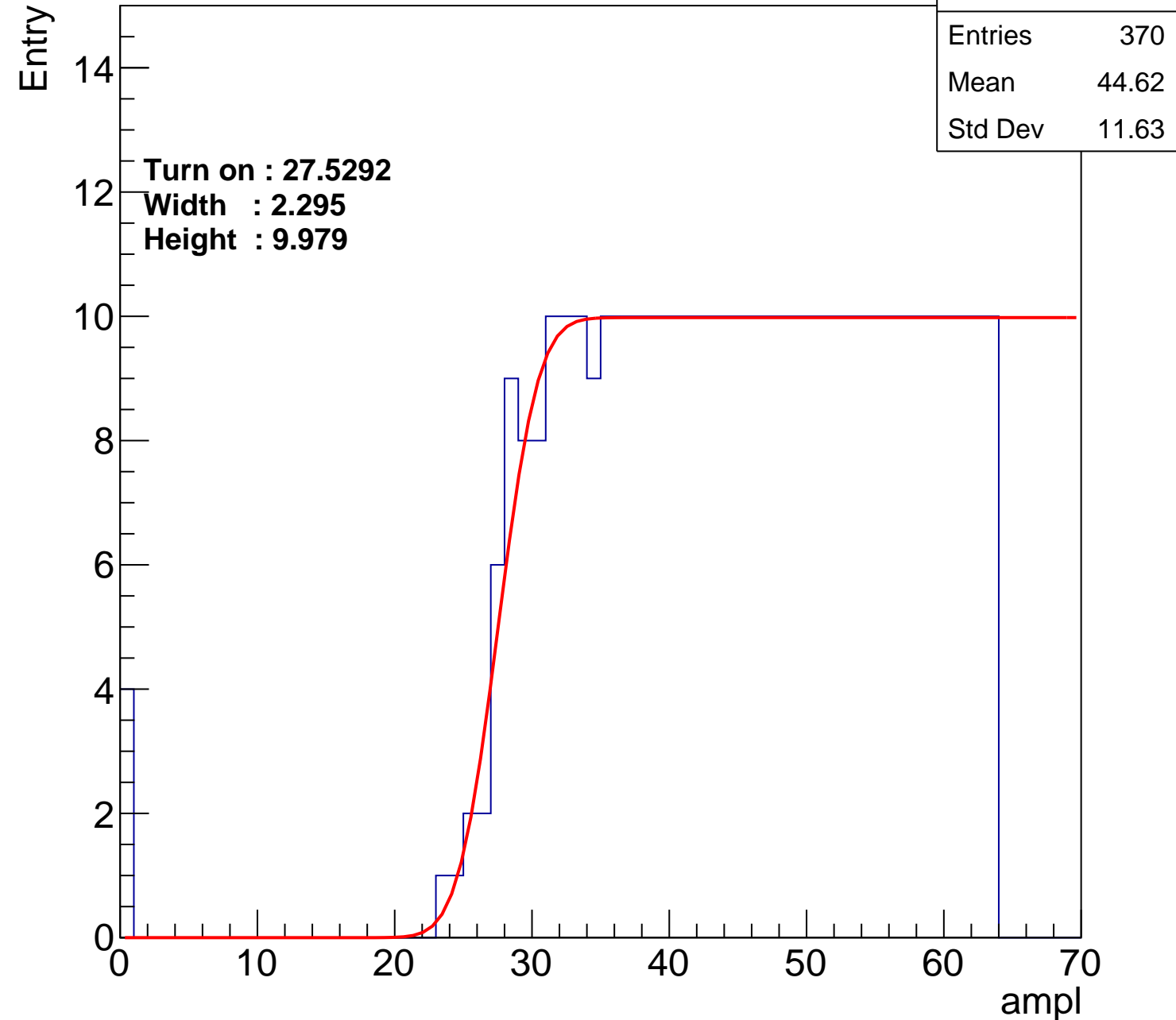
Width : 2.295

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch124

calib_packv5_042523_0143.root, FC#13, port D2

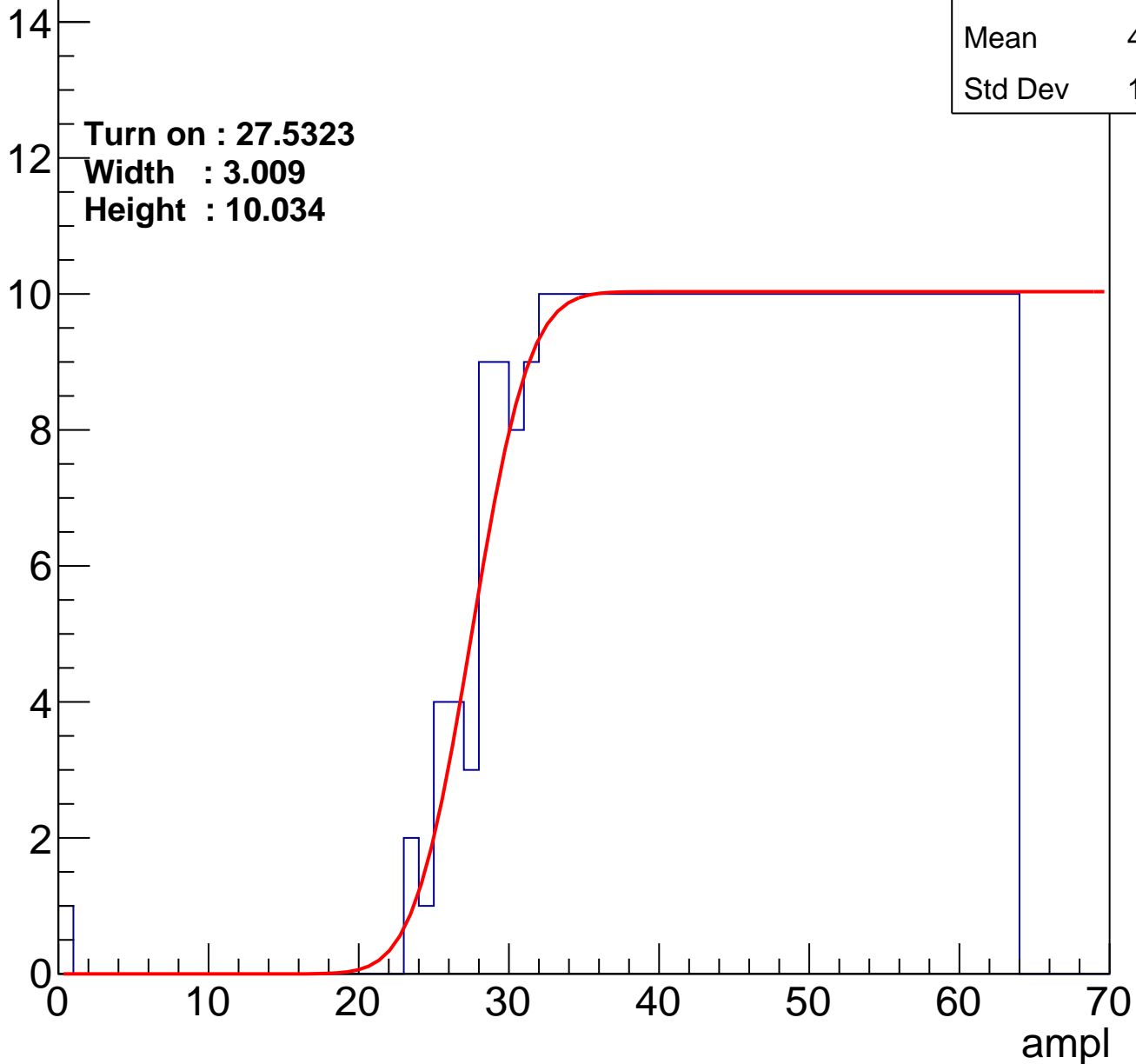
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.83 |
| Std Dev | 11.05 |

Turn on : 27.5323

Width : 3.009

Height : 10.034

Entry



B1L003S, U26-ch125

calib_packv5_042523_0143.root, FC#13, port D2

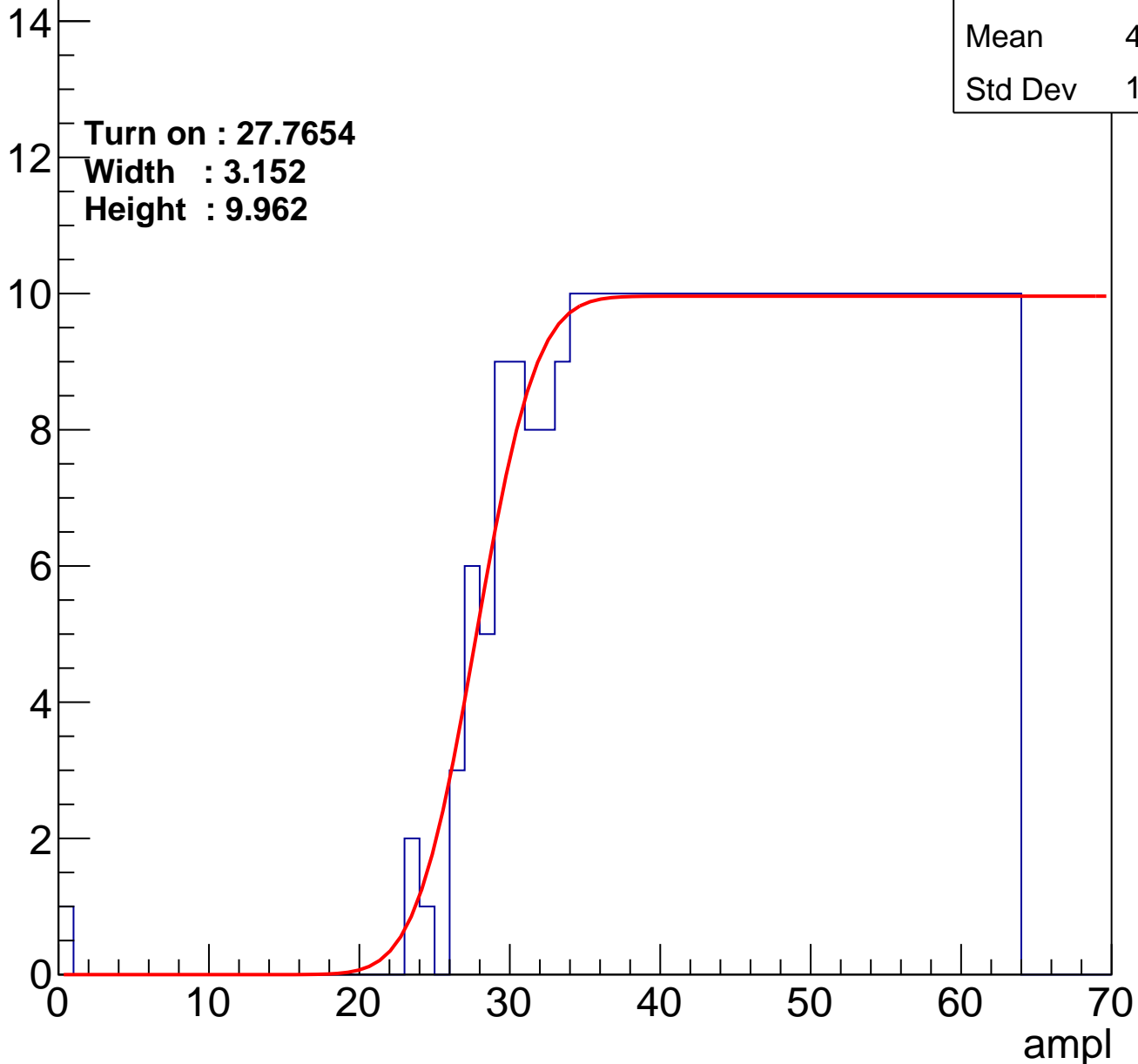
| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.24 |
| Std Dev | 10.86 |

Turn on : 27.7654

Width : 3.152

Height : 9.962

Entry



B1L003S, U26-ch126

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.77 |
| Std Dev | 11.89 |

Turn on : 25.4510

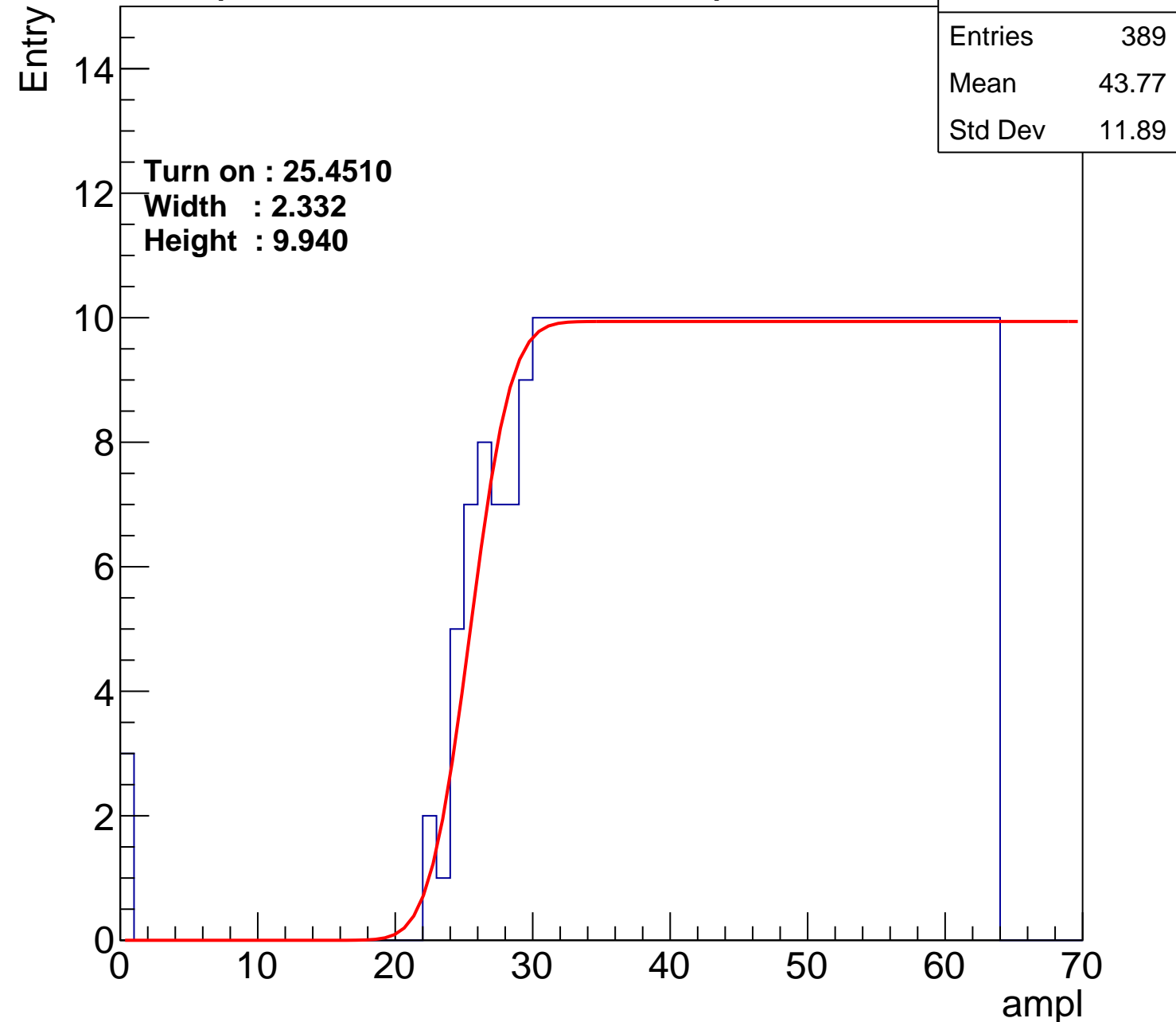
Width : 2.332

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch127

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.83 |
| Std Dev | 11.38 |

Turn on : 28.1926

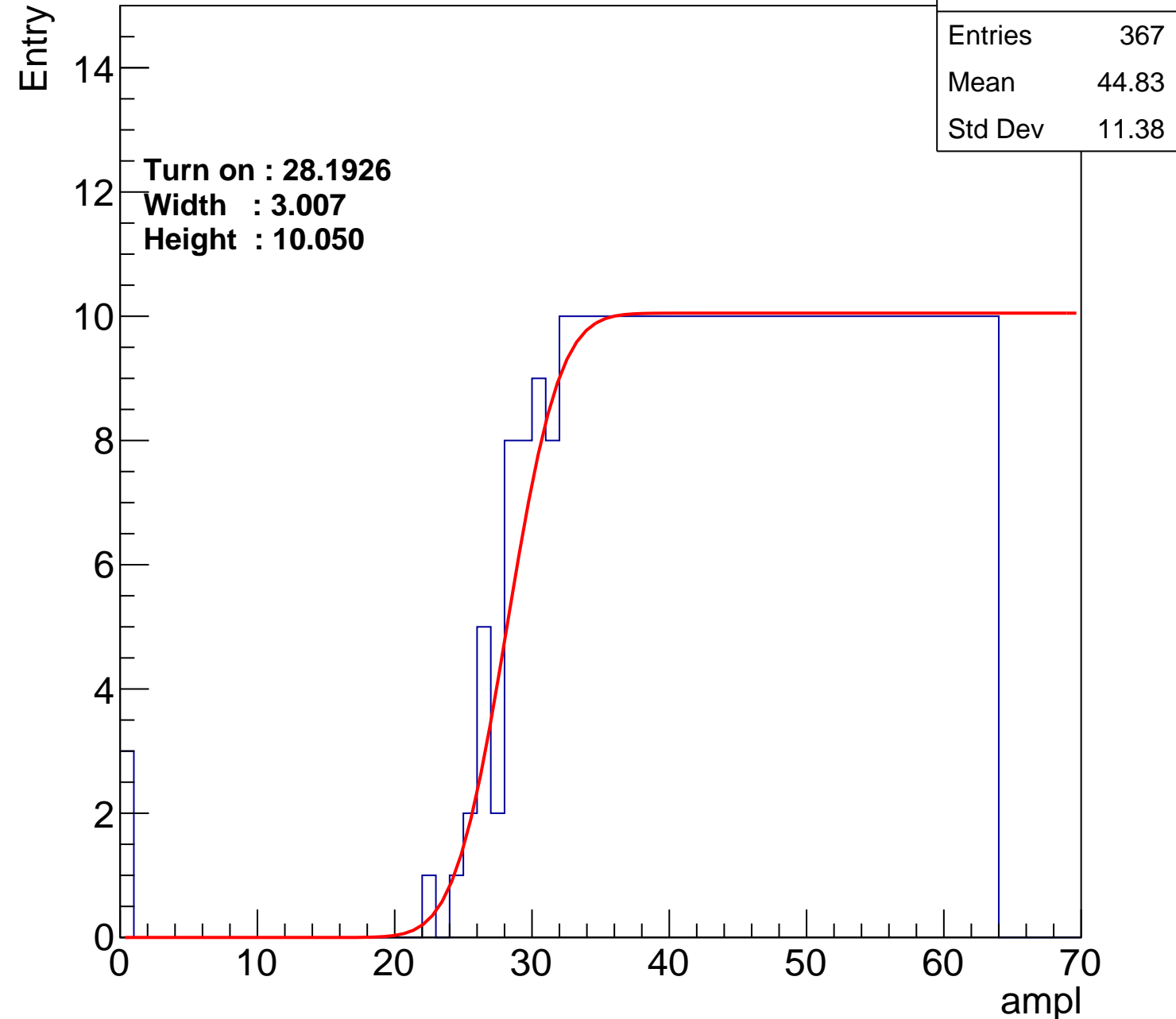
Width : 3.007

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U26-ch127

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.83 |
| Std Dev | 11.38 |

Turn on : 28.1926

Width : 3.007

Height : 10.050

Entry

