



# B0L102S, U17-ch0, adc0

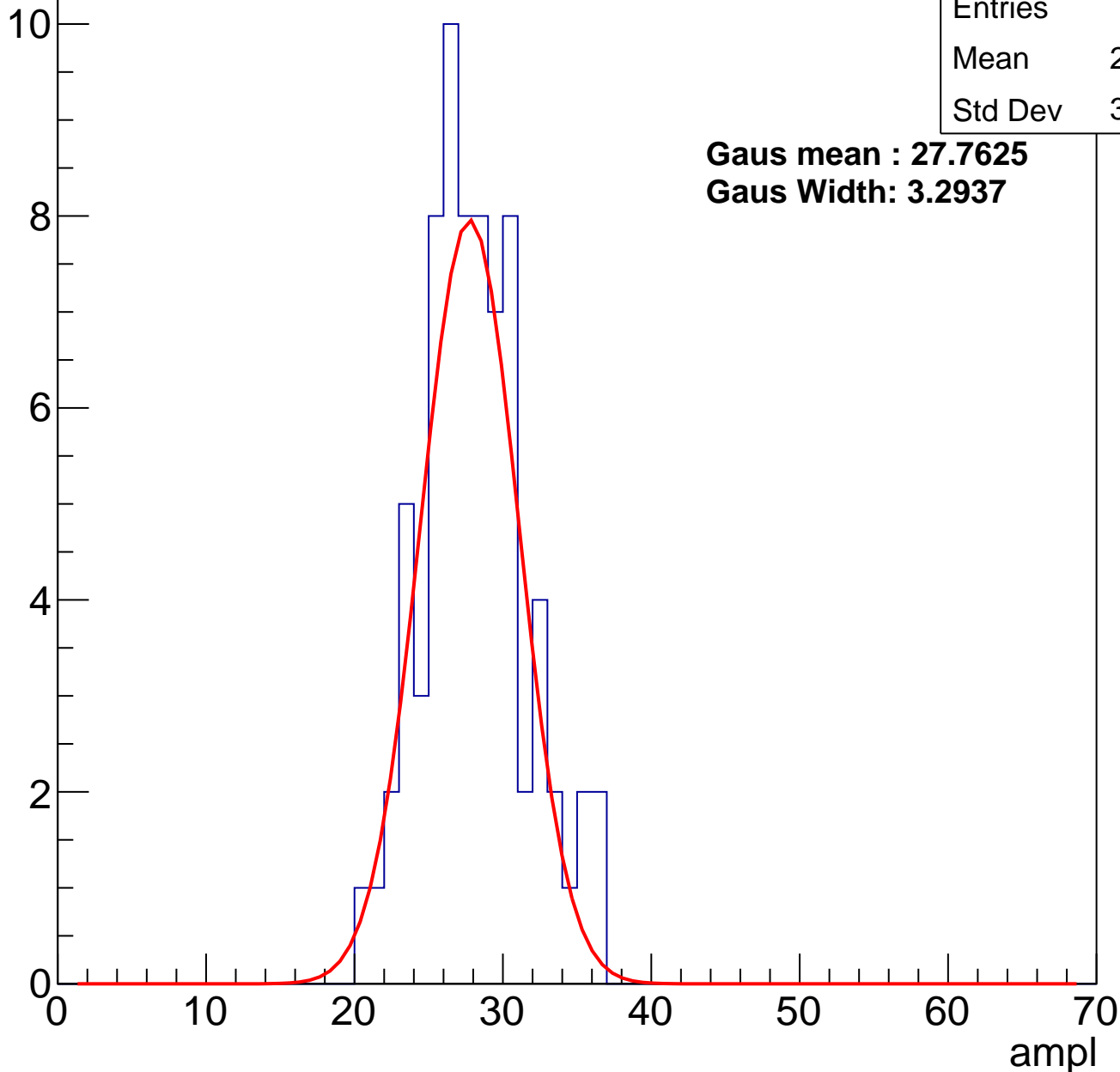
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	74
Mean	27.66
Std Dev	3.492

**Gaus mean : 27.7625**

**Gaus Width: 3.2937**

Entry



# B0L102S, U17-ch0, adc1

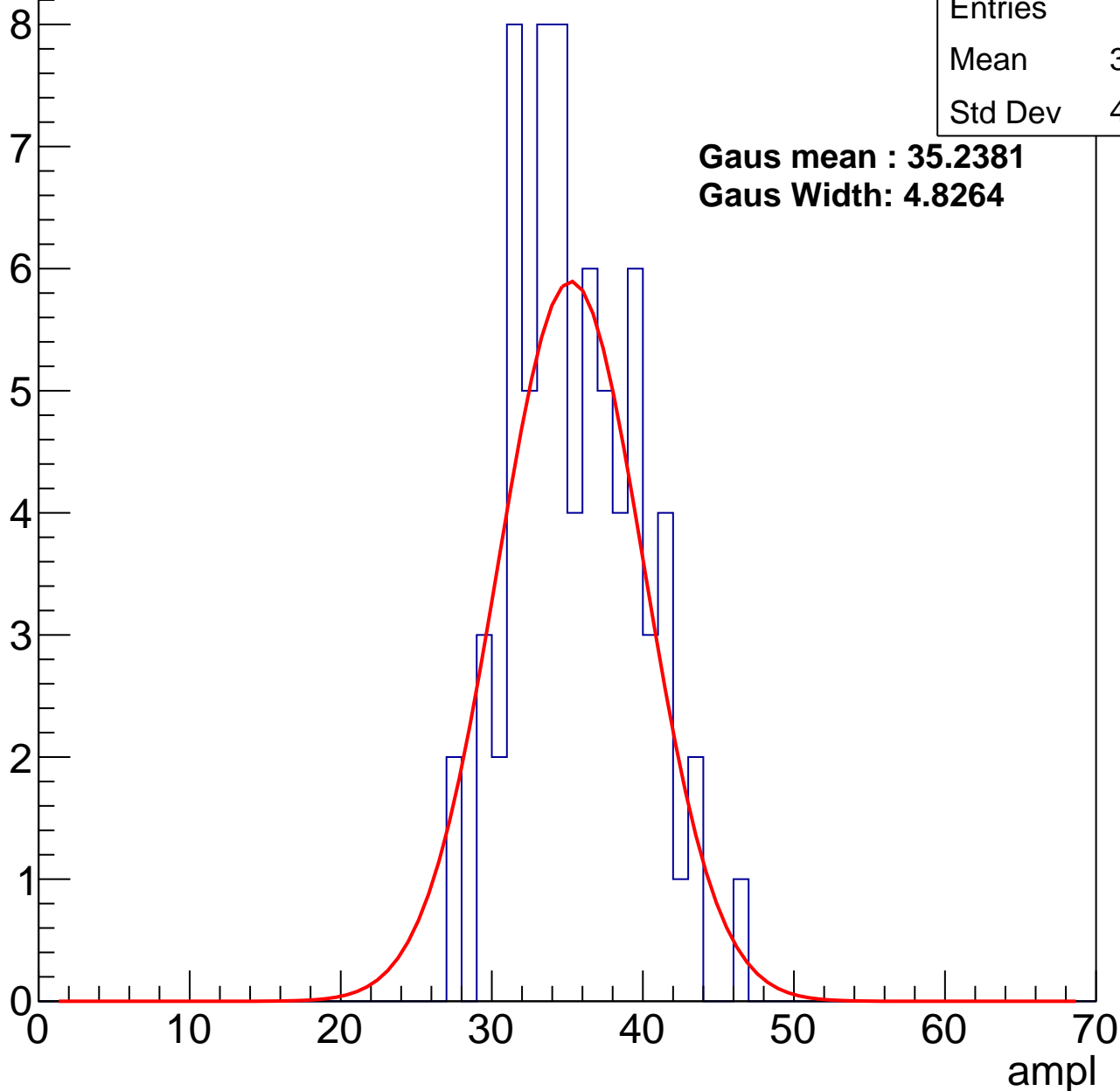
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	35.14
Std Dev	4.036

**Gaus mean : 35.2381**

**Gaus Width: 4.8264**



# B0L102S, U17-ch0, adc2

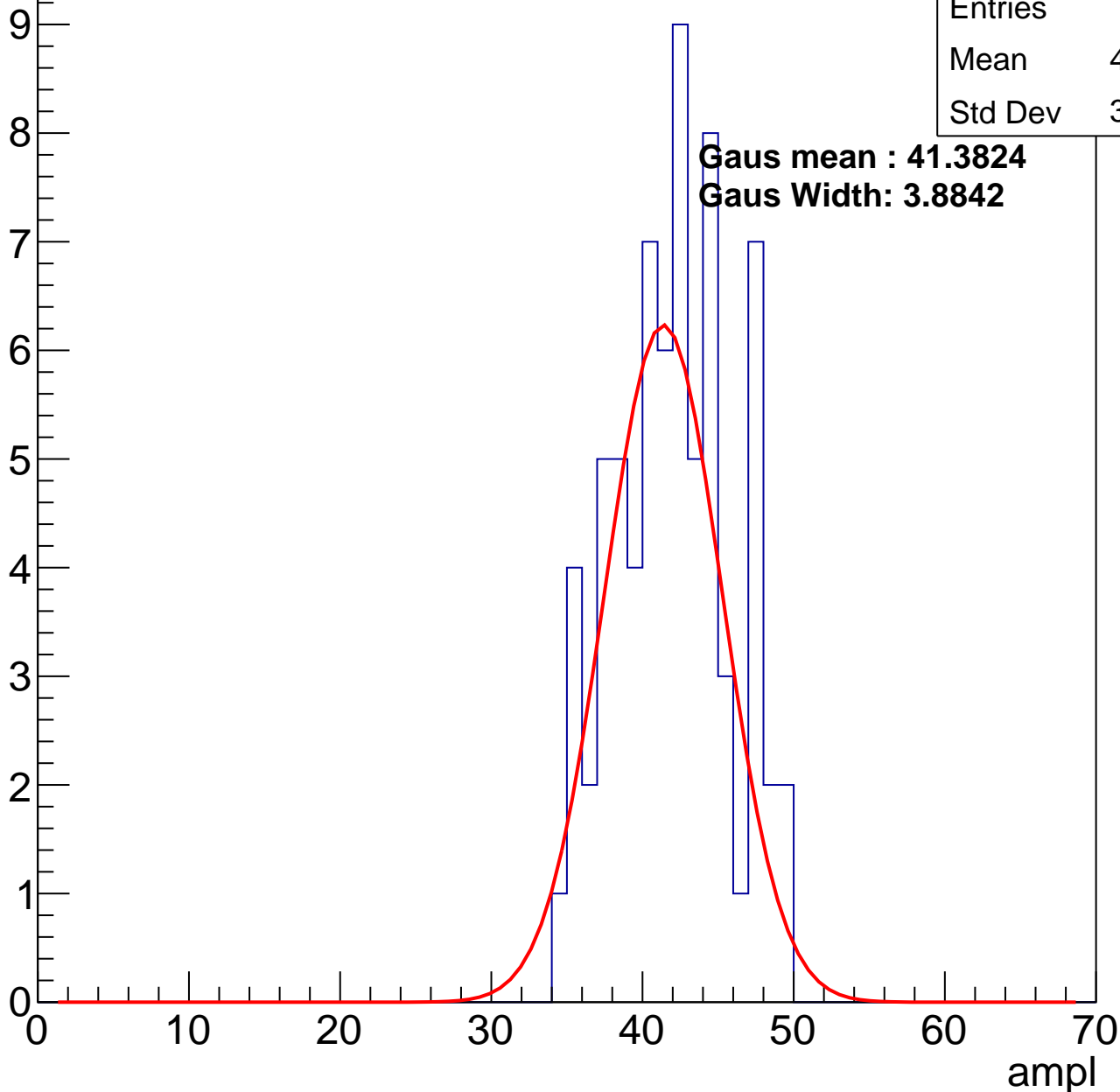
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.58
Std Dev	3.789

**Gaus mean : 41.3824**

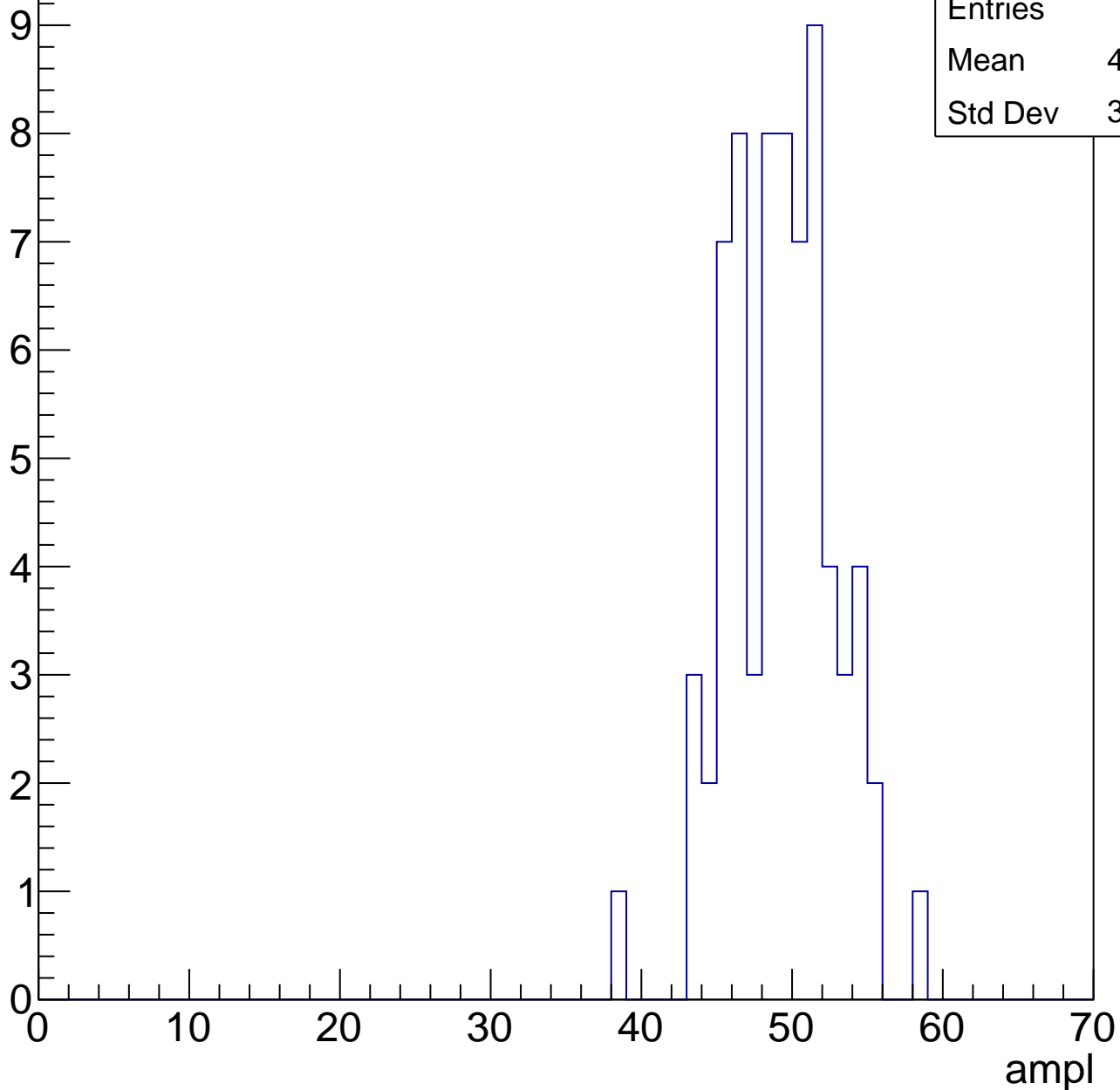
**Gaus Width: 3.8842**



# B0L102S, U17-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 54

Mean 55.15

Std Dev 2.724

3

2

1

0

0

1

2

3

4

5

6

7

0

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

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21

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293

294

295

296

297

298

299

300

301

302

303

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305

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310

311

312

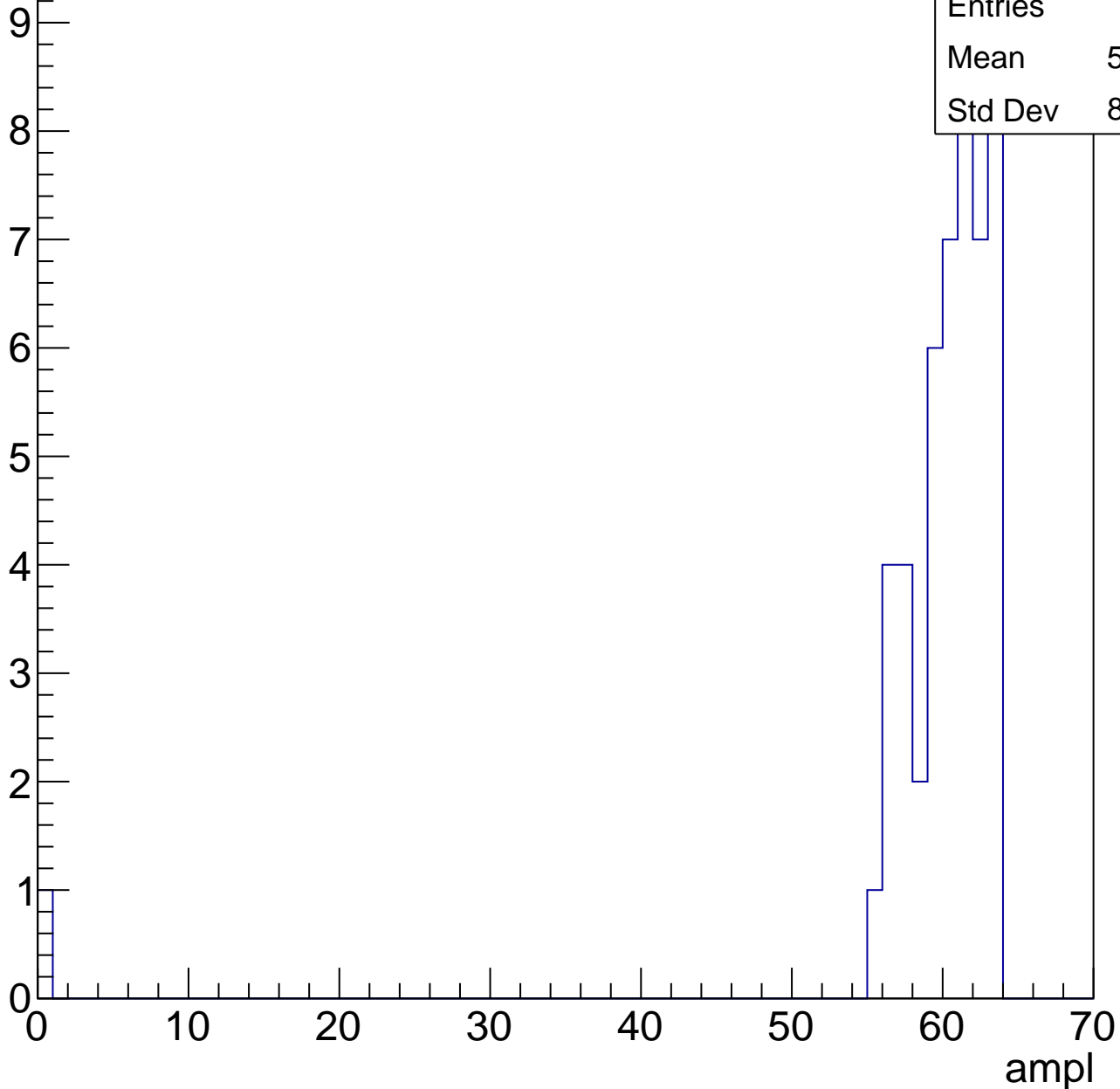
313

# B0L102S, U17-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	58.86
Std Dev	8.788



# B0L102S, U17-ch0, adc6

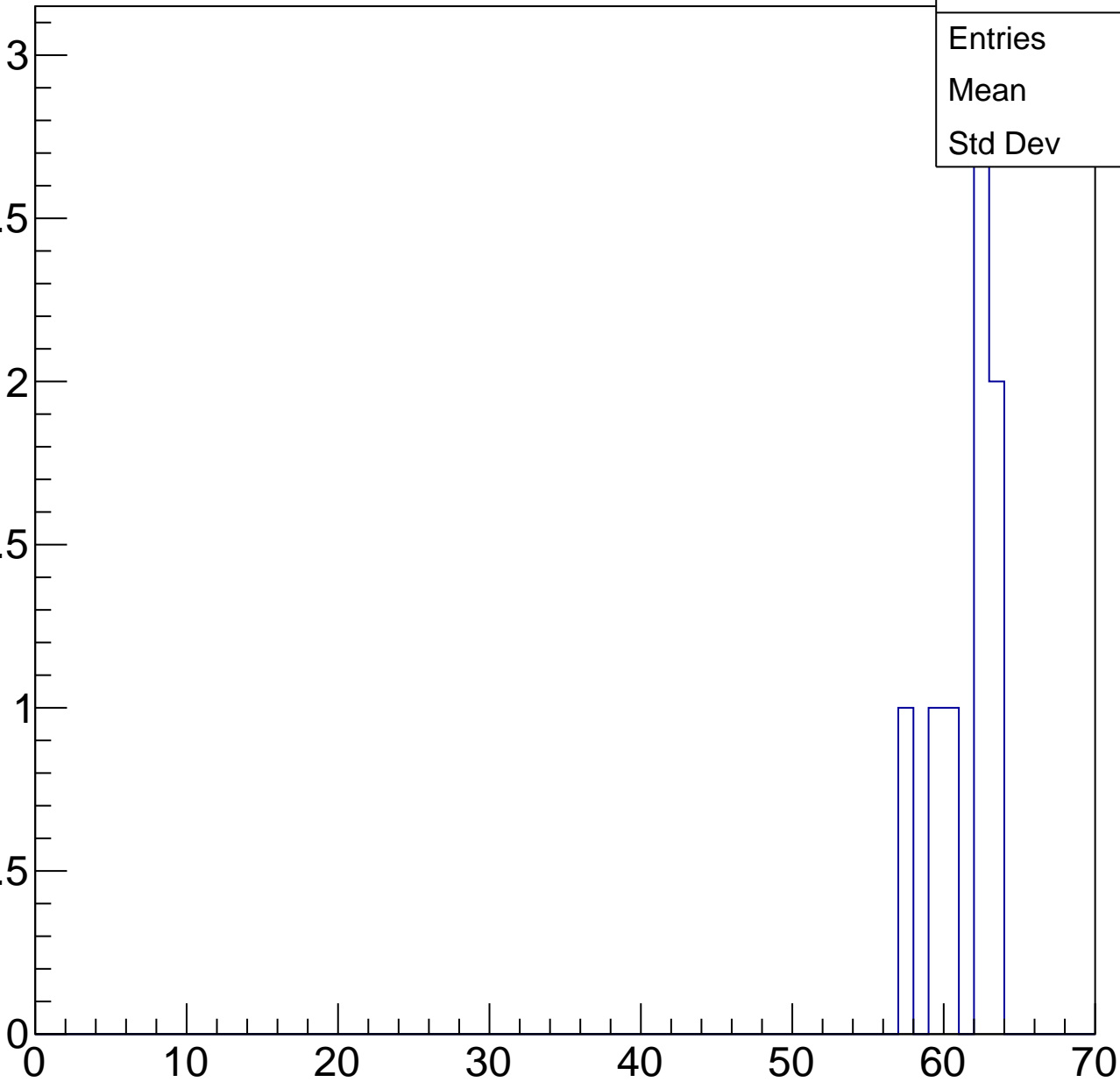
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	8
Mean	61
Std Dev	2

ampl





# B0L102S, U17-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch1, adc0

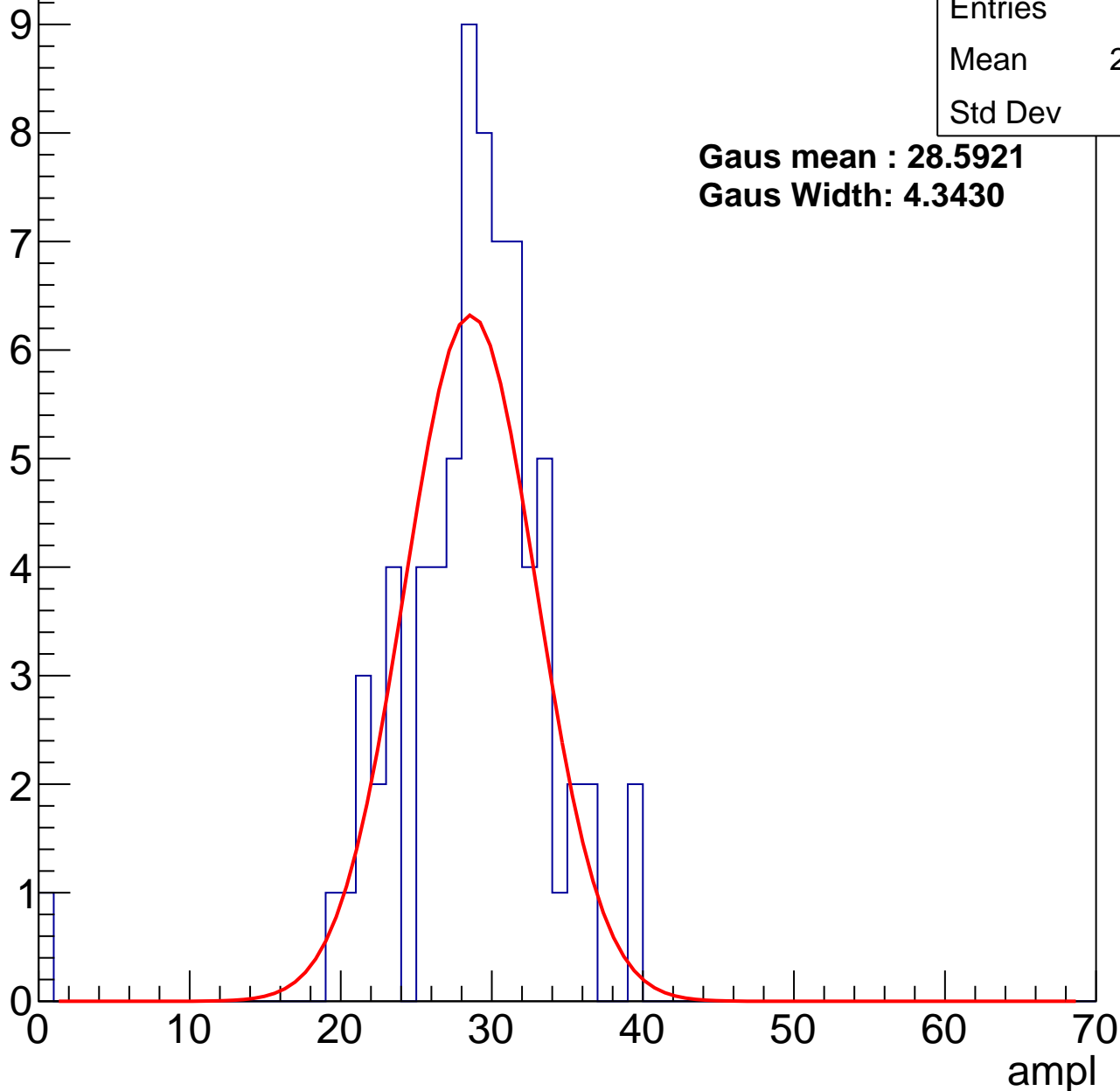
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	28.26
Std Dev	5.39

**Gaus mean : 28.5921**

**Gaus Width: 4.3430**



# B0L102S, U17-ch1, adc1

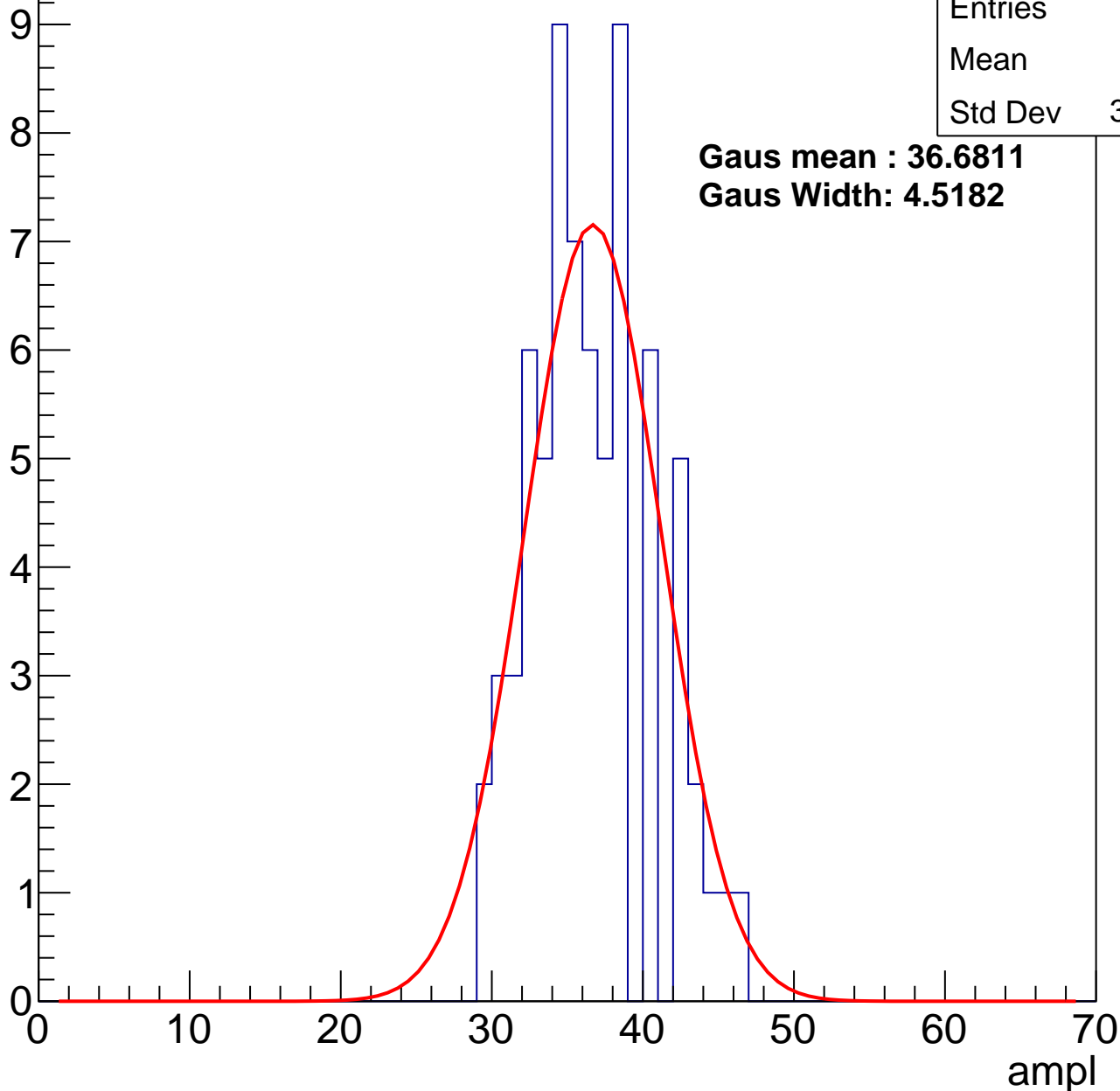
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.1
Std Dev	3.979

**Gaus mean : 36.6811**

**Gaus Width: 4.5182**



# B0L102S, U17-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	55
Mean	41.58
Std Dev	3.279

**Gaus mean : 42.0269**

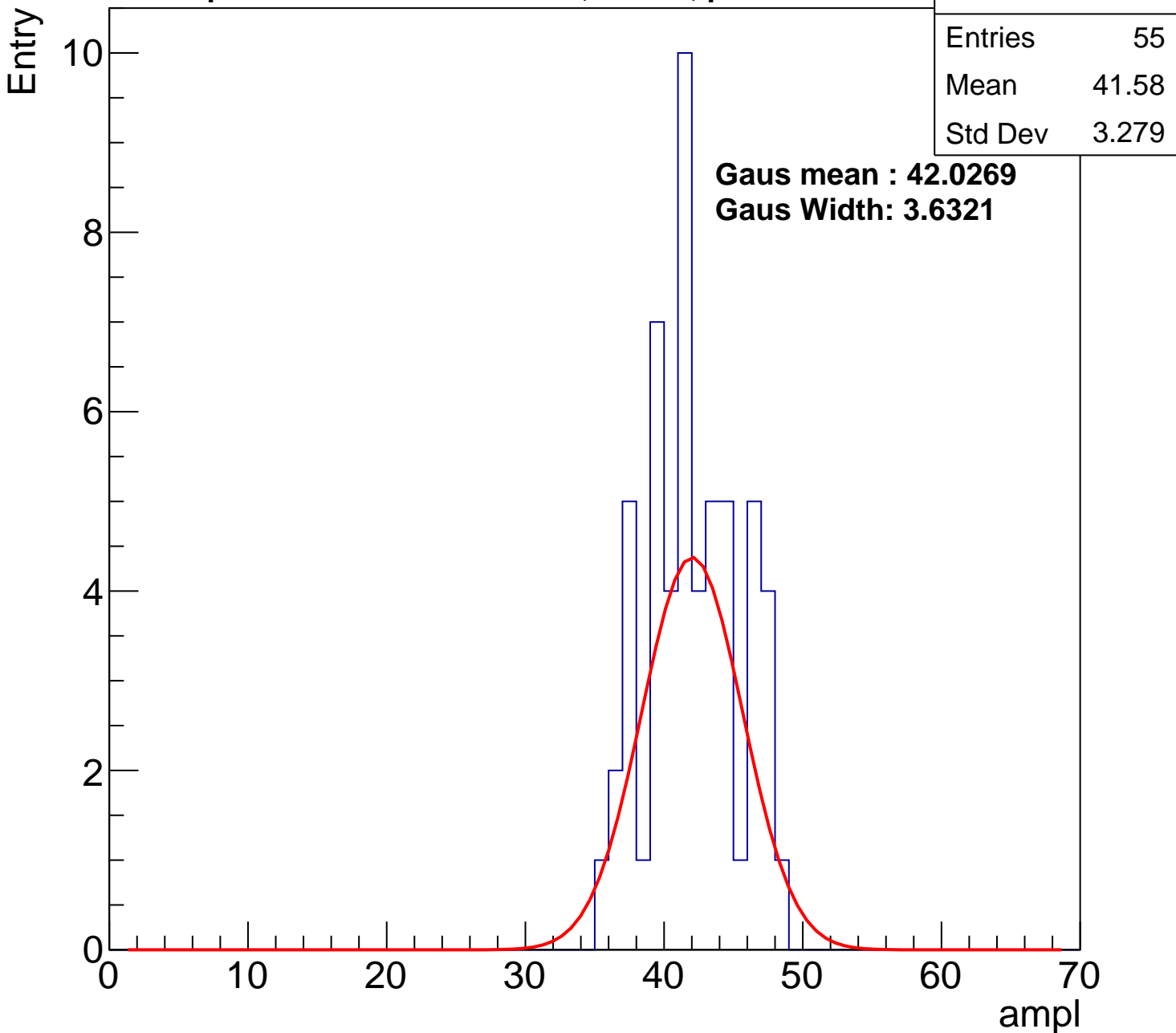
**Gaus Width: 3.6321**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

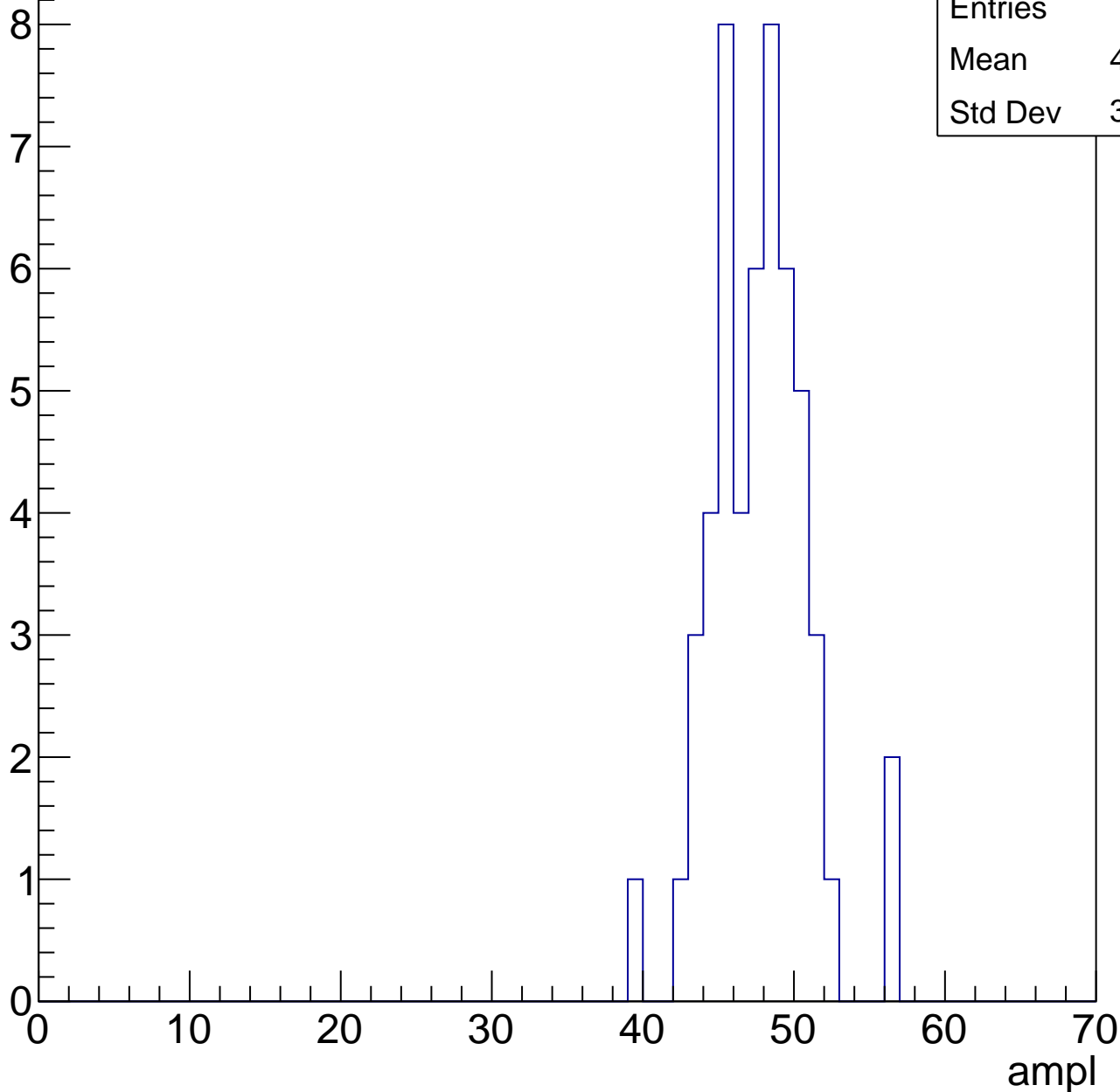


# B0L102S, U17-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

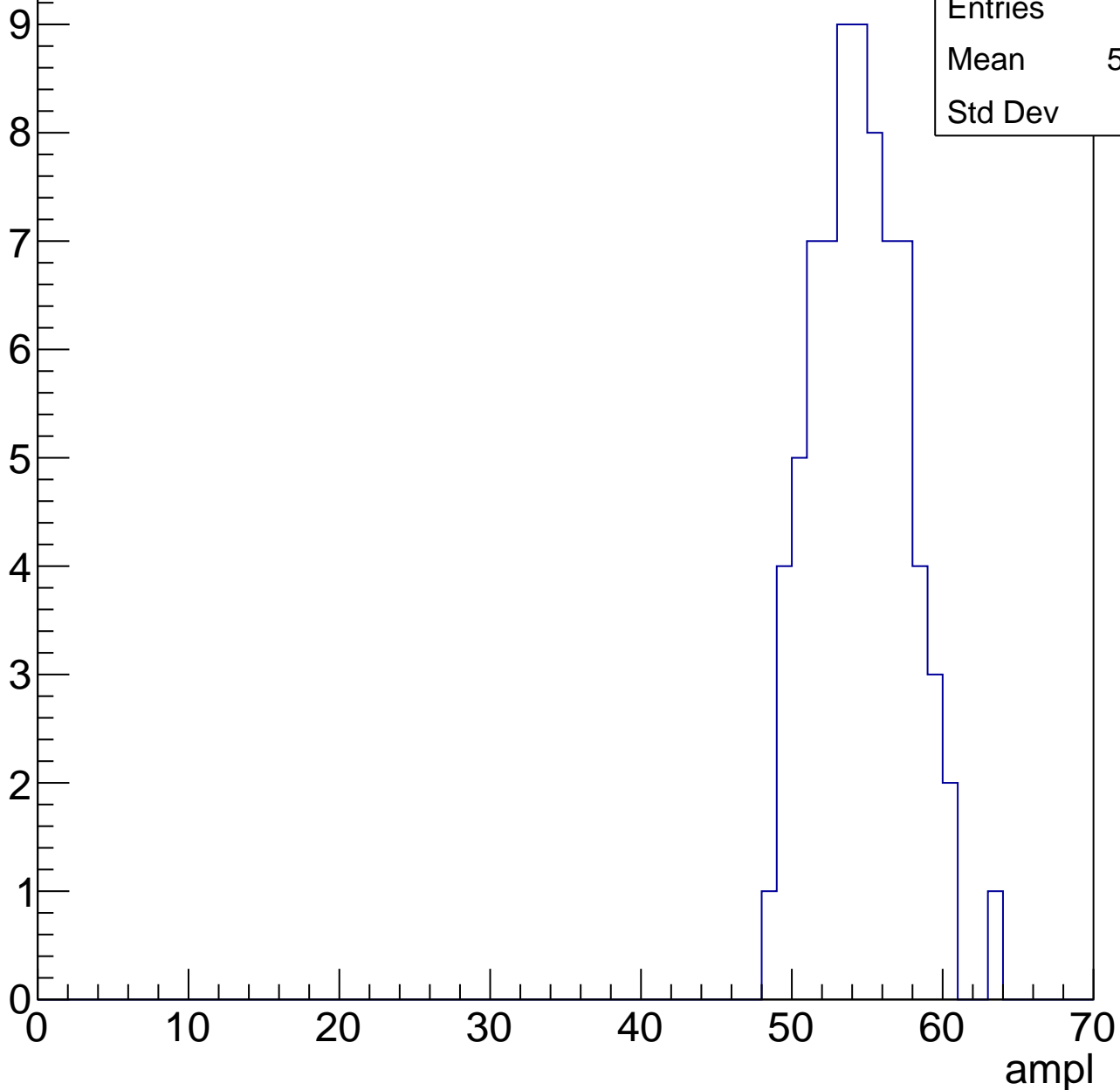
Entries	52
Mean	47.25
Std Dev	3.155



# B0L102S, U17-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



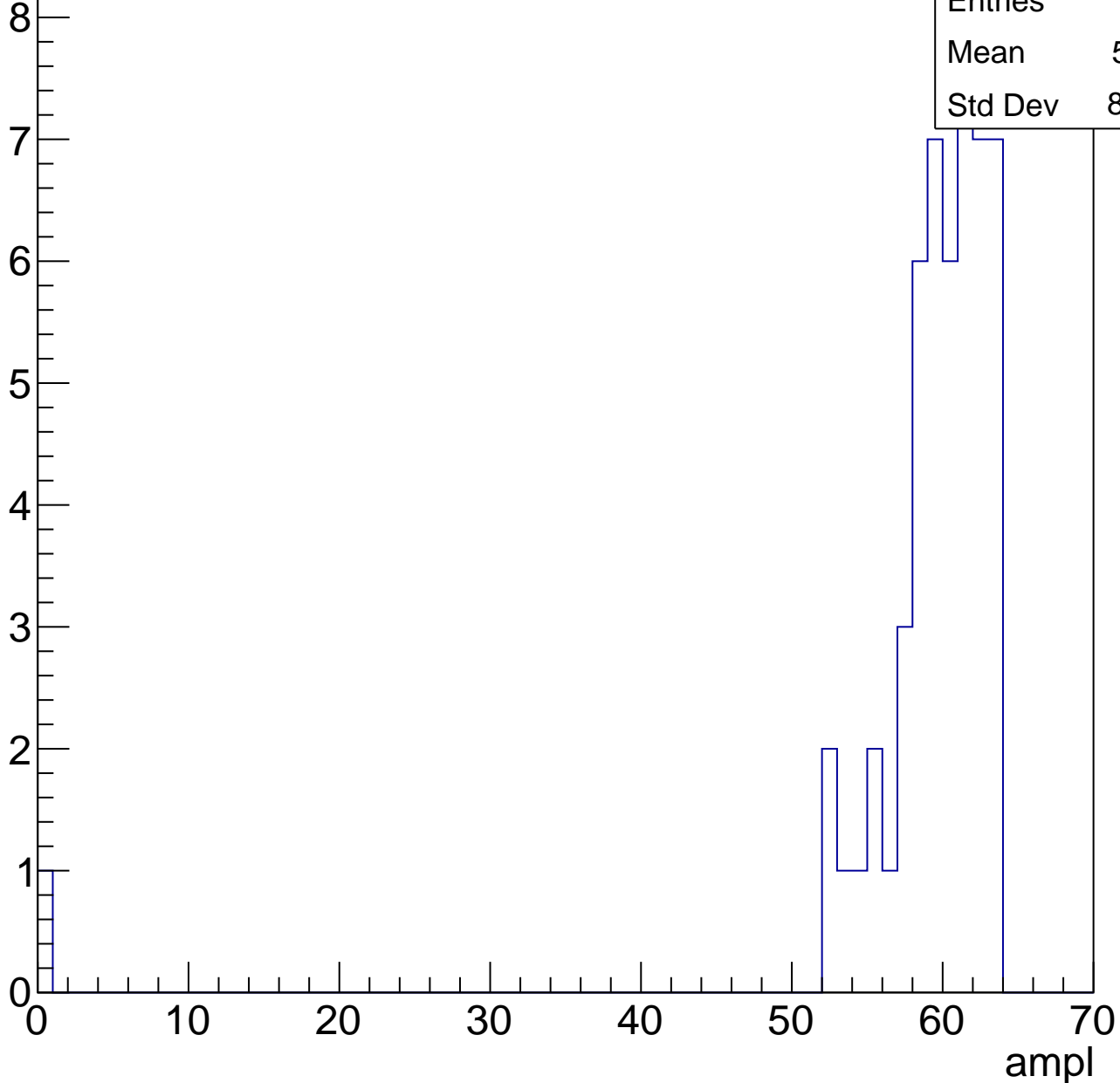
Entries	74
Mean	54.07
Std Dev	3.09

# B0L102S, U17-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

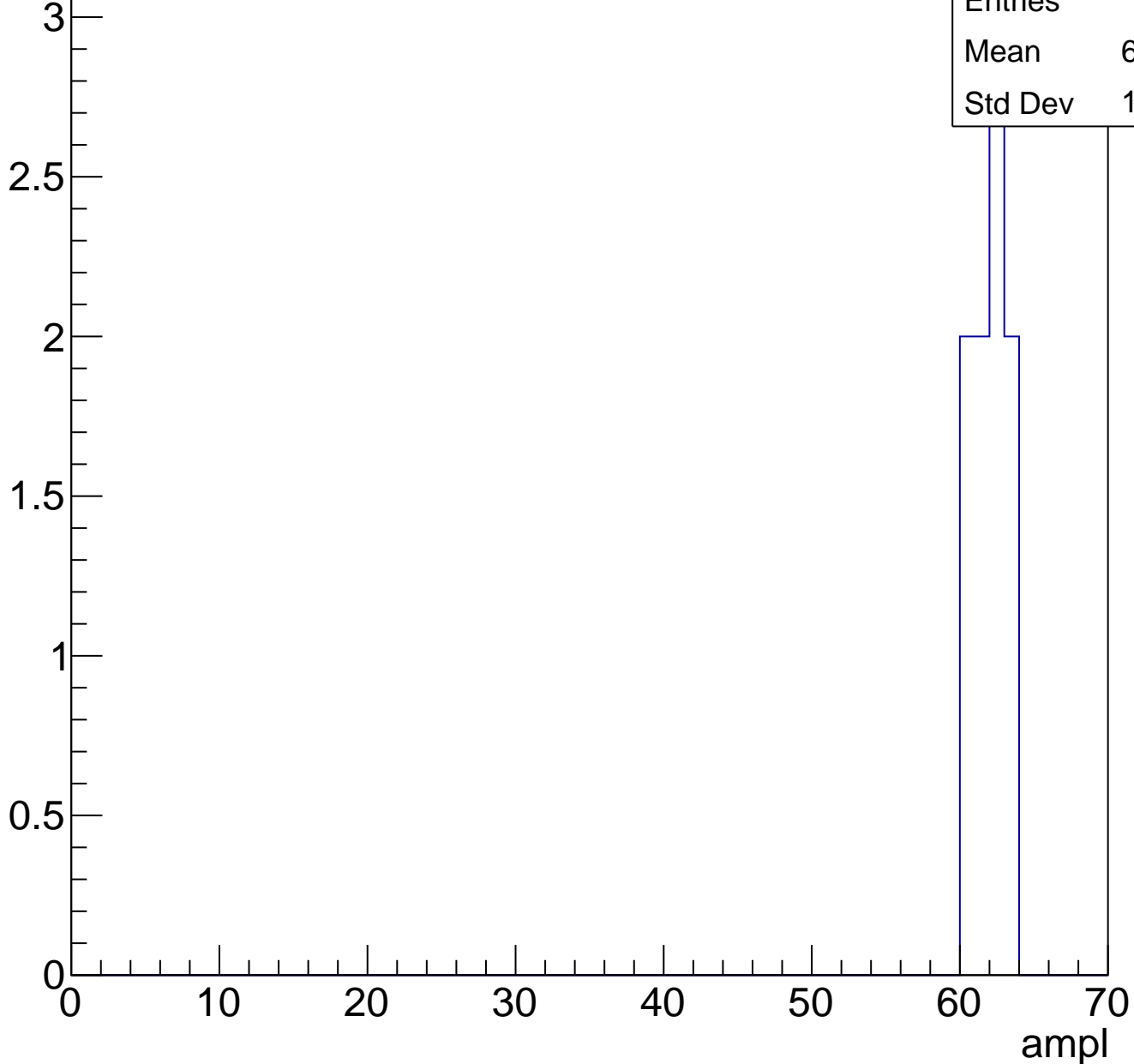
Entries	52
Mean	58.31
Std Dev	8.646



# B0L102S, U17-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch2, adc0

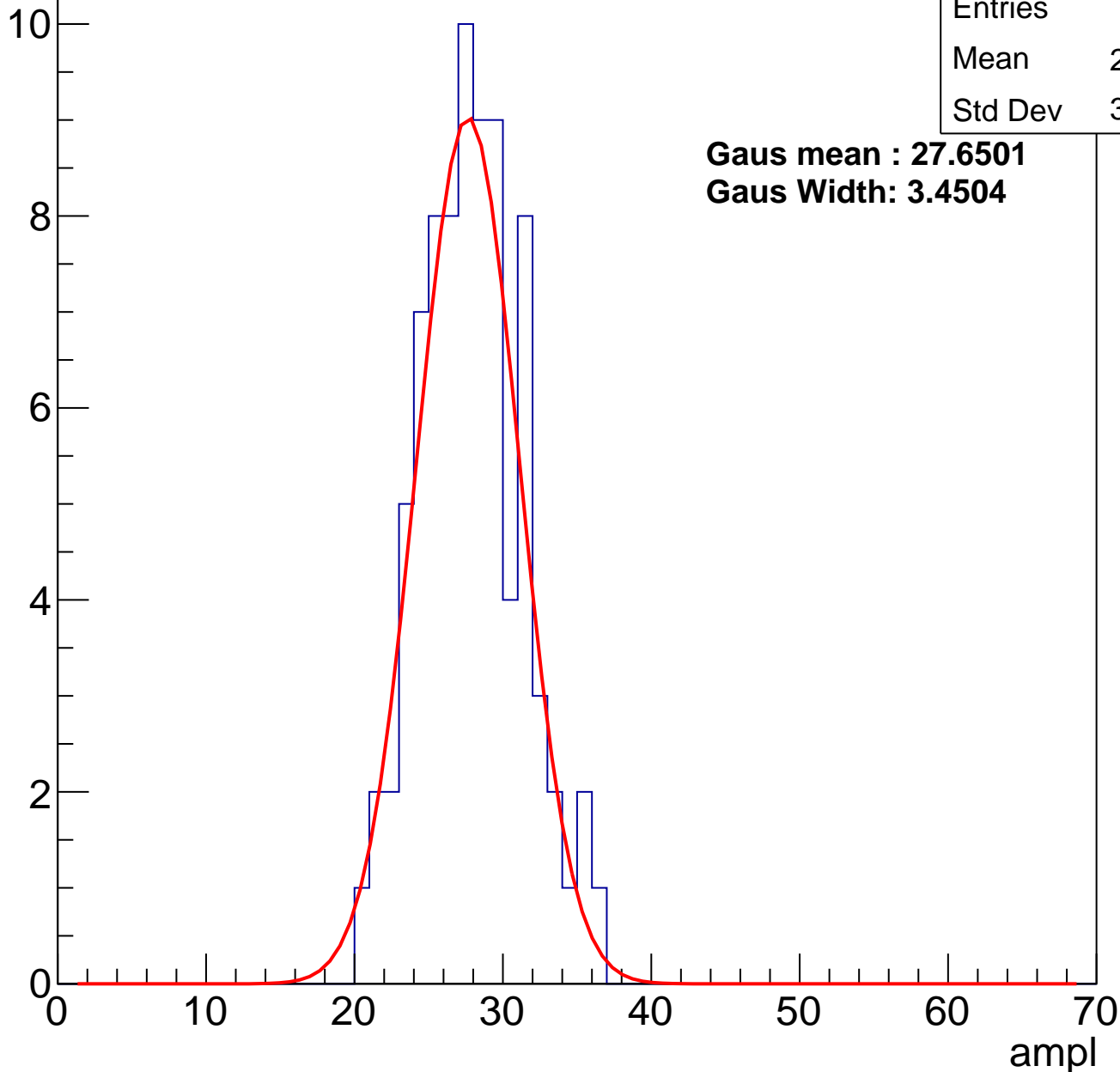
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	82
Mean	27.44
Std Dev	3.408

**Gaus mean : 27.6501**

**Gaus Width: 3.4504**

Entry



# B0L102S, U17-ch2, adc1

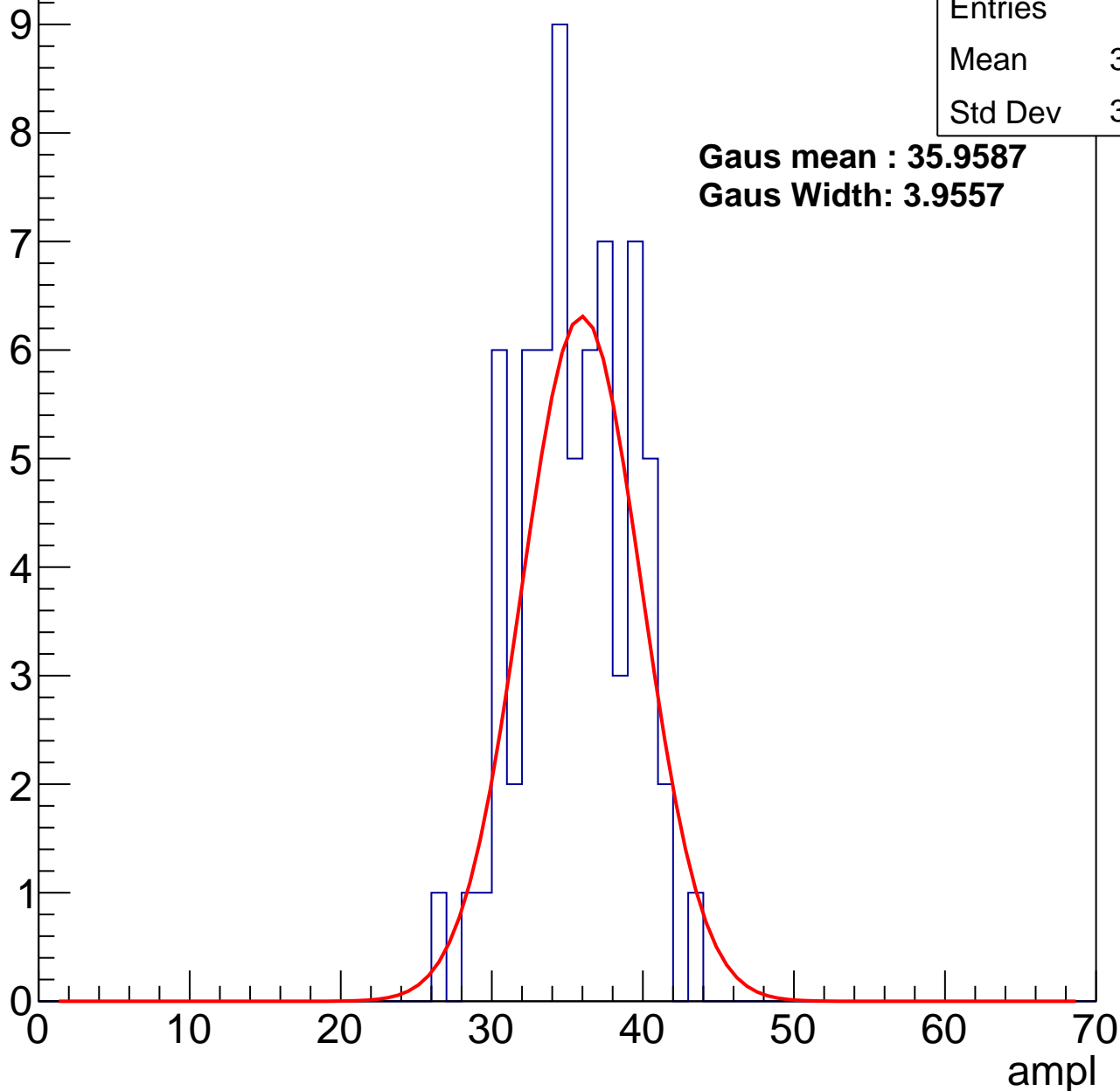
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	35.04
Std Dev	3.575

**Gaus mean : 35.9587**

**Gaus Width: 3.9557**



# B0L102S, U17-ch2, adc2

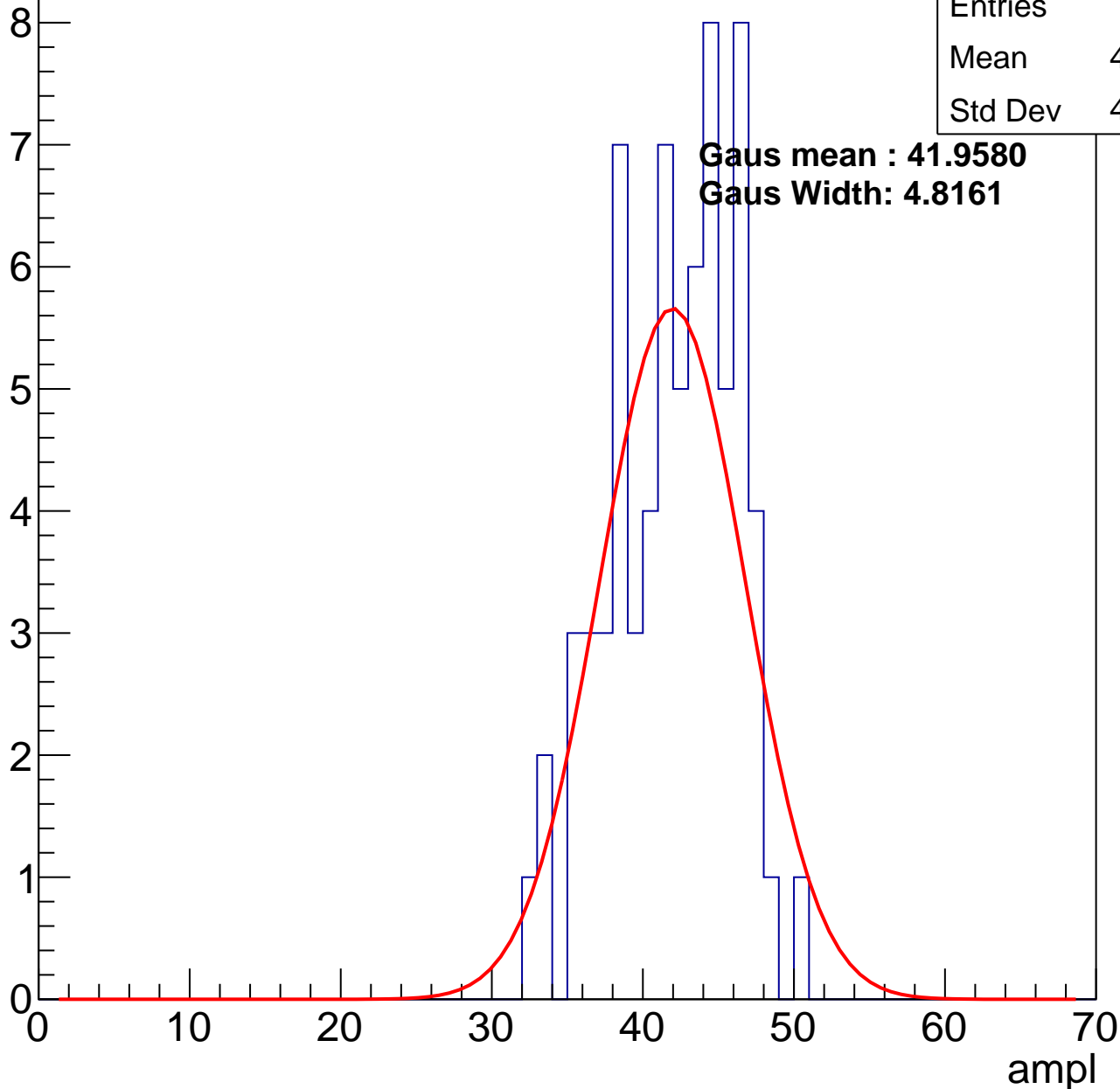
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.56
Std Dev	4.027

**Gaus mean : 41.9580**

**Gaus Width: 4.8161**

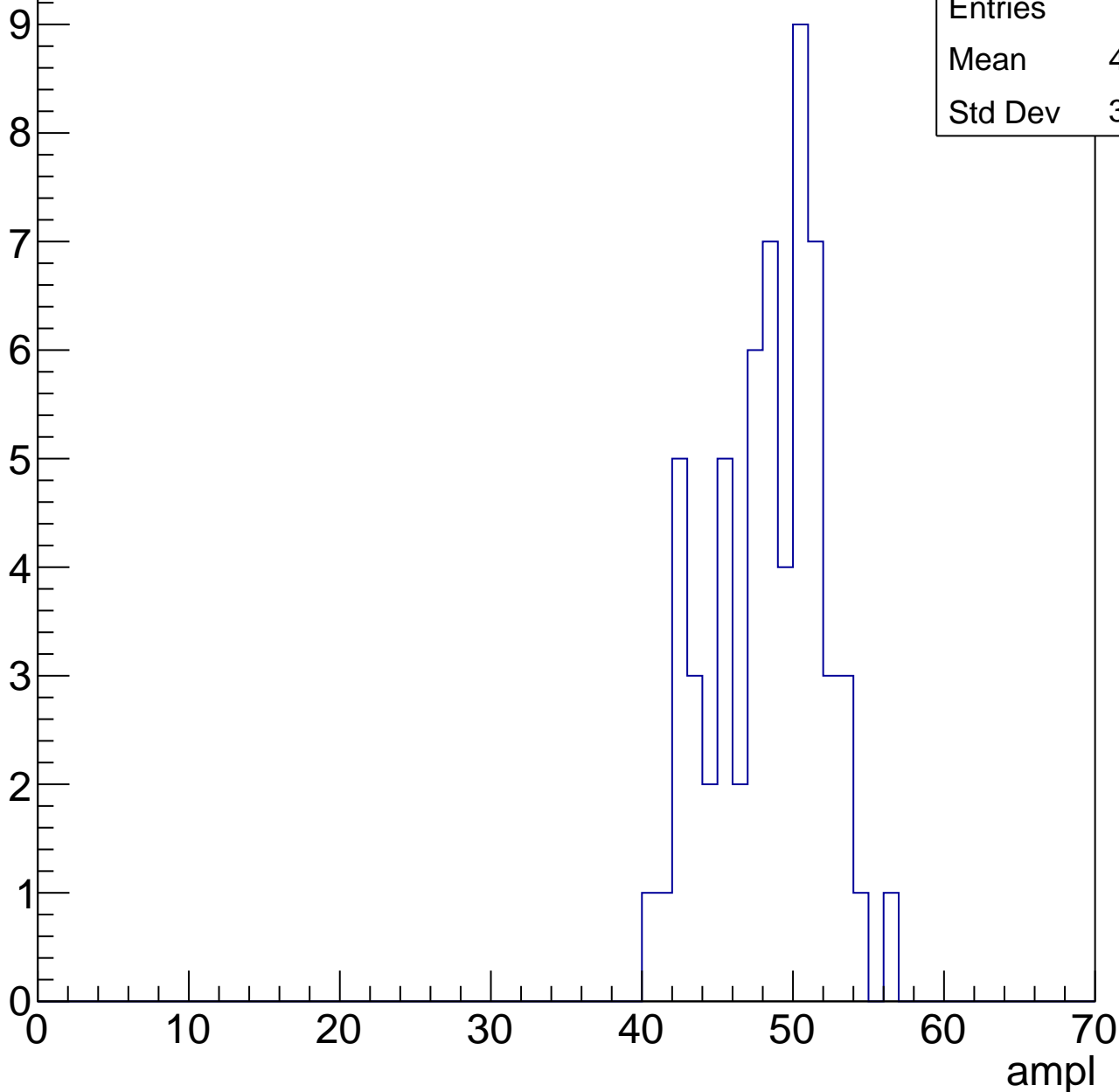


# B0L102S, U17-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	47.85
Std Dev	3.614

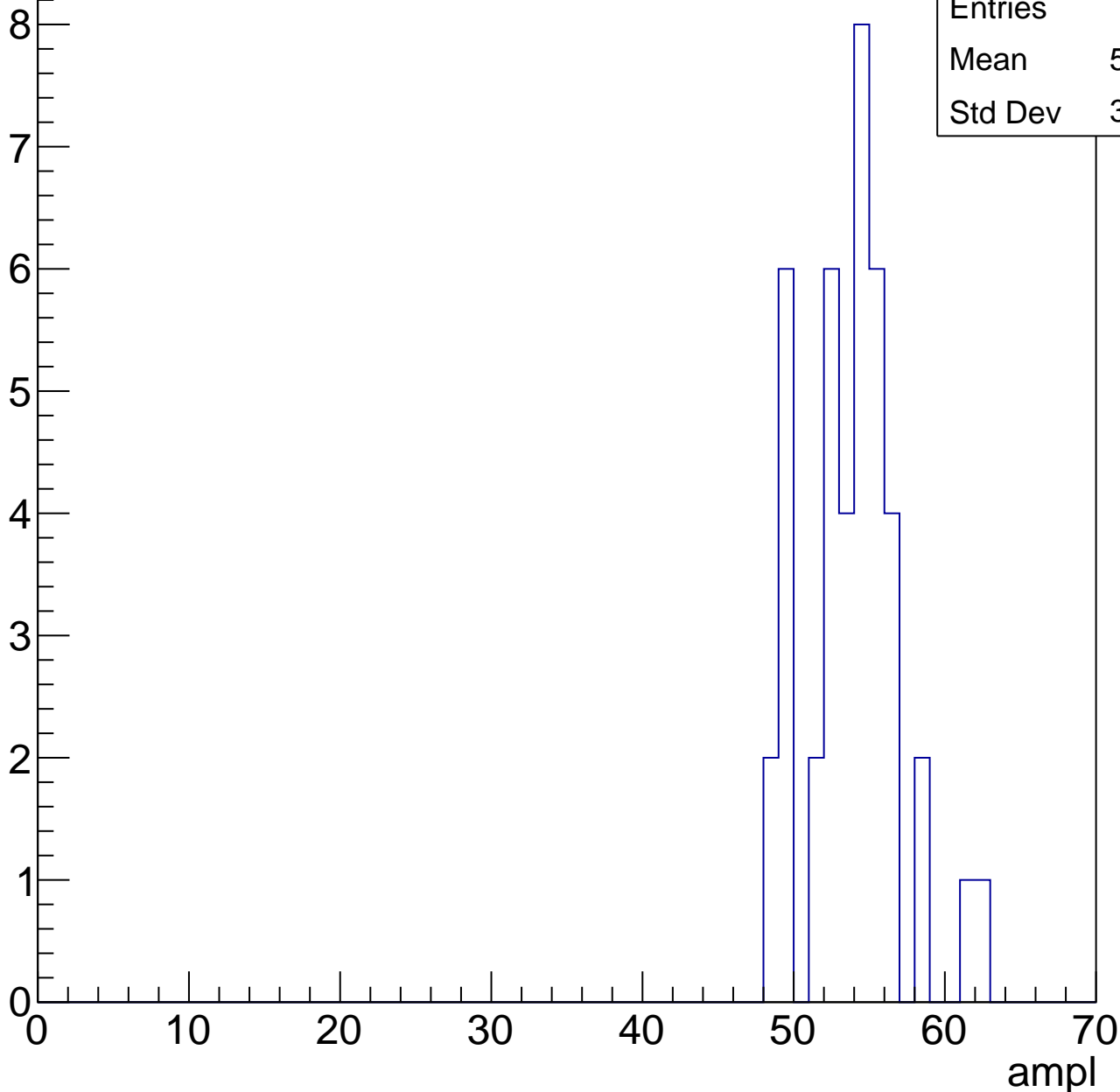


# B0L102S, U17-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	53.36
Std Dev	3.153



# B0L102S, U17-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	72
Mean	58.61
Std Dev	2.826

10

8

6

4

2

0

10

20

30

40

50

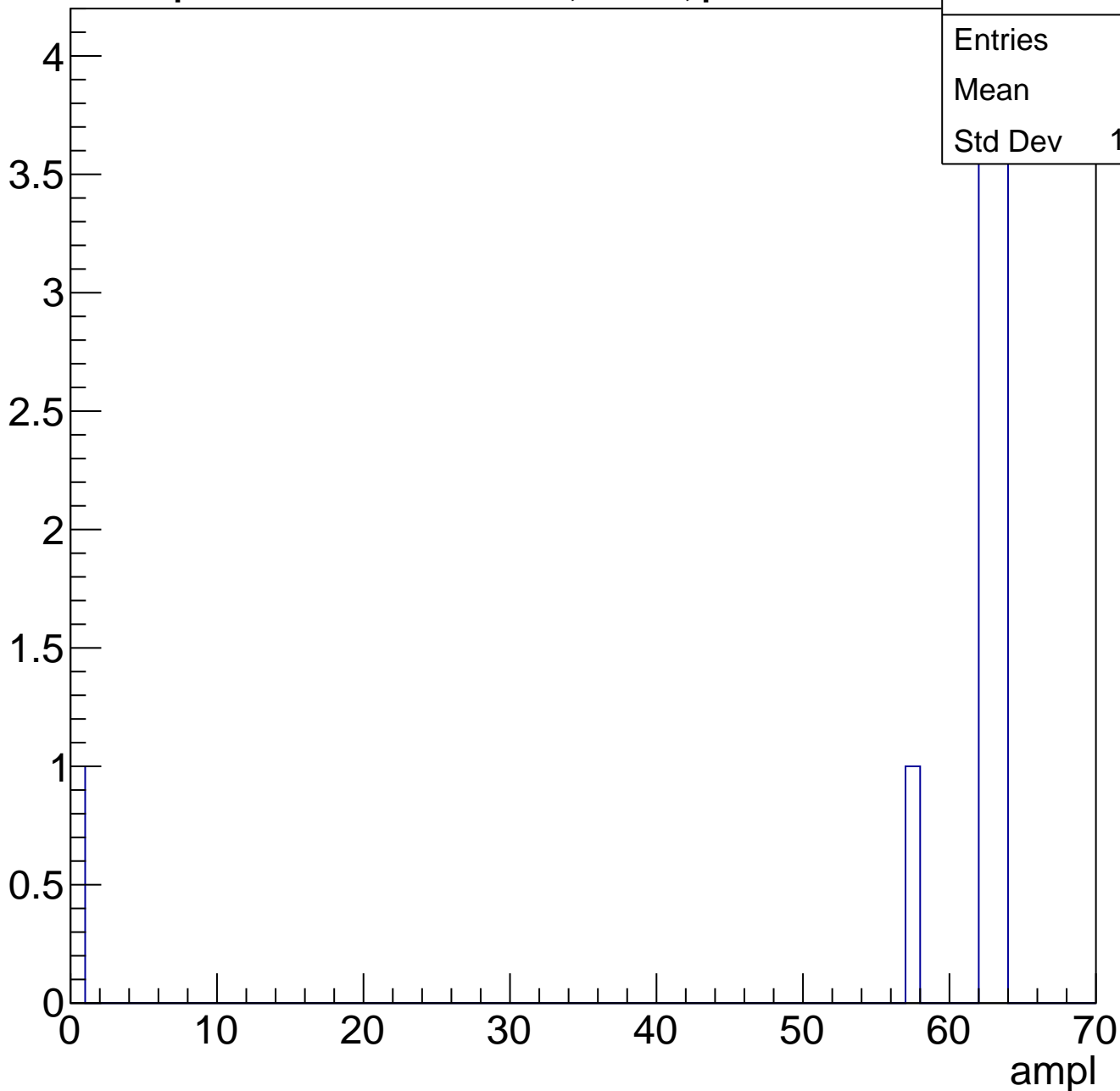
60

ampl

# B0L102S, U17-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	17
Std Dev	0

ampl

# B0L102S, U17-ch3, adc0

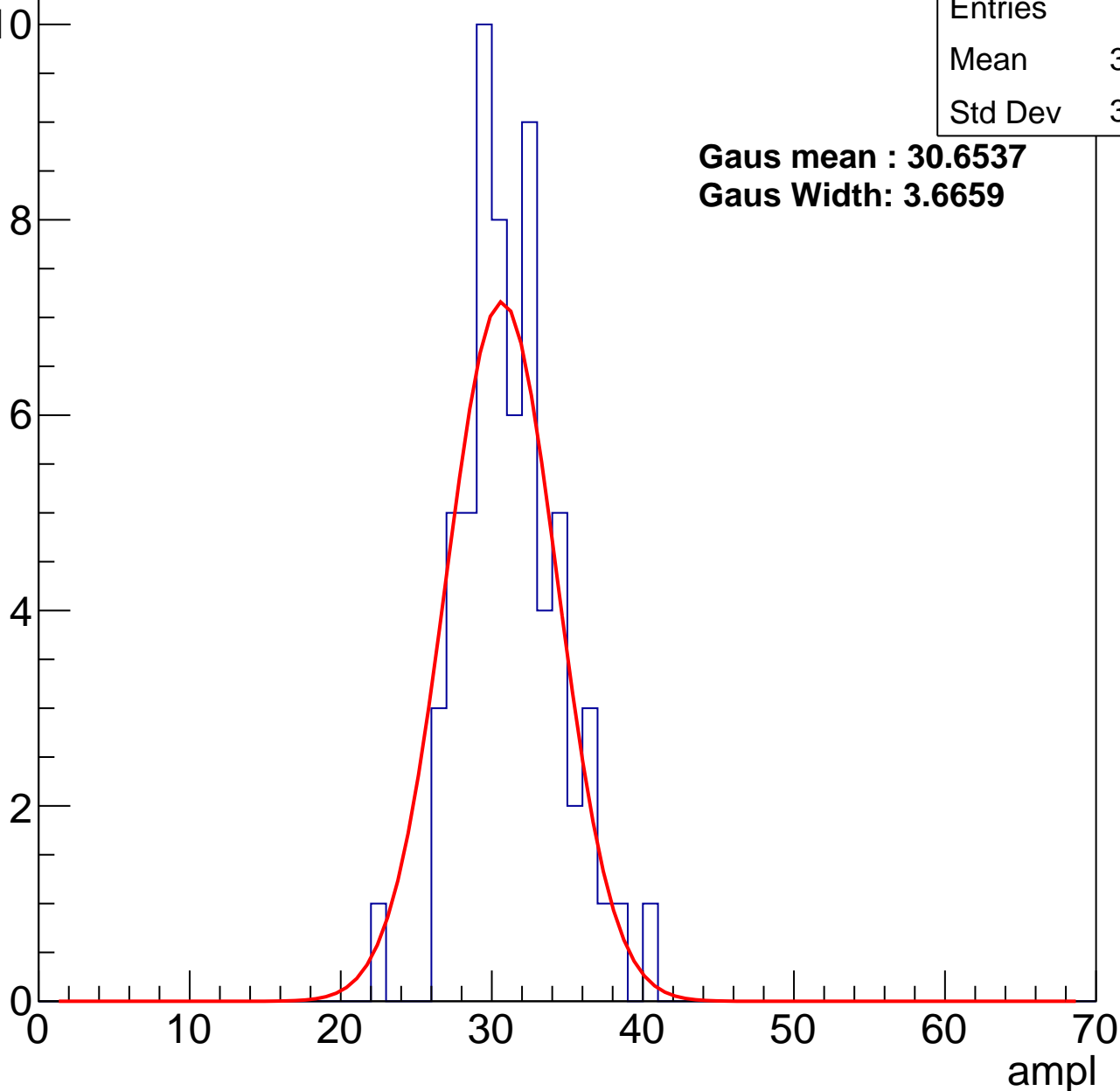
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	30.84
Std Dev	3.237

**Gaus mean : 30.6537**

**Gaus Width: 3.6659**



# B0L102S, U17-ch3, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	37.36
Std Dev	3.249

**Gaus mean : 38.0330**

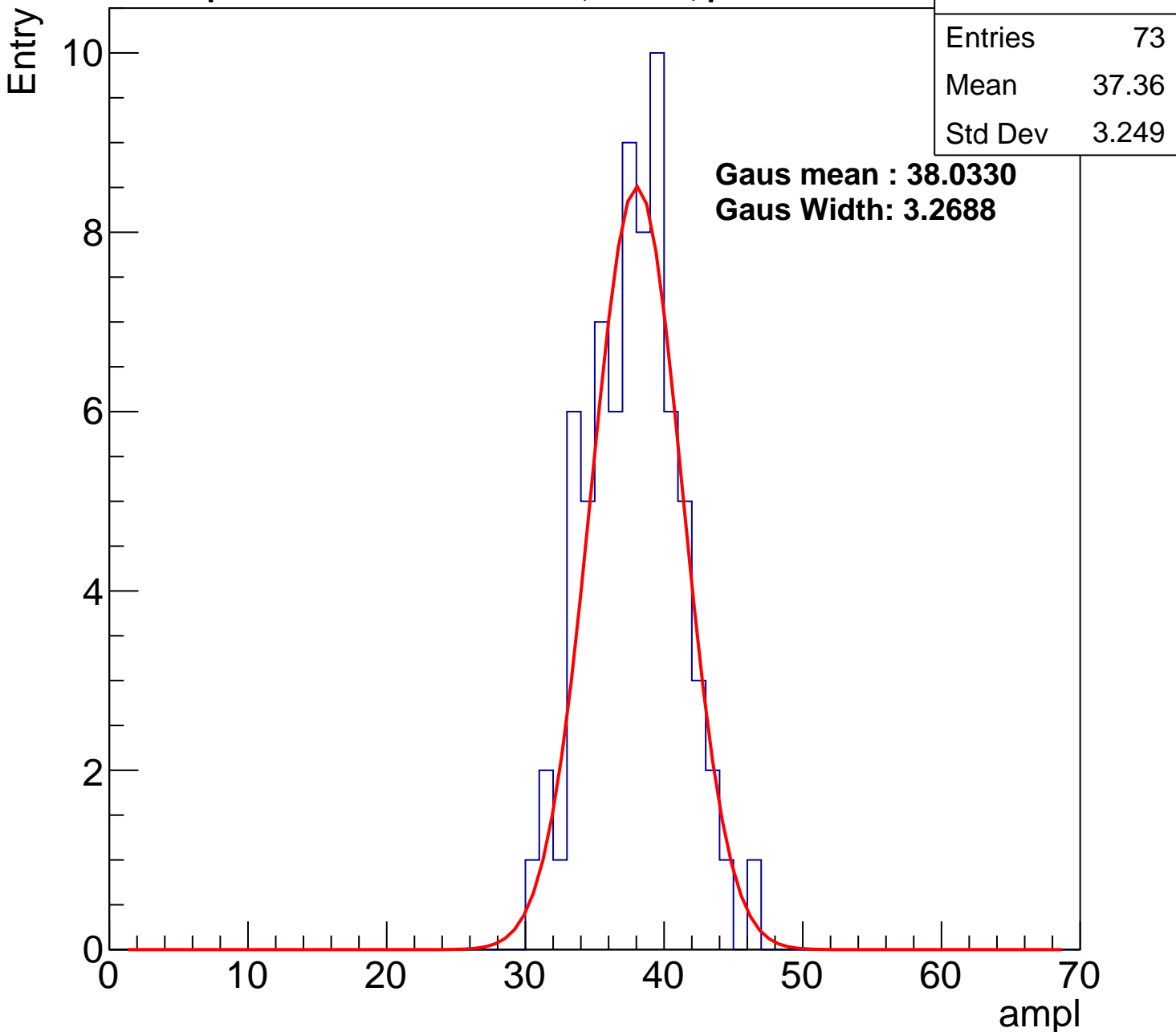
**Gaus Width: 3.2688**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U17-ch3, adc2

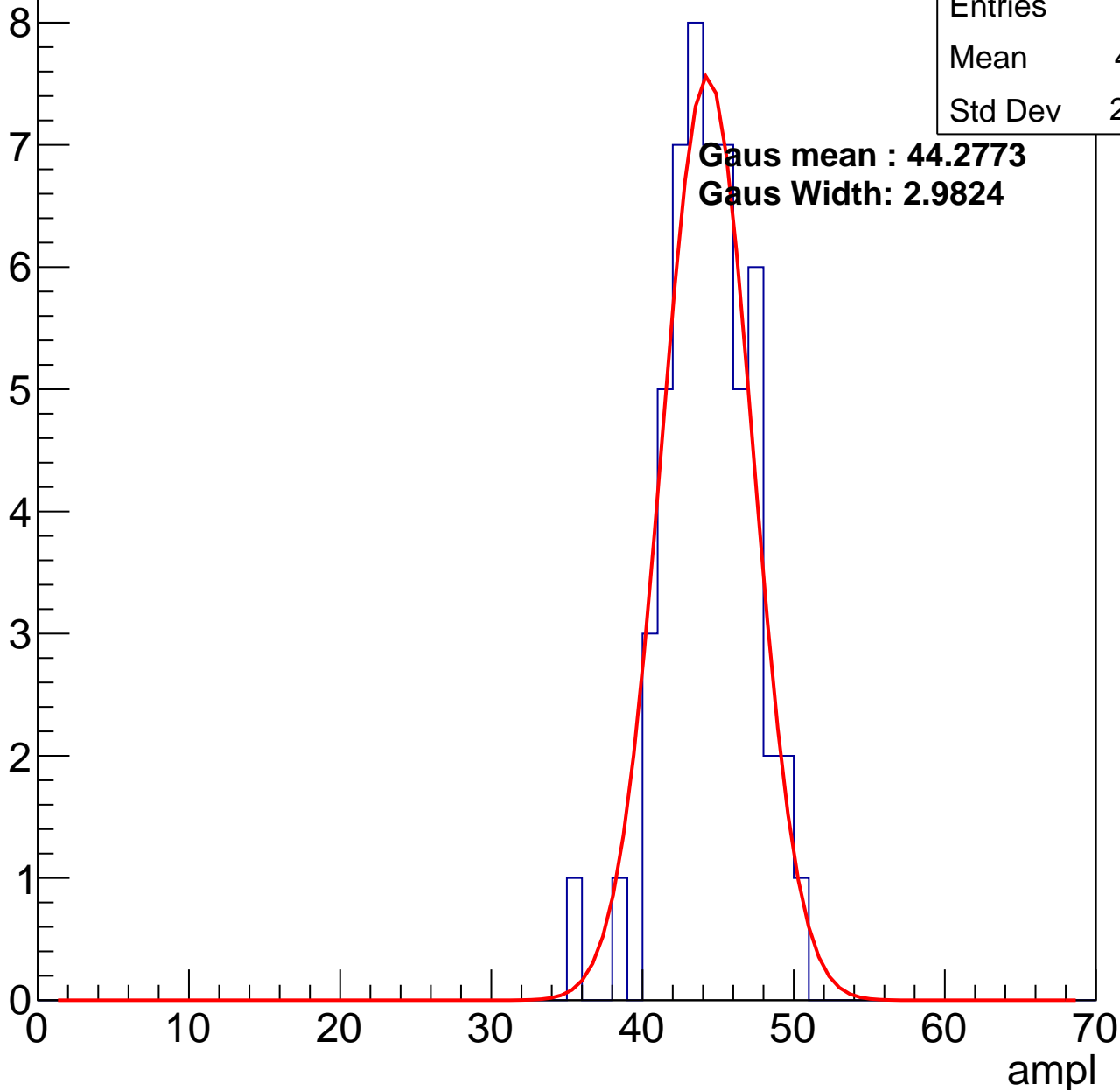
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	43.91
Std Dev	2.843

**Gaus mean : 44.2773**

**Gaus Width: 2.9824**

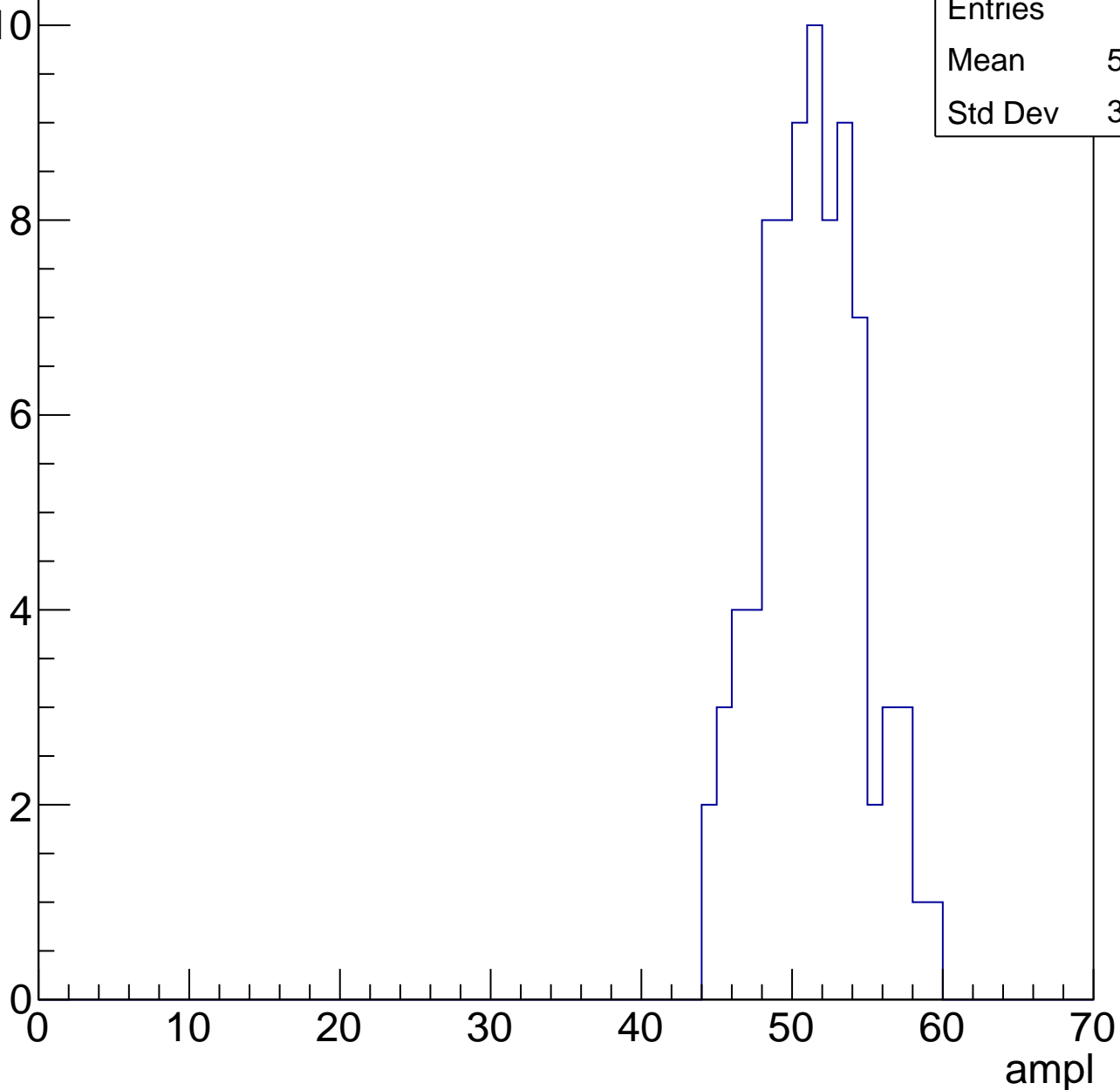


# B0L102S, U17-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	82
Mean	50.83
Std Dev	3.342

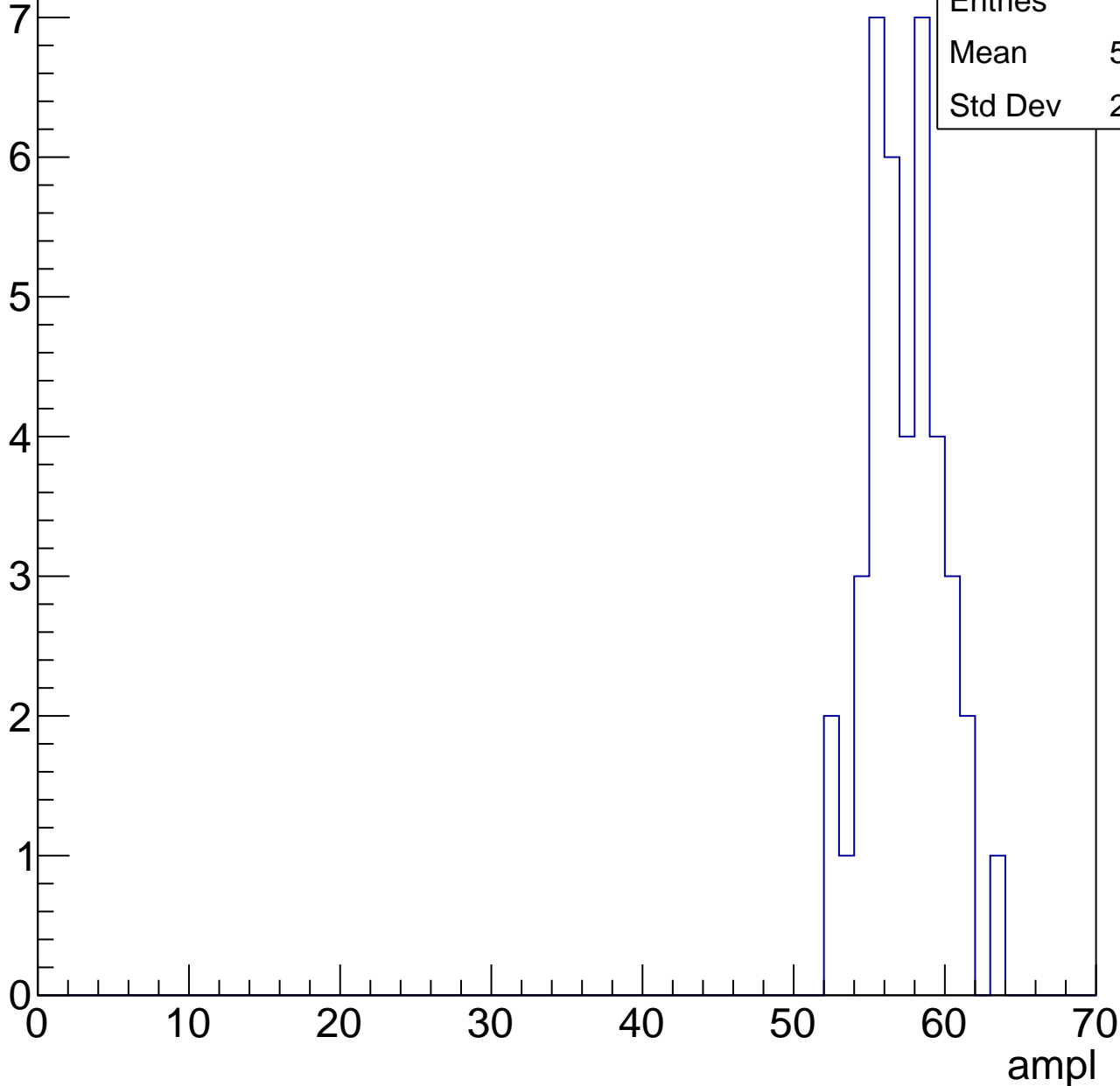


# B0L102S, U17-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	56.88
Std Dev	2.472



# B0L102S, U17-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	59.2
Std Dev	9.157

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

# B0L102S, U17-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch4, adc0

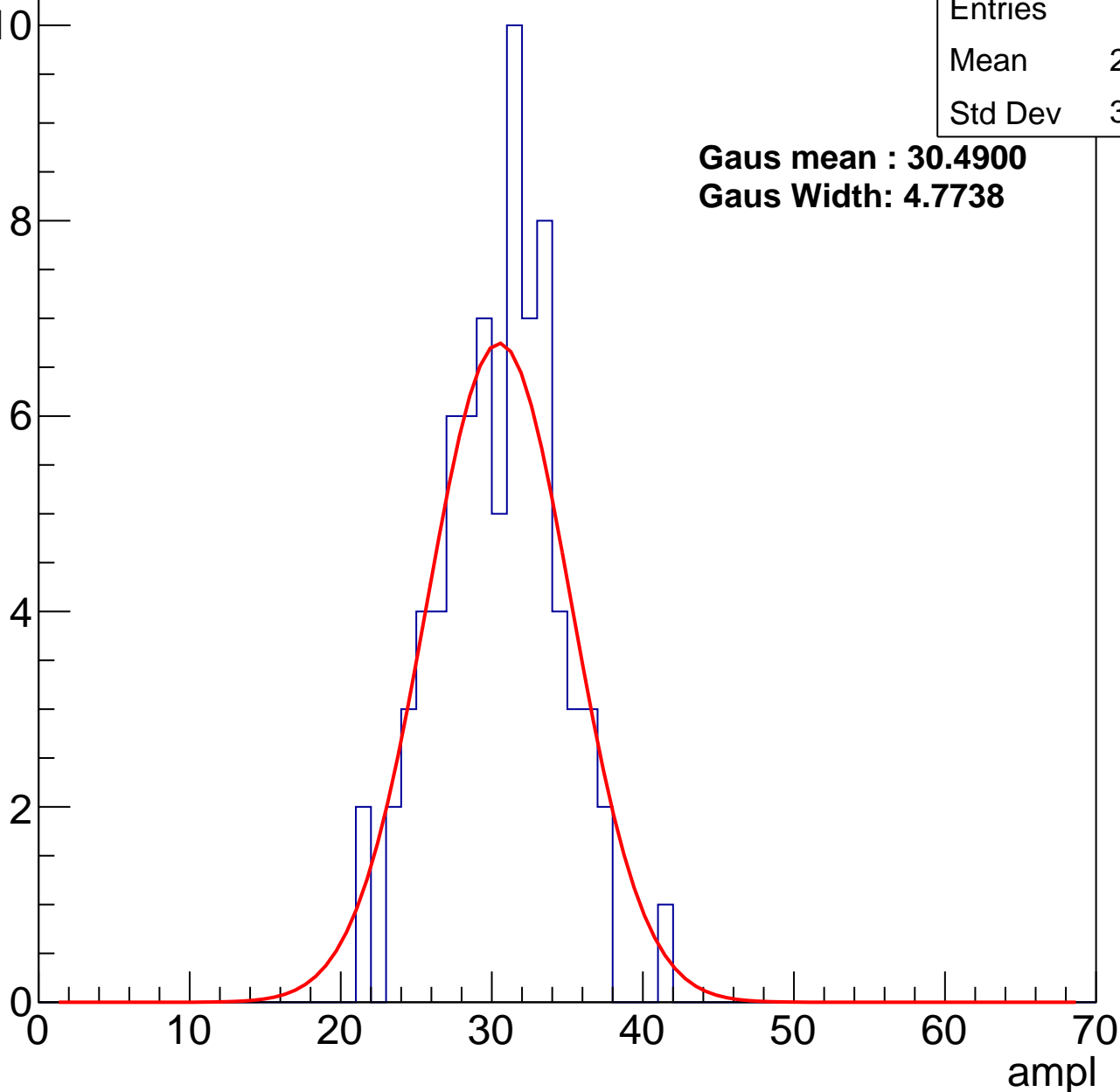
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	29.99
Std Dev	3.936

**Gaus mean : 30.4900**

**Gaus Width: 4.7738**



# B0L102S, U17-ch4, adc1

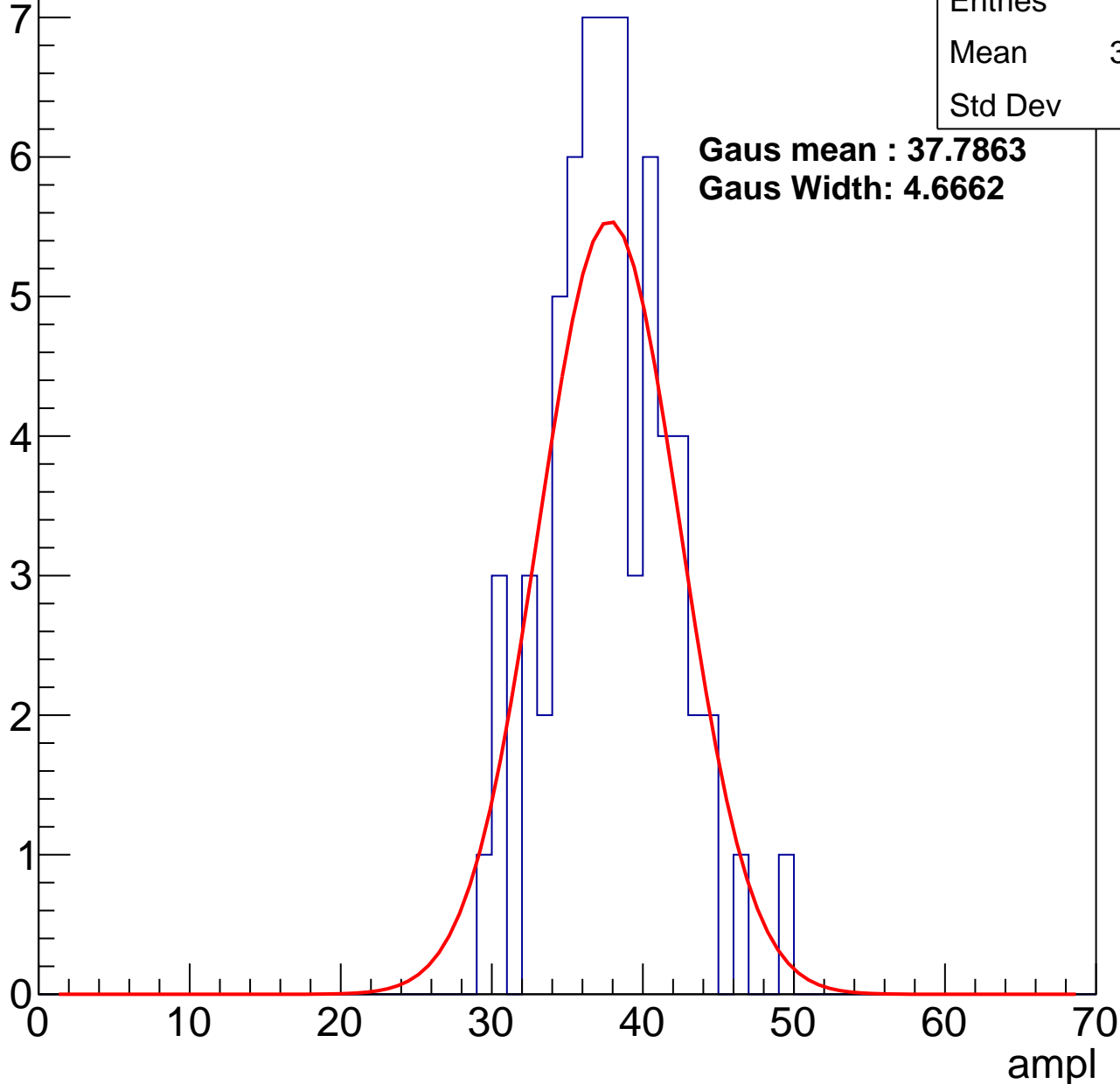
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	37.44
Std Dev	3.98

**Gaus mean : 37.7863**

**Gaus Width: 4.6662**



# B0L102S, U17-ch4, adc2

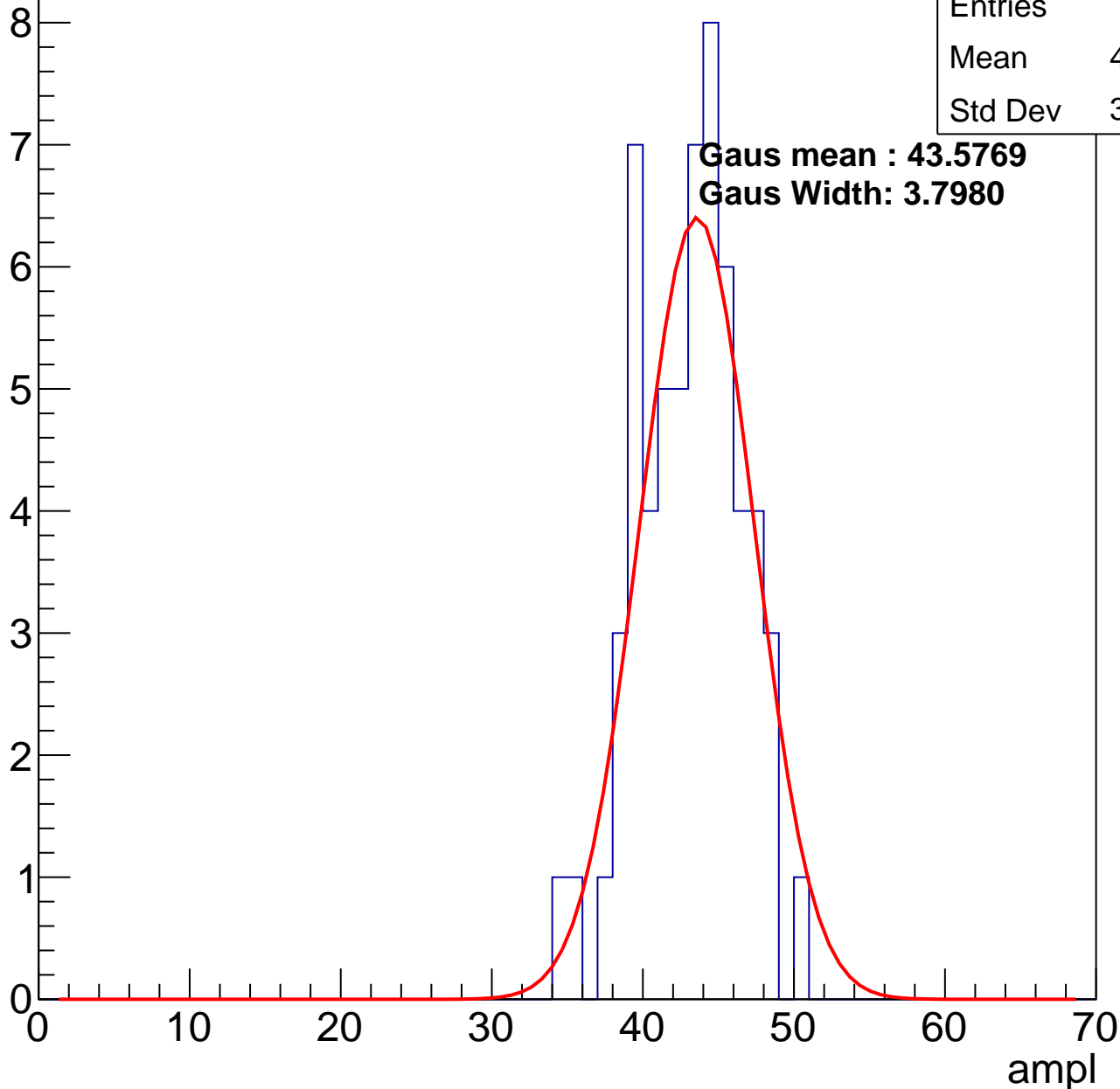
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.62
Std Dev	3.357

**Gaus mean : 43.5769**

**Gaus Width: 3.7980**

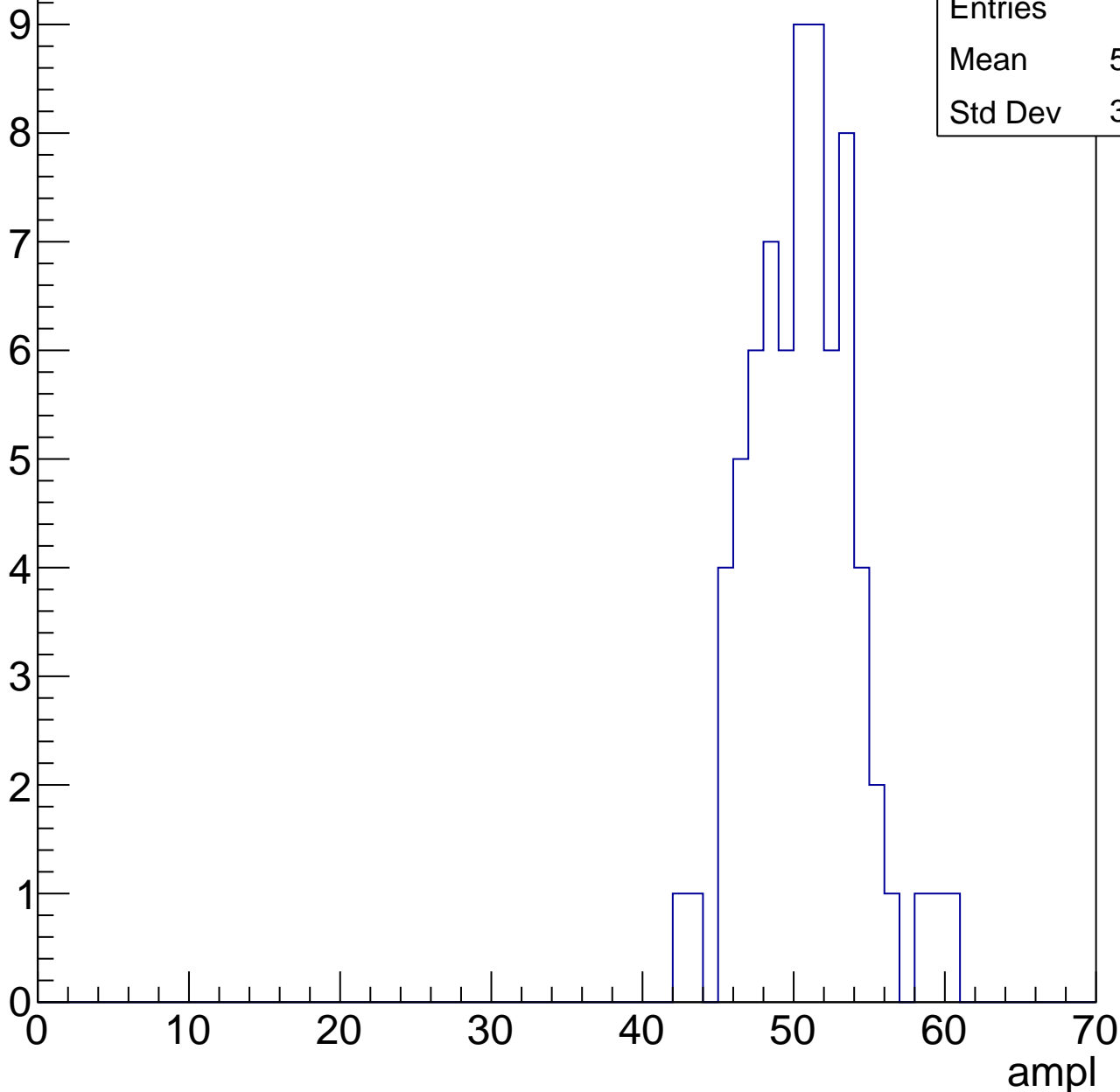


# B0L102S, U17-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	50.15
Std Dev	3.495



# B0L102S, U17-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	55
Mean	55.96
Std Dev	3.156

Entry

10

8

6

4

2

0

0

10

20

30

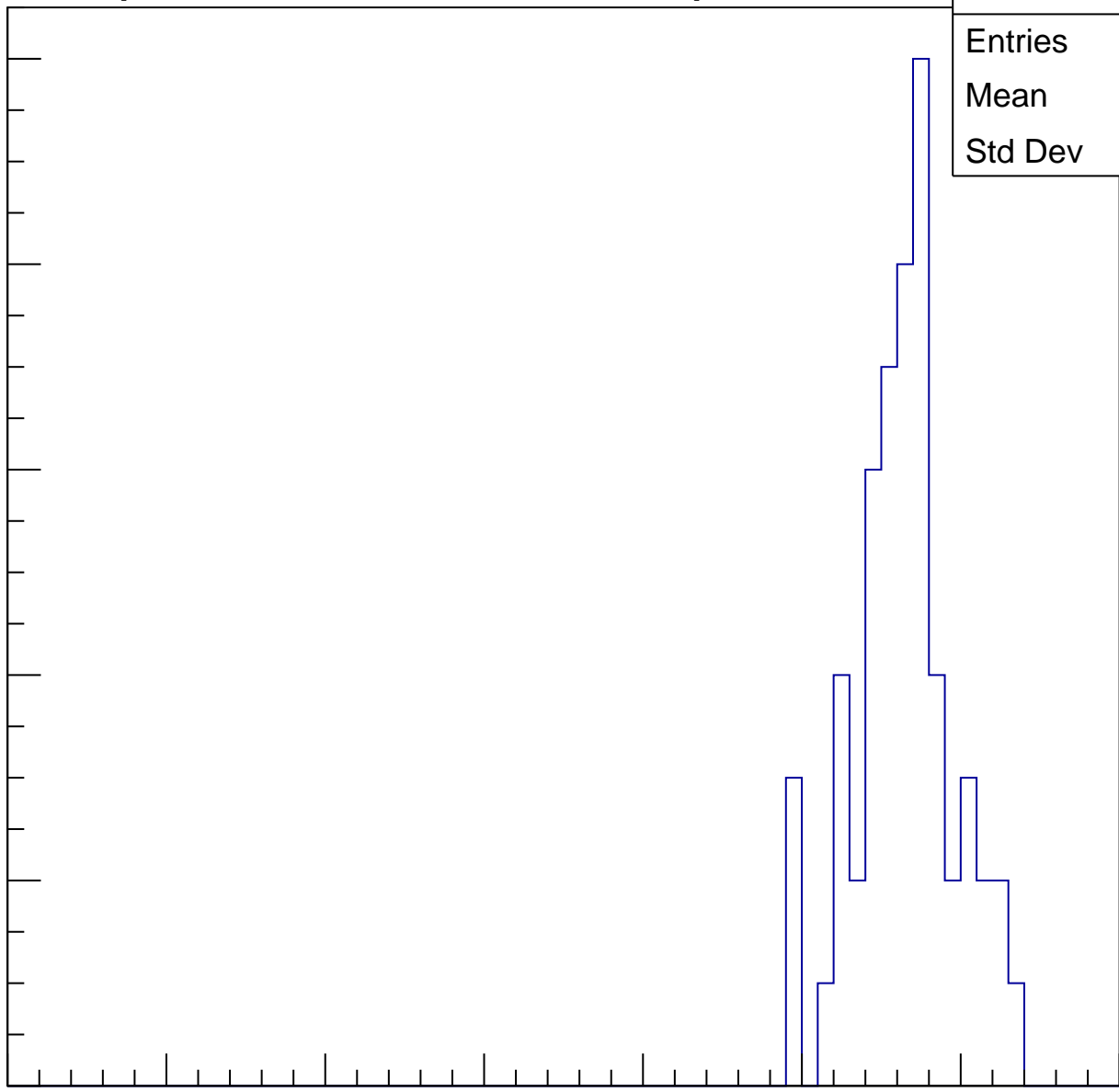
40

50

60

70

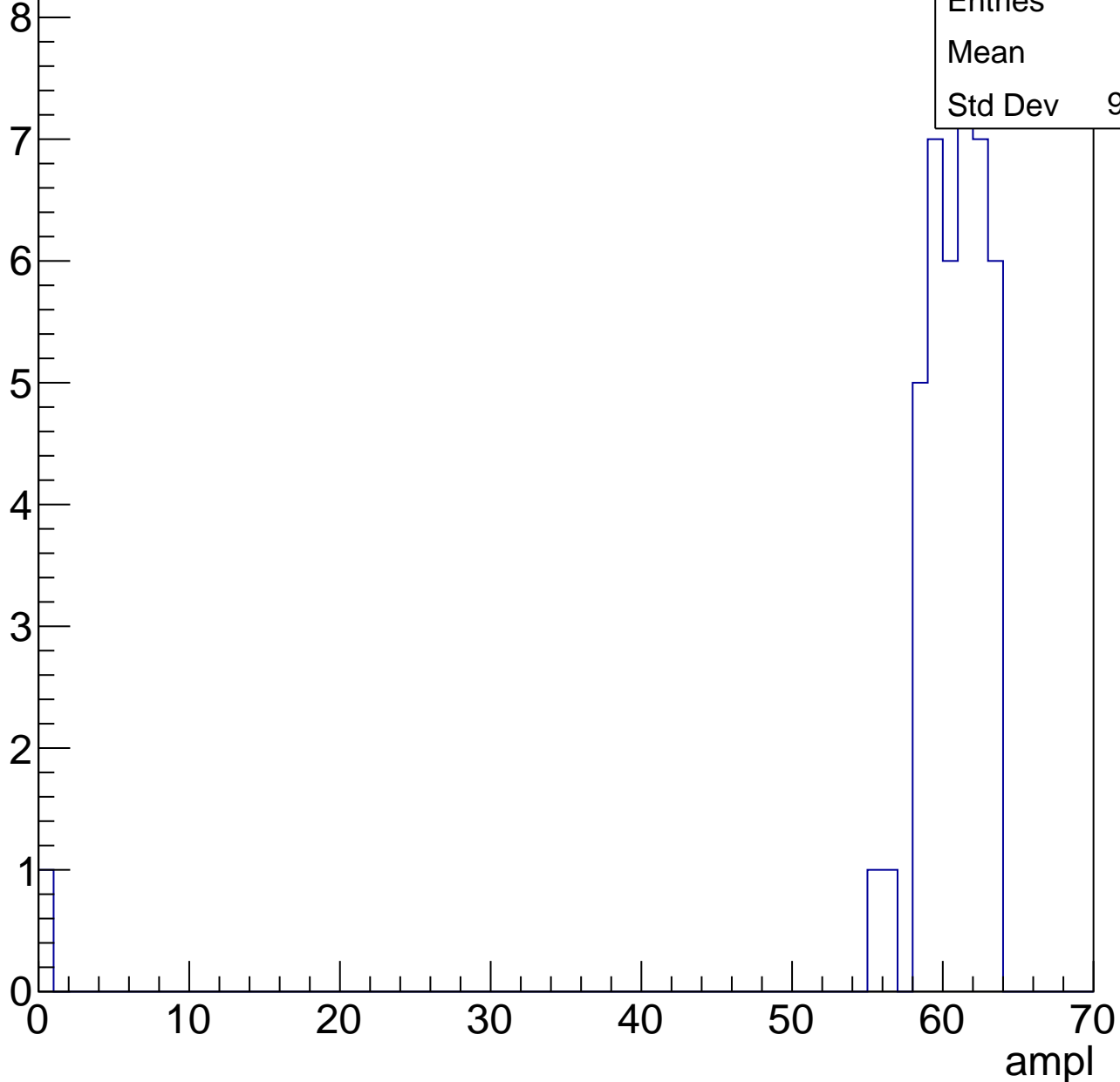
ampl



# B0L102S, U17-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B0L102S, U17-ch5, adc0

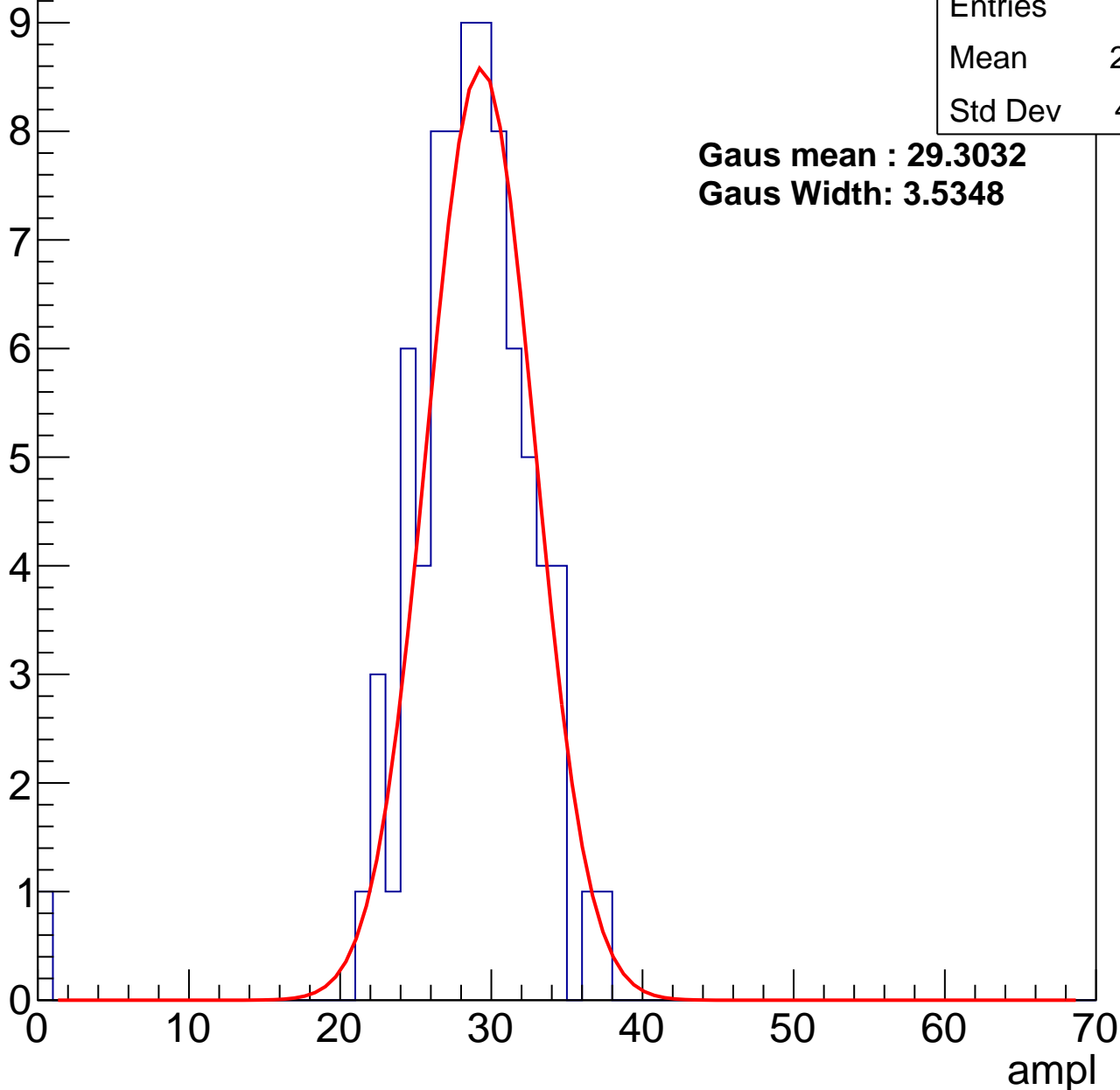
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	28.08
Std Dev	4.641

**Gaus mean : 29.3032**

**Gaus Width: 3.5348**



# B0L102S, U17-ch5, adc1

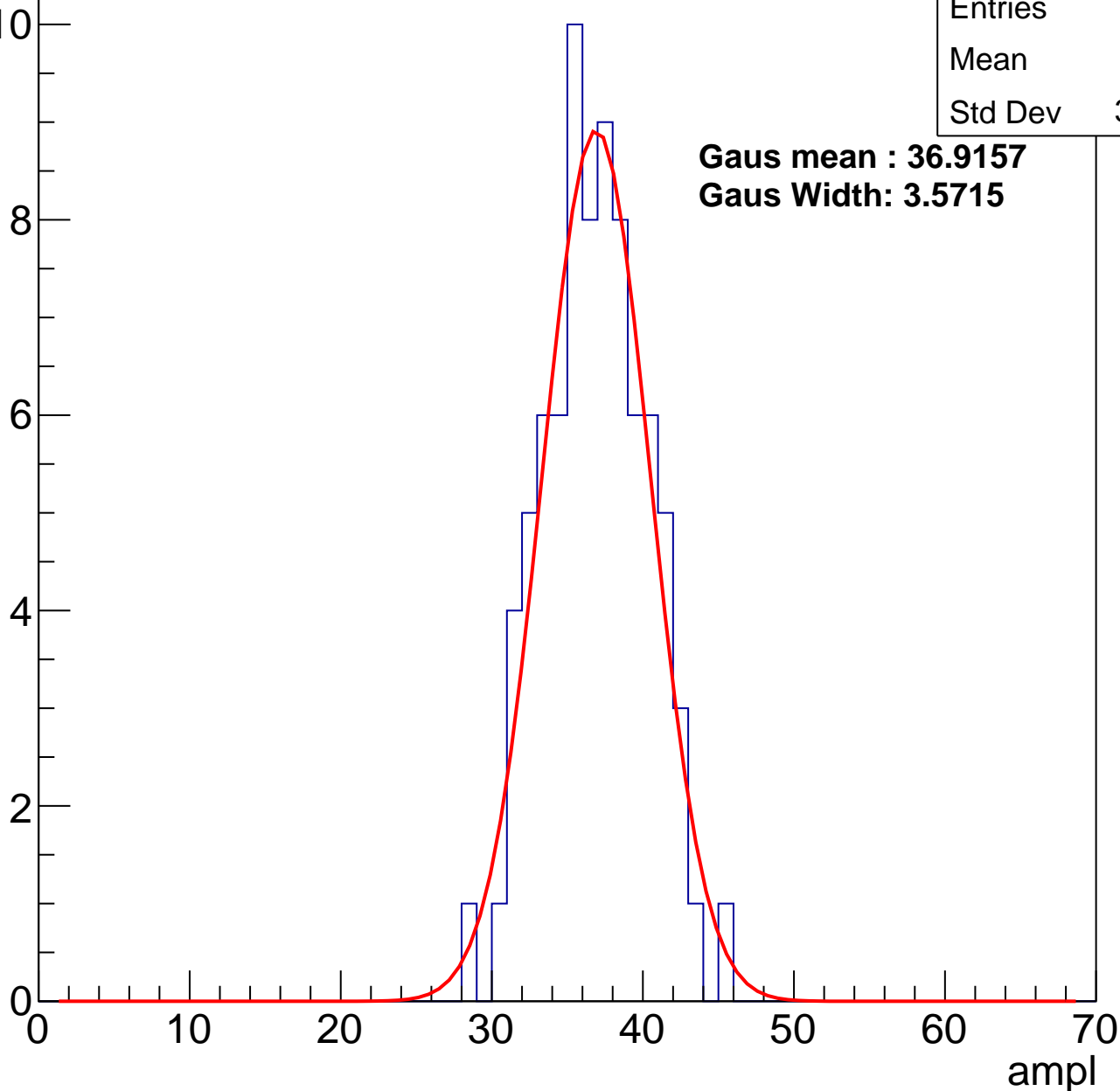
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	36.4
Std Dev	3.371

**Gaus mean : 36.9157**

**Gaus Width: 3.5715**



# B0L102S, U17-ch5, adc2

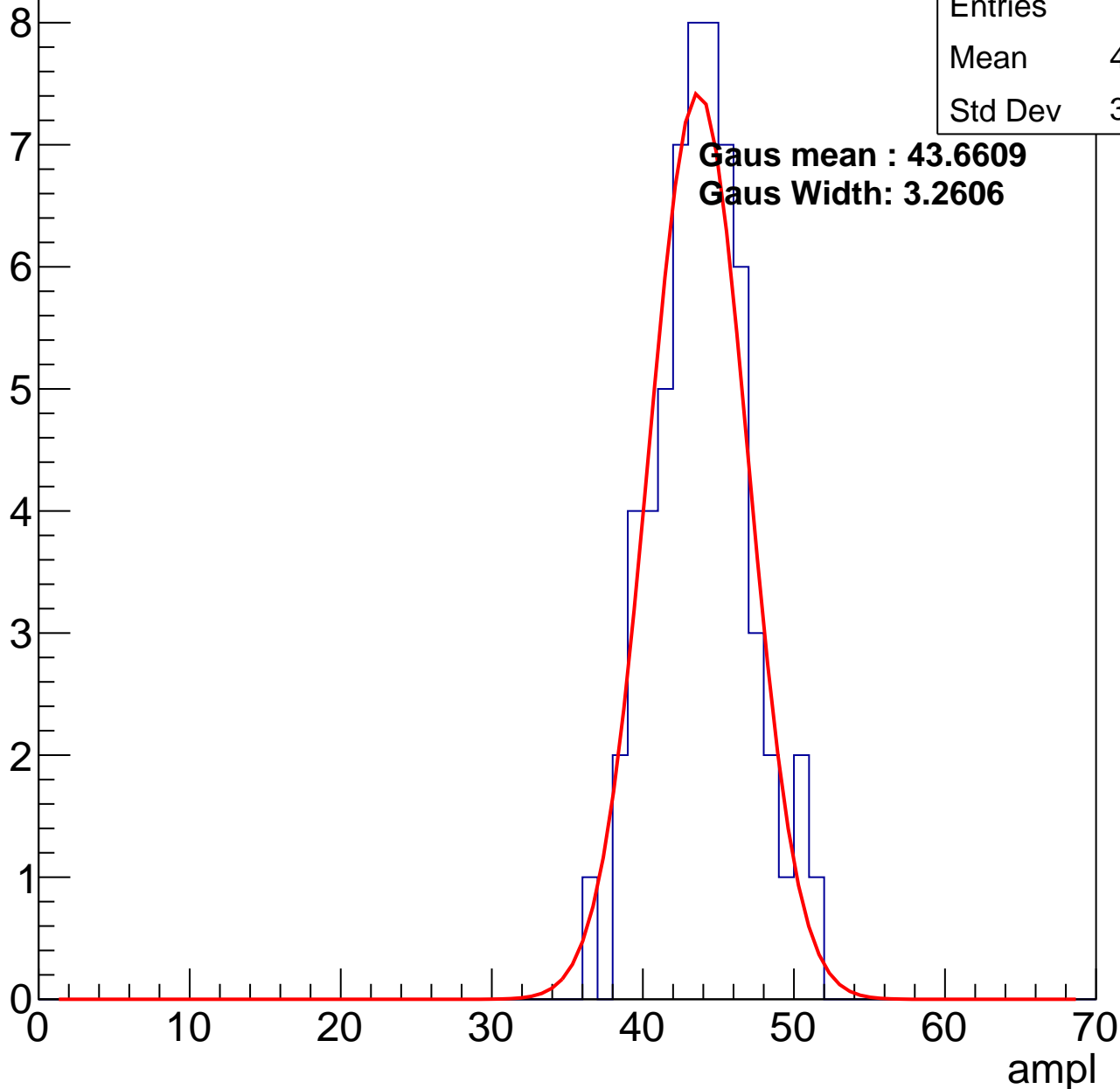
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	43.46
Std Dev	3.134

**Gaus mean : 43.6609**

**Gaus Width: 3.2606**

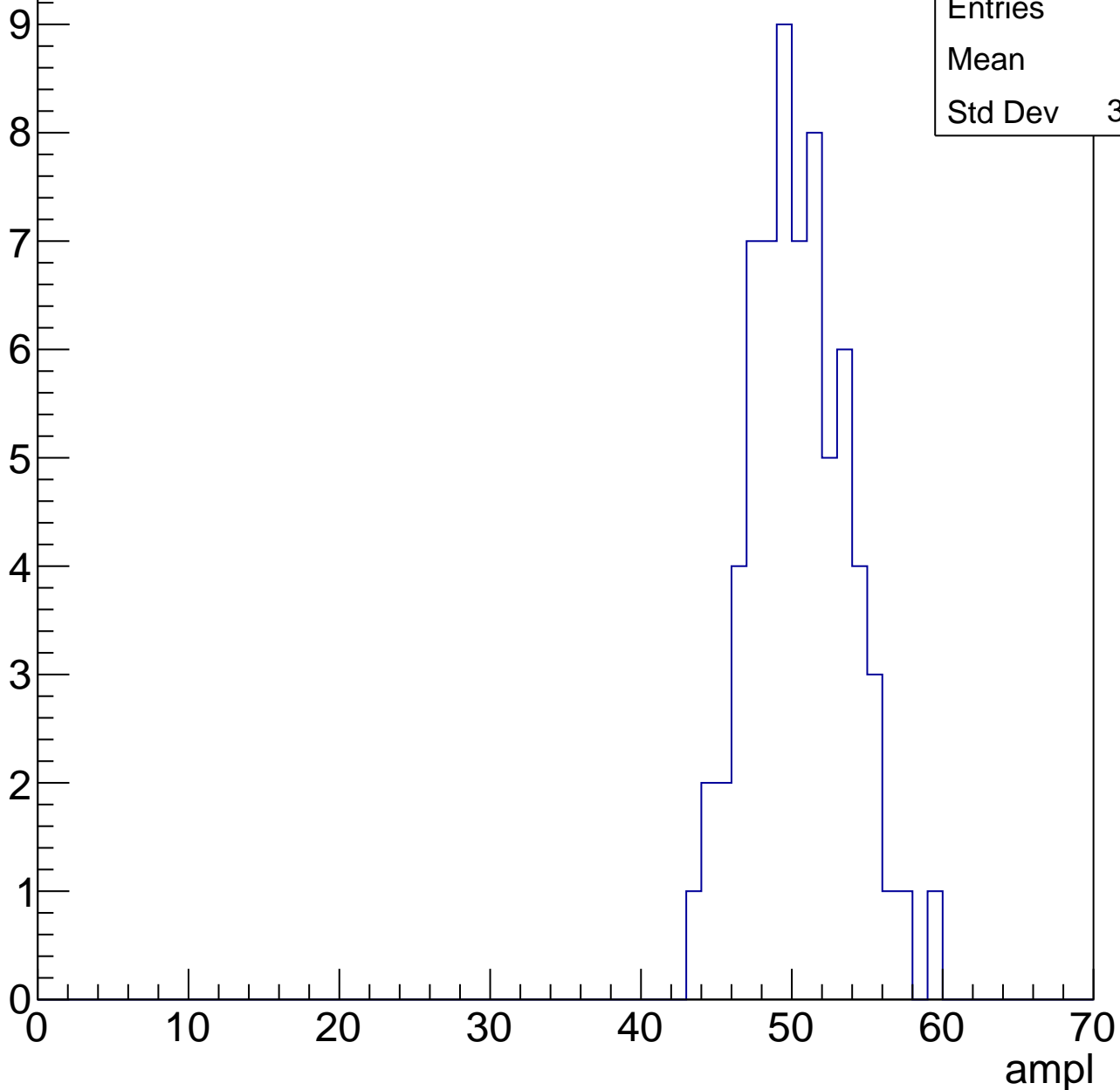


# B0L102S, U17-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50
Std Dev	3.258

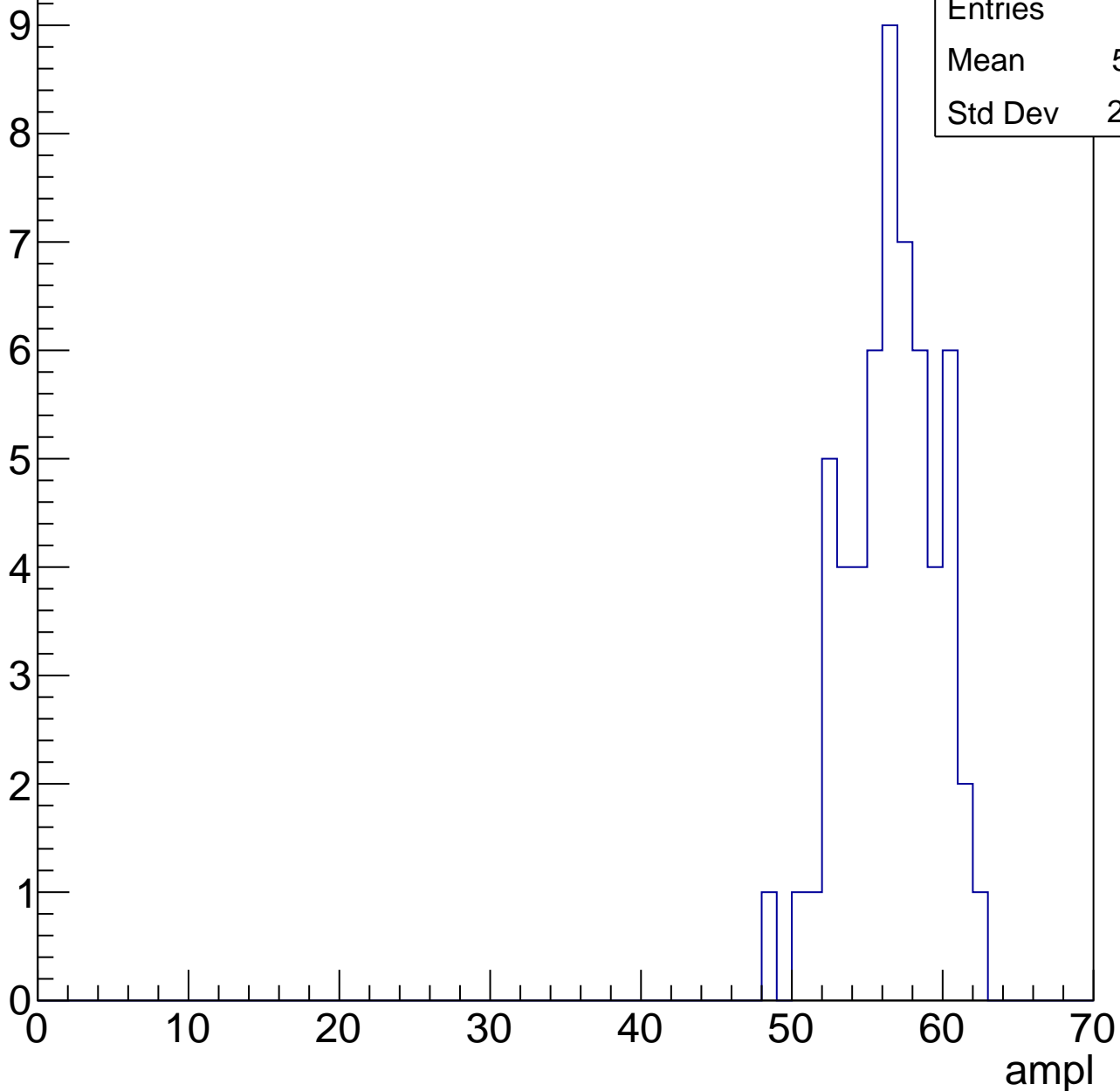


# B0L102S, U17-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	56.11
Std Dev	2.995

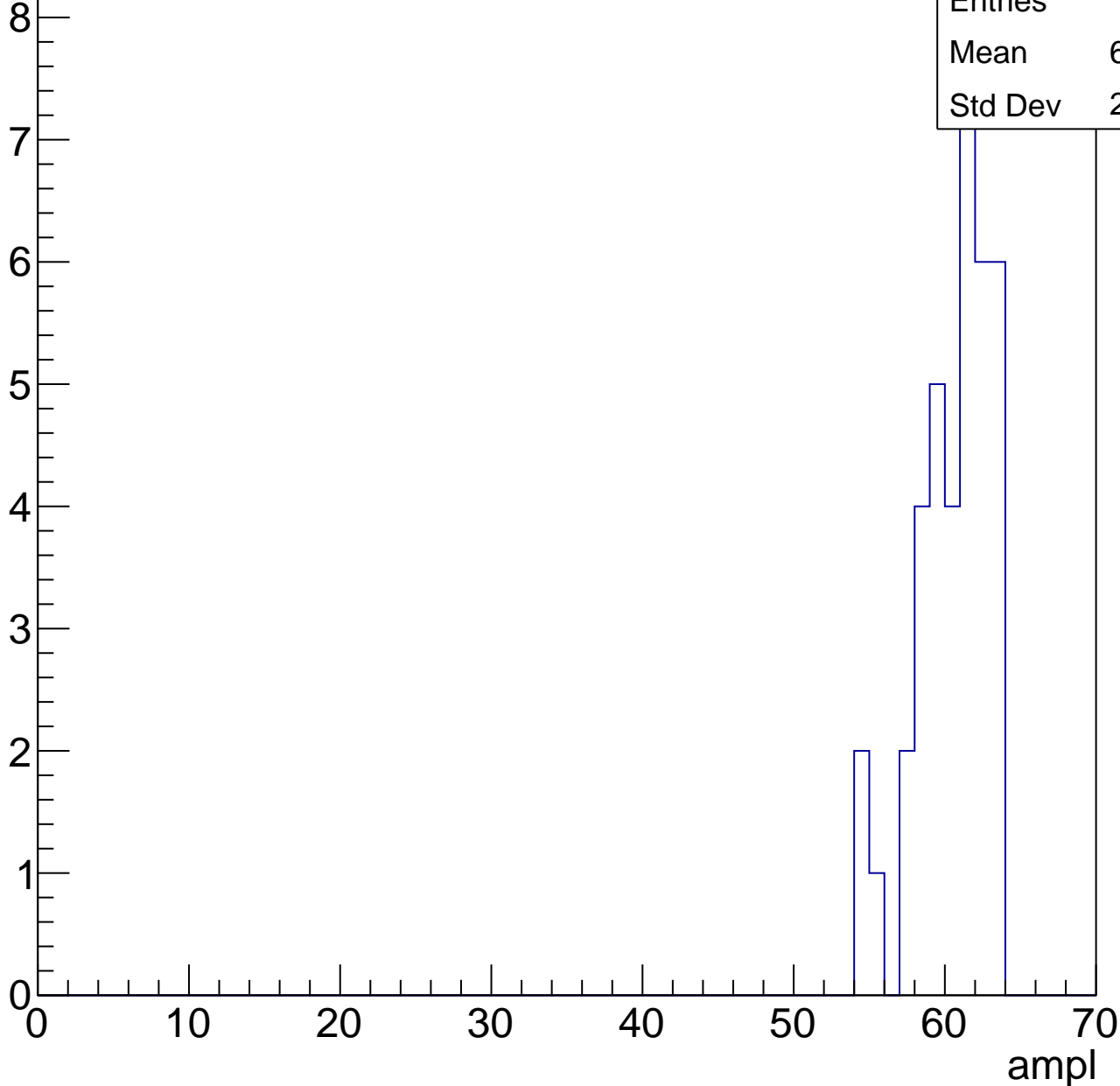


# B0L102S, U17-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

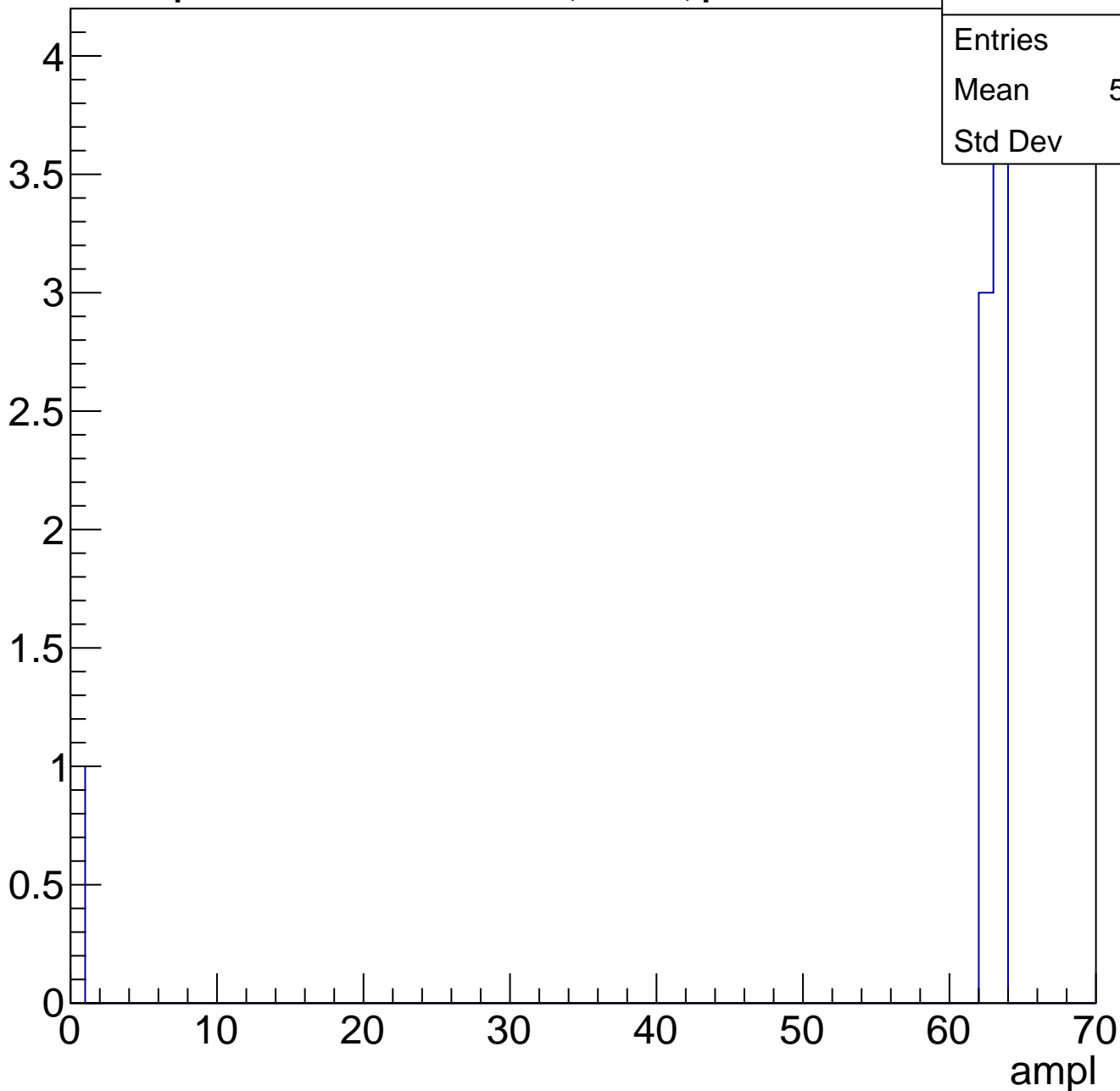
Entries	38
Mean	60.05
Std Dev	2.416



# B0L102S, U17-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch6, adc0

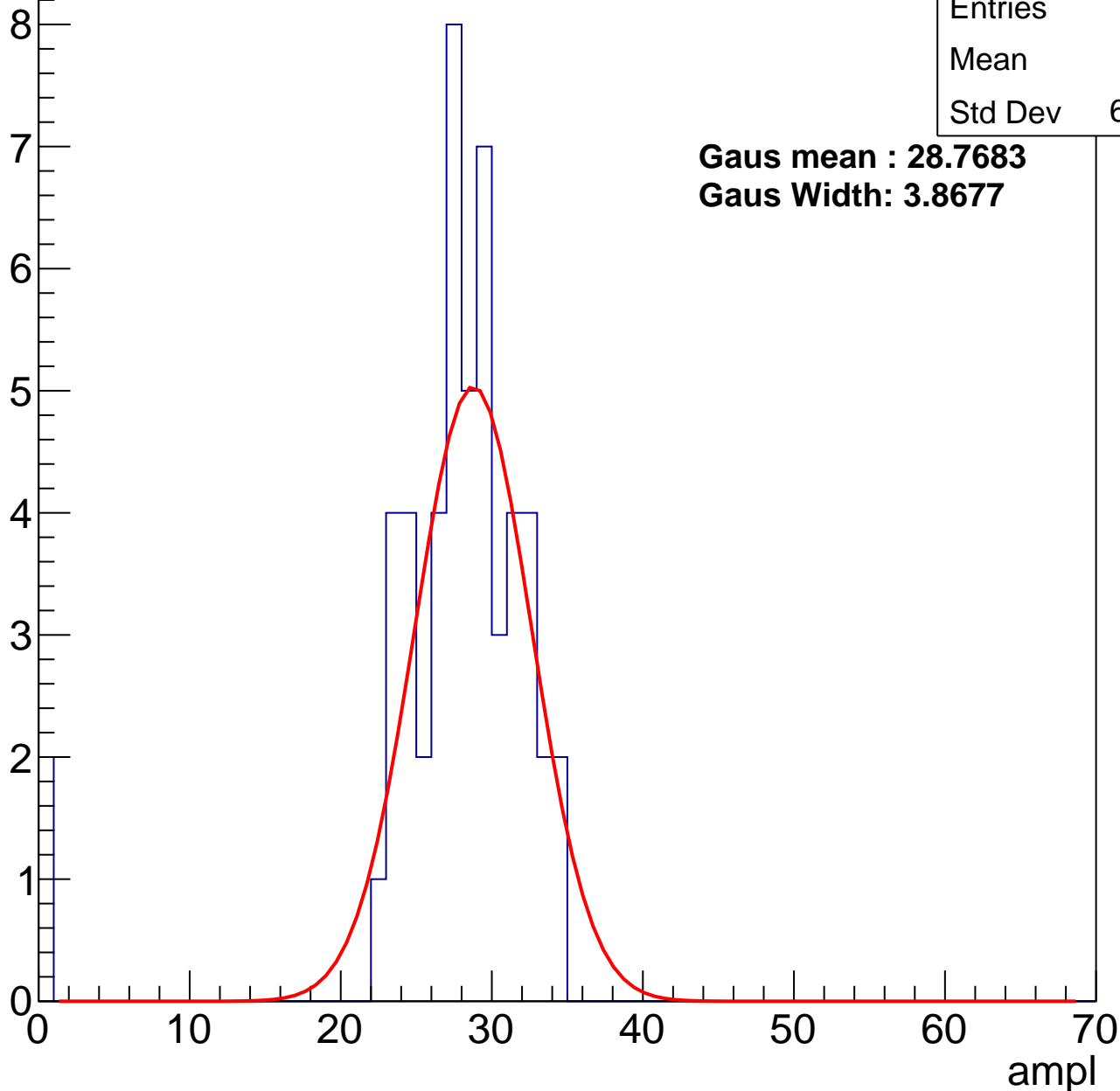
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	26.9
Std Dev	6.184

**Gaus mean : 28.7683**

**Gaus Width: 3.8677**



# B0L102S, U17-ch6, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	34.11
Std Dev	3.608

**Gaus mean : 34.1359**

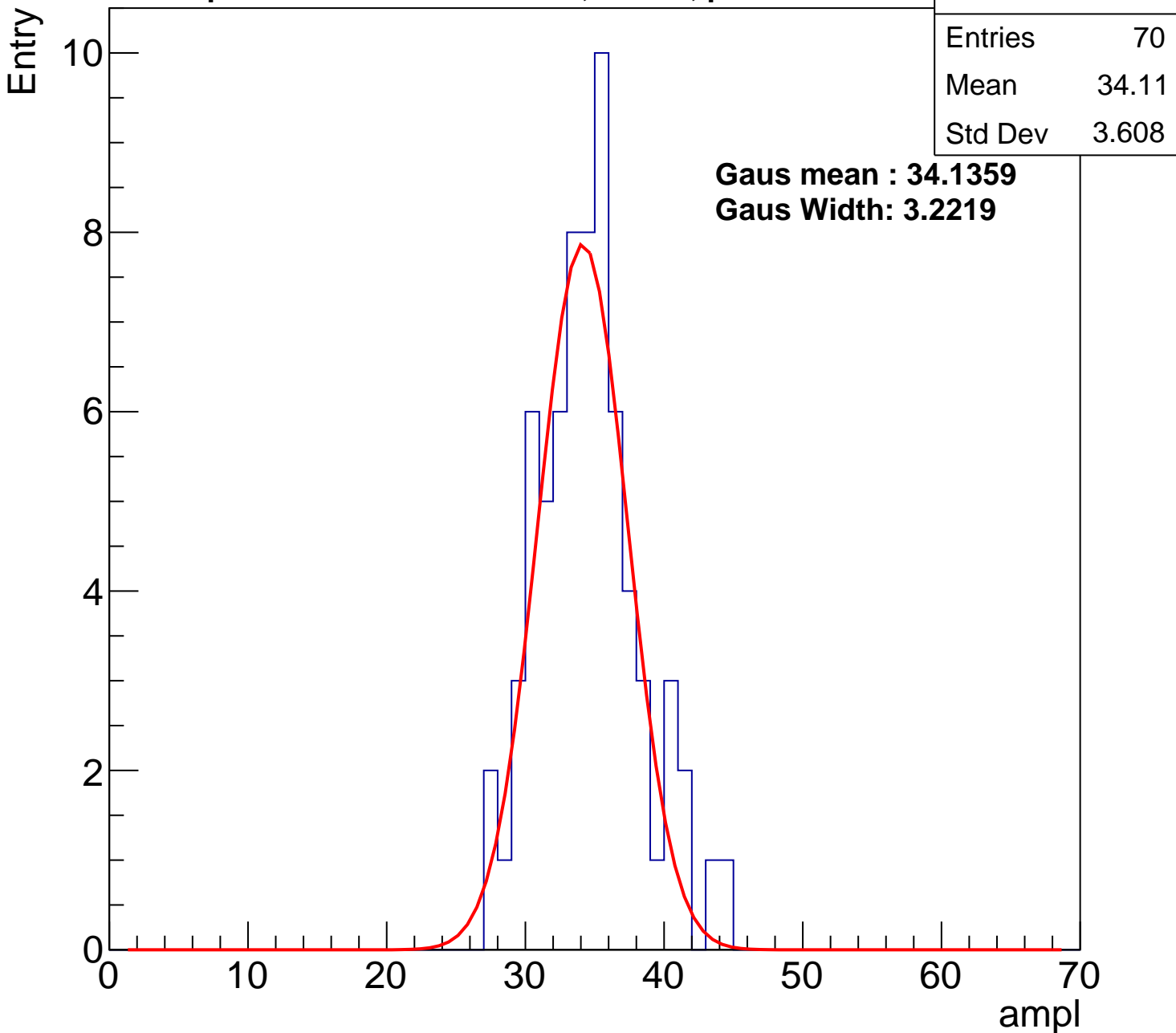
**Gaus Width: 3.2219**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch6, adc2

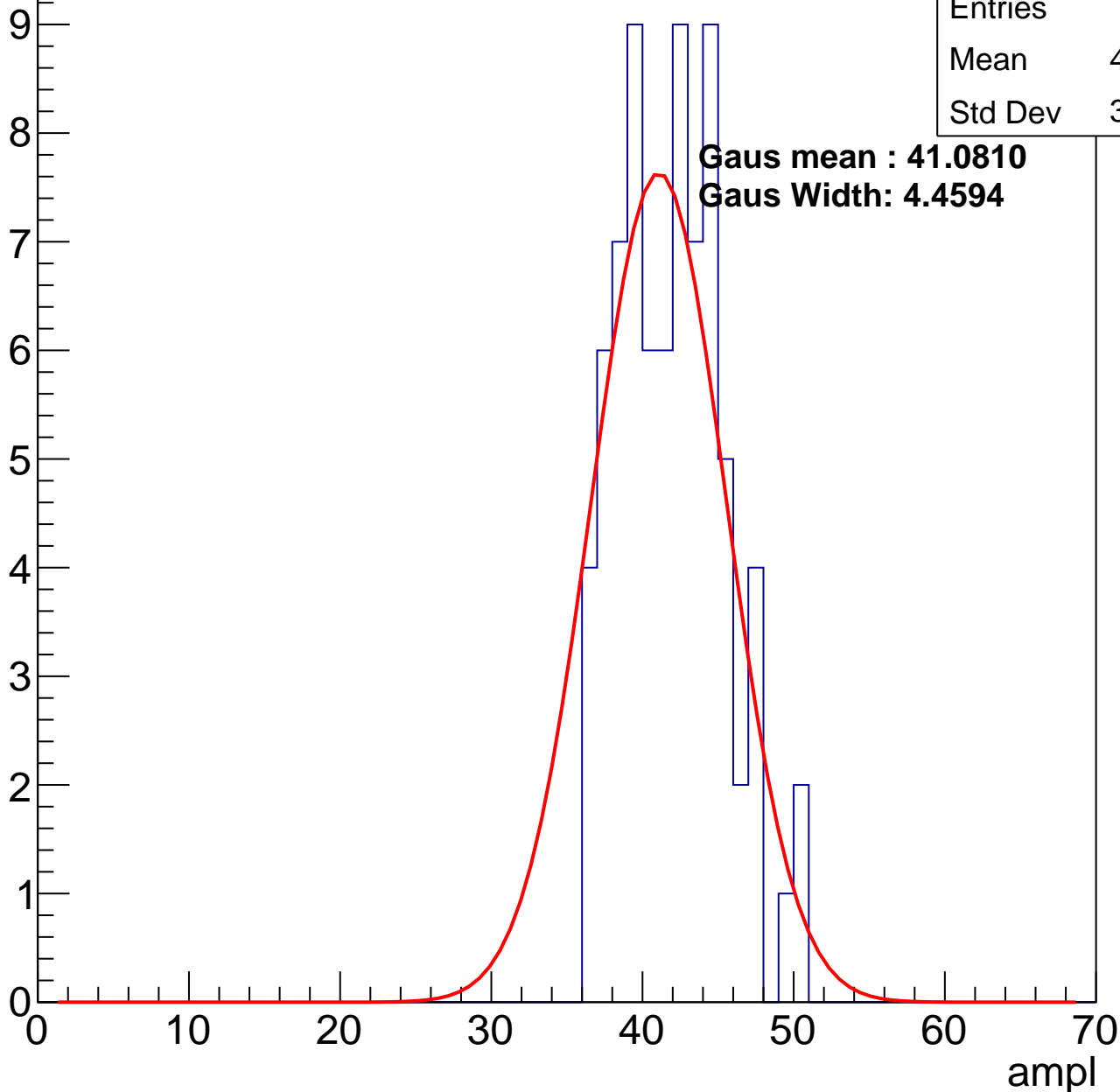
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	41.53
Std Dev	3.413

**Gaus mean : 41.0810**

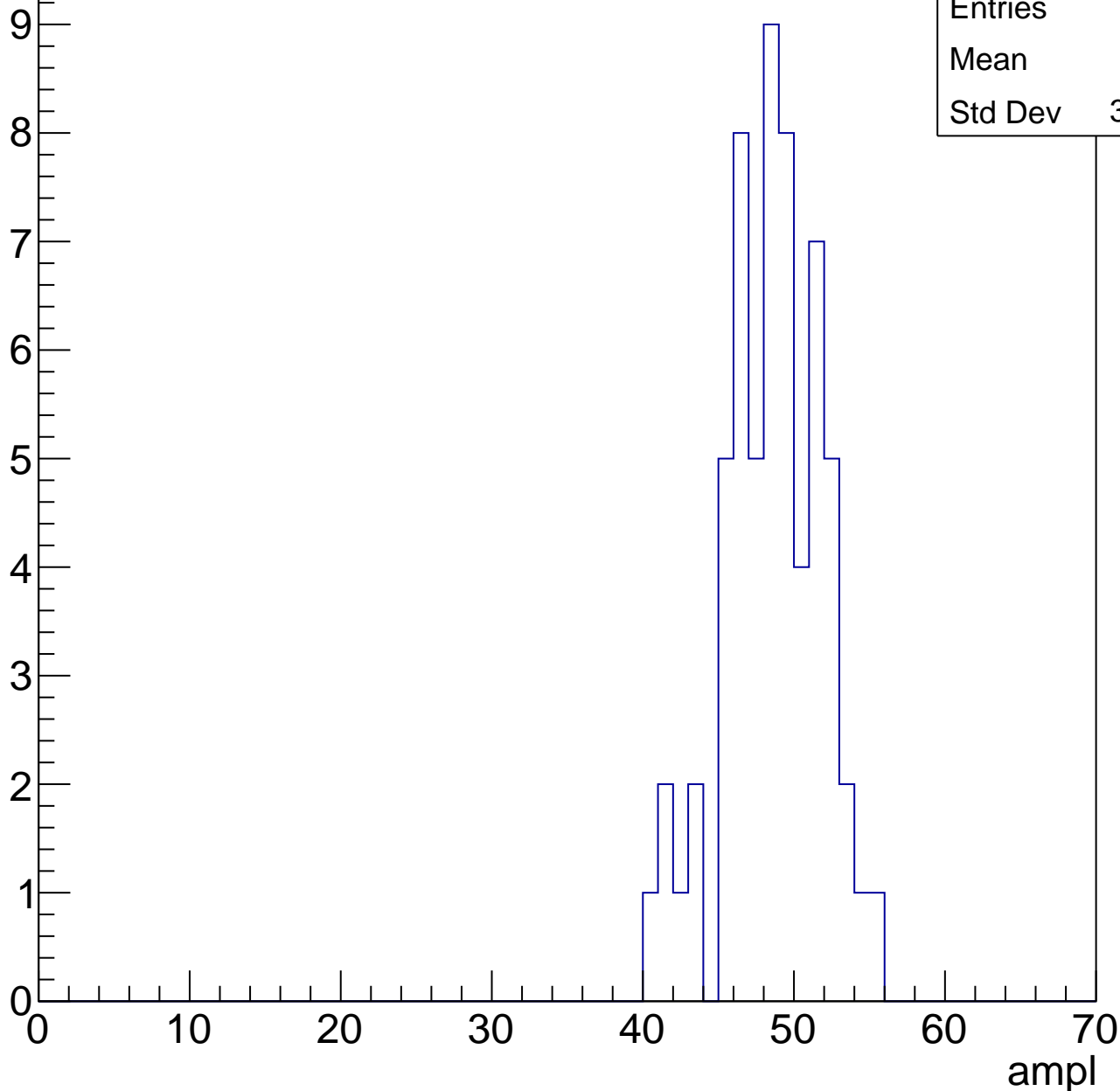
**Gaus Width: 4.4594**



# B0L102S, U17-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

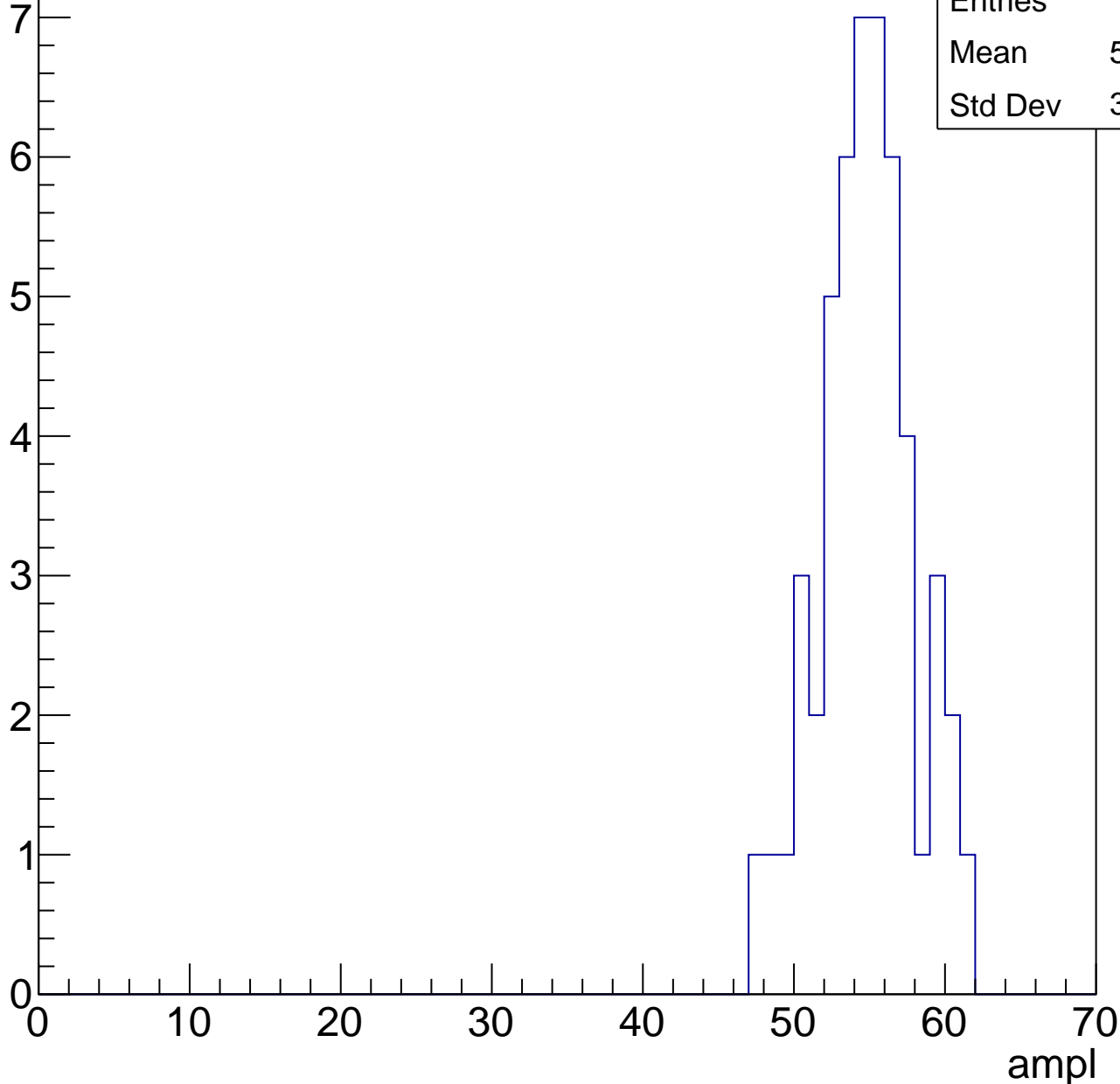
Entry



# B0L102S, U17-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	50
Mean	54.34
Std Dev	3.083

# B0L102S, U17-ch6, adc5

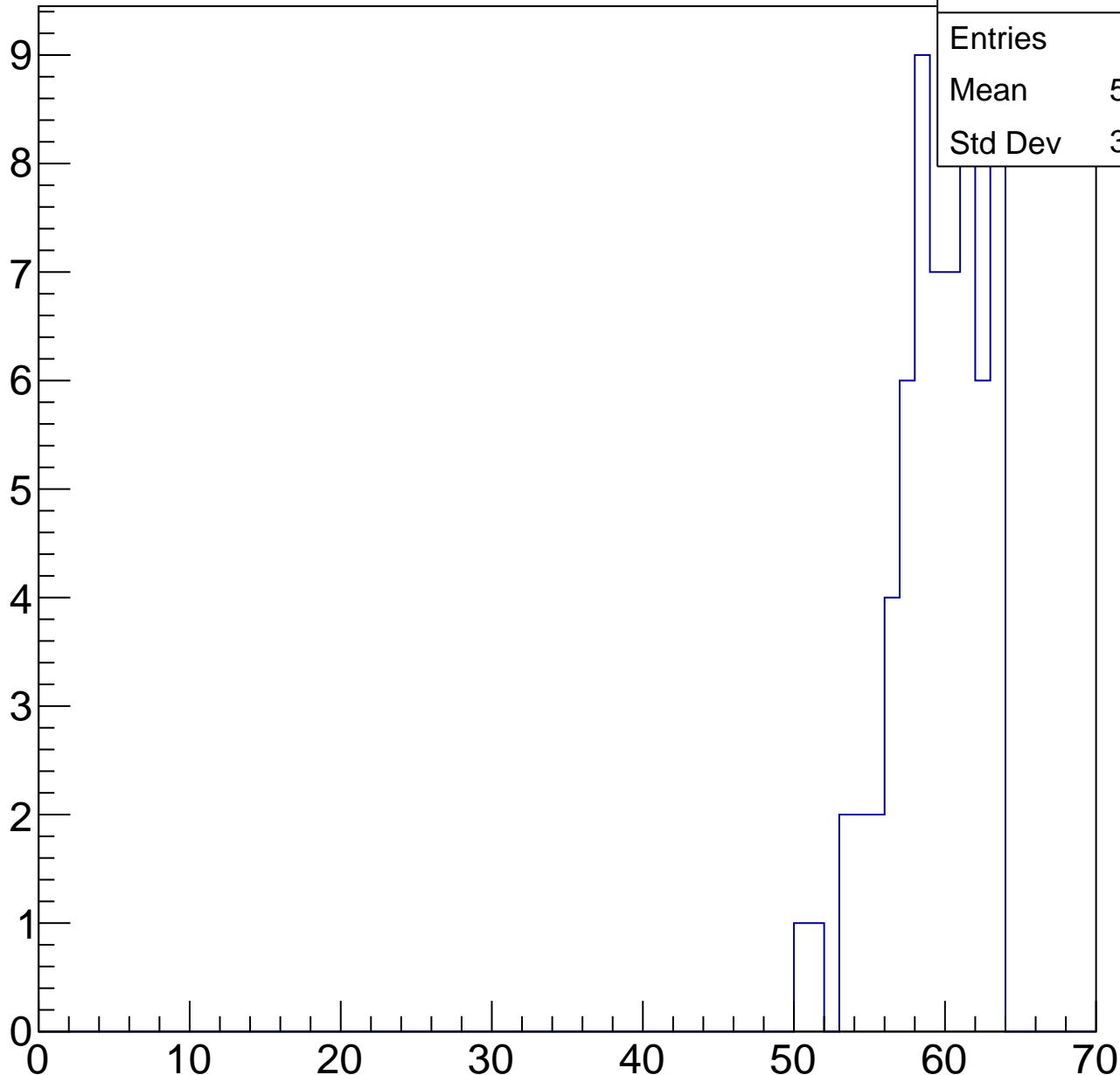
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.89
Std Dev	3.056

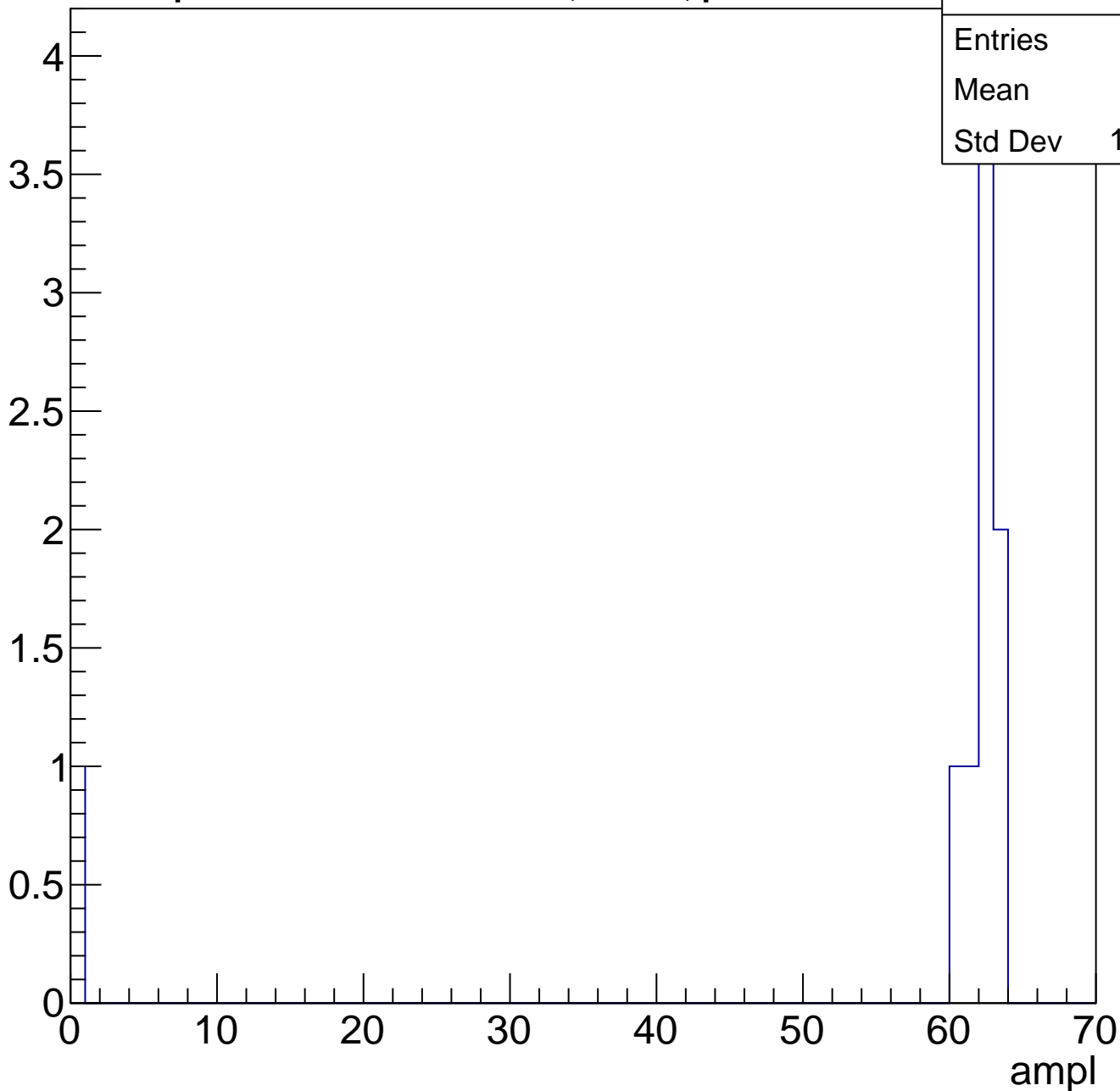
ampl



# B0L102S, U17-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

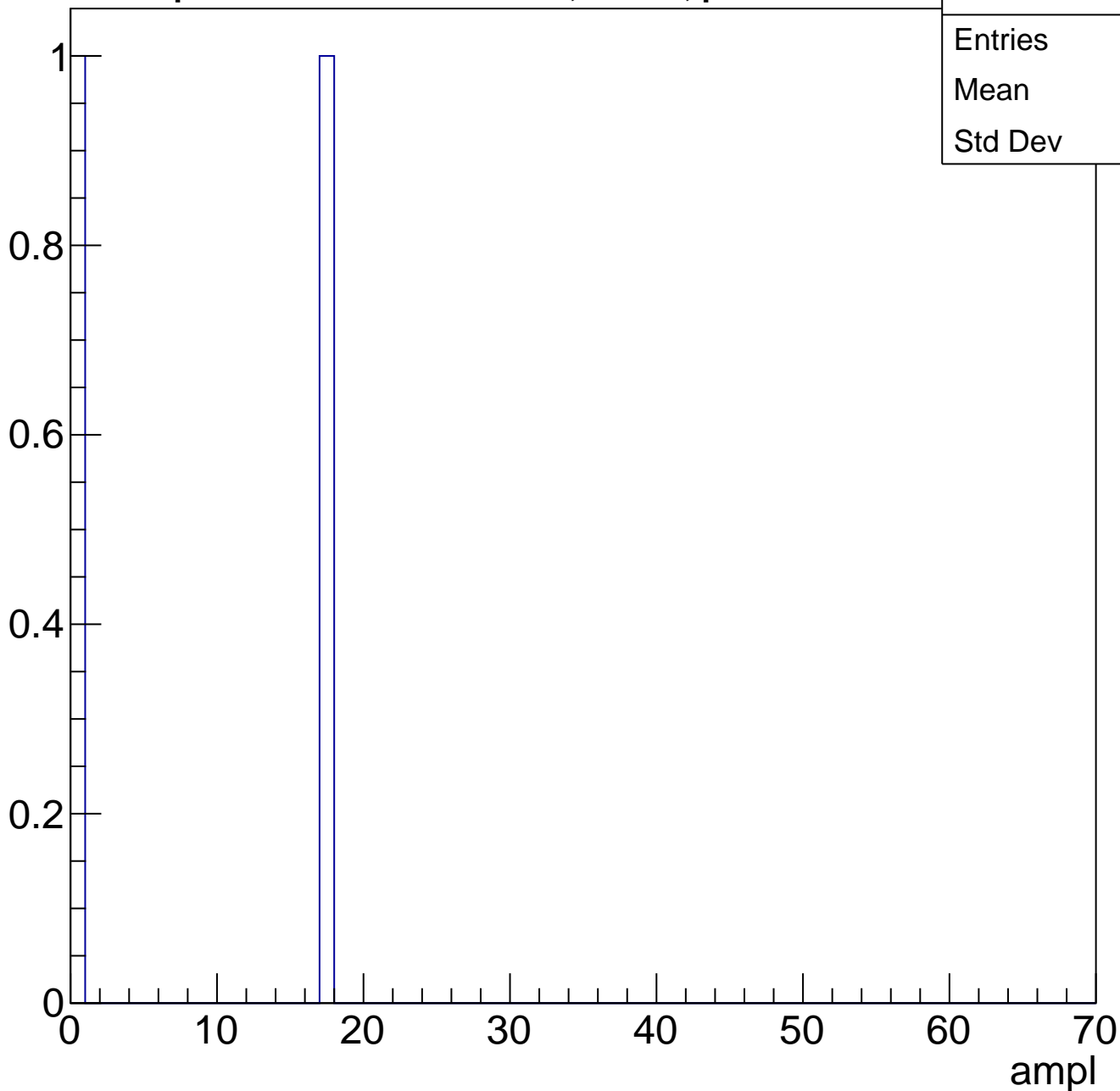




# B0L102S, U17-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	8.5
Std Dev	8.5

# B0L102S, U17-ch7, adc0

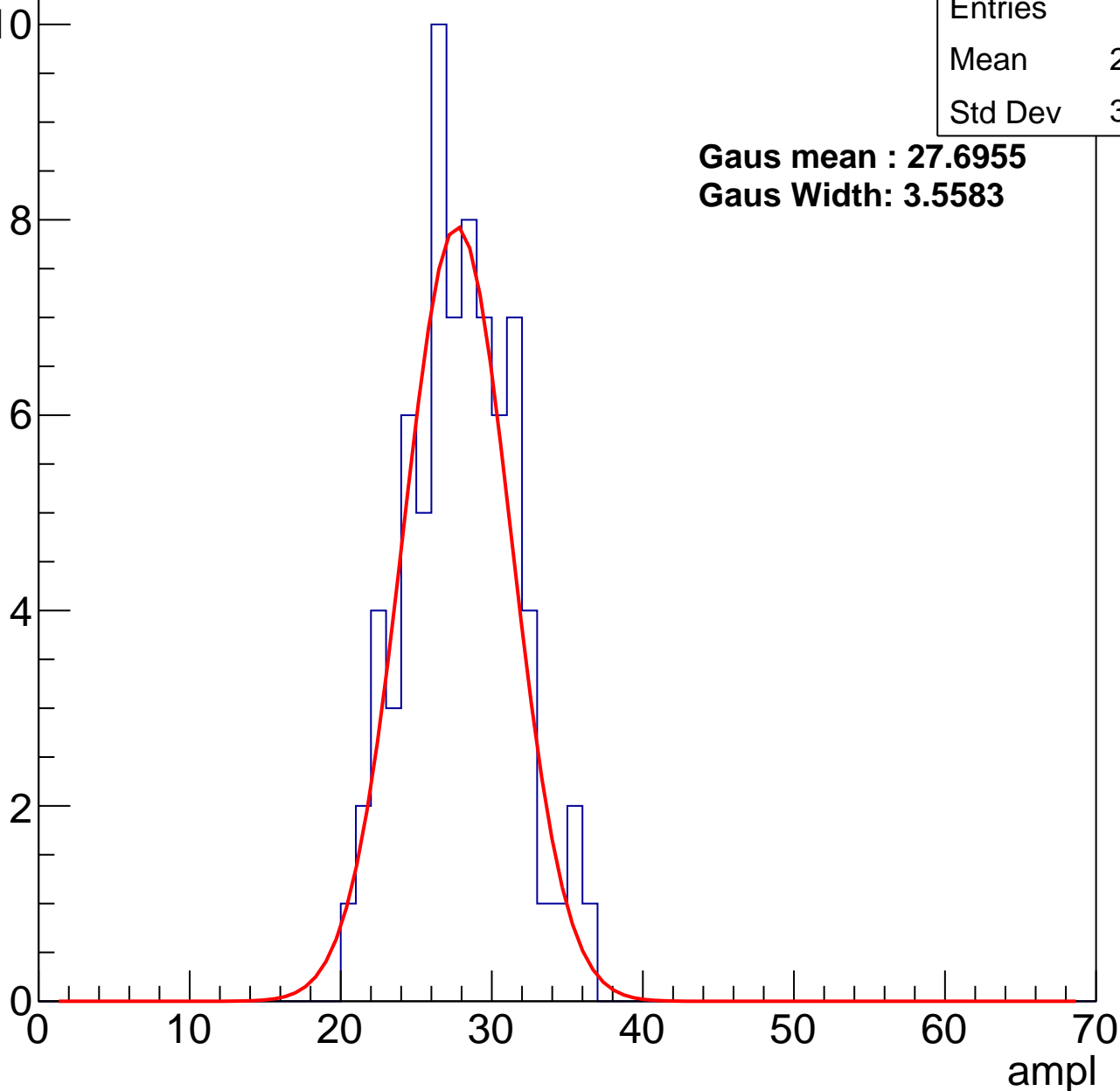
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	27.49
Std Dev	3.523

**Gaus mean : 27.6955**

**Gaus Width: 3.5583**



# B0L102S, U17-ch7, adc1

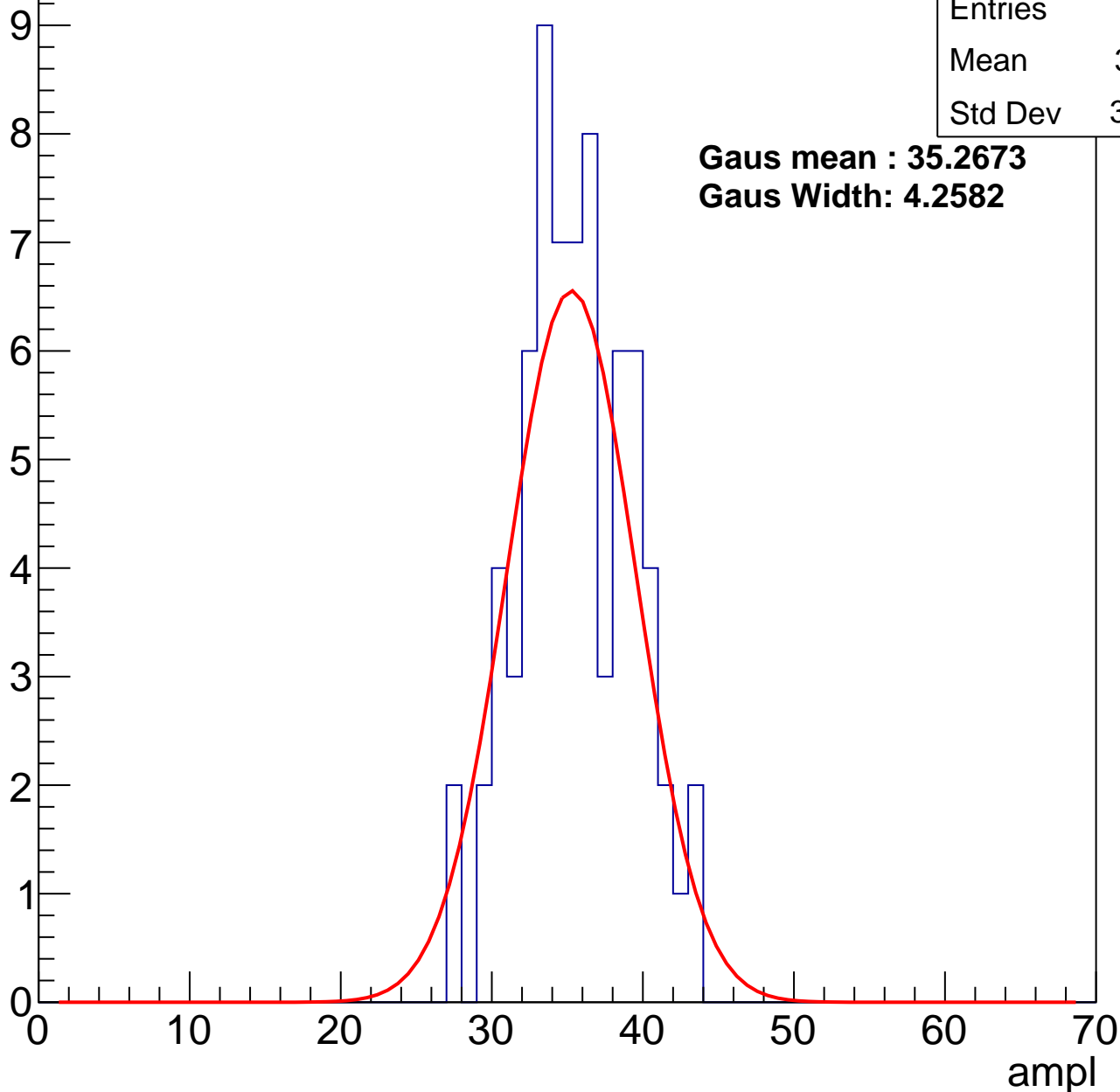
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	35.11
Std Dev	3.657

**Gaus mean : 35.2673**

**Gaus Width: 4.2582**



# B0L102S, U17-ch7, adc2

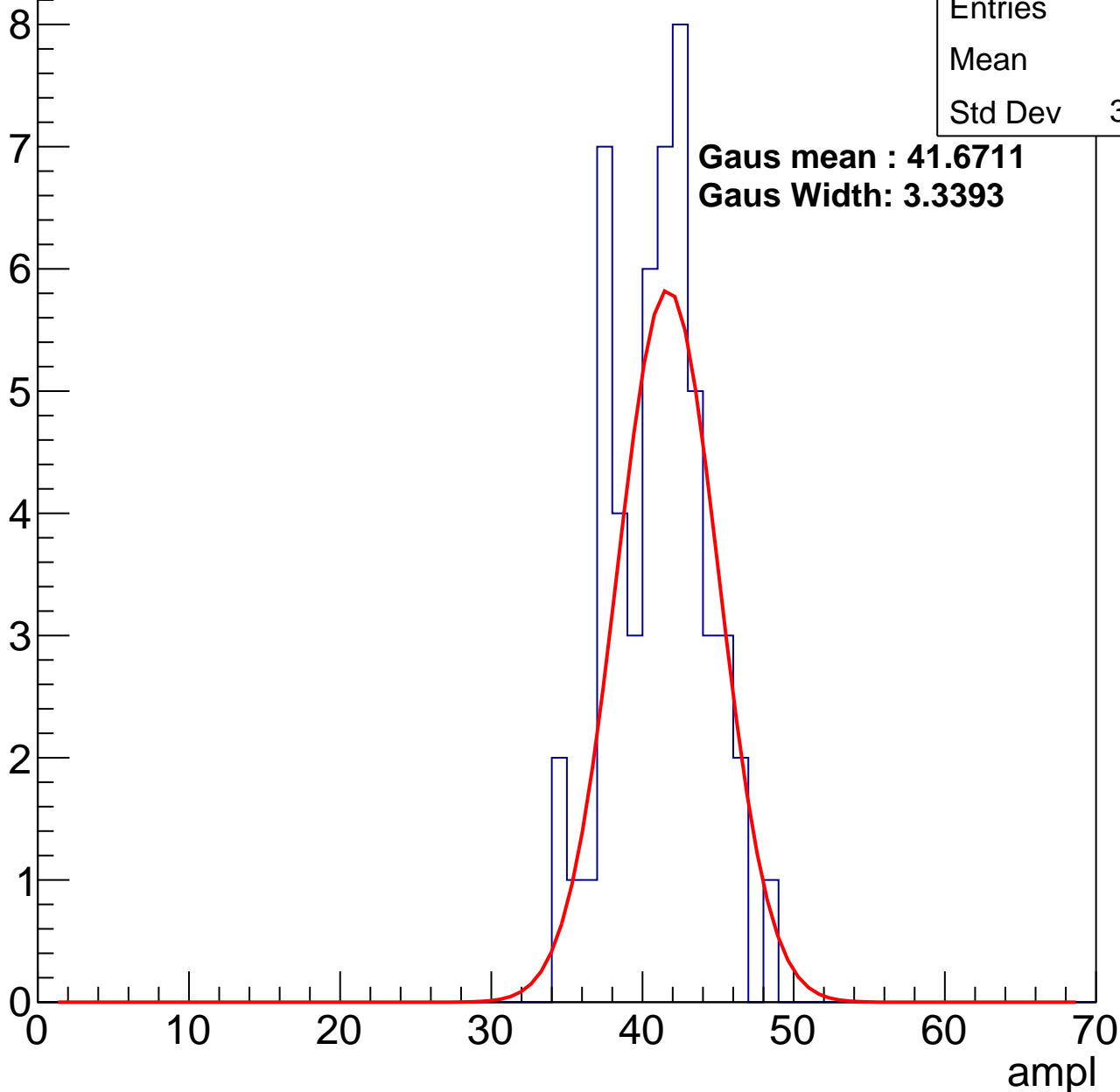
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	40.6
Std Dev	3.128

**Gaus mean : 41.6711**

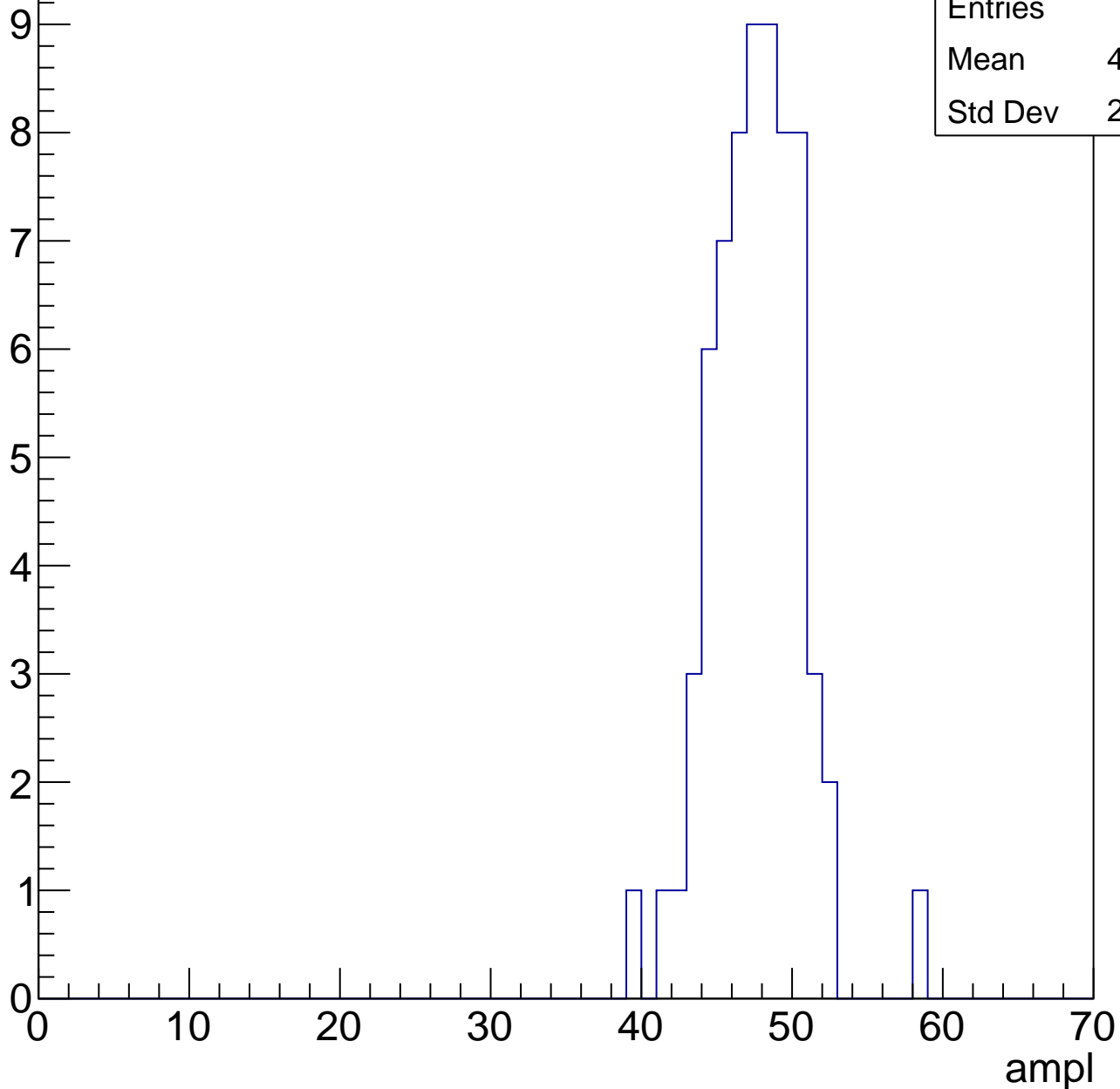
**Gaus Width: 3.3393**



# B0L102S, U17-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

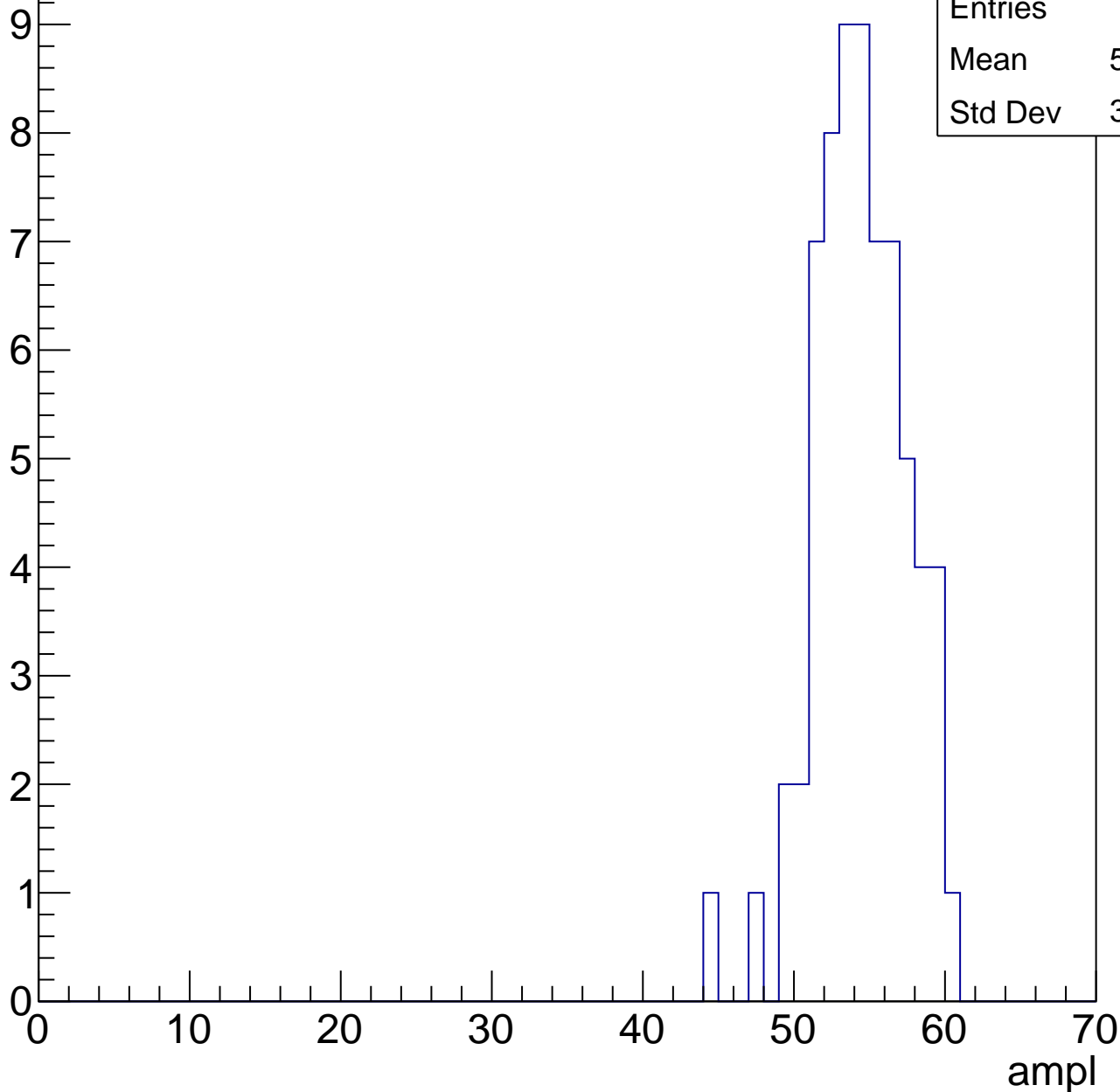
Entry



# B0L102S, U17-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch7, adc5

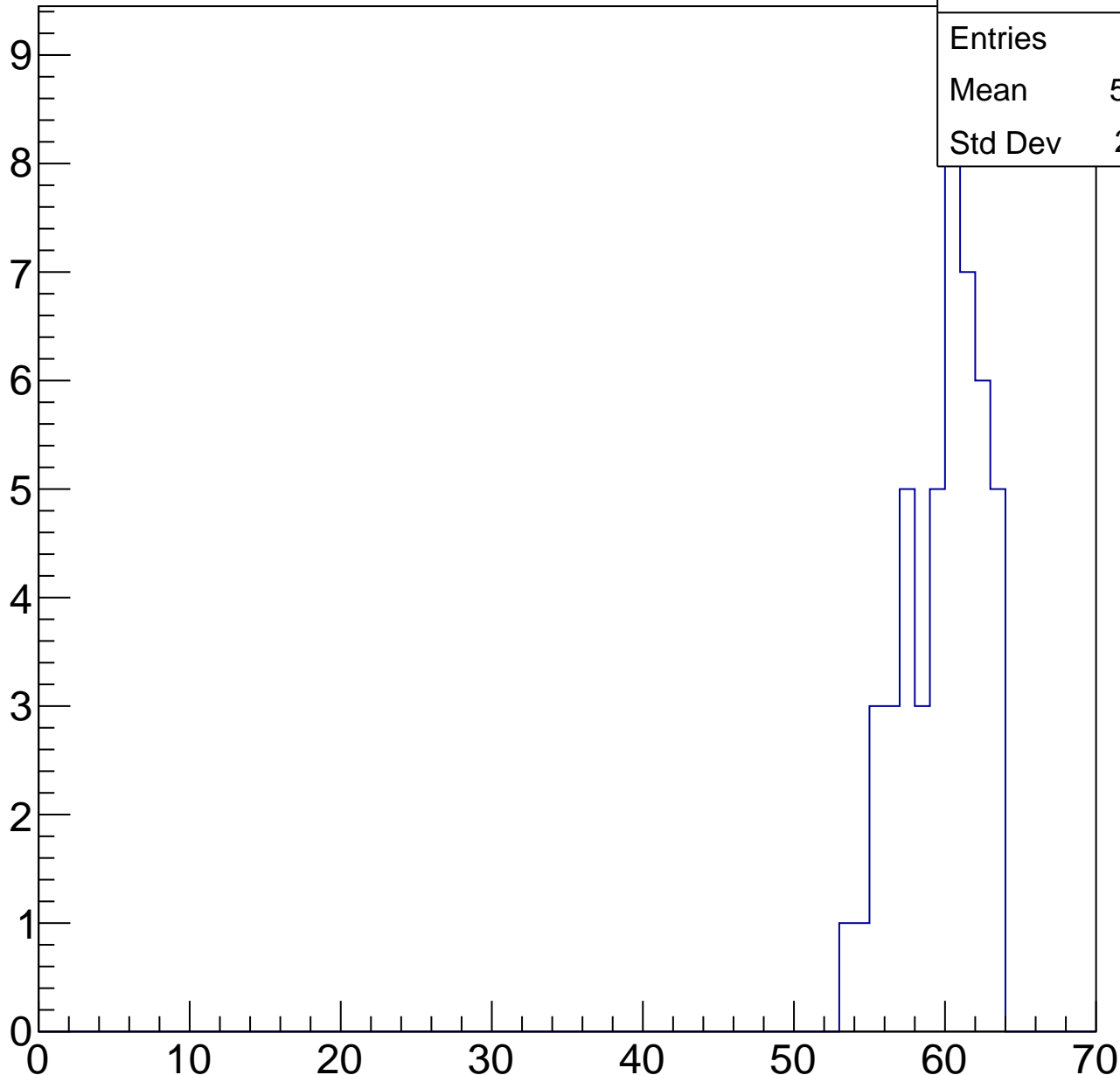
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	59.33
Std Dev	2.601

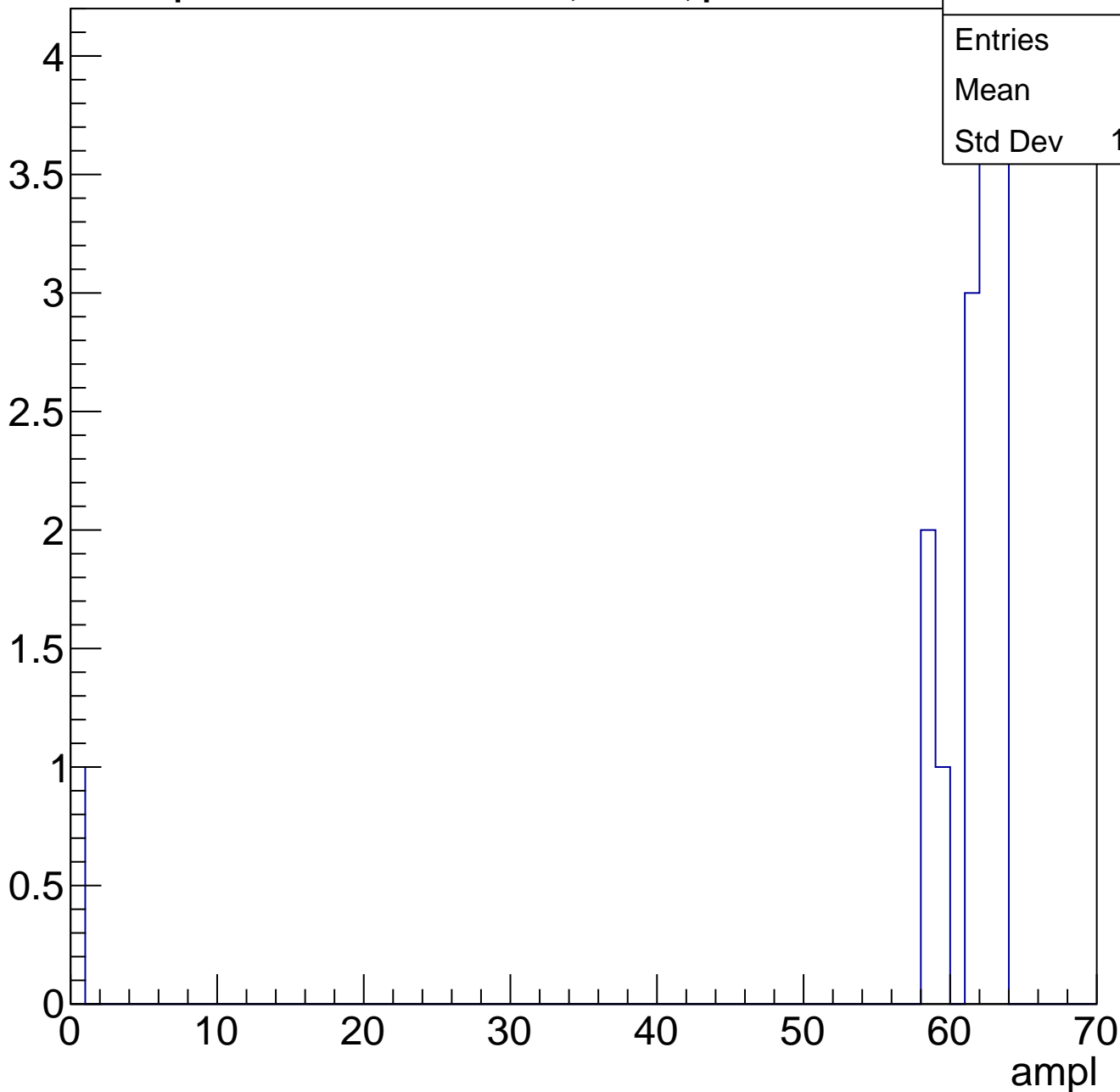
ampl



# B0L102S, U17-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	39.5
Std Dev	23.5

ampl

# B0L102S, U17-ch8, adc0

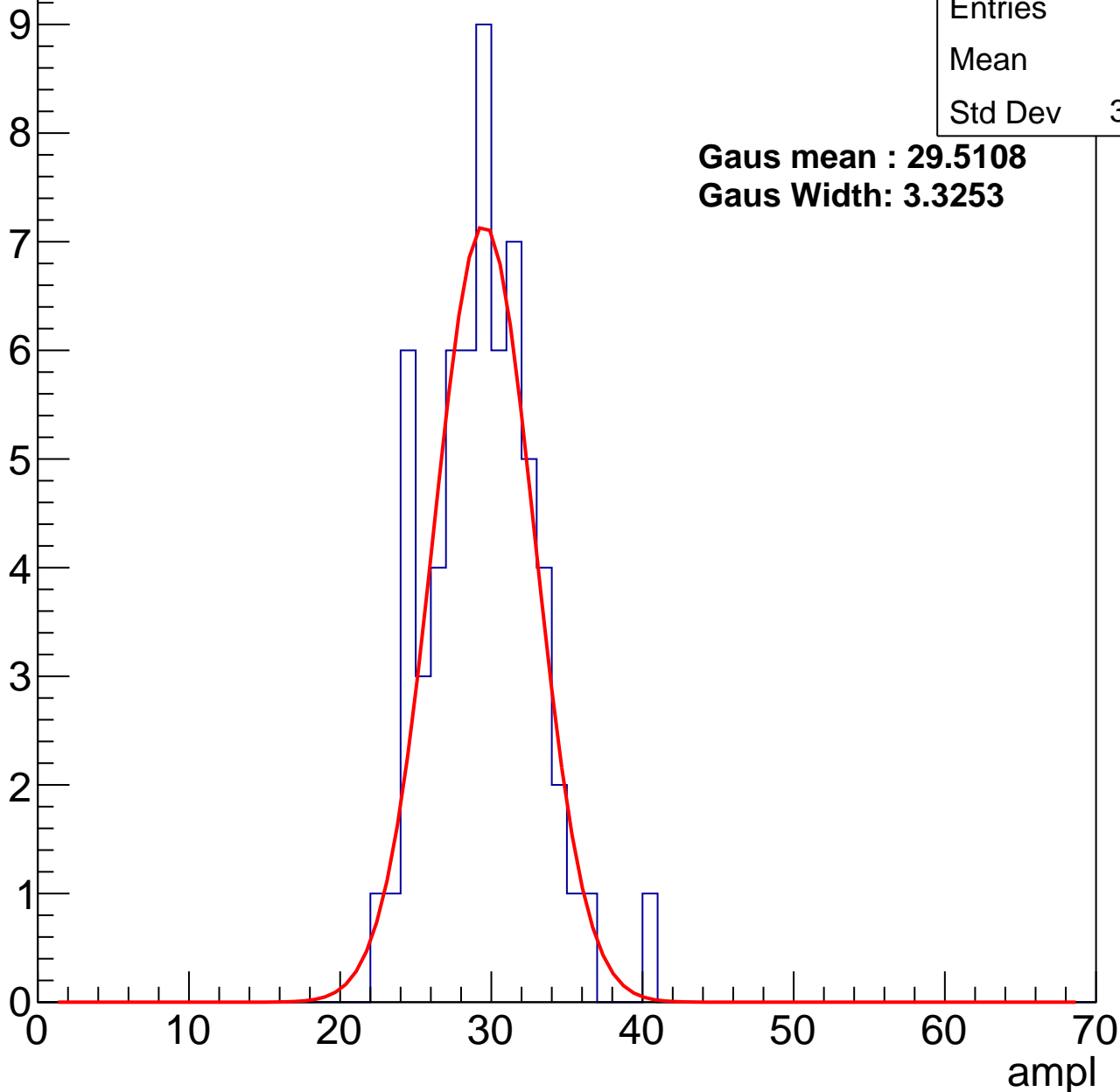
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	29
Std Dev	3.446

**Gaus mean : 29.5108**

**Gaus Width: 3.3253**



# B0L102S, U17-ch8, adc1

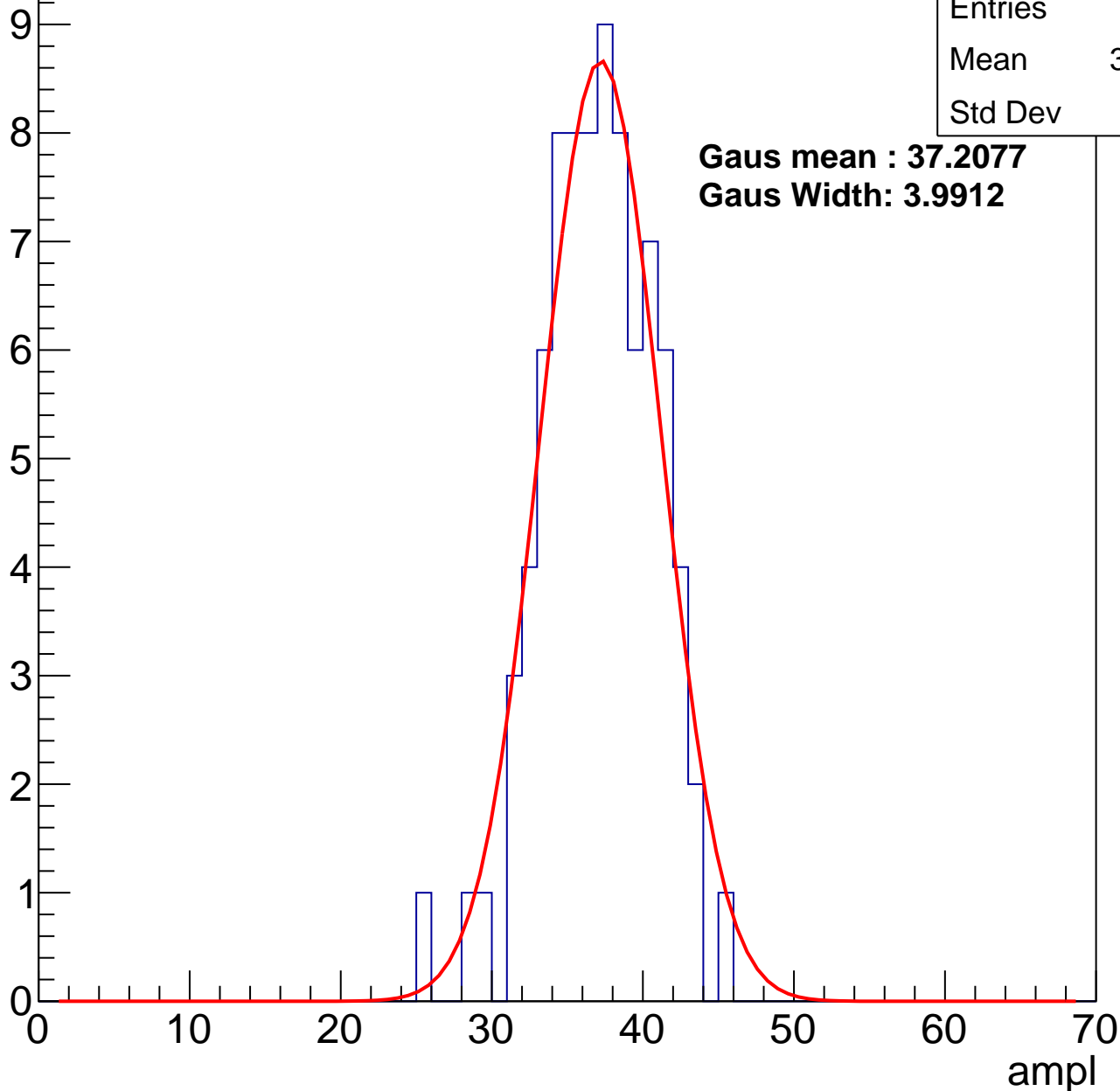
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	36.59
Std Dev	3.67

**Gaus mean : 37.2077**

**Gaus Width: 3.9912**



# B0L102S, U17-ch8, adc2

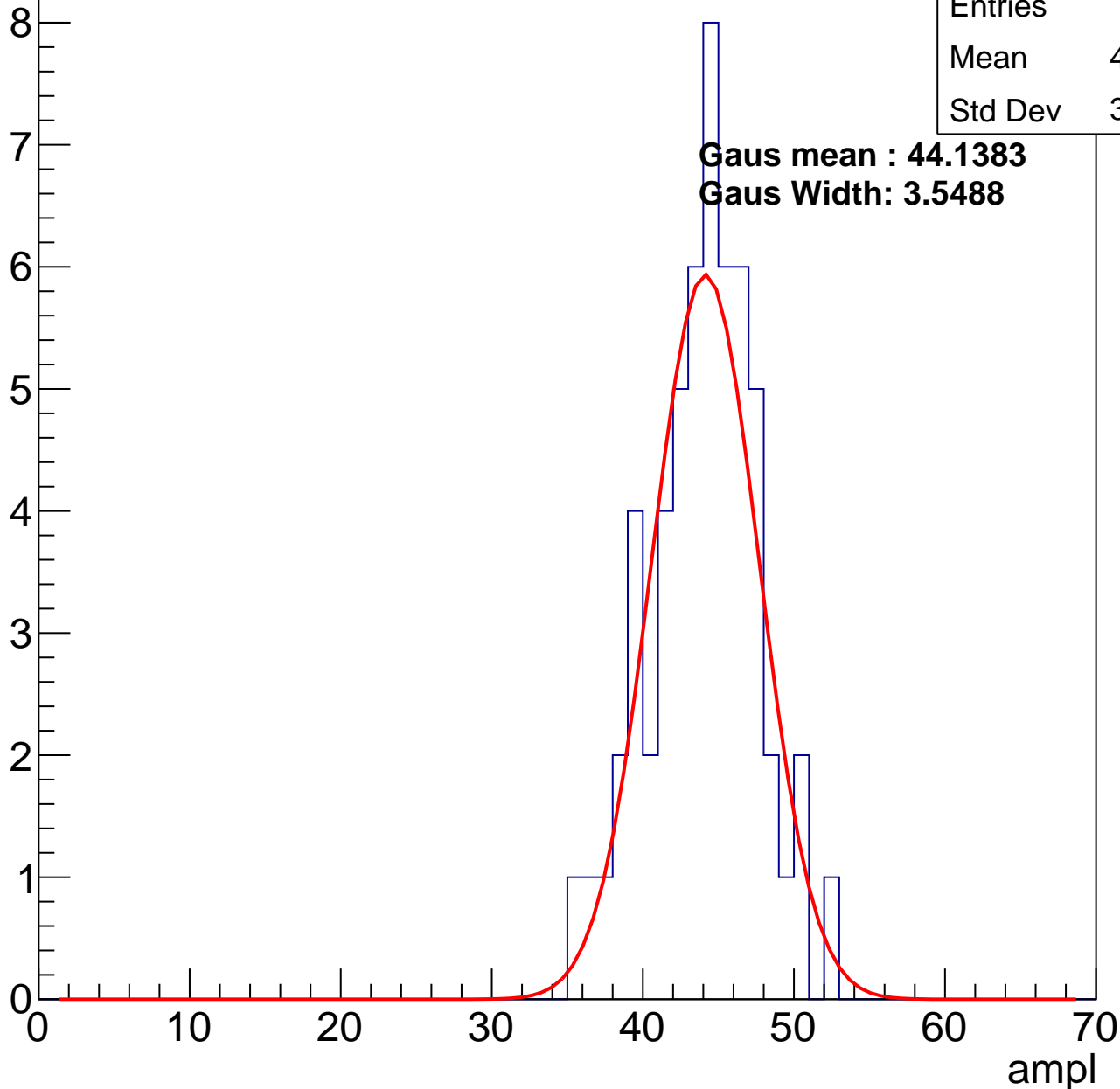
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	43.54
Std Dev	3.545

**Gaus mean : 44.1383**

**Gaus Width: 3.5488**

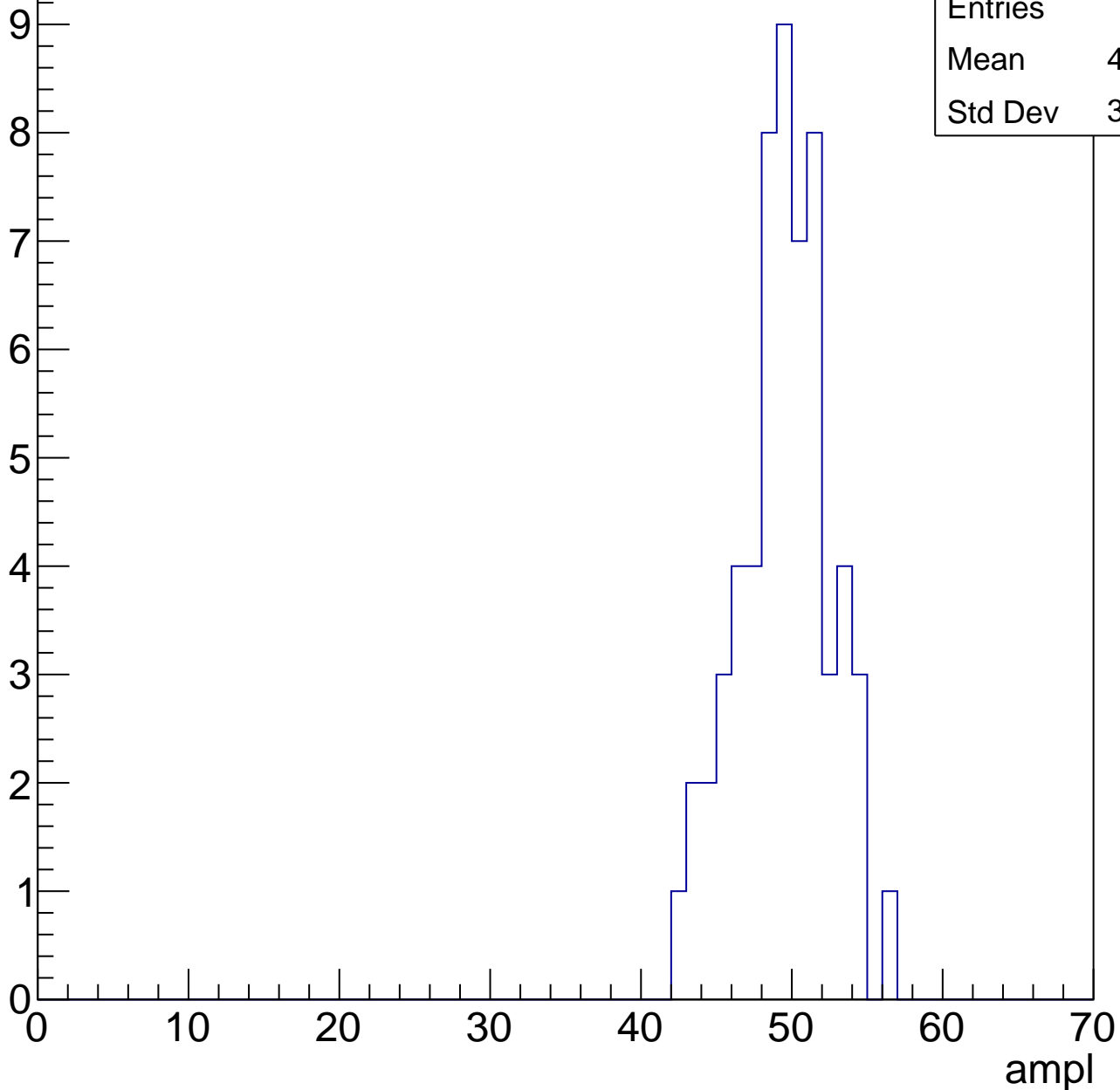


# B0L102S, U17-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	49.02
Std Dev	3.006

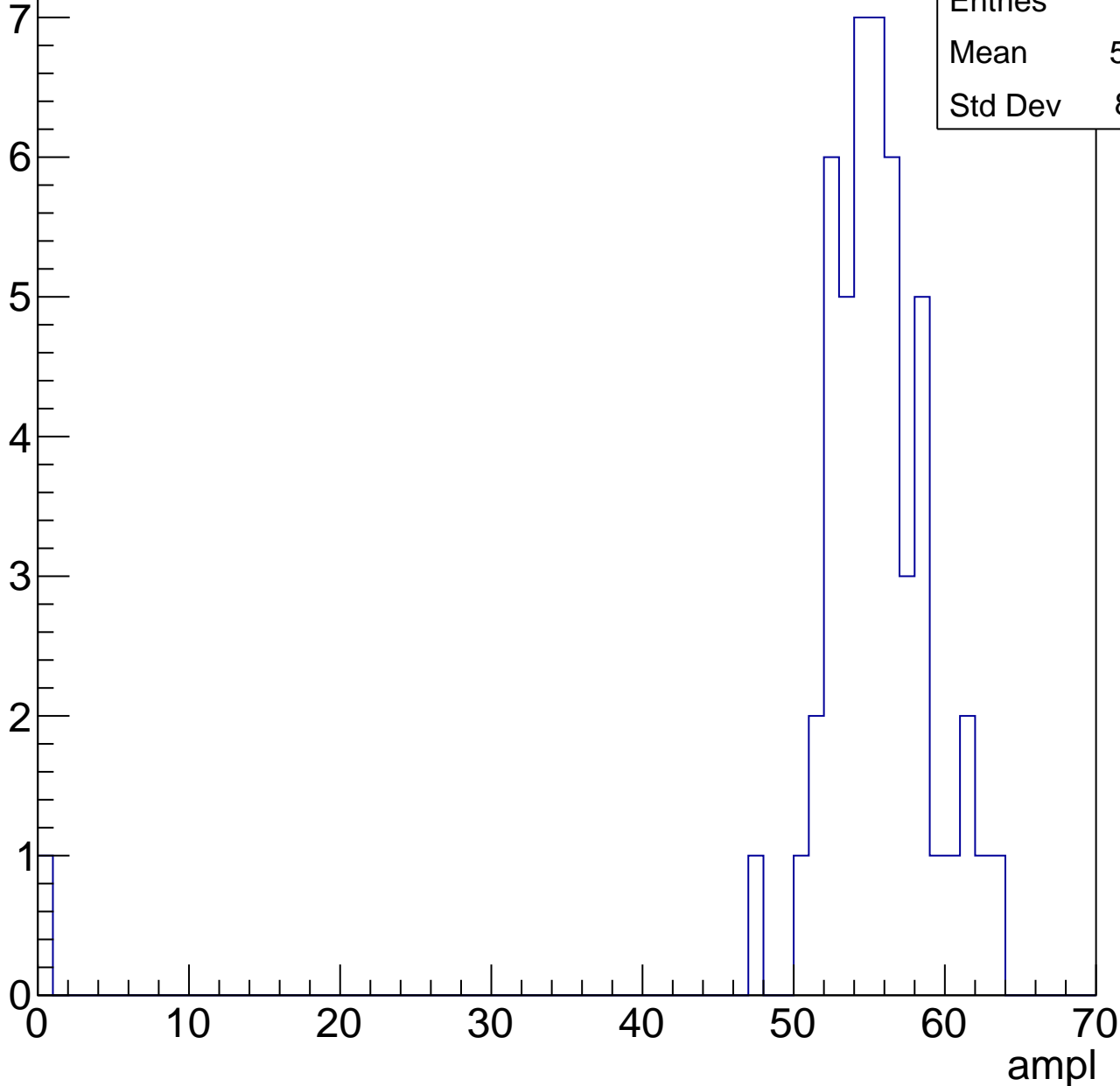


# B0L102S, U17-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	54.04
Std Dev	8.331



# B0L102S, U17-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	59.3
Std Dev	2.401

ampl

0

10

20

30

40

50

60

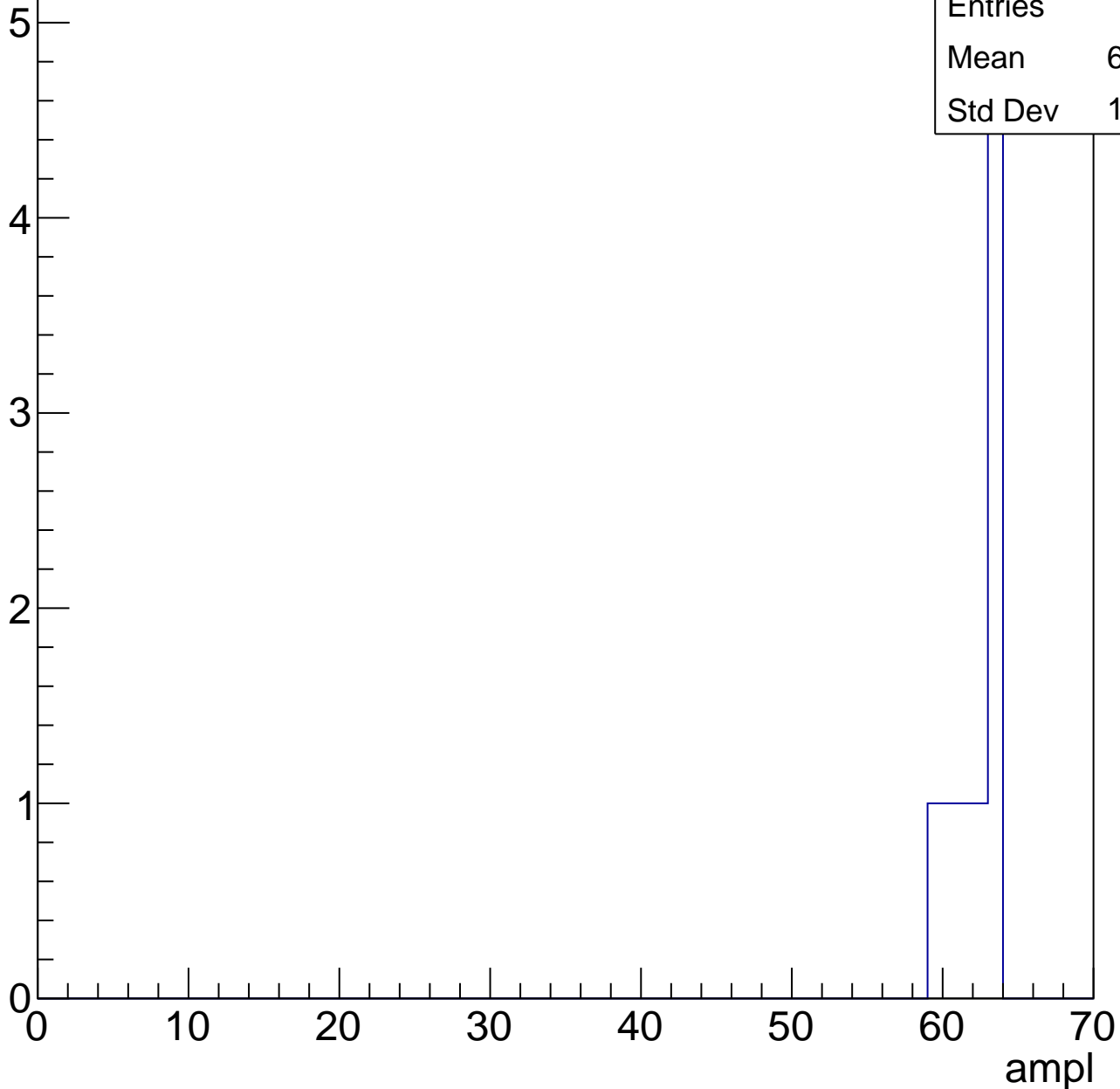
70

# B0L102S, U17-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	9
Mean	61.89
Std Dev	1.449

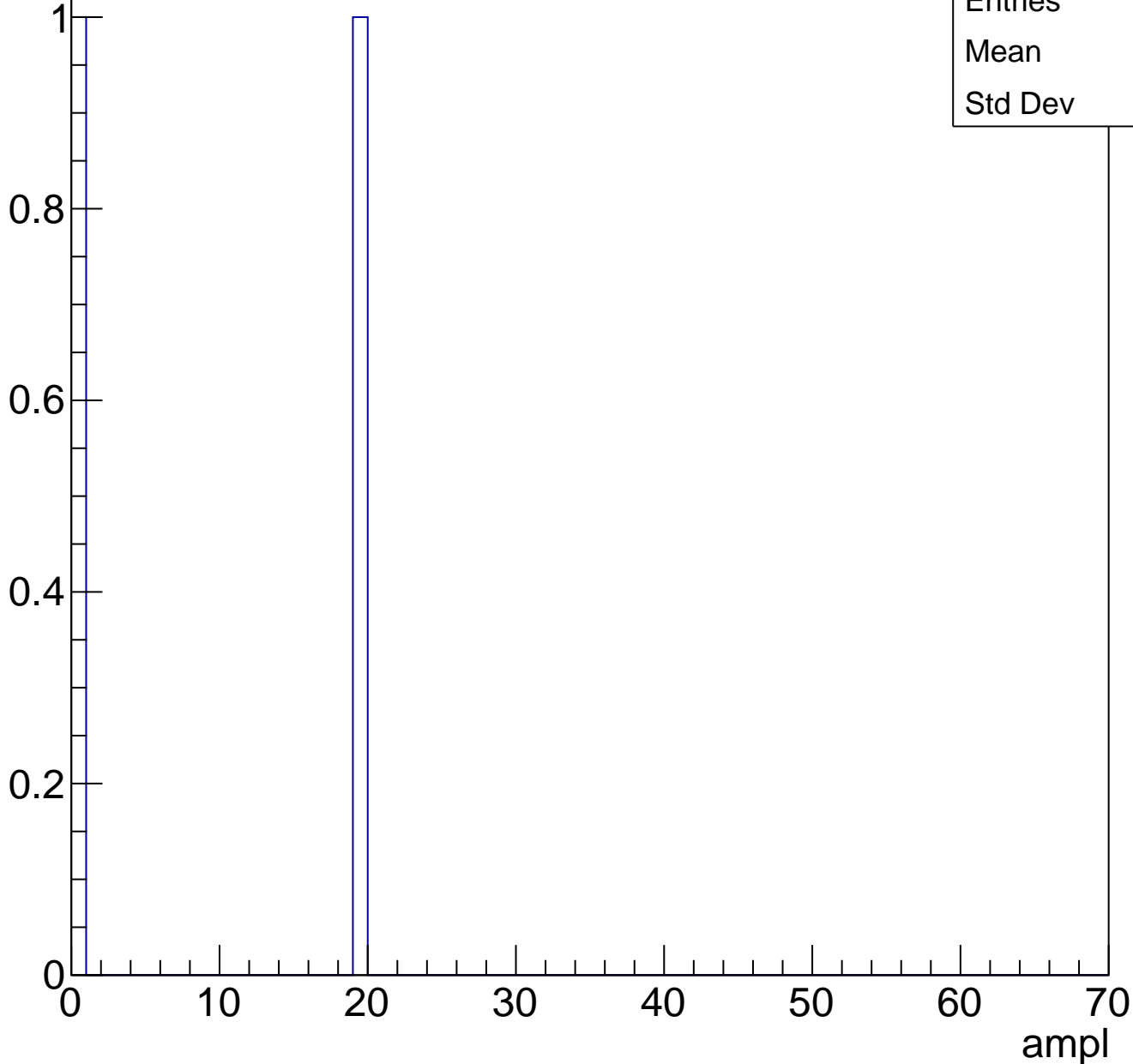




# B0L102S, U17-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B0L102S, U17-ch9, adc0

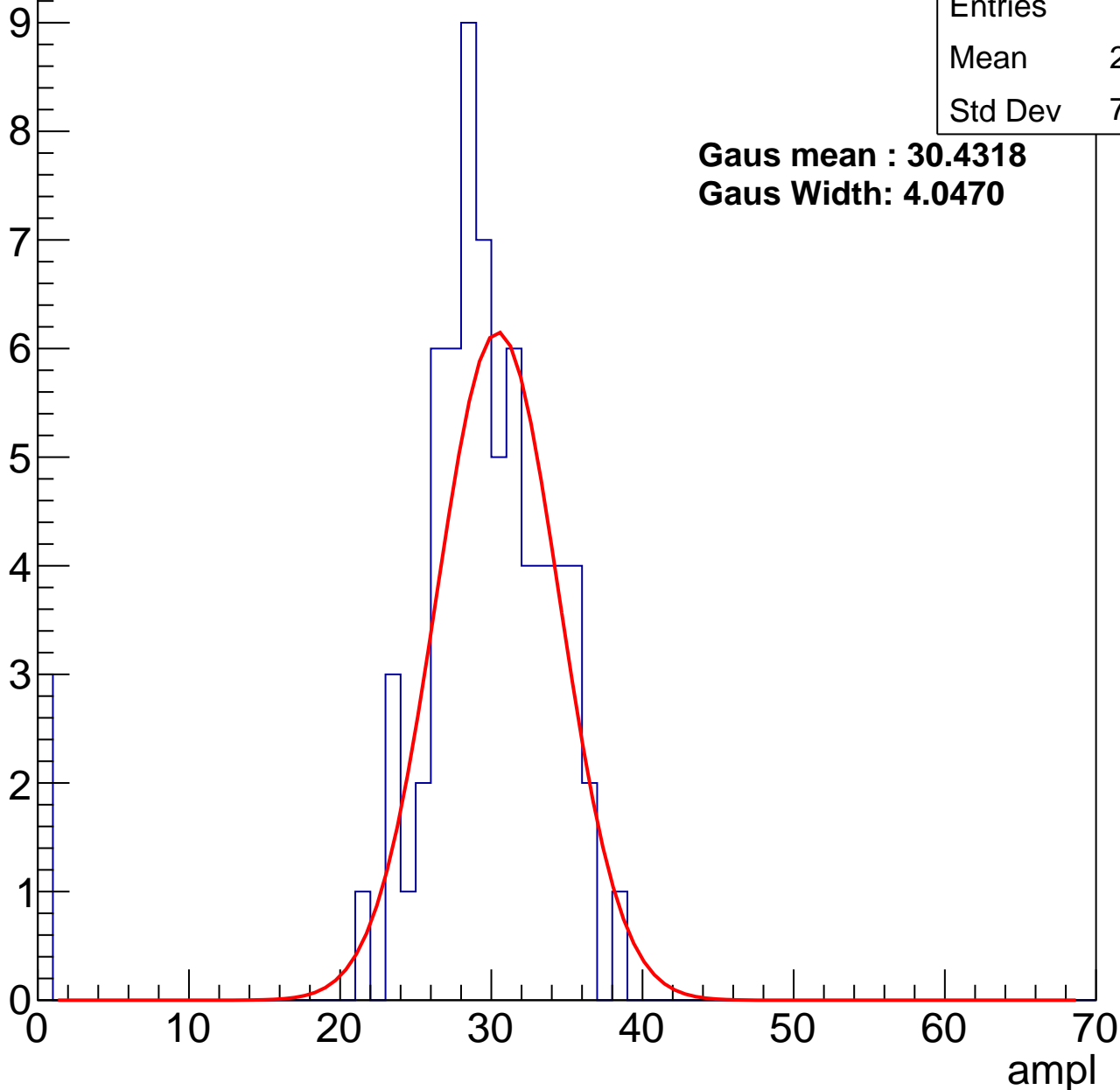
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.22
Std Dev	7.019

**Gaus mean : 30.4318**

**Gaus Width: 4.0470**



# B0L102S, U17-ch9, adc1

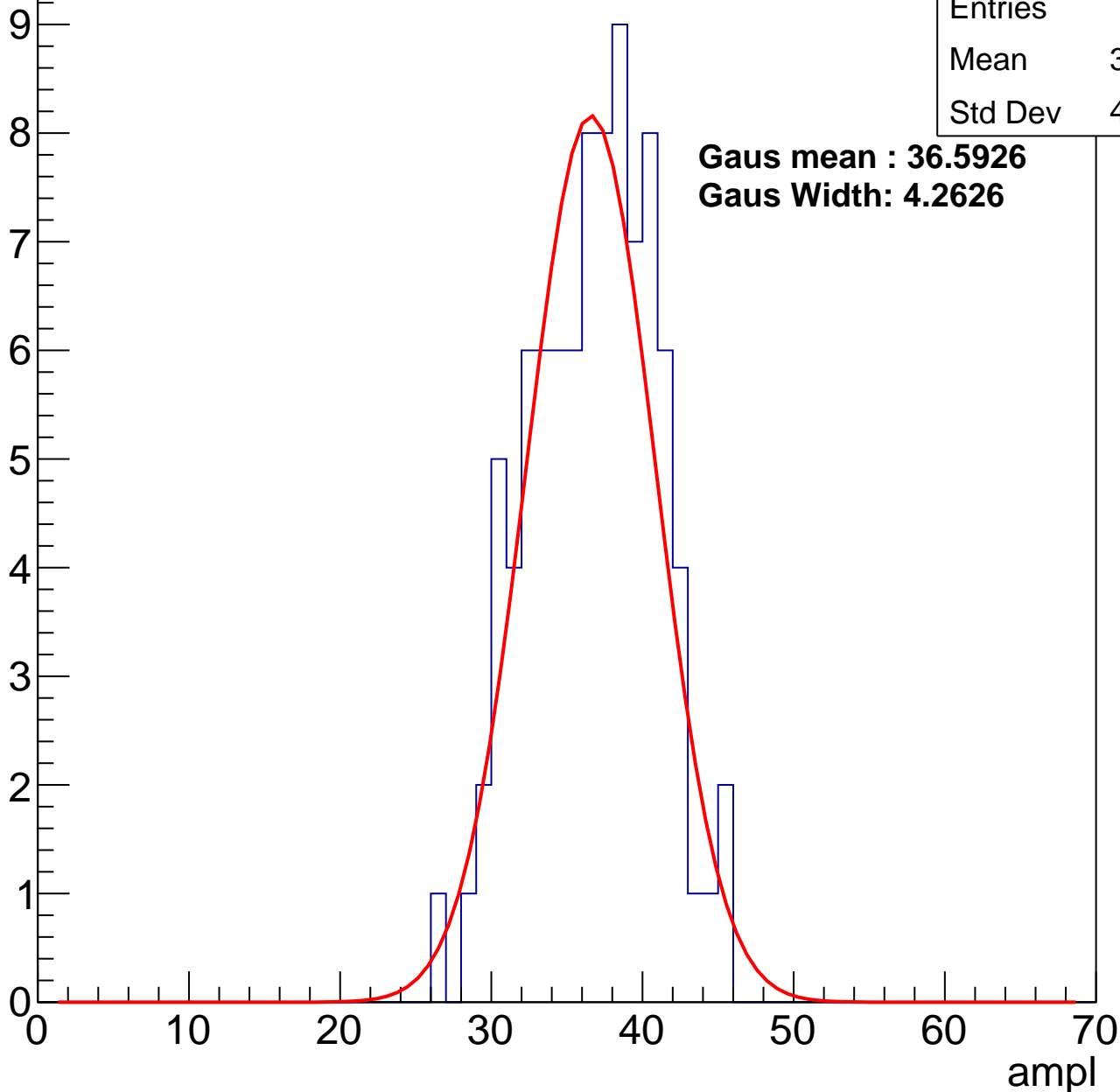
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	91
Mean	36.26
Std Dev	4.087

**Gaus mean : 36.5926**

**Gaus Width: 4.2626**



# B0L102S, U17-ch9, adc2

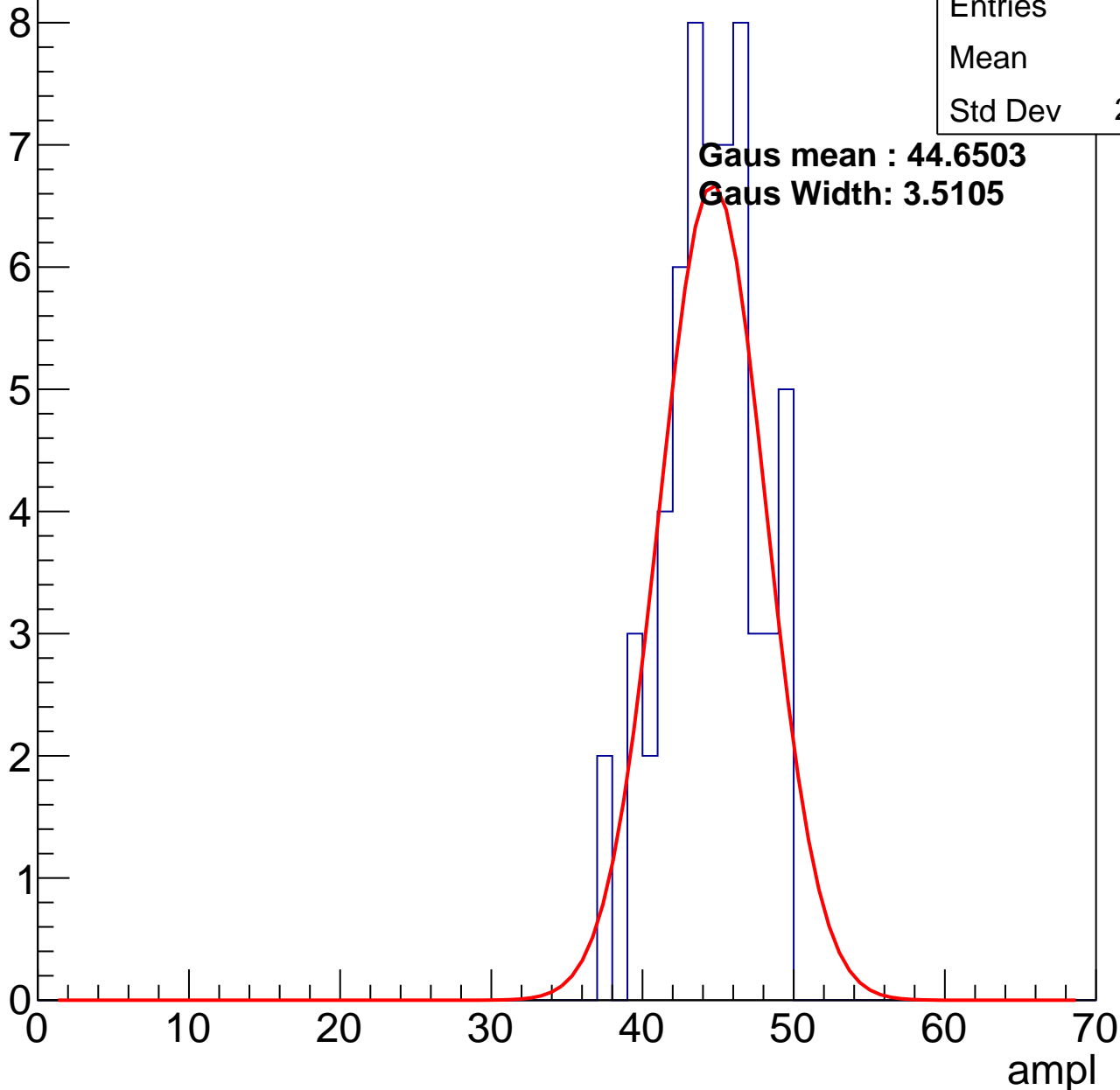
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	44
Std Dev	2.971

**Gaus mean : 44.6503**

**Gaus Width: 3.5105**

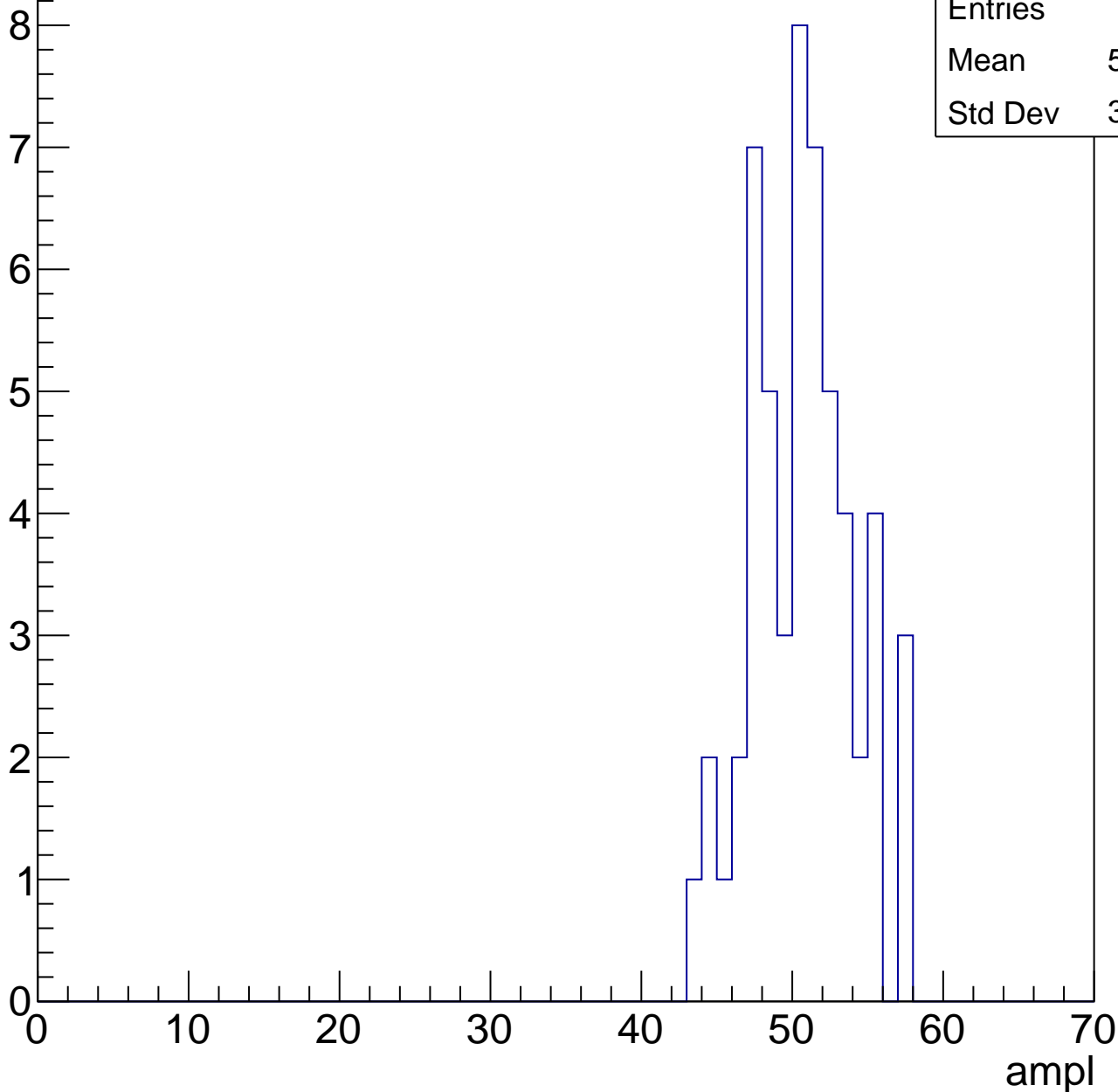


# B0L102S, U17-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	50.22
Std Dev	3.343



# B0L102S, U17-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 54

Mean 54.8

Std Dev 3.429

2

3

4

5

6

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9

10

11

12

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324

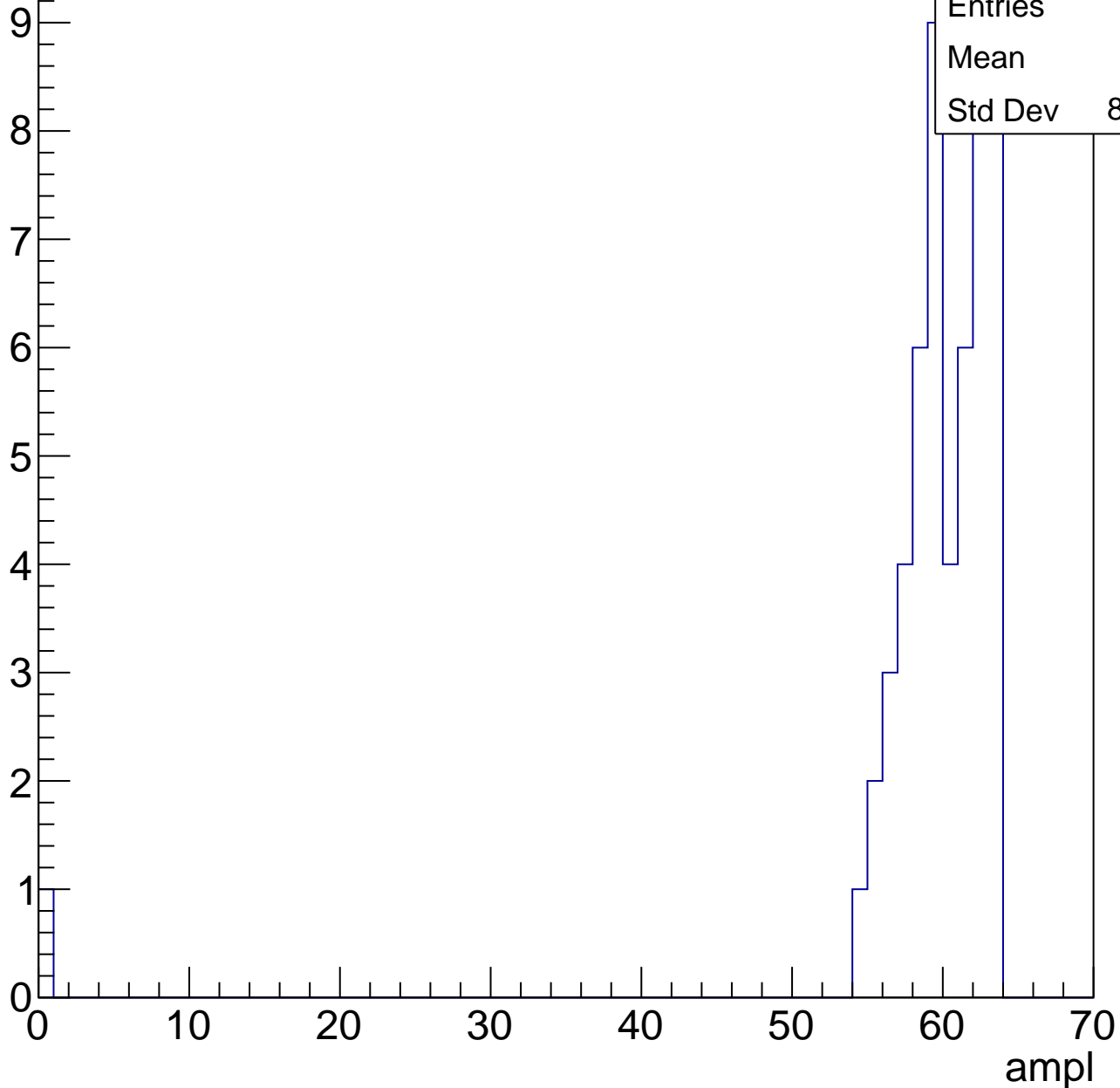
325

326

# B0L102S, U17-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch10, adc0

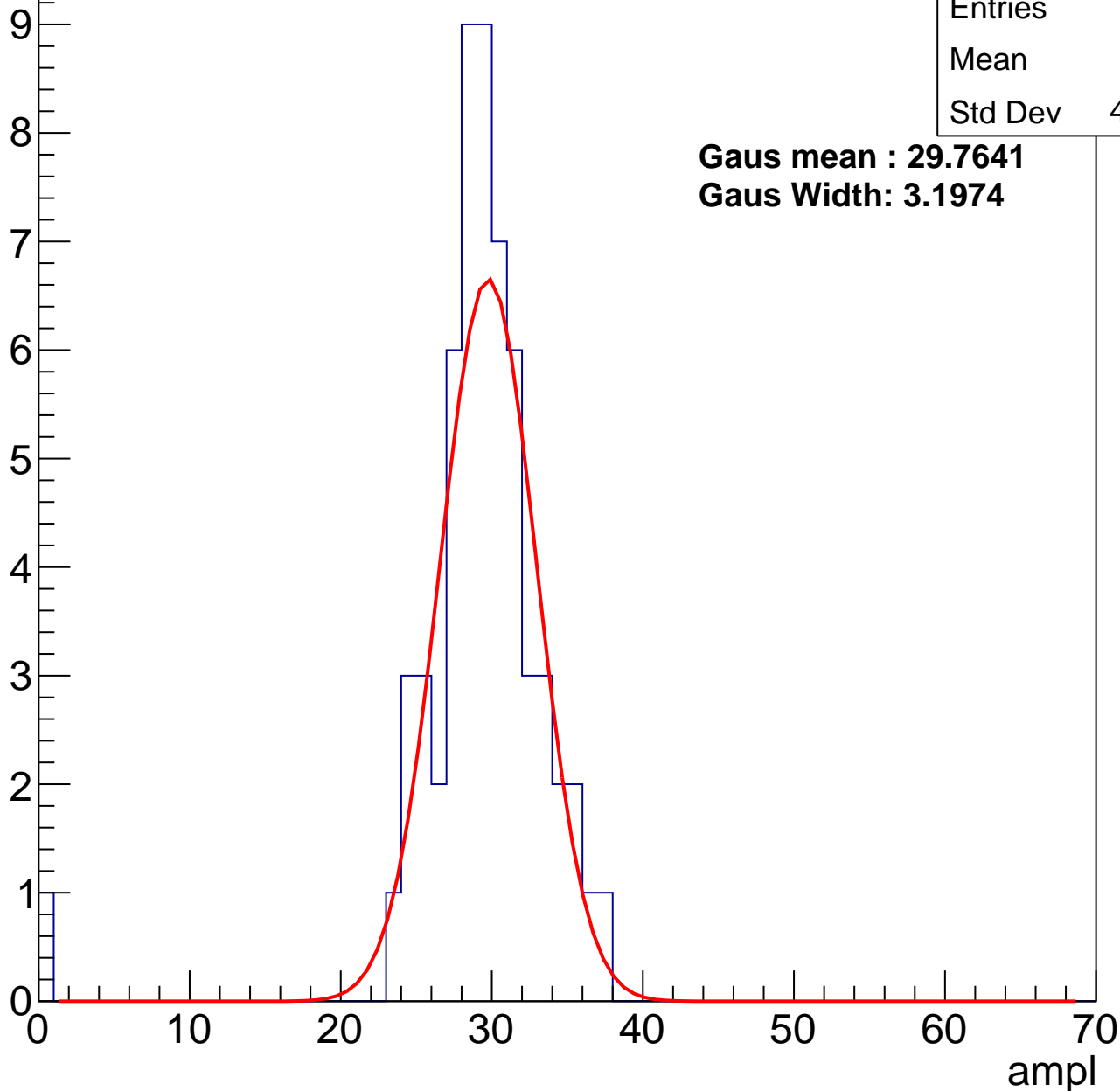
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	28.8
Std Dev	4.853

**Gaus mean : 29.7641**

**Gaus Width: 3.1974**



# B0L102S, U17-ch10, adc1

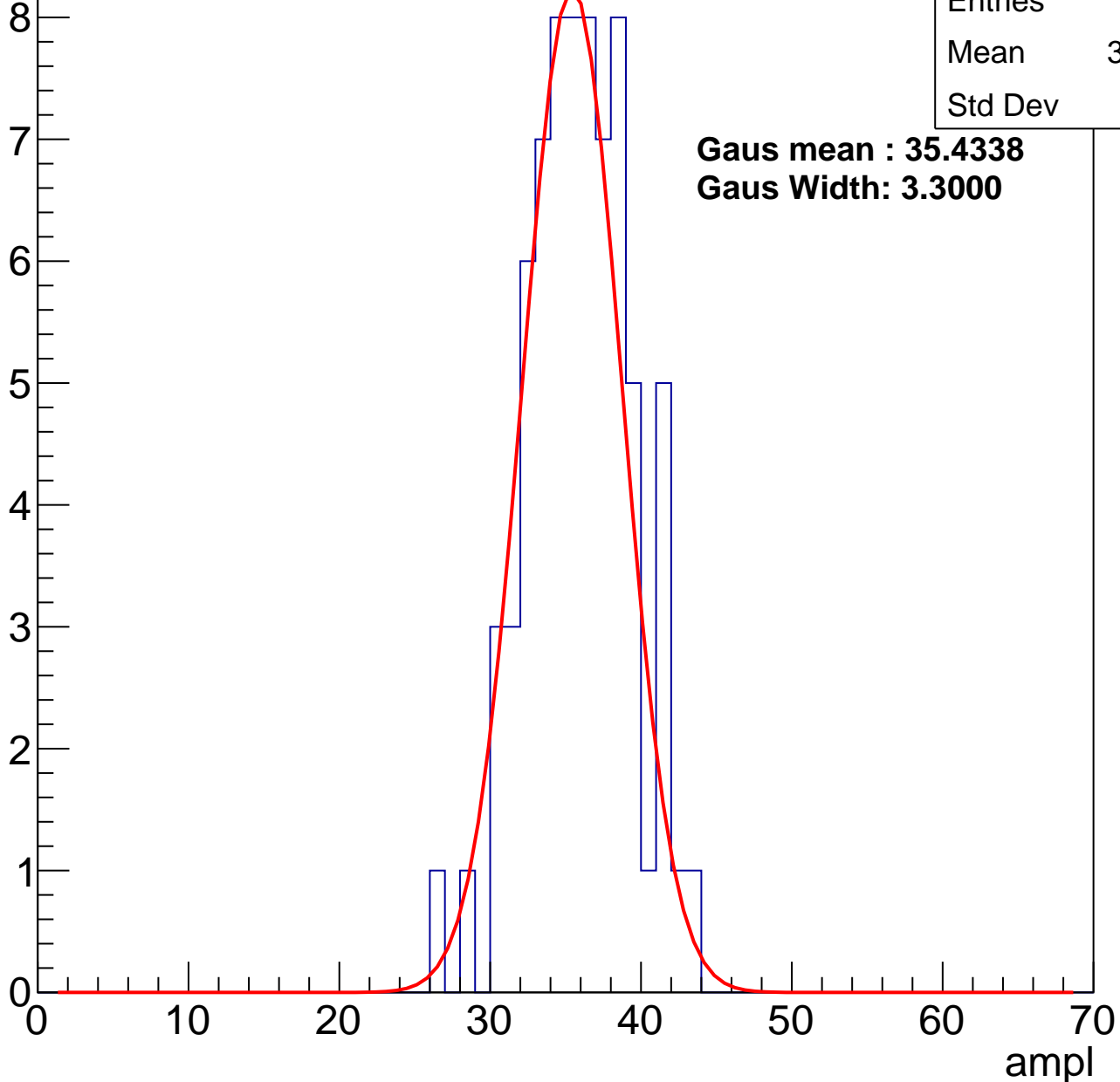
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.45
Std Dev	3.38

**Gaus mean : 35.4338**

**Gaus Width: 3.3000**



# B0L102S, U17-ch10, adc2

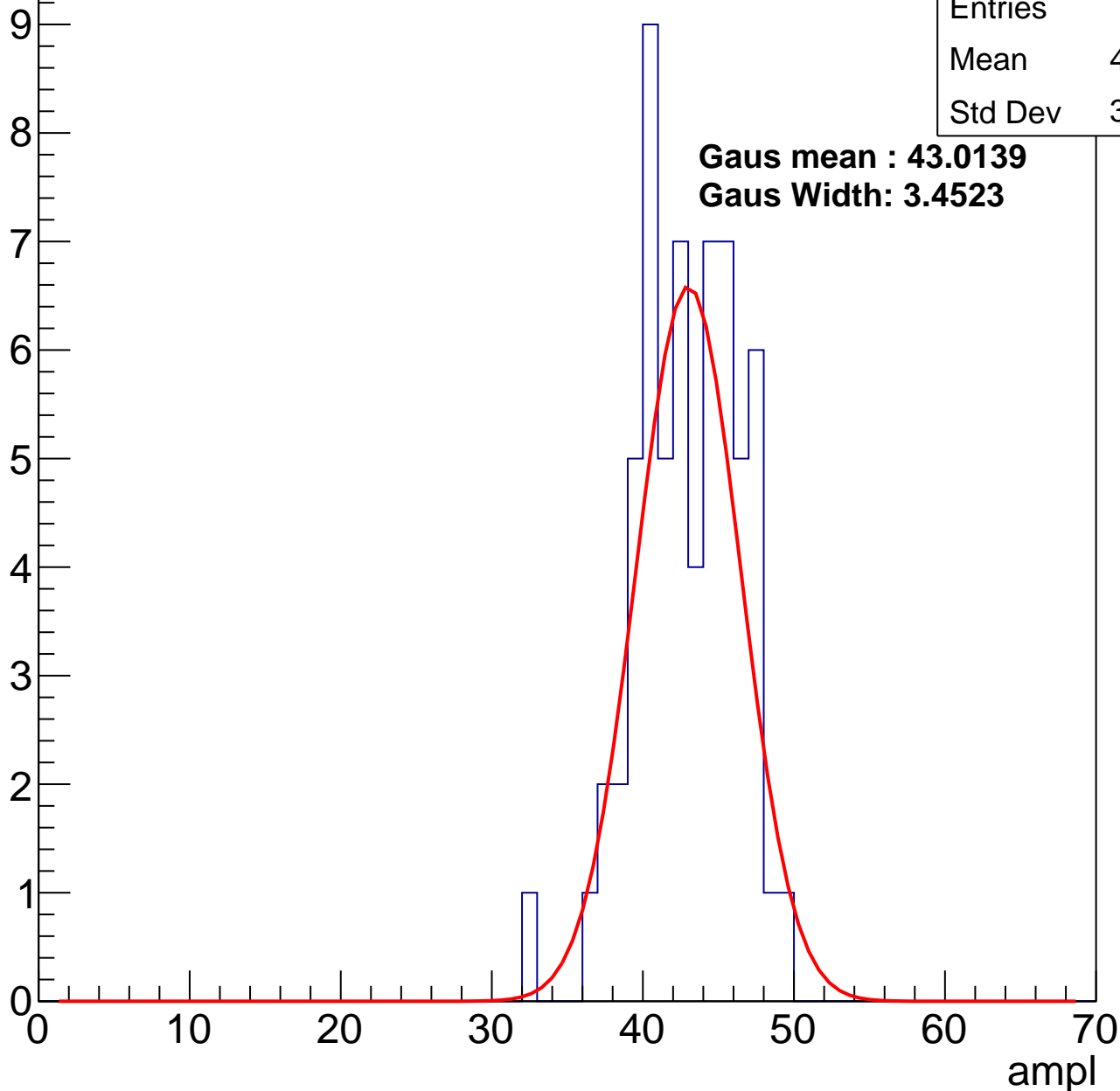
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	42.48
Std Dev	3.333

**Gaus mean : 43.0139**

**Gaus Width: 3.4523**

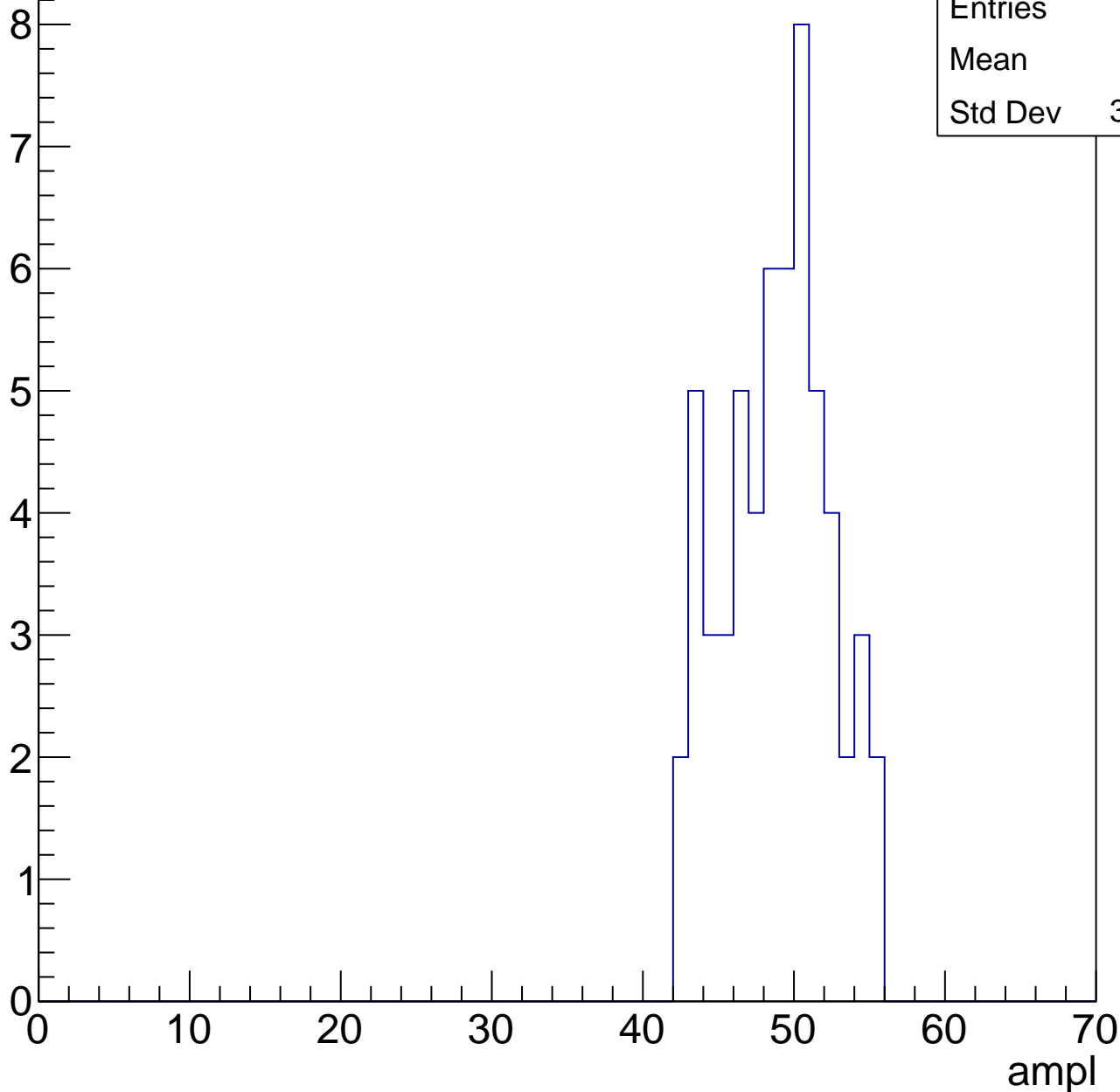


# B0L102S, U17-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	48.4
Std Dev	3.449

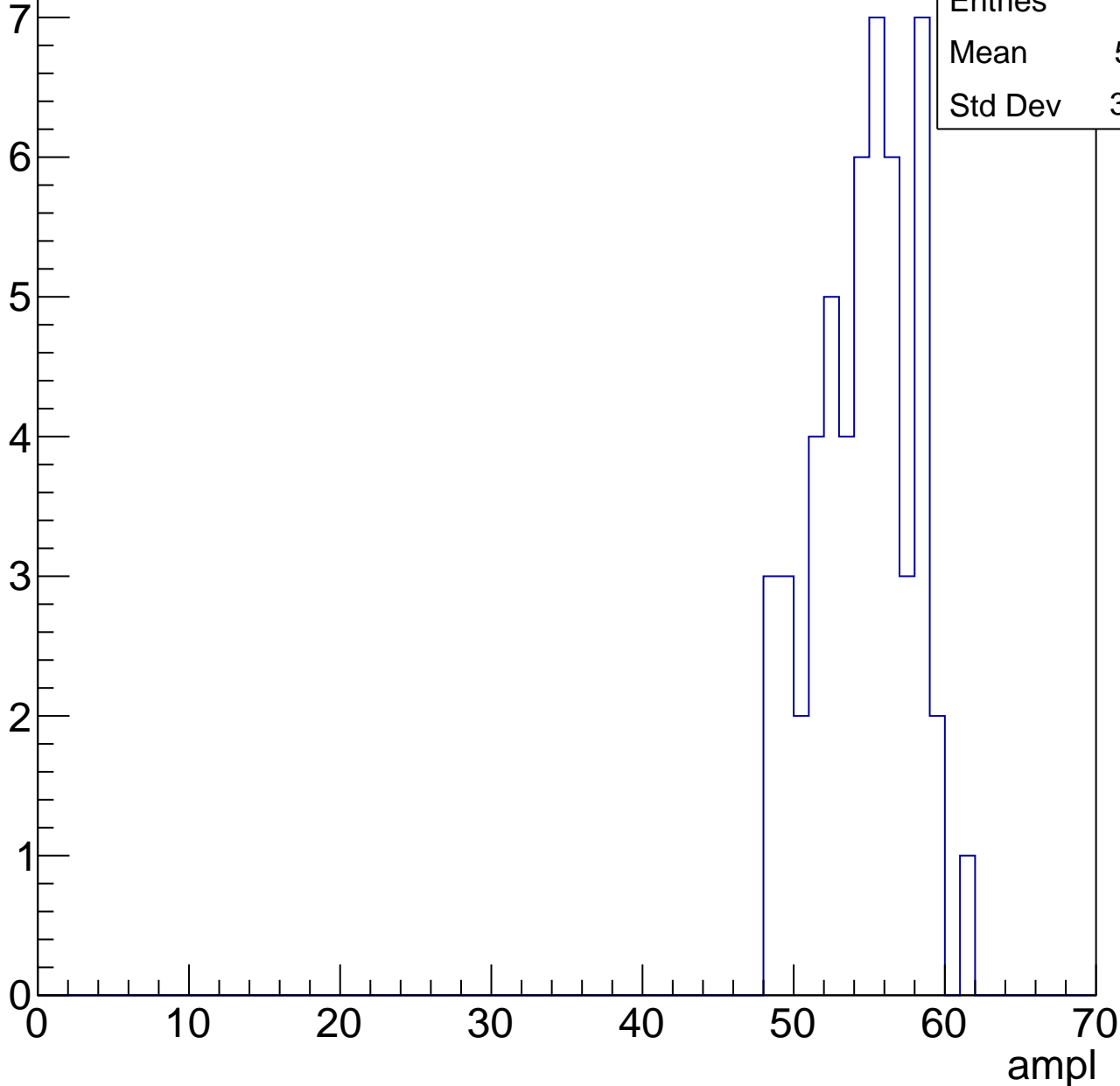


# B0L102S, U17-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	54.11
Std Dev	3.202

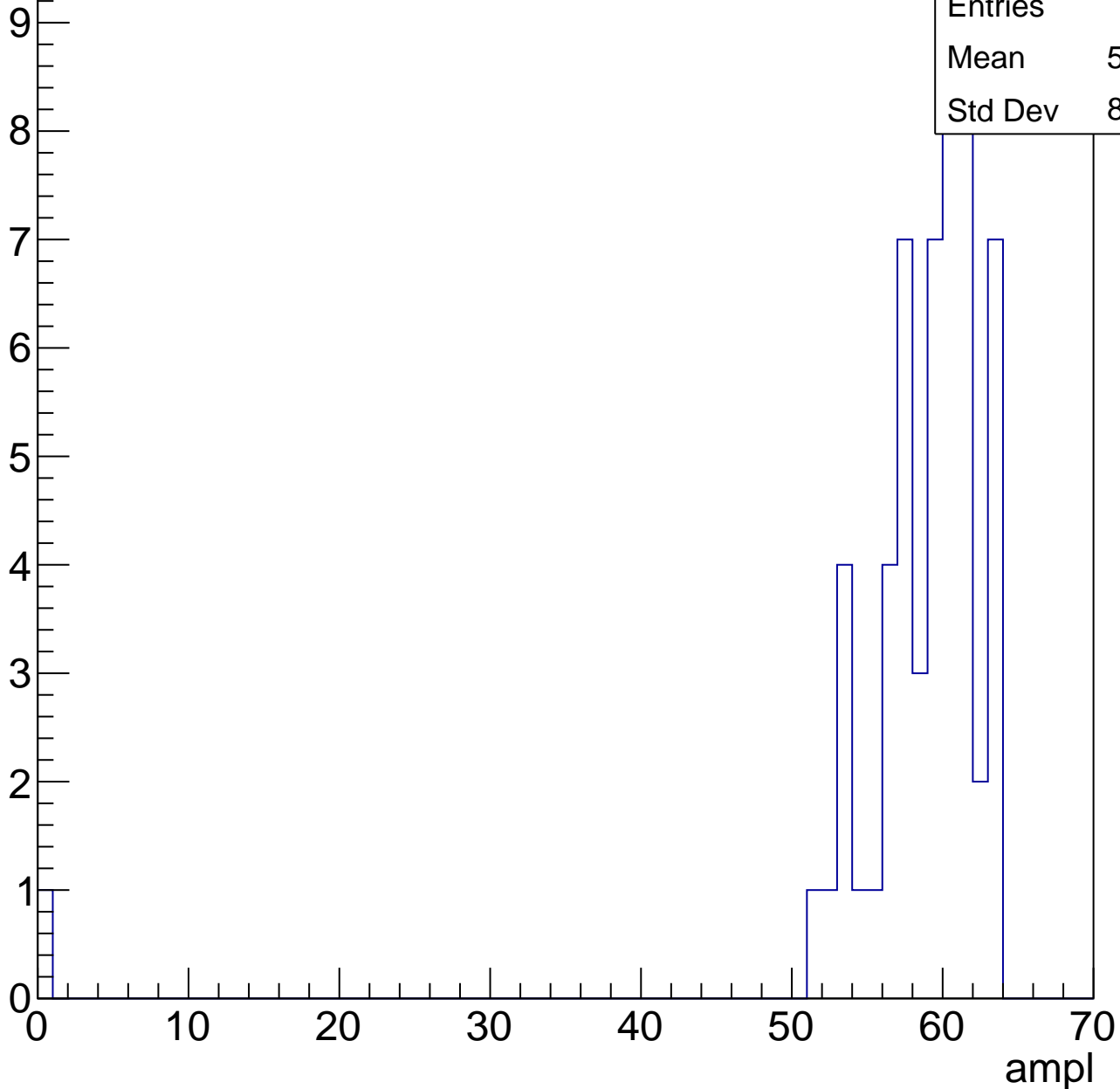


# B0L102S, U17-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	57.68
Std Dev	8.295

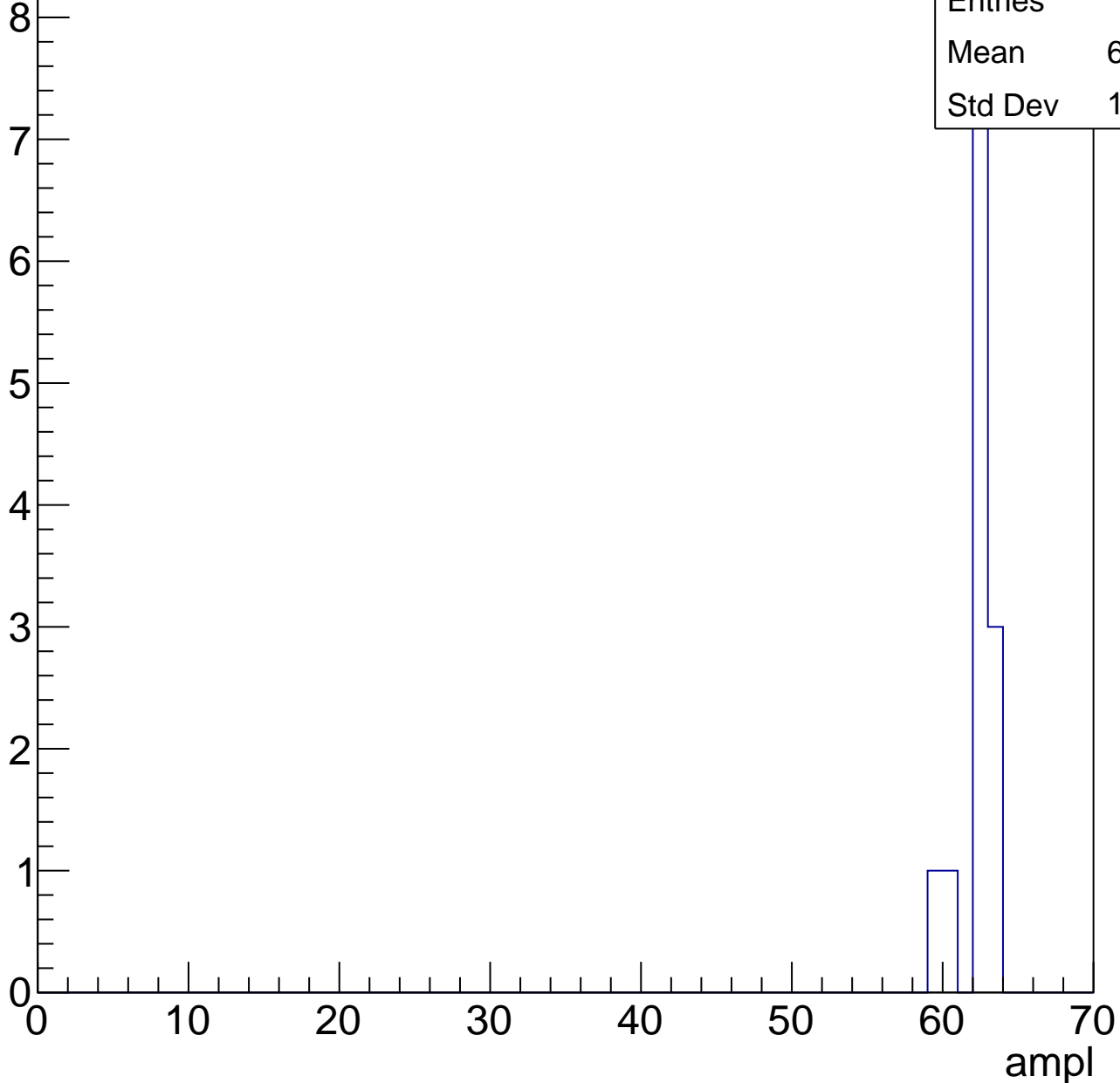


# B0L102S, U17-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	61.85
Std Dev	1.099





# B0L102S, U17-ch10, adc7

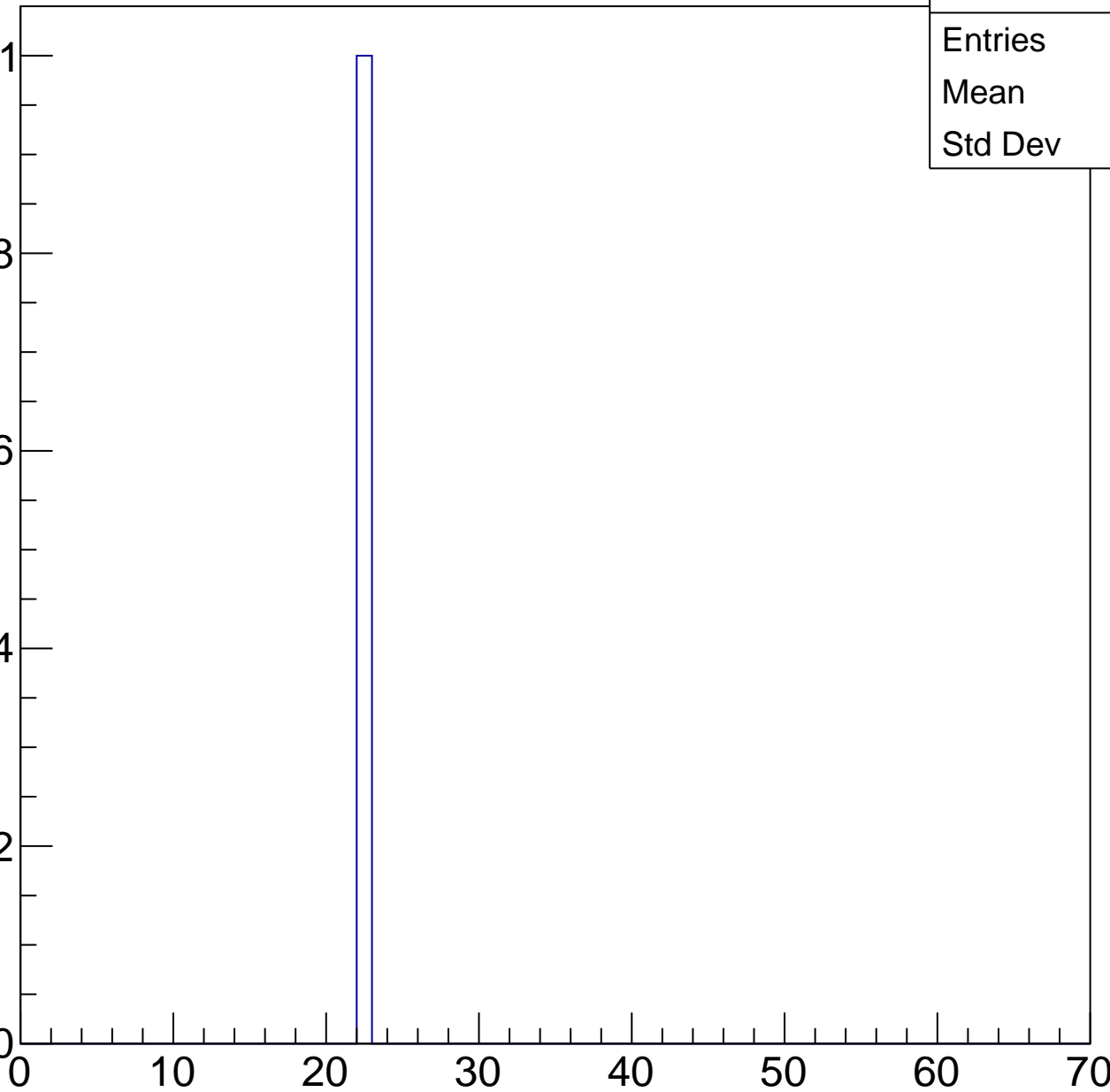
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl



# B0L102S, U17-ch11, adc0

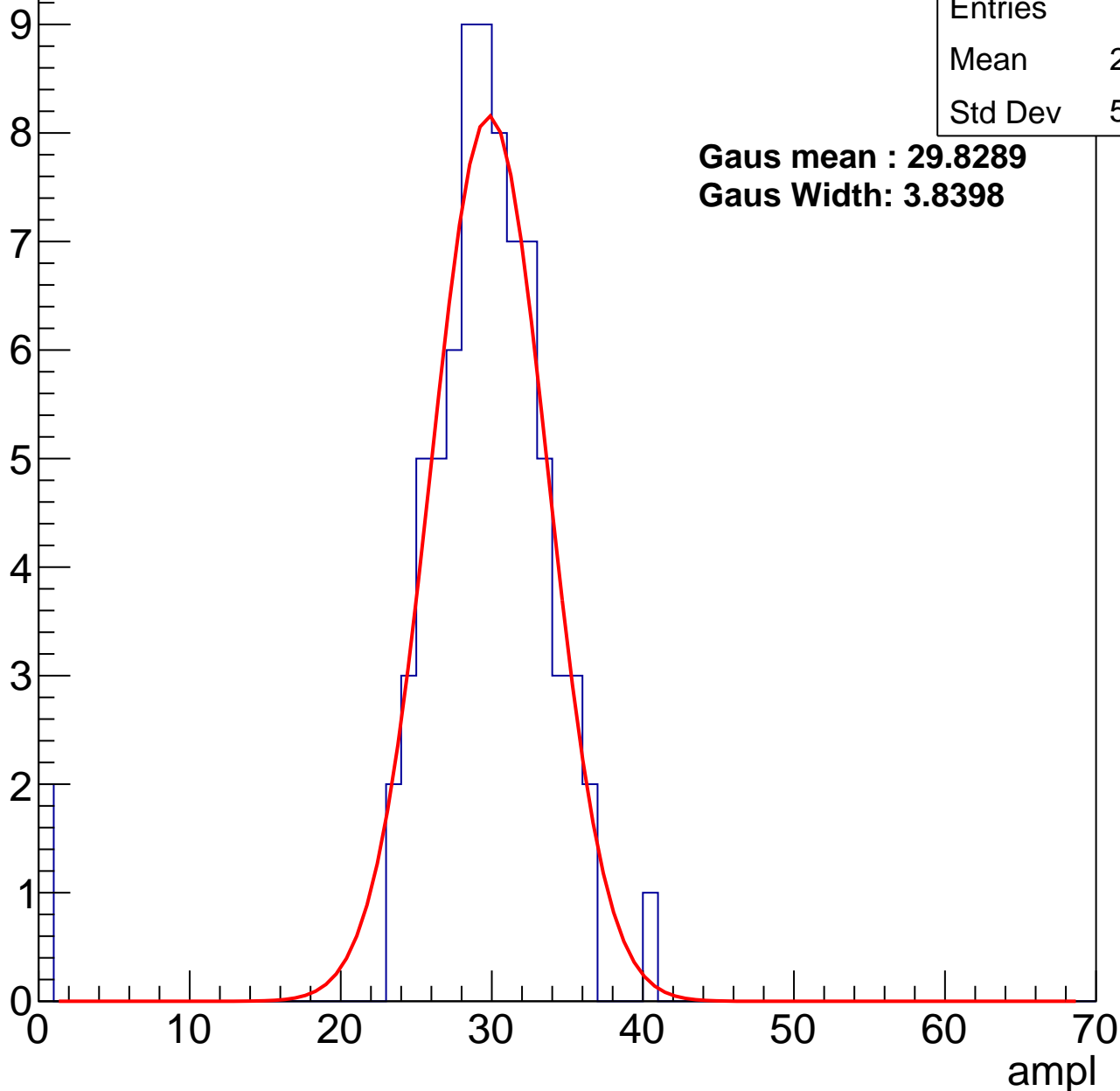
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.74
Std Dev	5.769

**Gaus mean : 29.8289**

**Gaus Width: 3.8398**



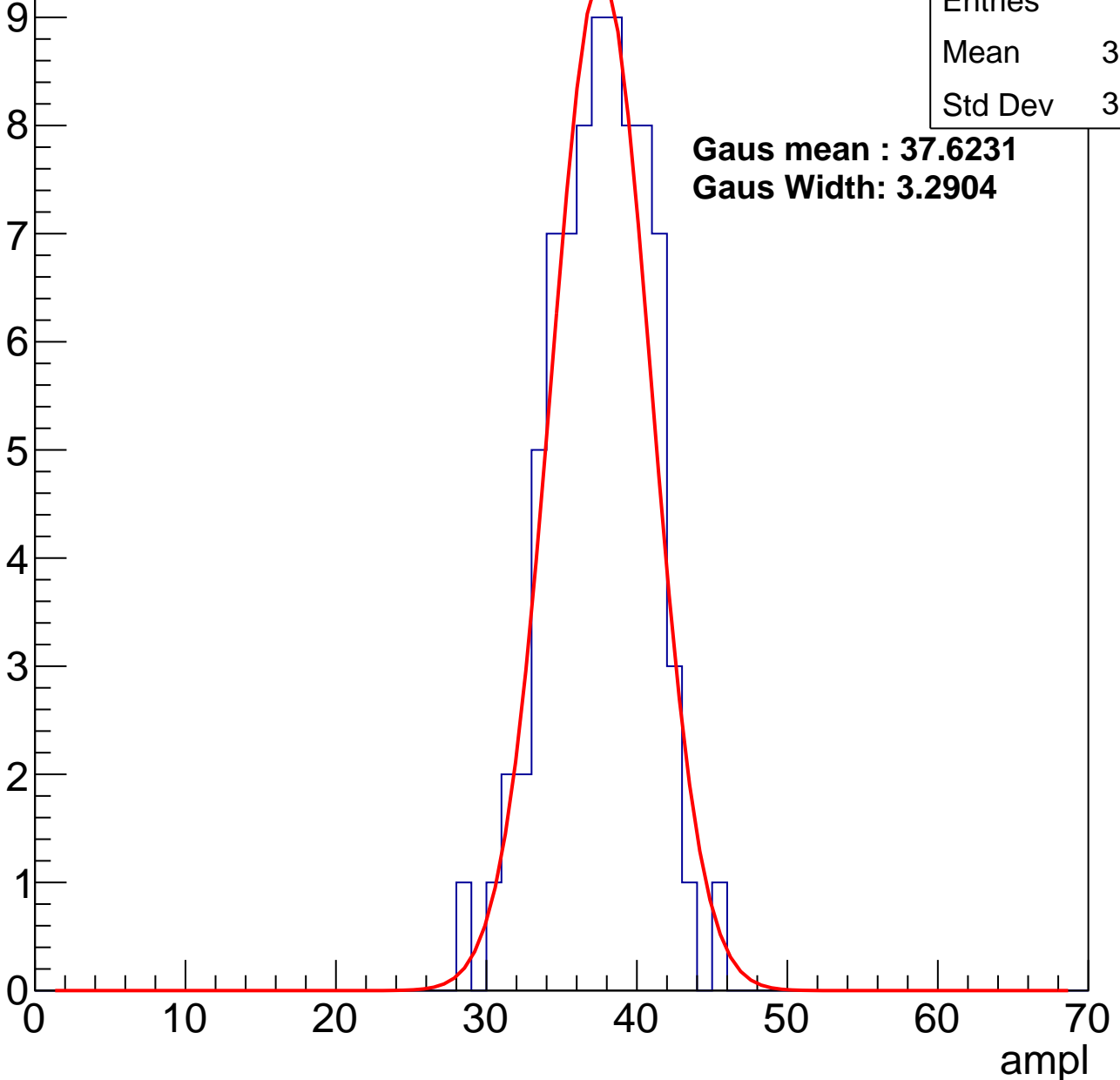
# B0L102S, U17-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	37.06
Std Dev	3.243

**Gaus mean : 37.6231**  
**Gaus Width: 3.2904**



# B0L102S, U17-ch11, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	45.26
Std Dev	3.425

**Gaus mean : 45.9047**

**Gaus Width: 3.4273**

Entry

10

8

6

4

2

0

0

10

20

30

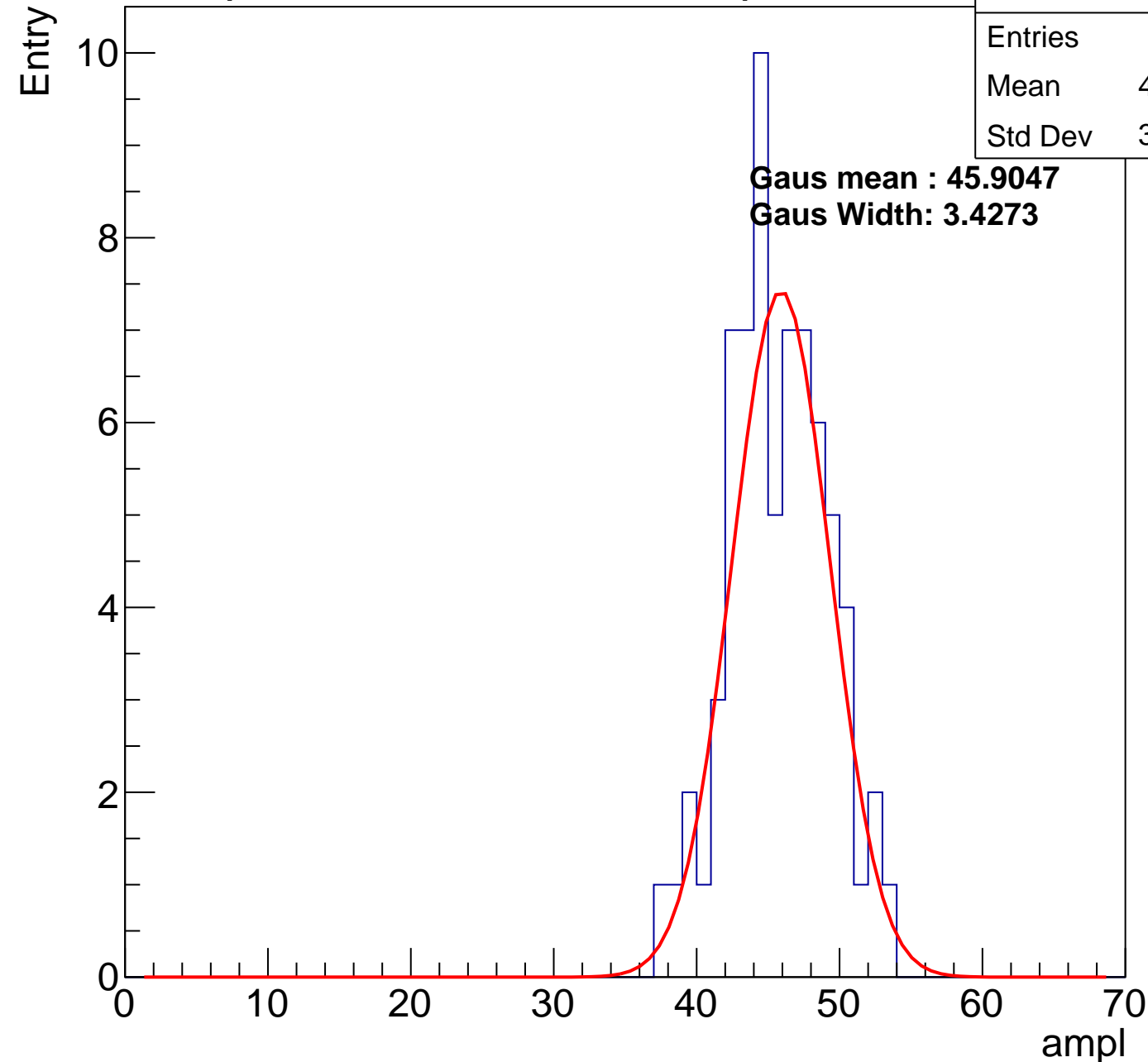
40

50

60

70

ampl

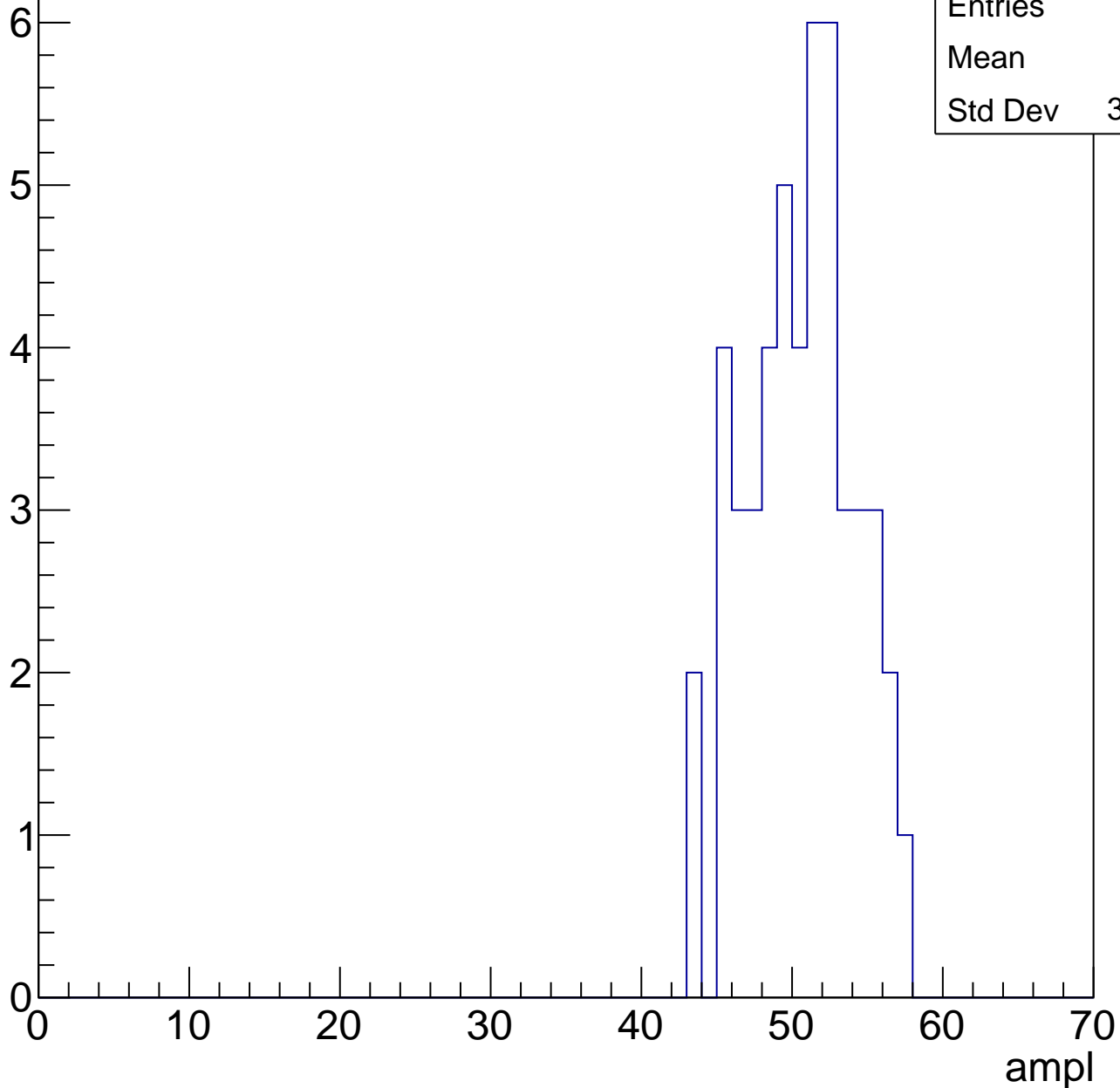


# B0L102S, U17-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	50.1
Std Dev	3.483

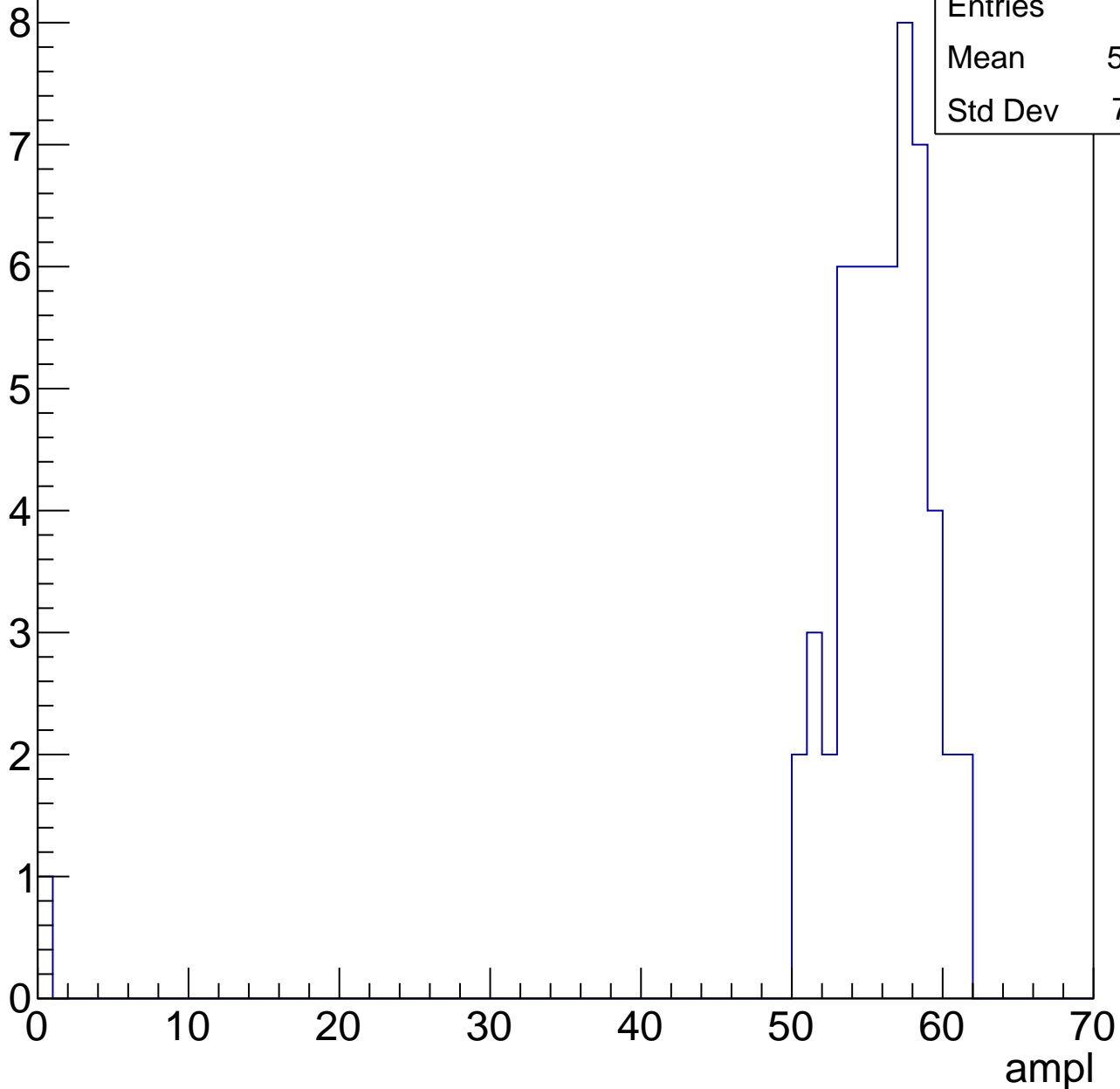


# B0L102S, U17-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	54.64
Std Dev	7.921

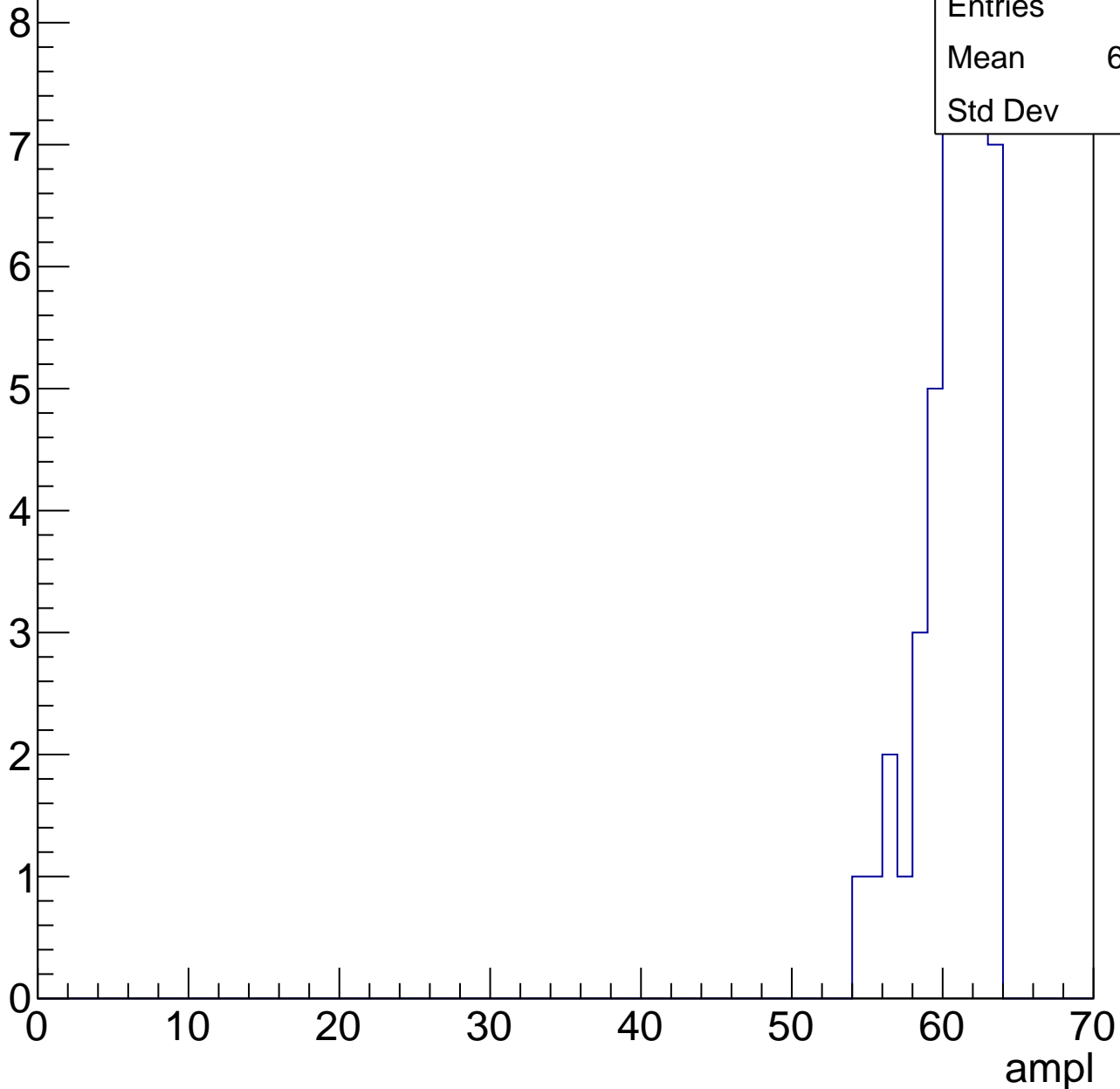


# B0L102S, U17-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	60.27
Std Dev	2.23



# B0L102S, U17-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch12, adc0

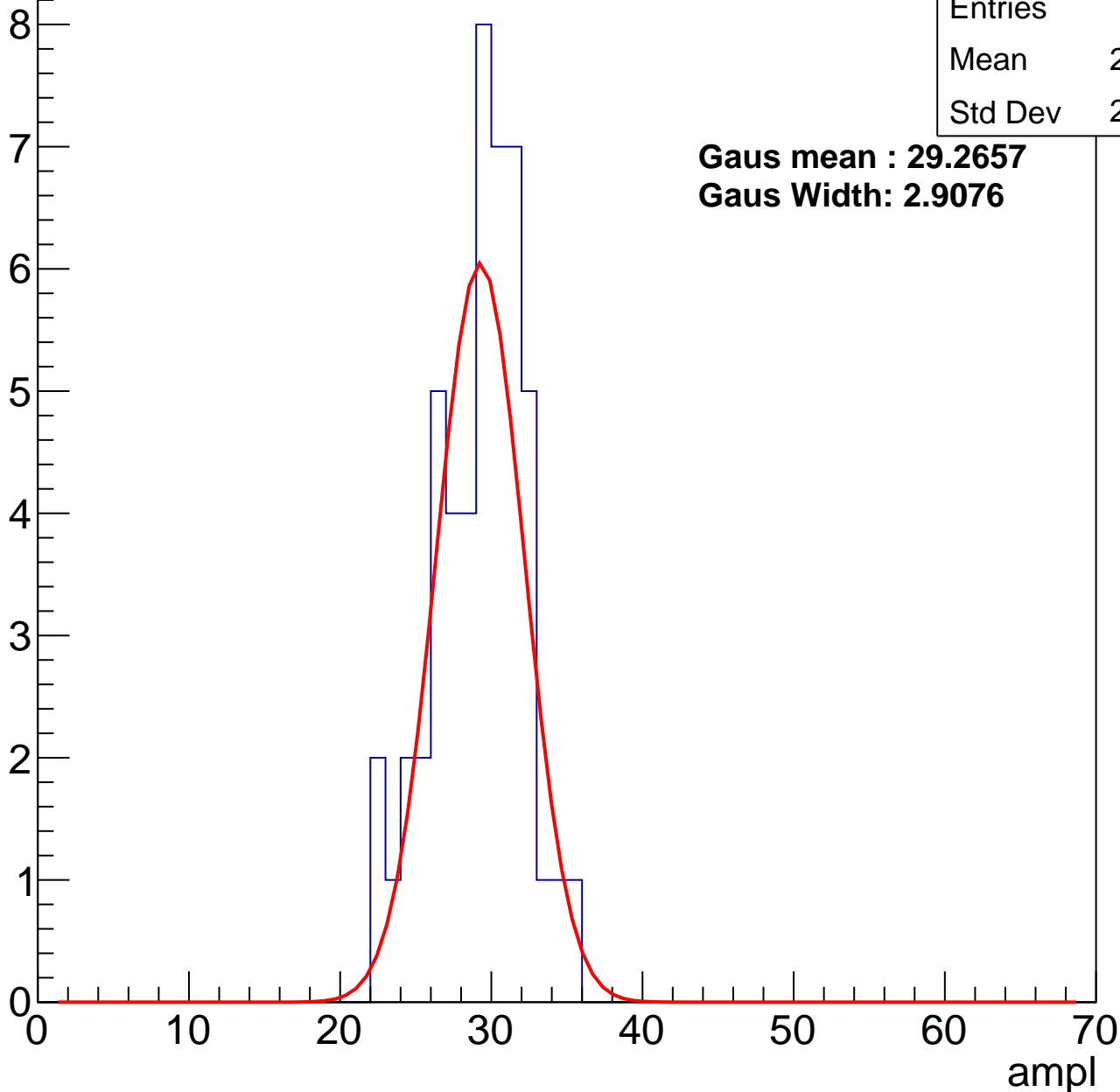
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	28.72
Std Dev	2.946

**Gaus mean : 29.2657**

**Gaus Width: 2.9076**



# B0L102S, U17-ch12, adc1

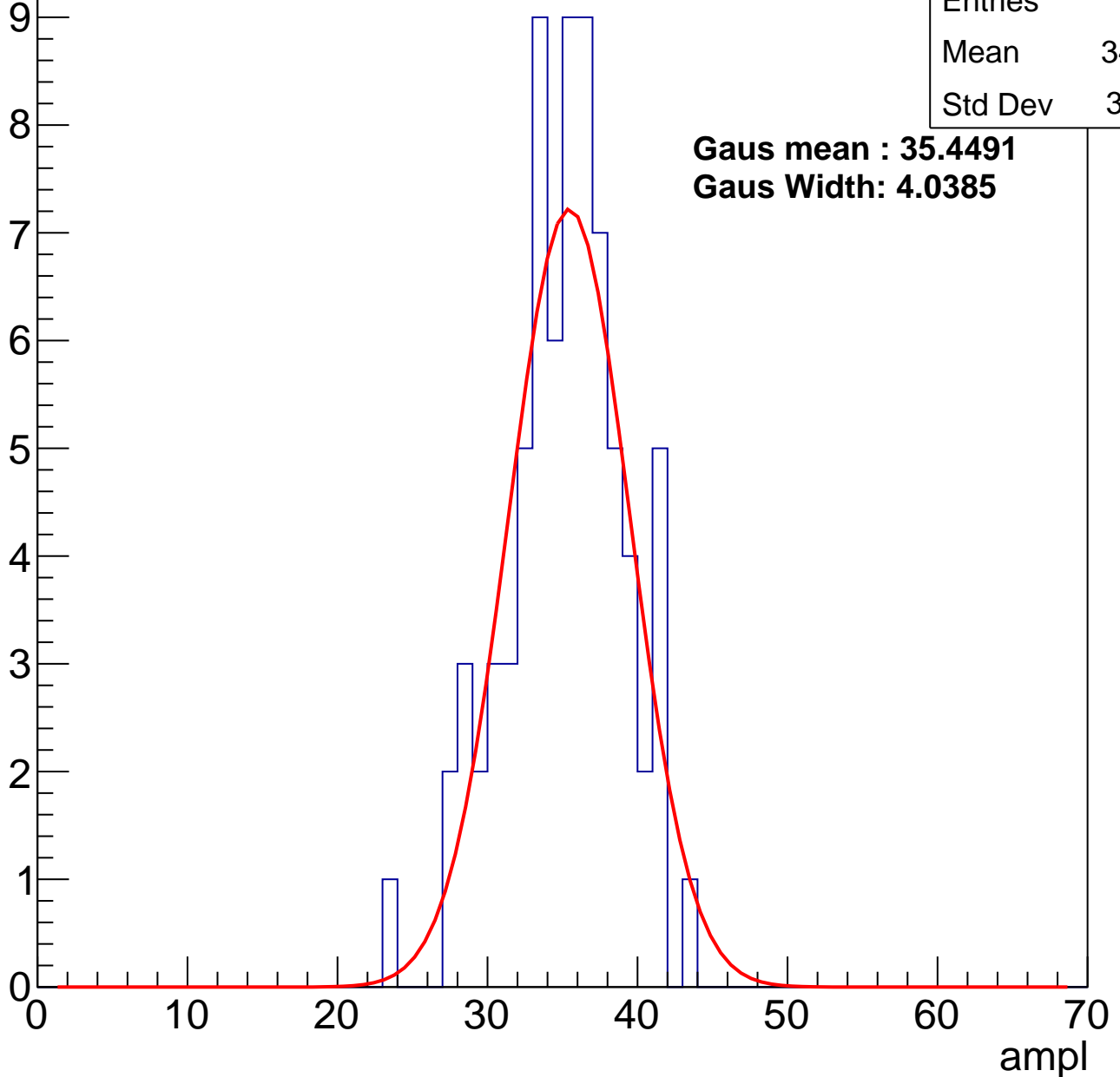
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	34.67
Std Dev	3.861

**Gaus mean : 35.4491**

**Gaus Width: 4.0385**



# B0L102S, U17-ch12, adc2

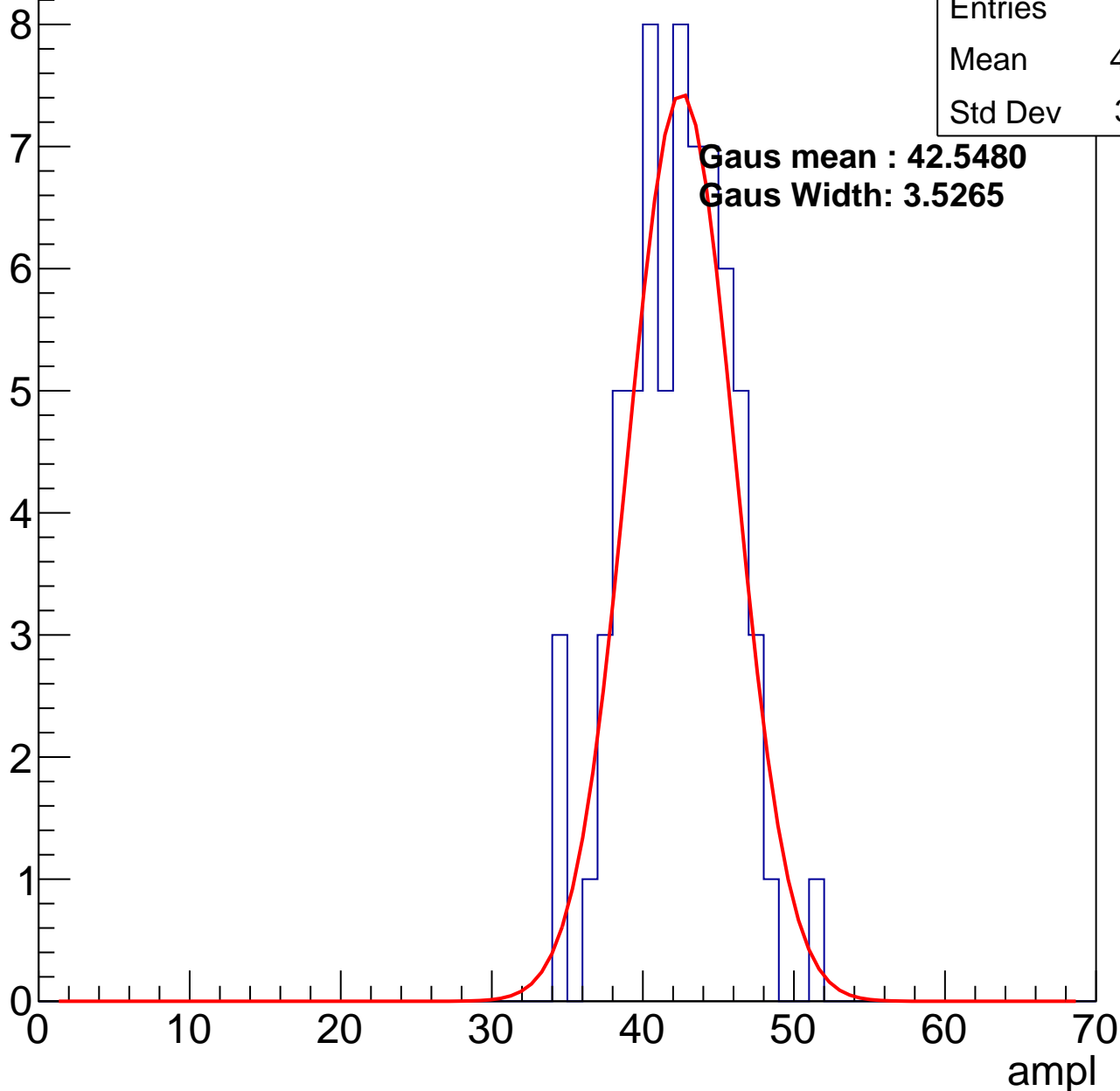
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.82
Std Dev	3.481

**Gaus mean : 42.5480**

**Gaus Width: 3.5265**

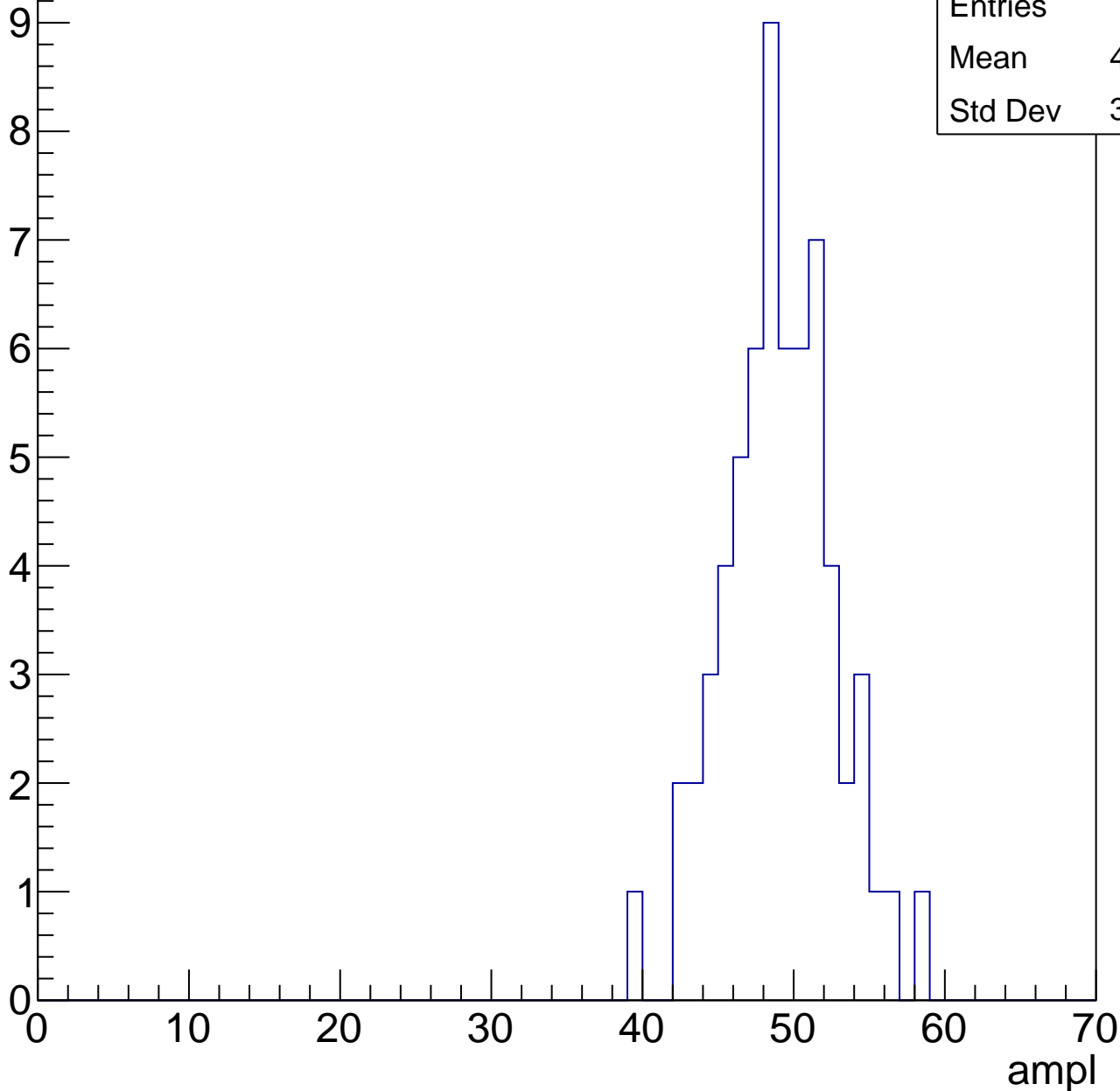


# B0L102S, U17-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	48.59
Std Dev	3.606

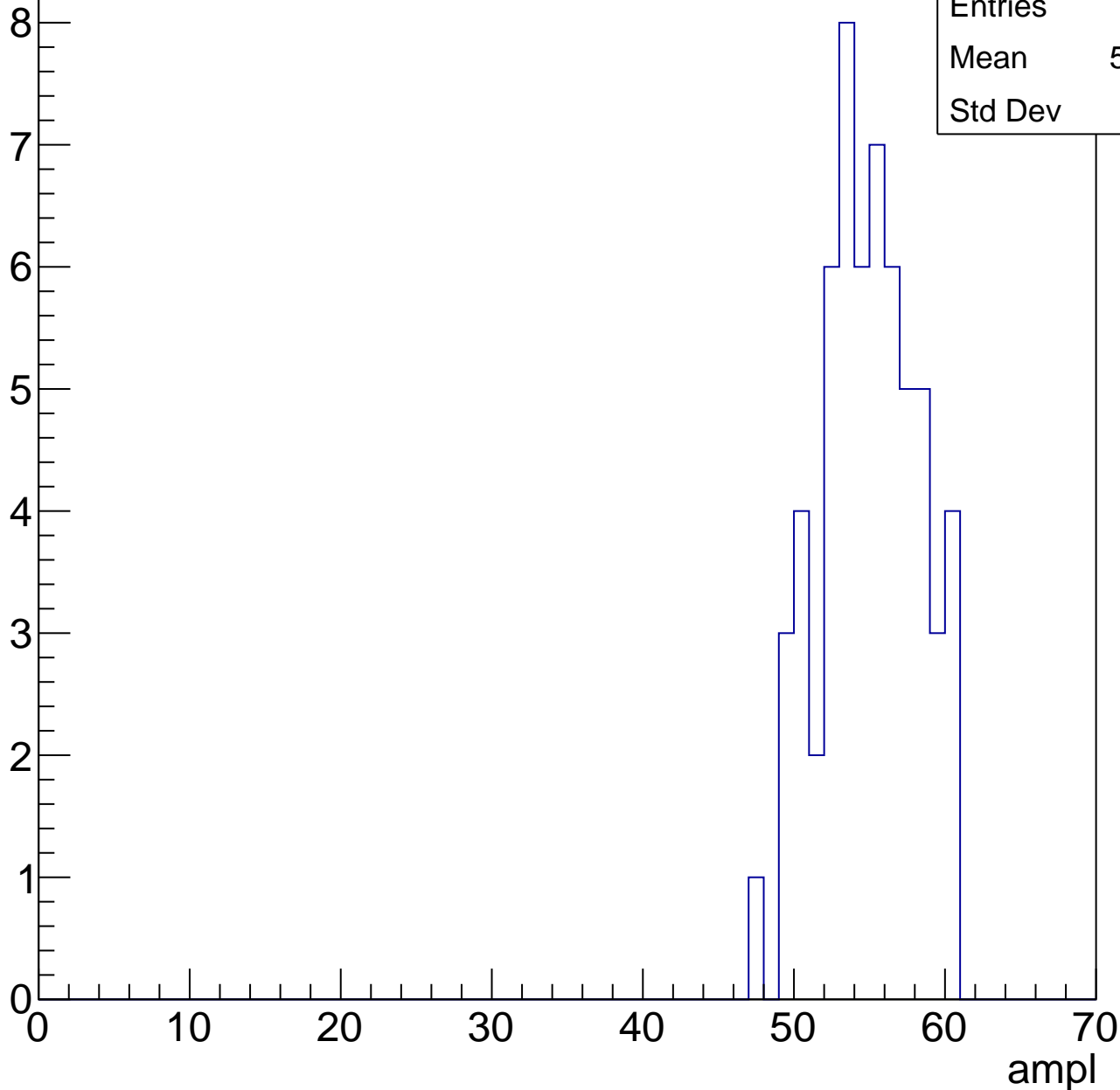


# B0L102S, U17-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.48
Std Dev	3.16

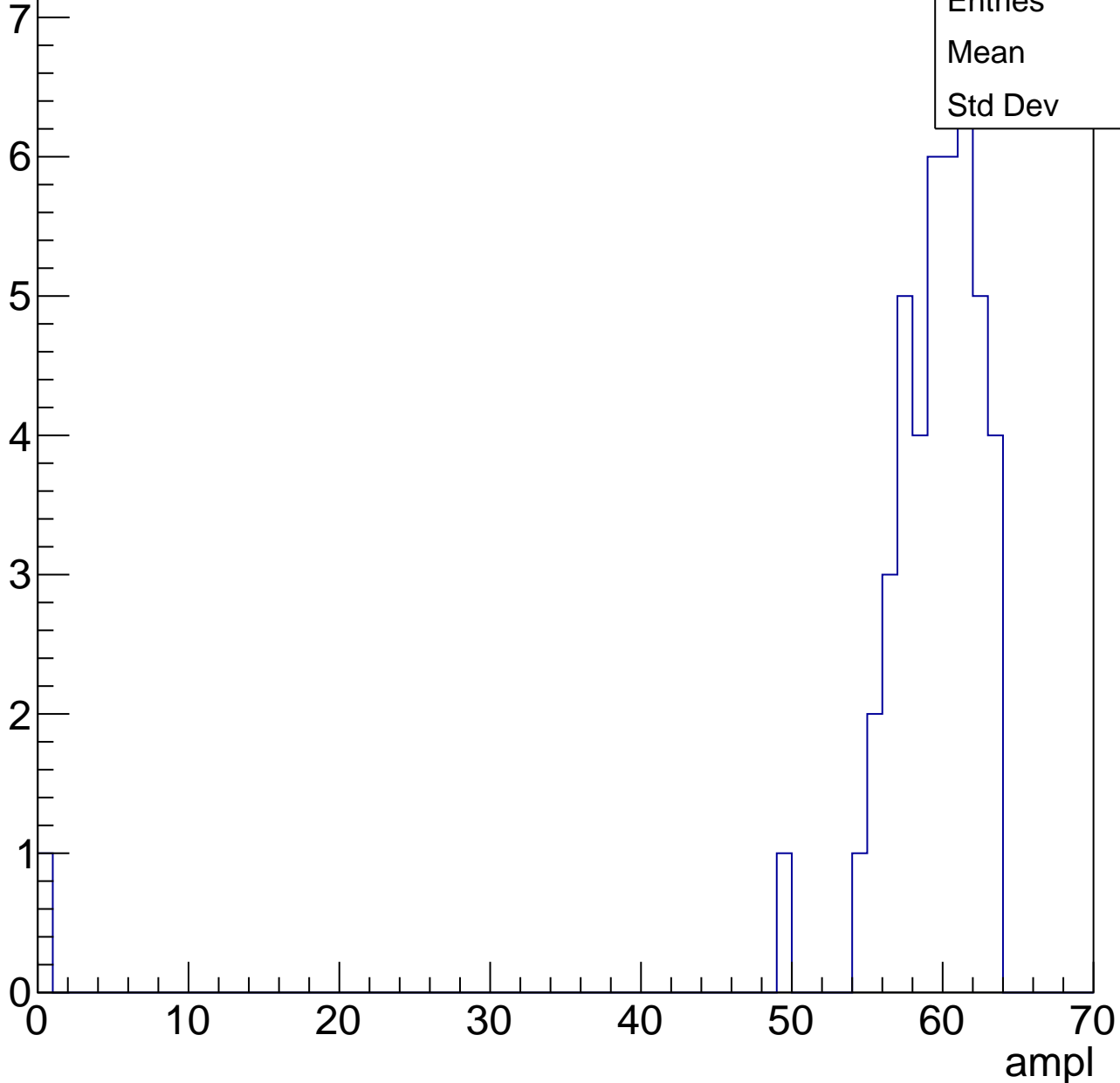


# B0L102S, U17-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	57.8
Std Dev	9.15

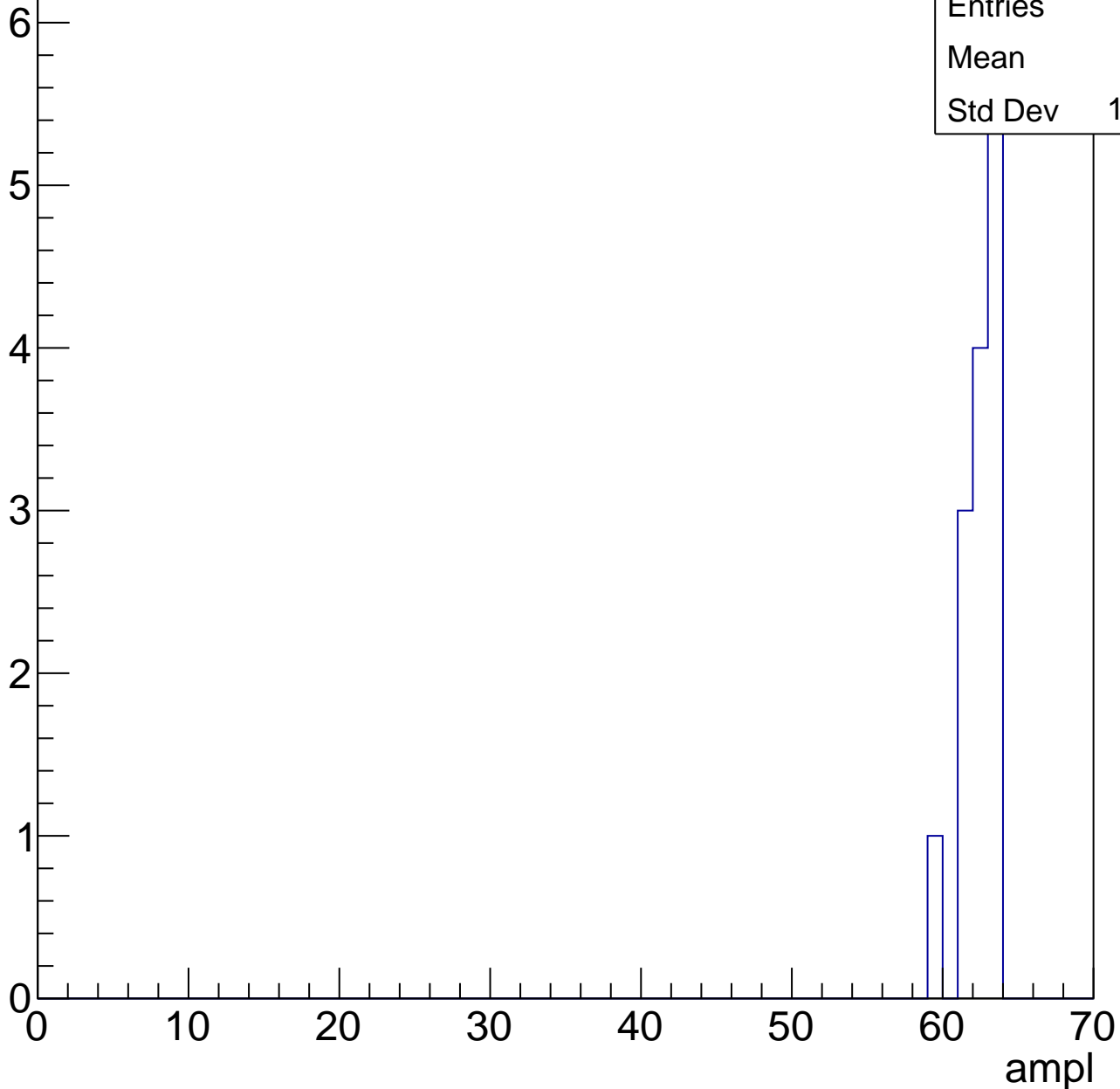


# B0L102S, U17-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	62
Std Dev	1.134





# B0L102S, U17-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	76
Mean	28.8
Std Dev	5.717

**Gaus mean : 30.2797**

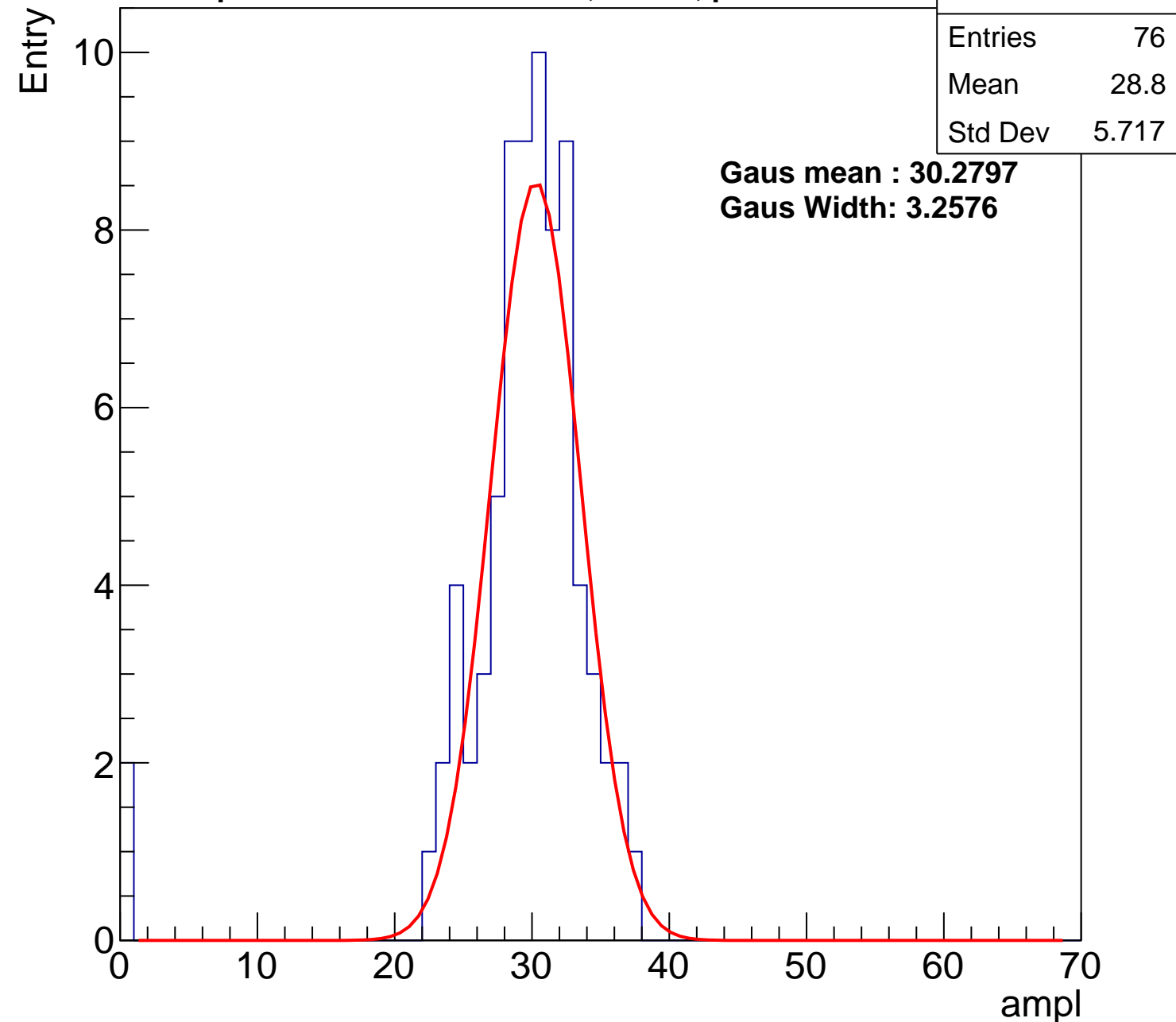
**Gaus Width: 3.2576**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch13, adc1

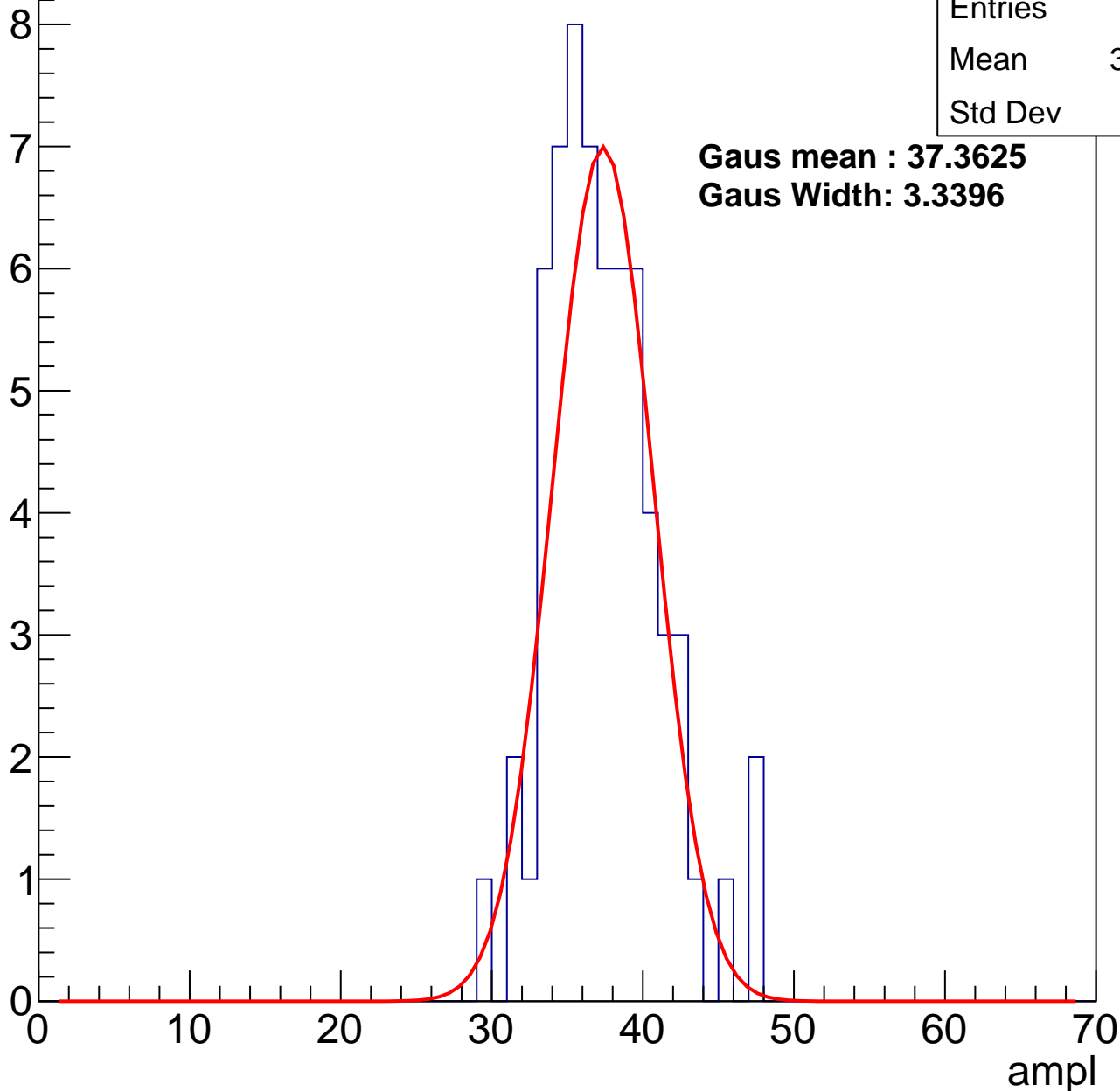
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	36.97
Std Dev	3.64

**Gaus mean : 37.3625**

**Gaus Width: 3.3396**

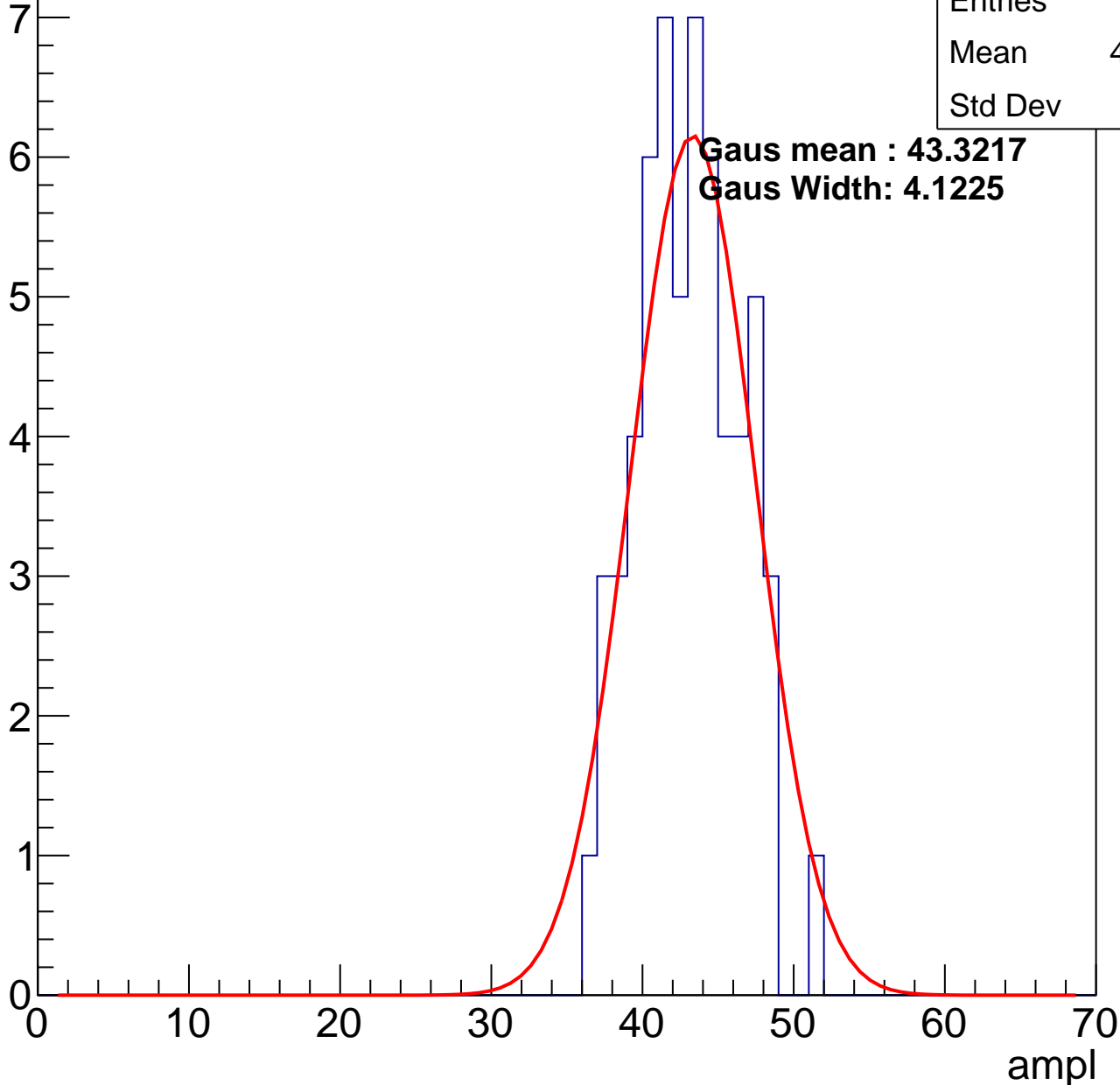


# B0L102S, U17-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.59
Std Dev	3.32

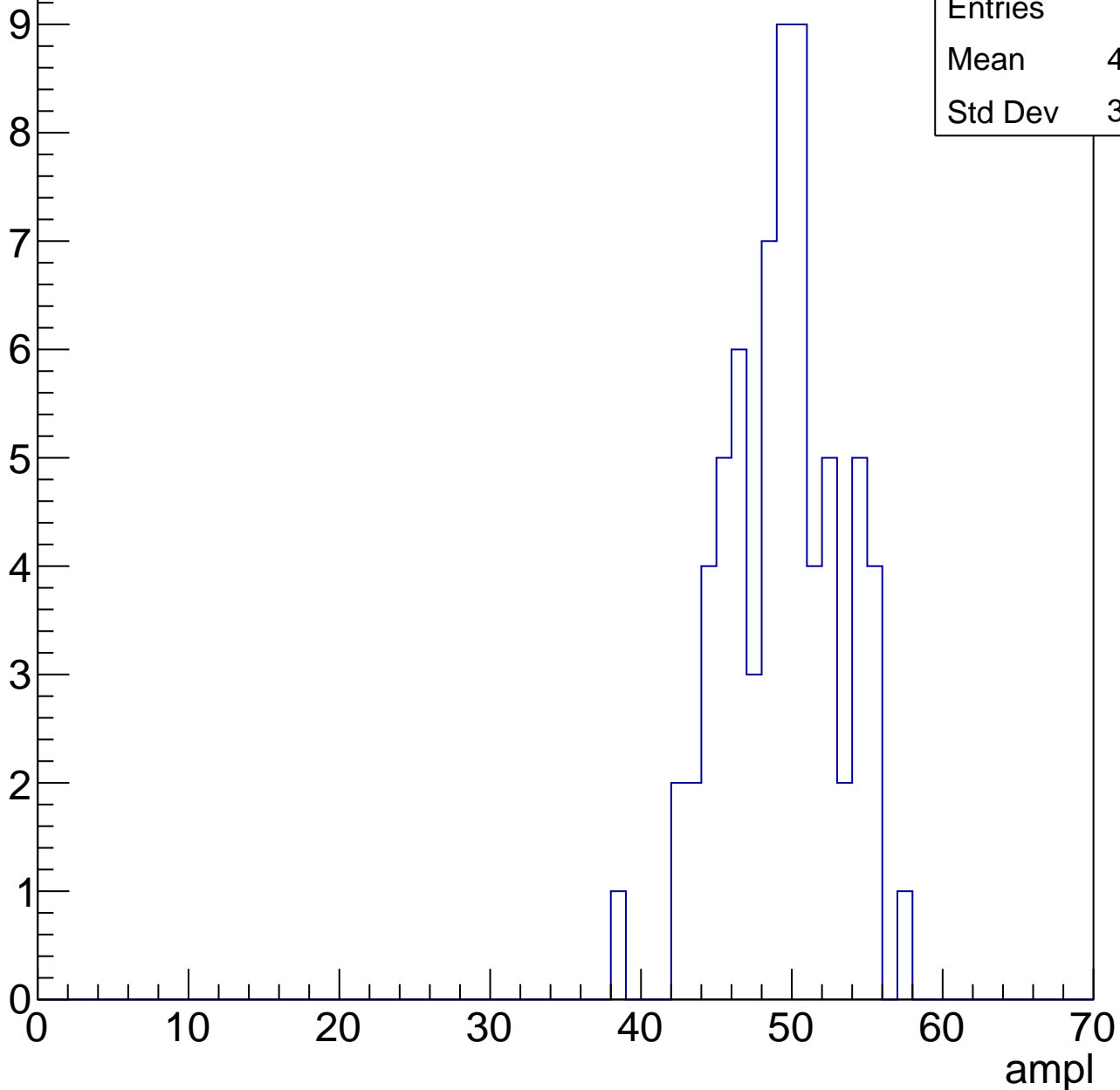


# B0L102S, U17-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	48.84
Std Dev	3.763

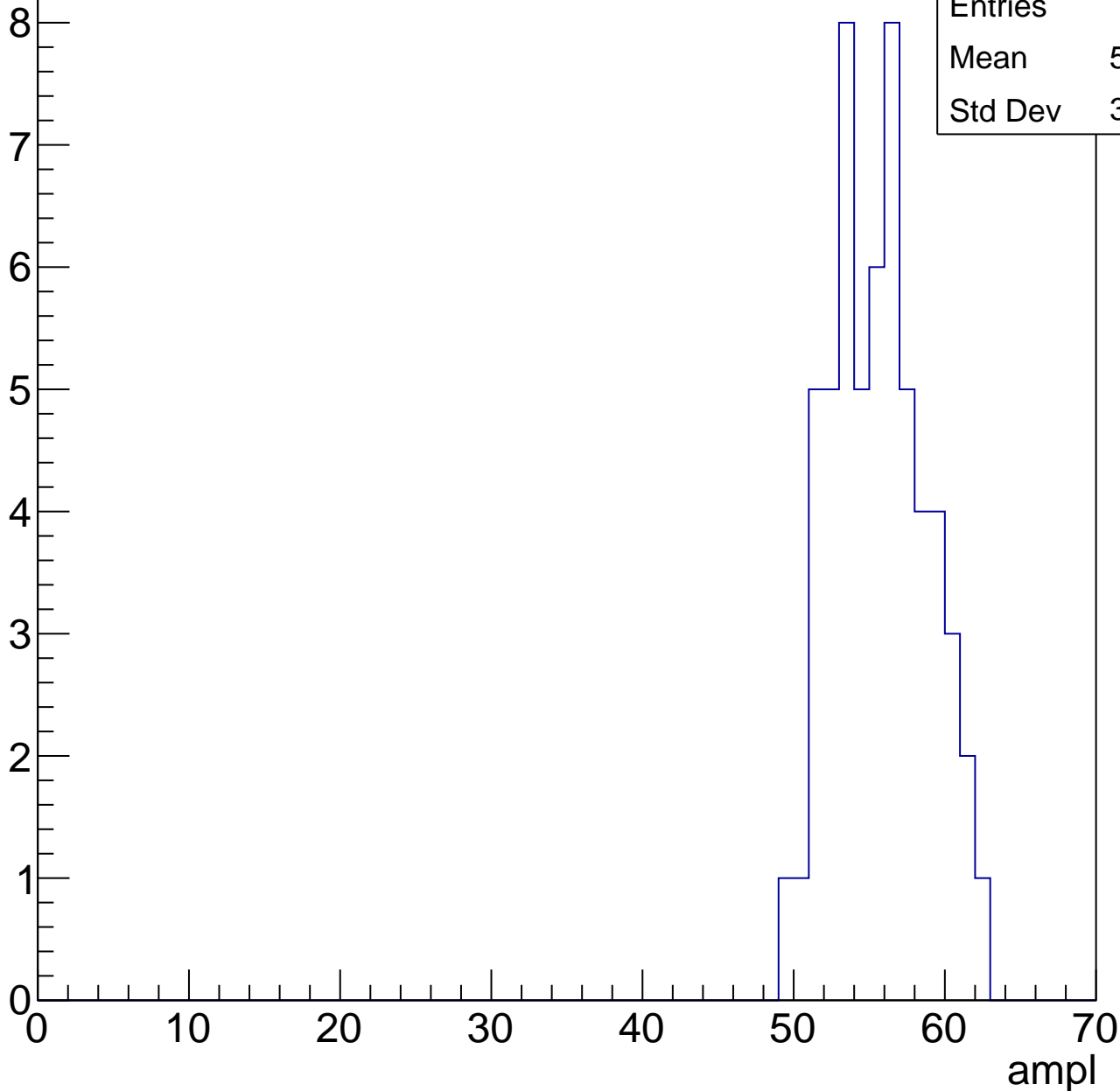


# B0L102S, U17-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	55.22
Std Dev	3.063

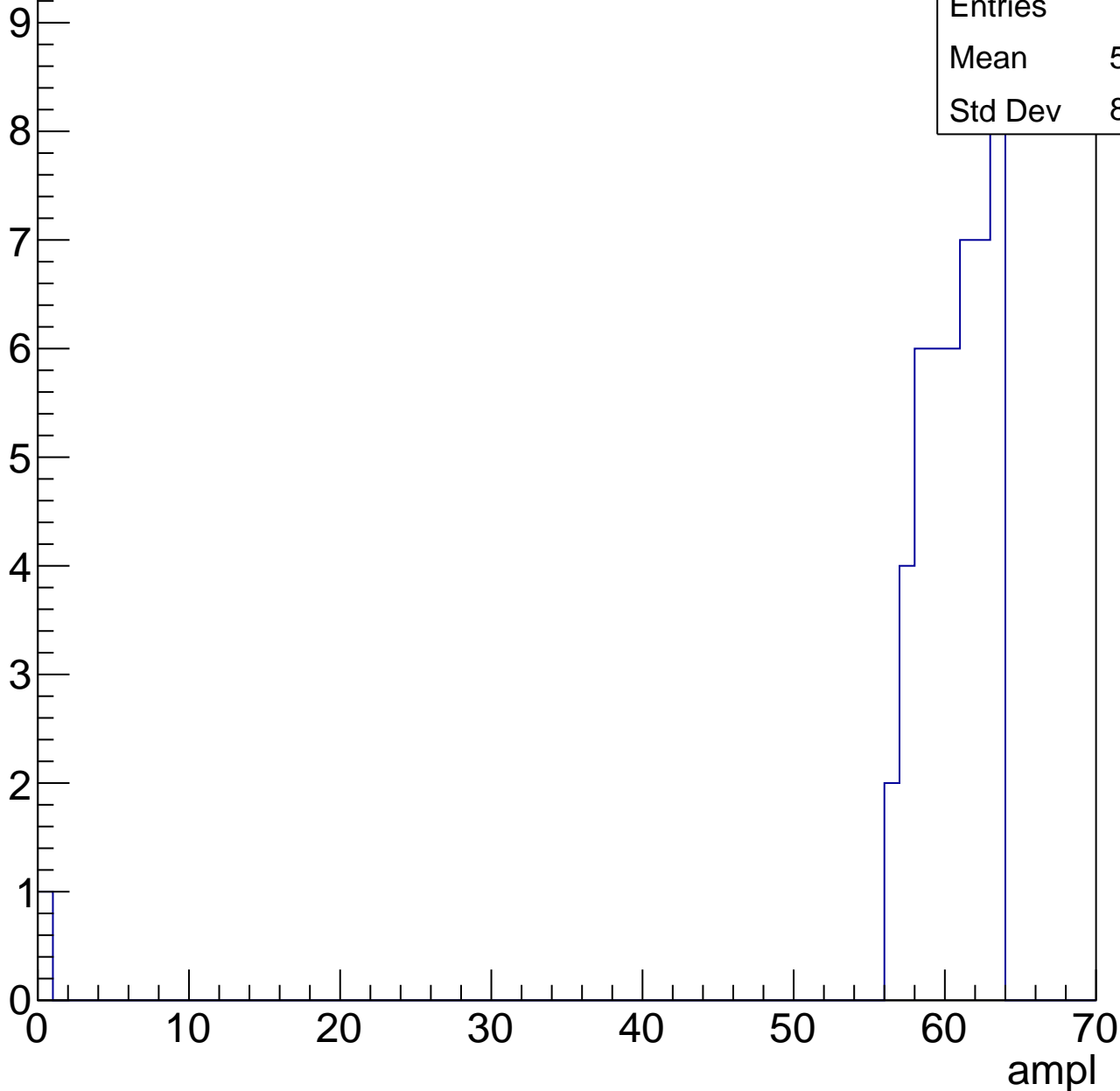


# B0L102S, U17-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	58.96
Std Dev	8.853



# B0L102S, U17-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

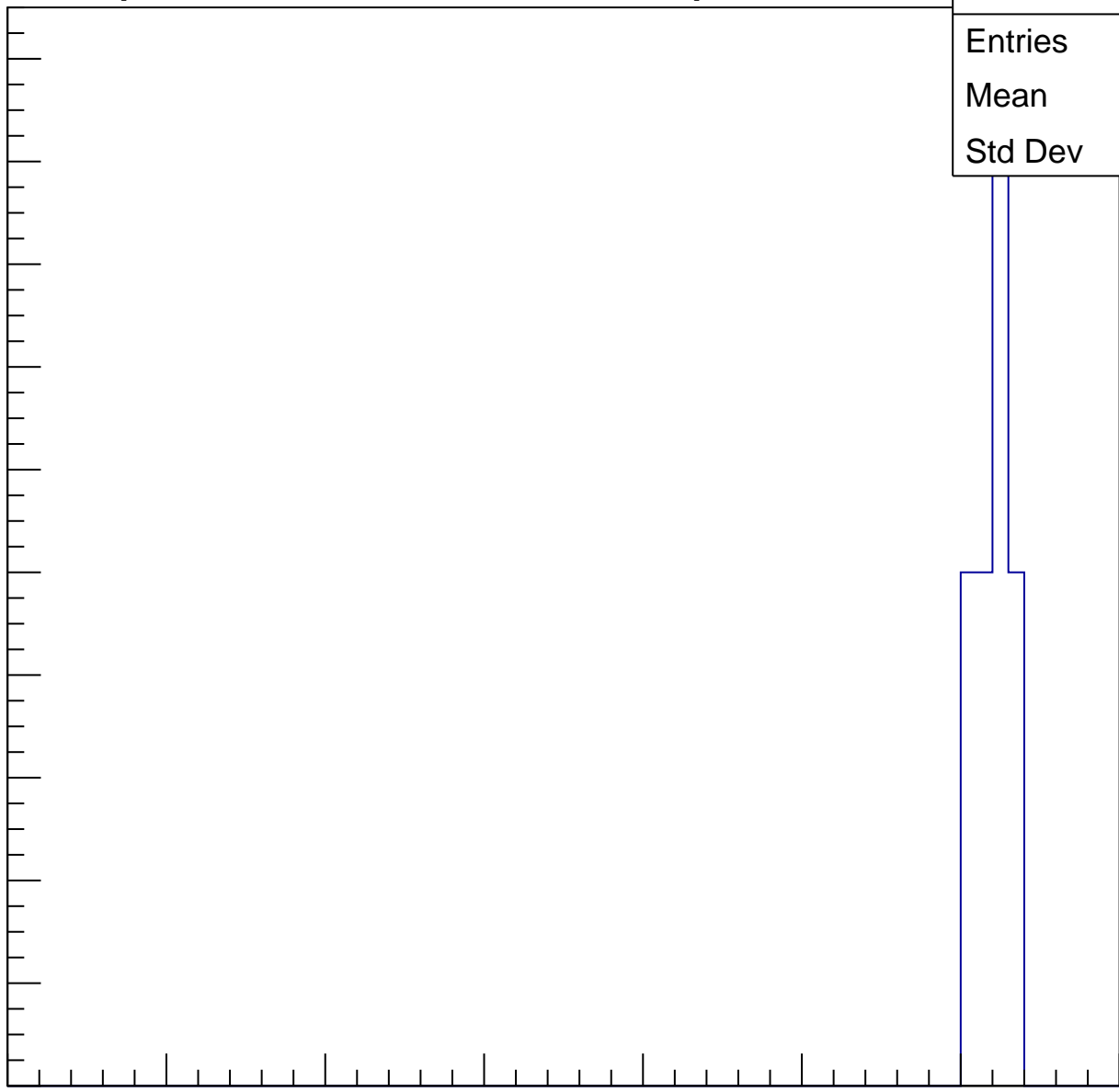
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.6
Std Dev	1.02

ampl

0 10 20 30 40 50 60 70





# B0L102S, U17-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	30.44
Std Dev	3.427

**Gaus mean : 31.5763**

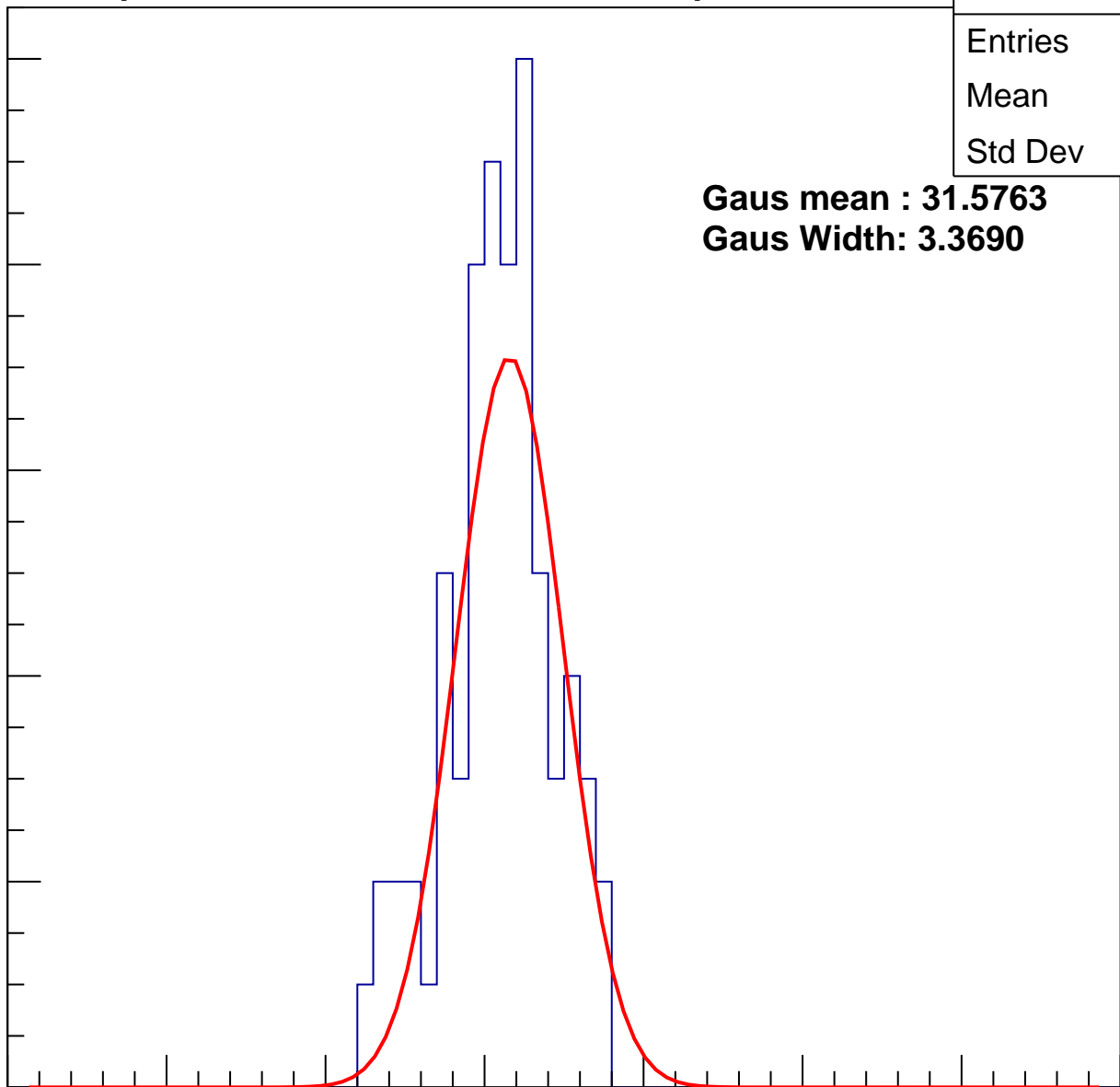
**Gaus Width: 3.3690**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch14, adc1

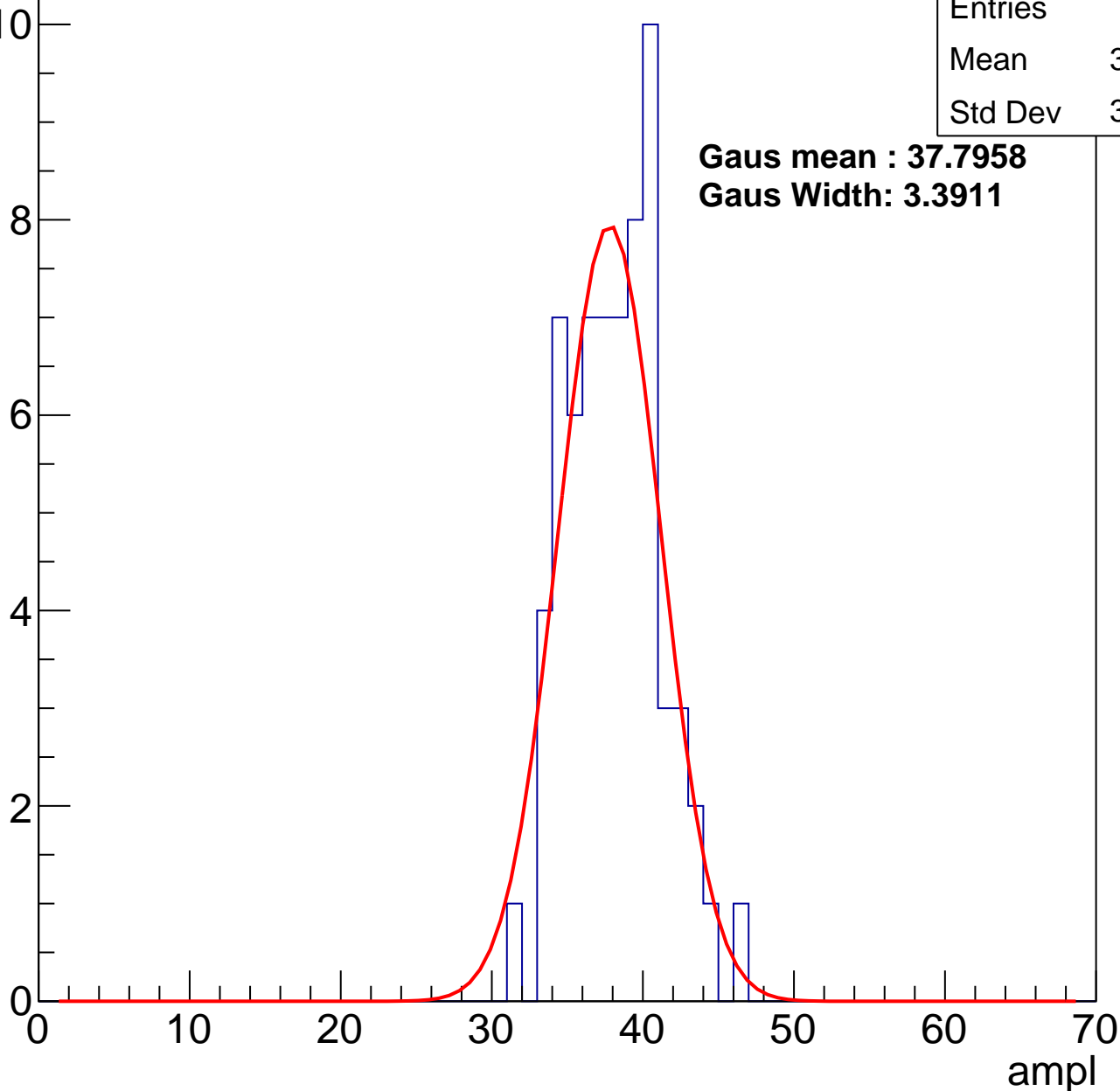
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	37.69
Std Dev	3.033

**Gaus mean : 37.7958**

**Gaus Width: 3.3911**



# B0L102S, U17-ch14, adc2

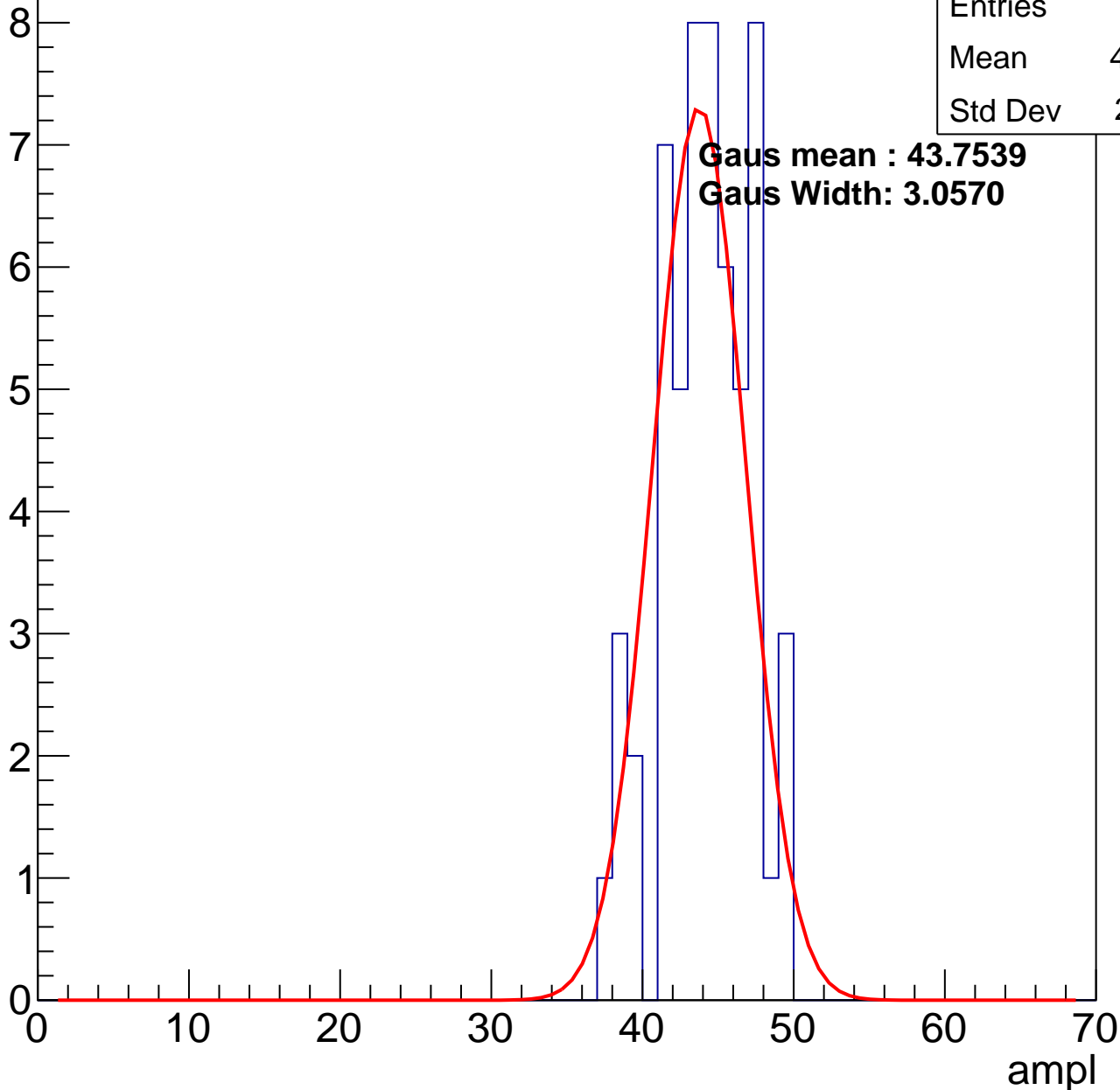
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	43.74
Std Dev	2.911

**Gaus mean : 43.7539**

**Gaus Width: 3.0570**

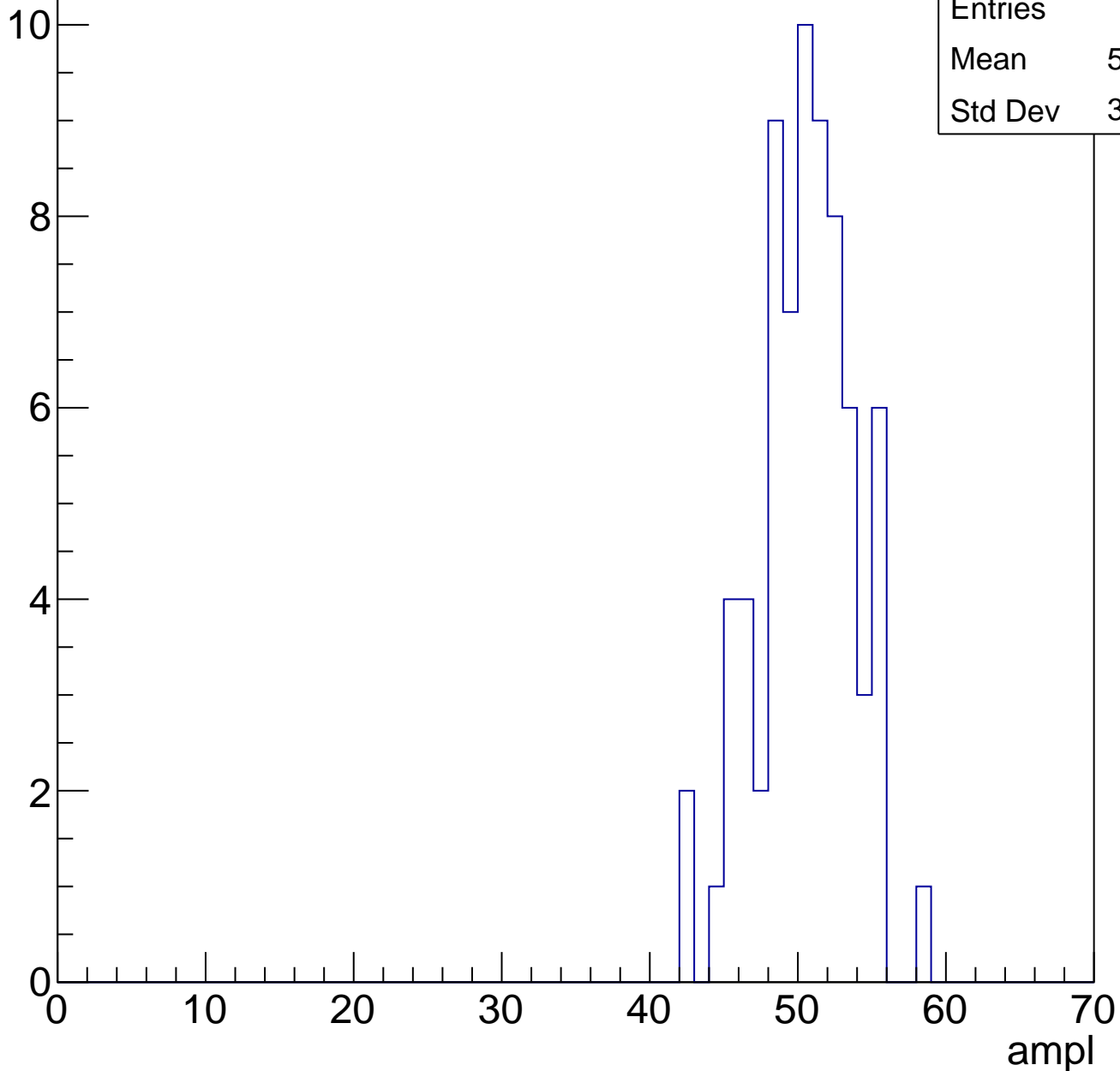


# B0L102S, U17-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	72
Mean	50.06
Std Dev	3.218

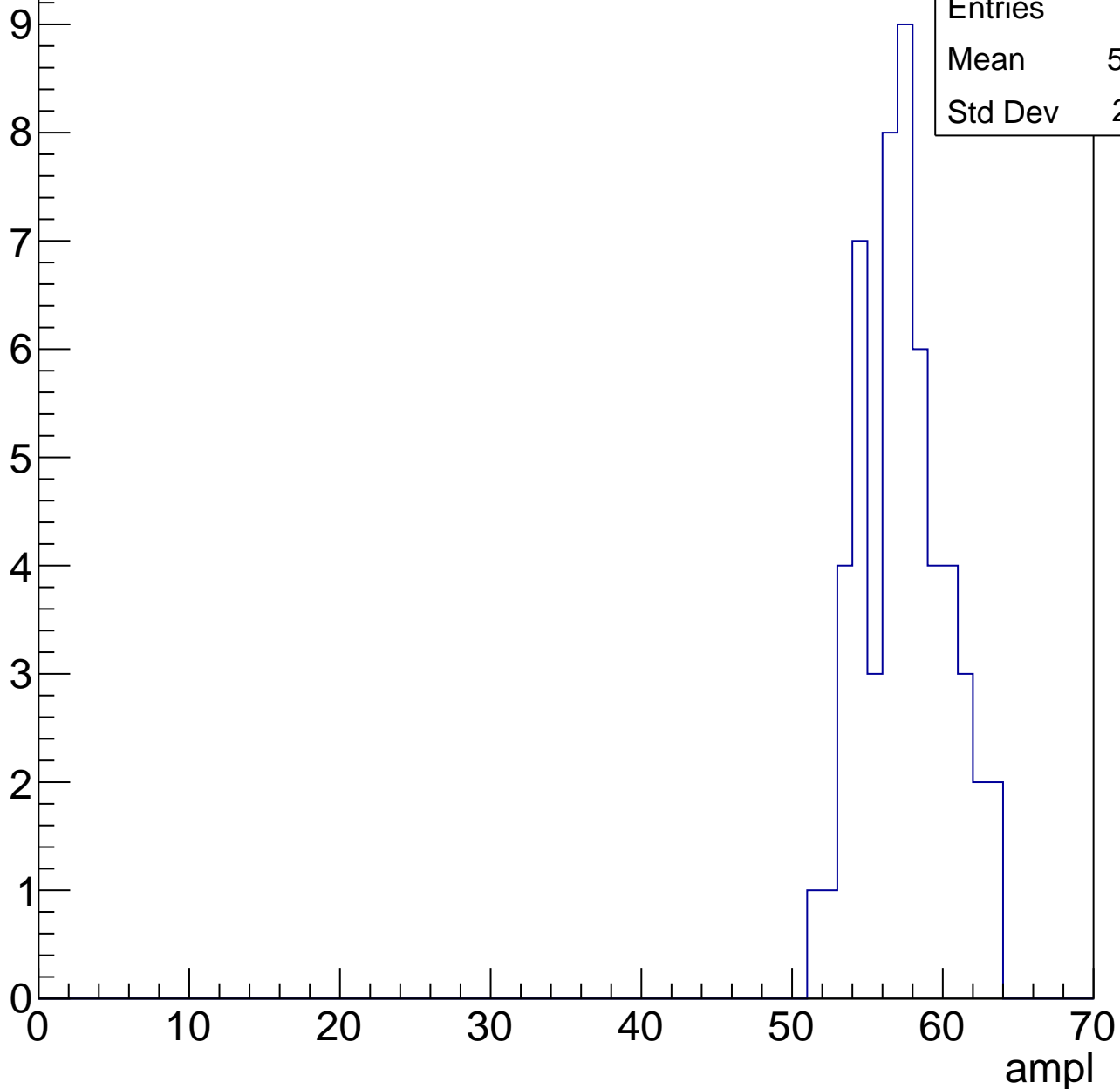
Entry



# B0L102S, U17-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

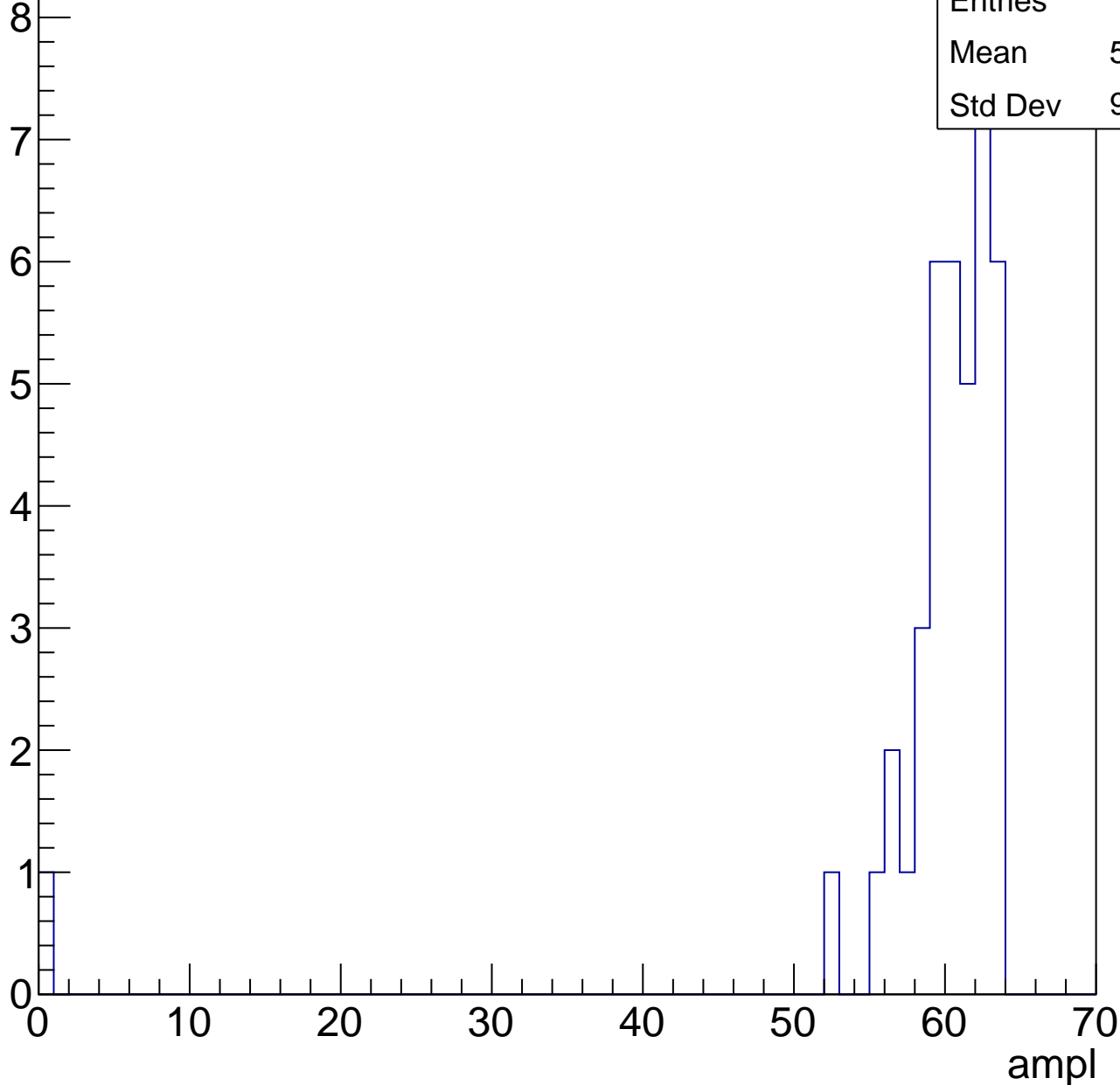


# B0L102S, U17-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	58.58
Std Dev	9.692



# B0L102S, U17-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

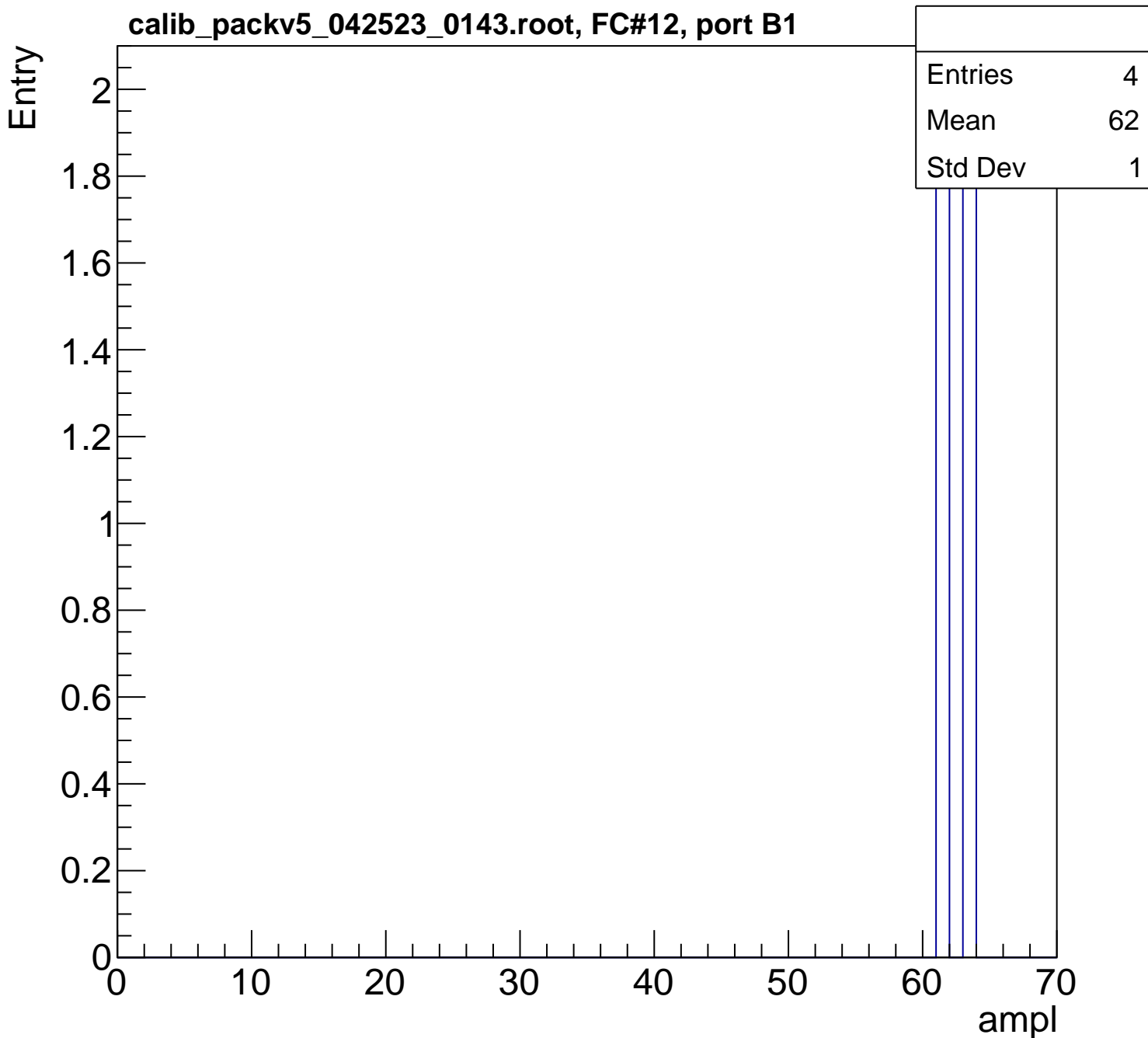
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl





# B0L102S, U17-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch15, adc0

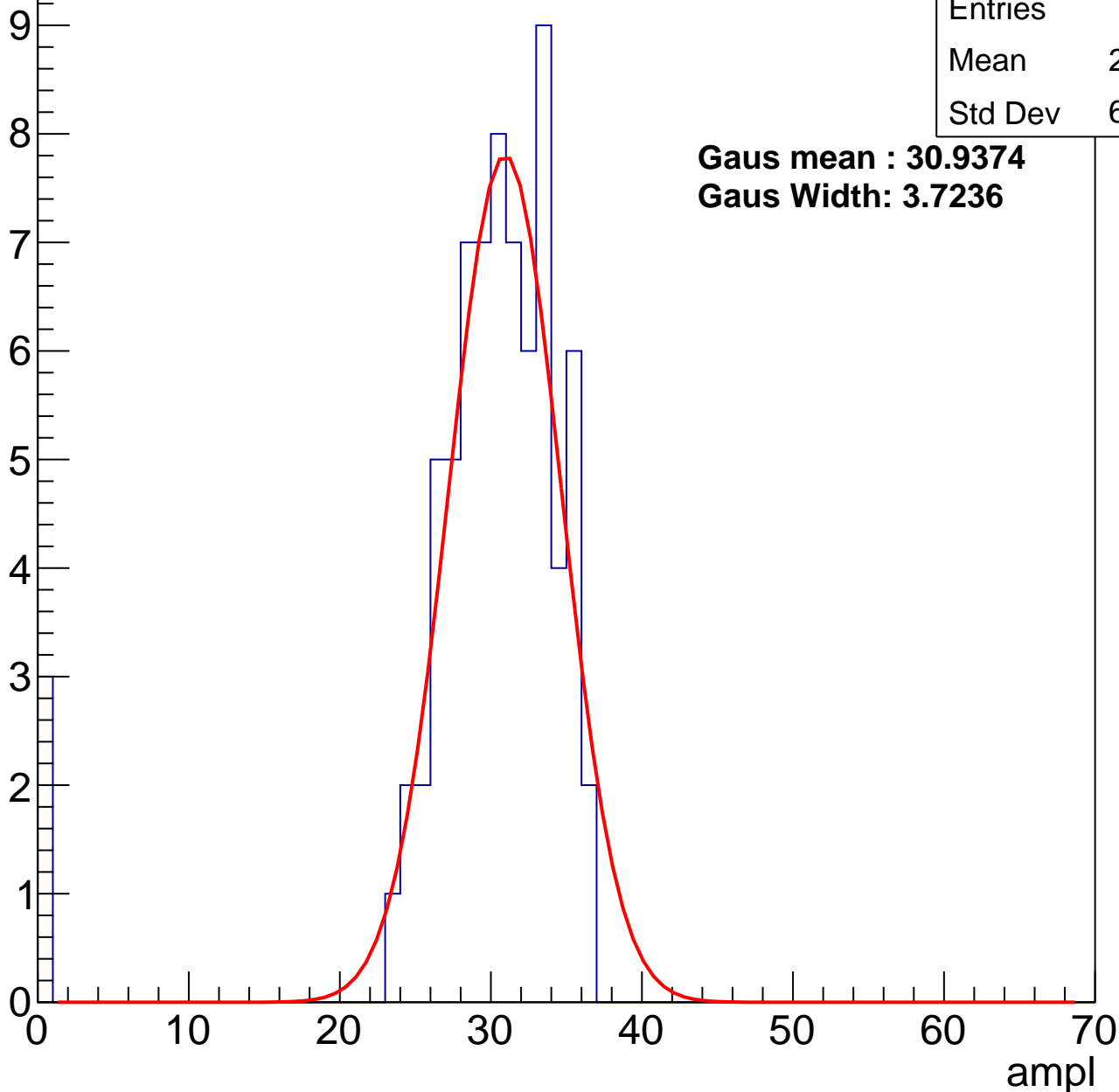
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	29.04
Std Dev	6.739

**Gaus mean : 30.9374**

**Gaus Width: 3.7236**



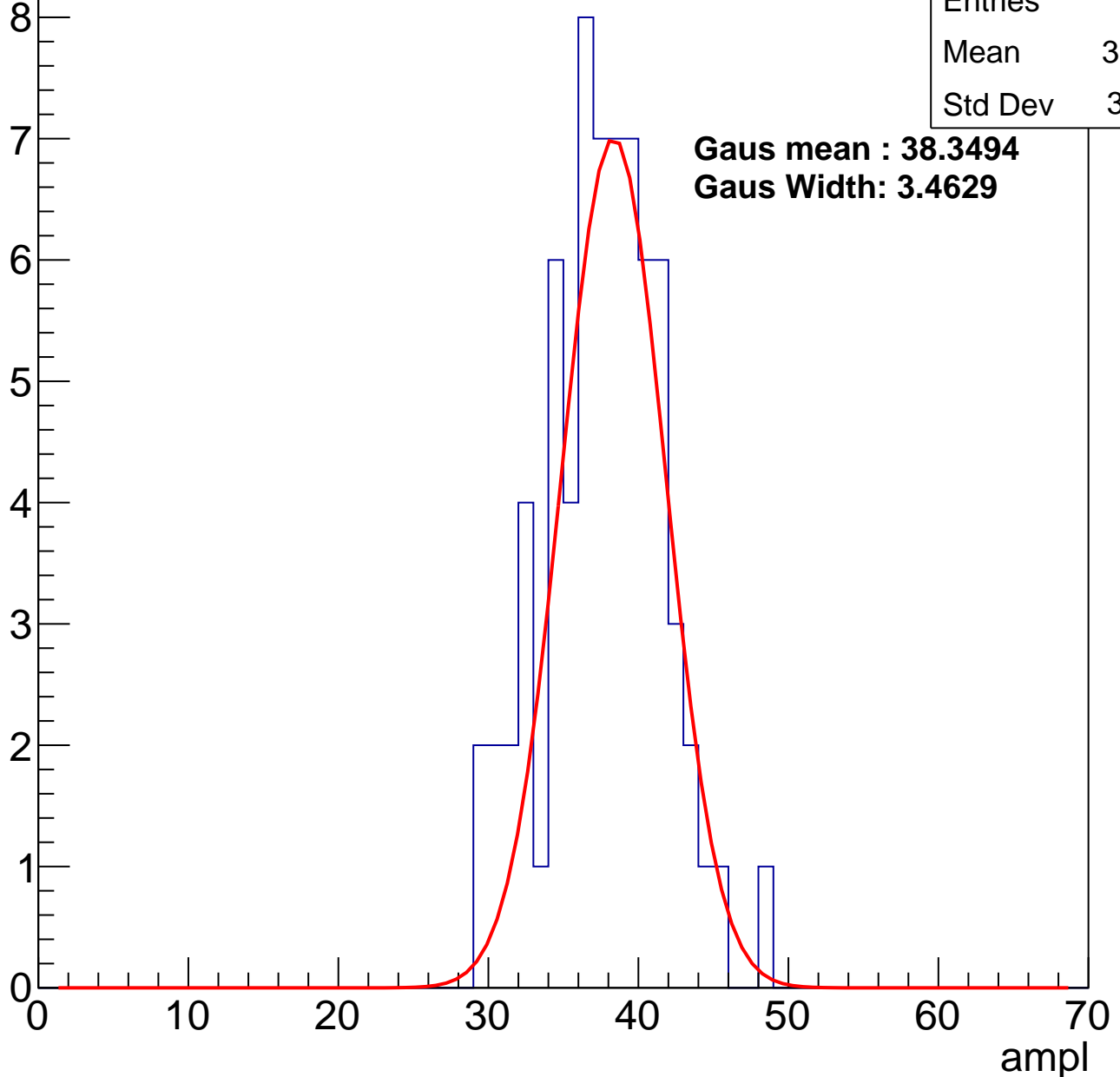
# B0L102S, U17-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	37.23
Std Dev	3.881

**Gaus mean : 38.3494**  
**Gaus Width: 3.4629**



# B0L102S, U17-ch15, adc2

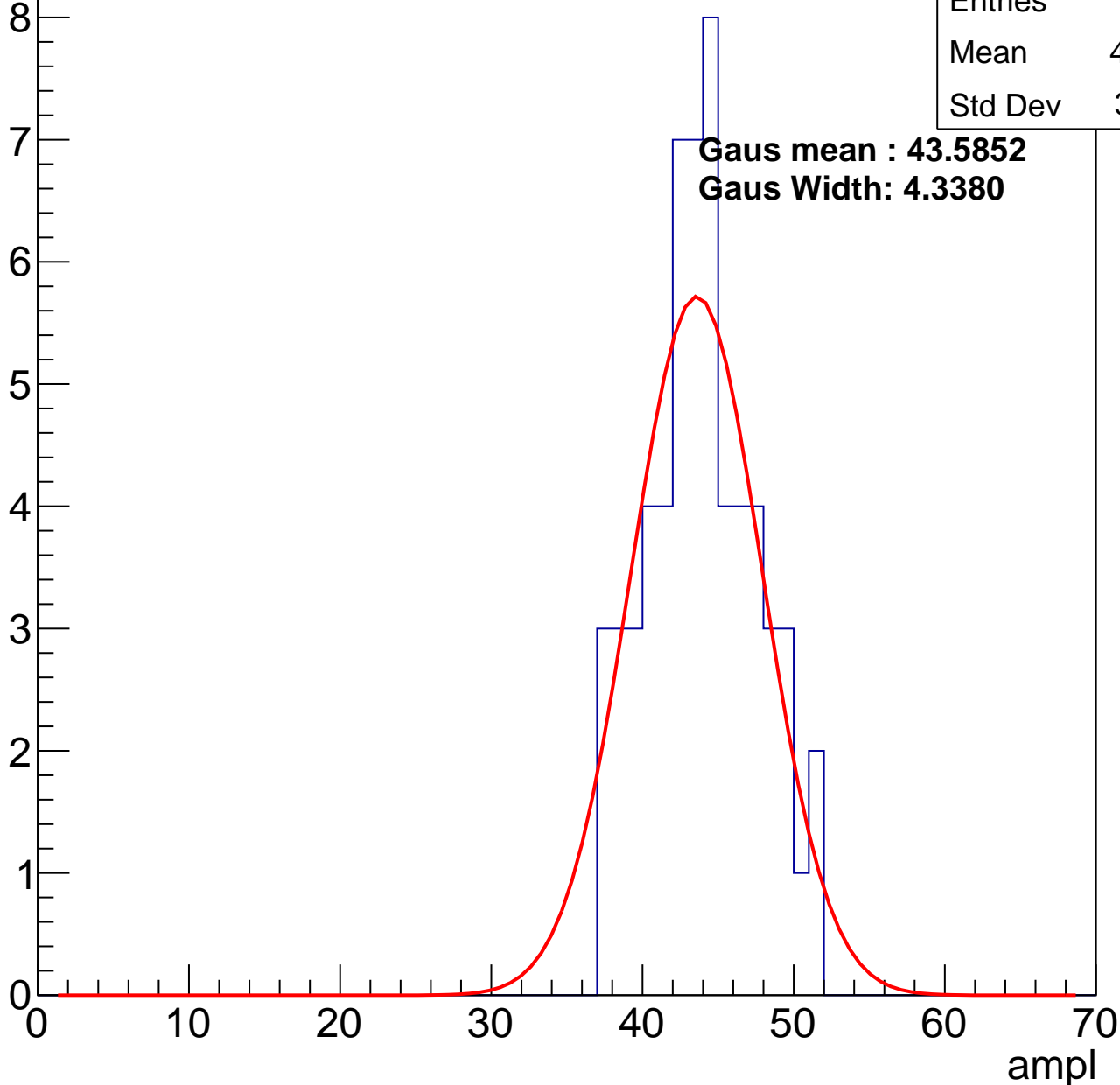
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	43.47
Std Dev	3.561

**Gaus mean : 43.5852**

**Gaus Width: 4.3380**

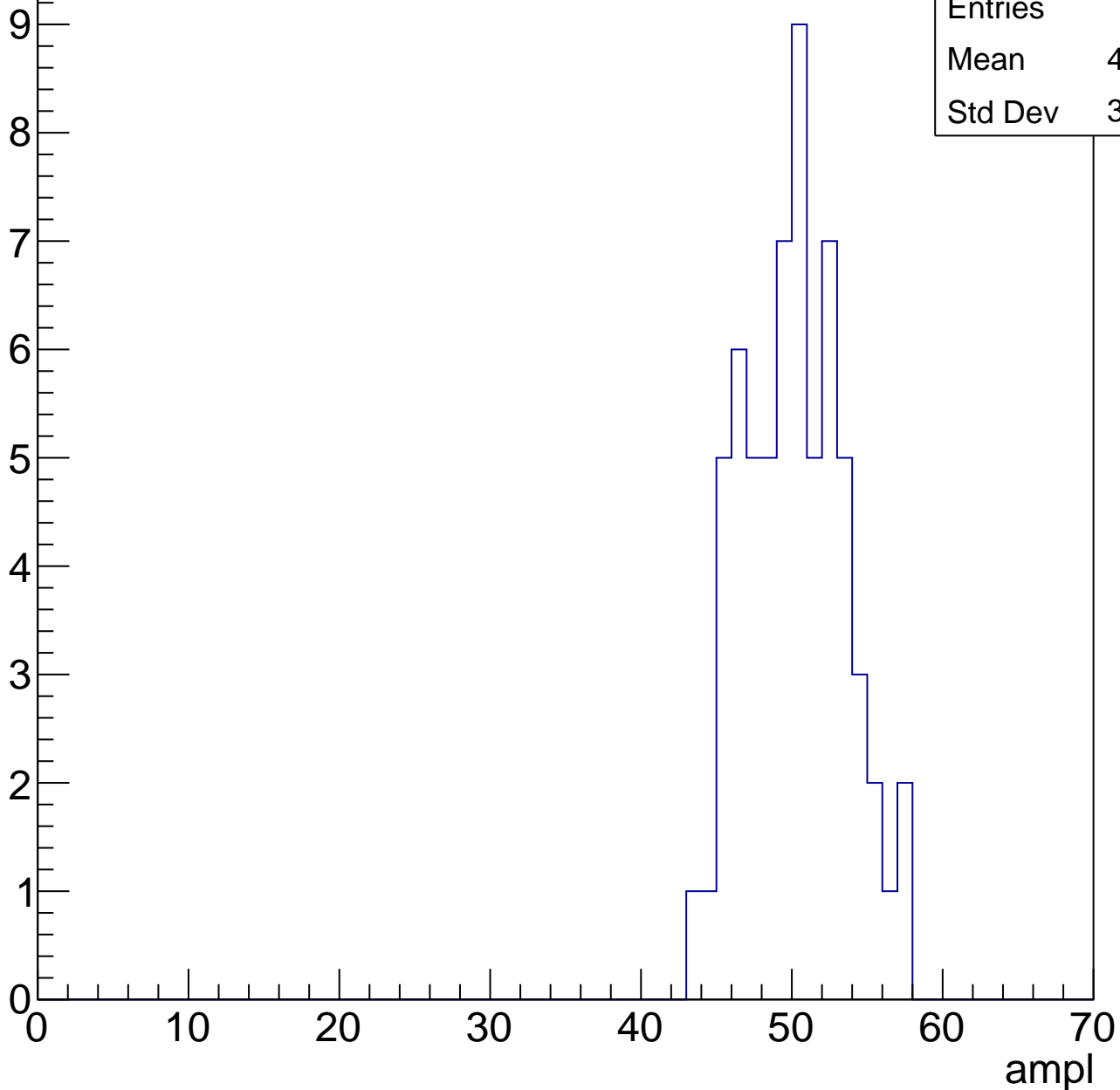


# B0L102S, U17-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	49.72
Std Dev	3.267

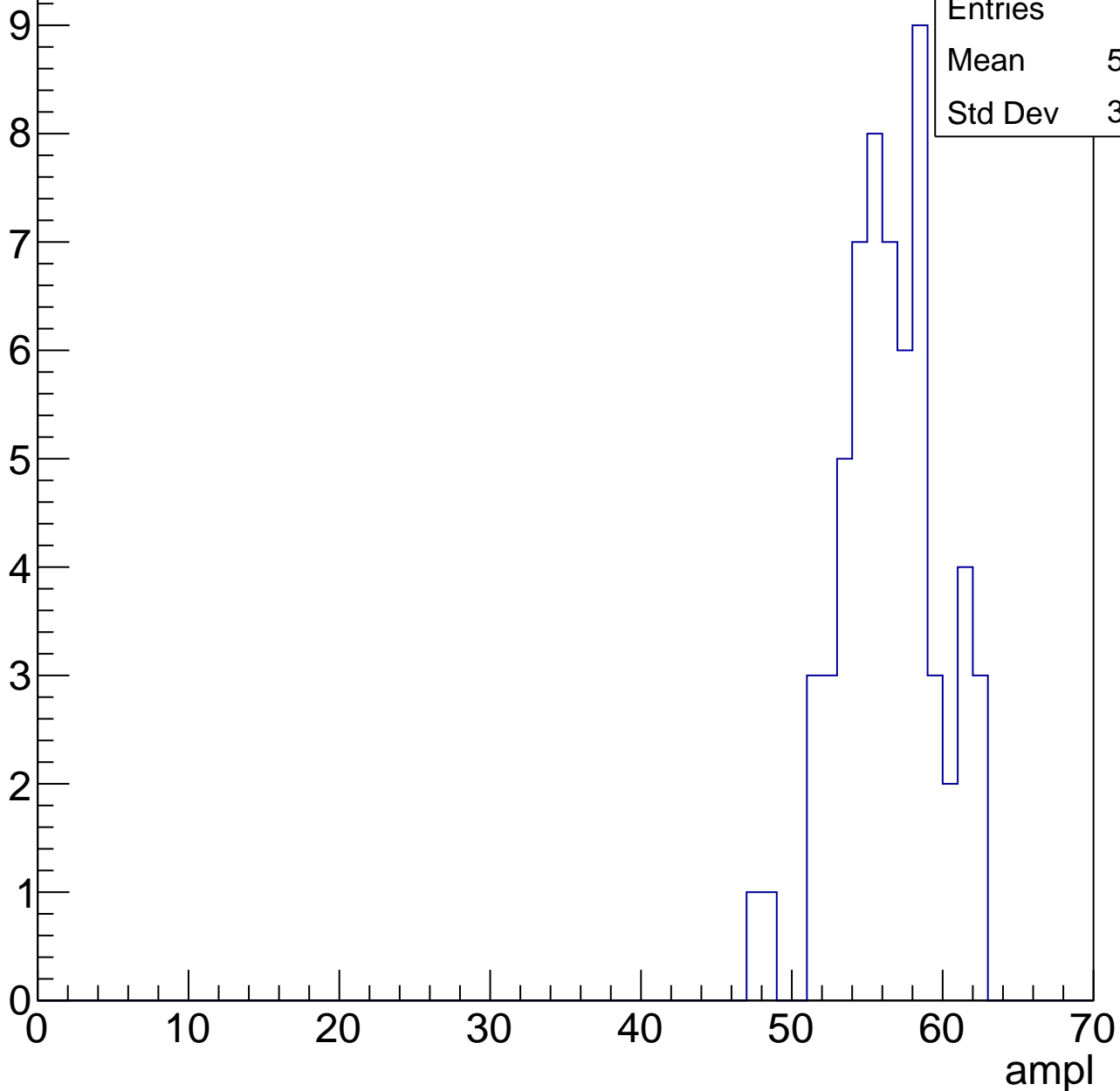


# B0L102S, U17-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	55.97
Std Dev	3.258

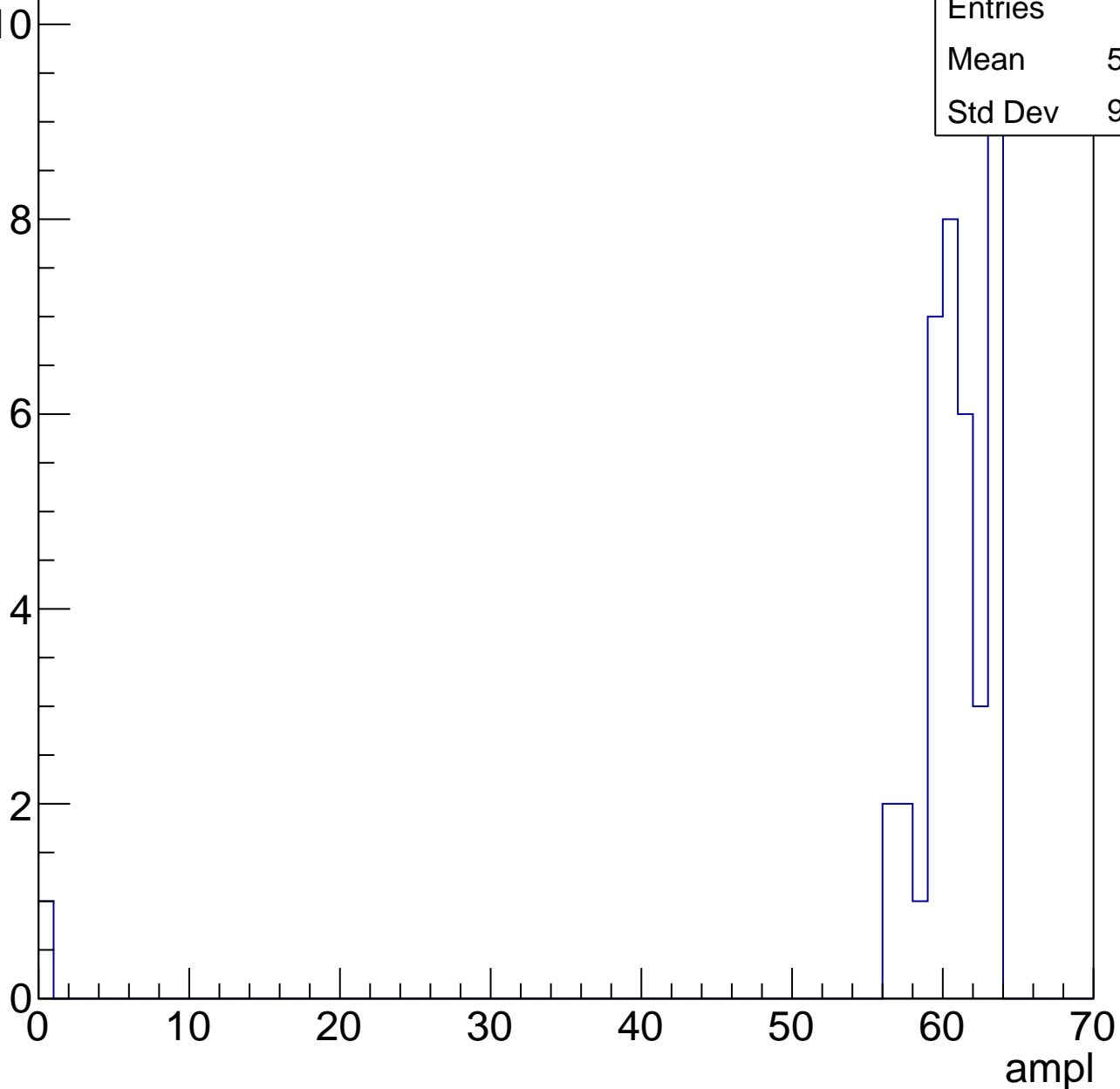


# B0L102S, U17-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

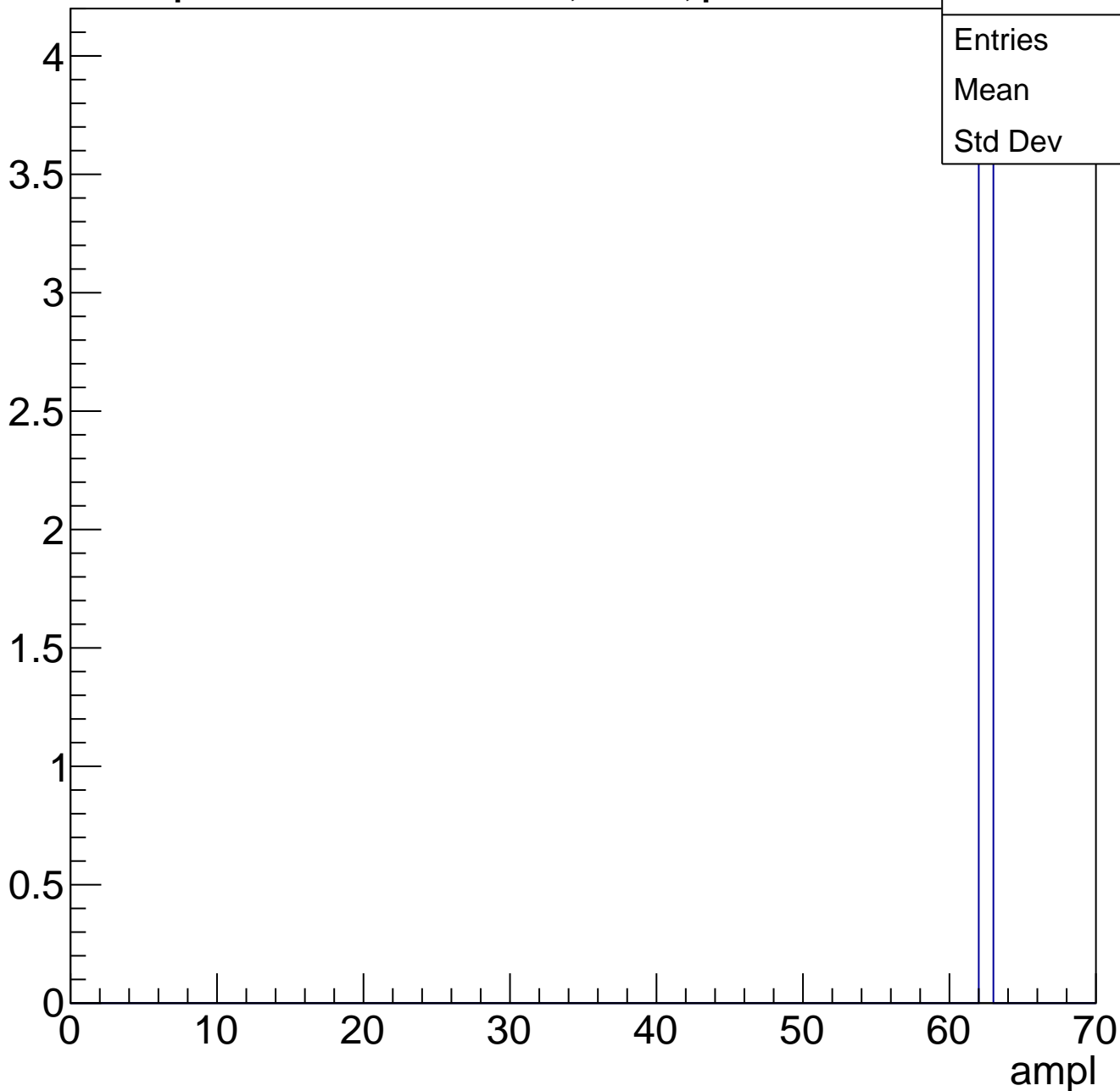
Entries	40
Mean	58.98
Std Dev	9.653



# B0L102S, U17-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L102S, U17-ch16, adc0

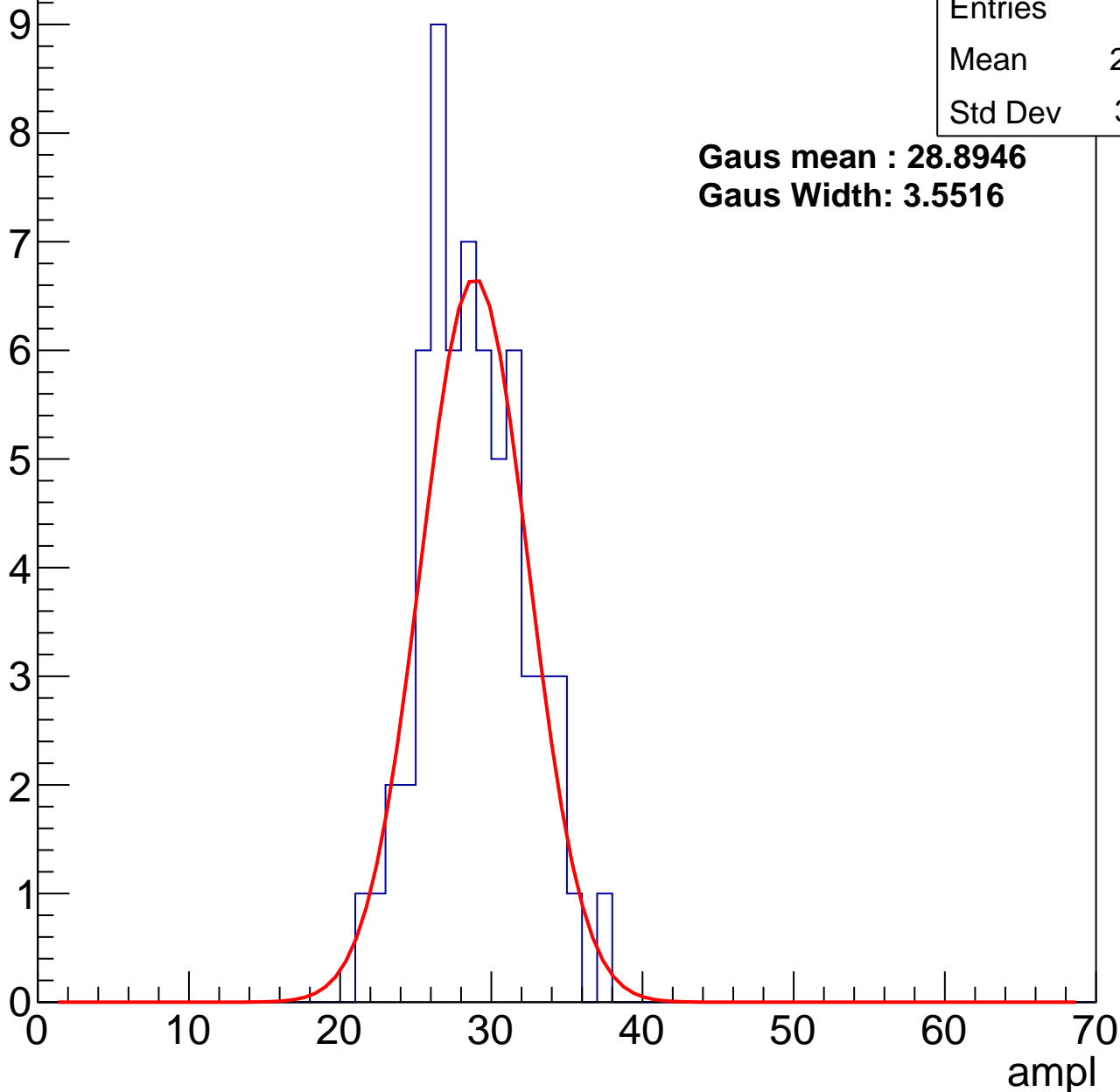
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	28.35
Std Dev	3.351

**Gaus mean : 28.8946**

**Gaus Width: 3.5516**



# B0L102S, U17-ch16, adc1

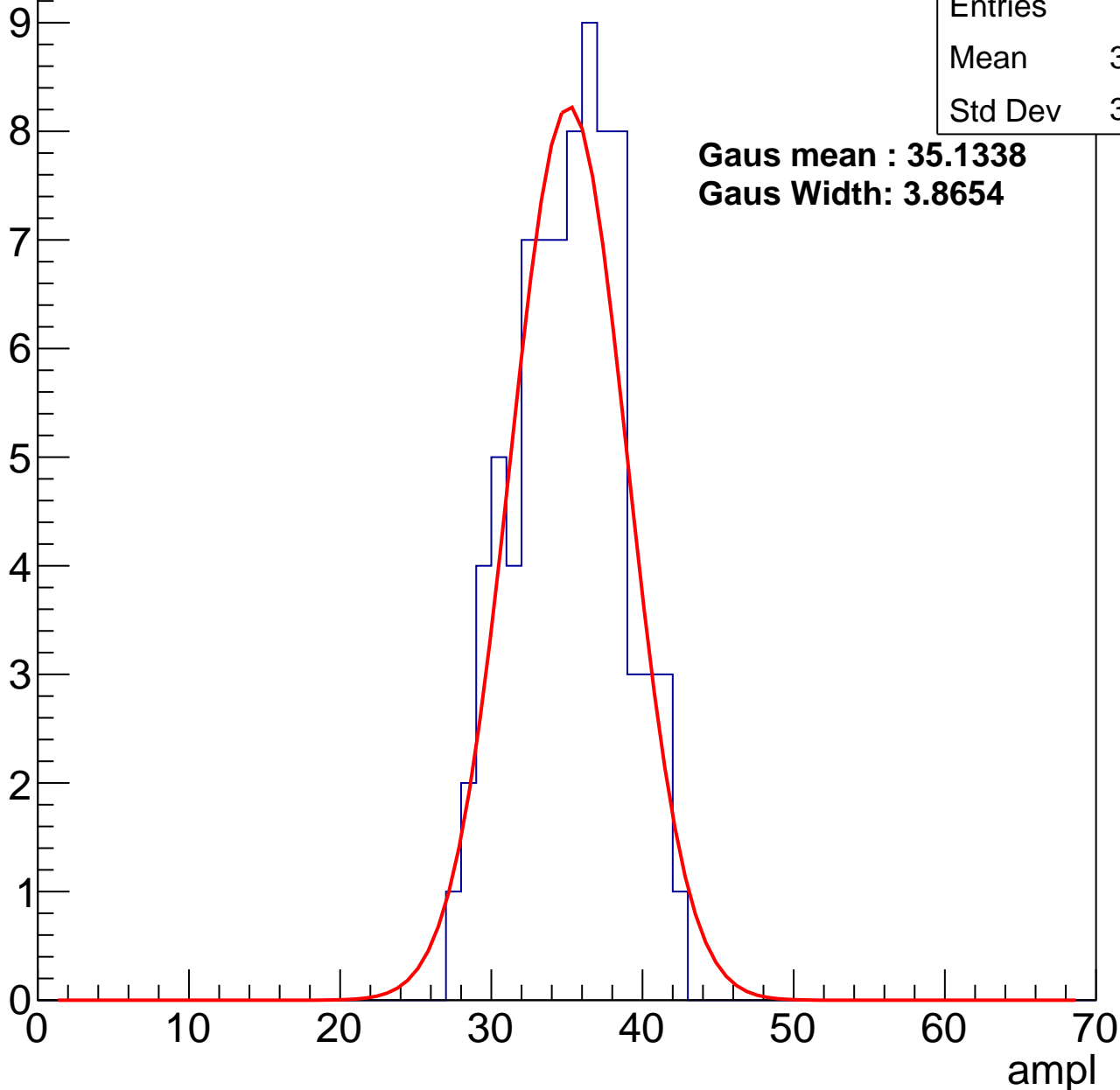
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	34.65
Std Dev	3.493

**Gaus mean : 35.1338**

**Gaus Width: 3.8654**



# B0L102S, U17-ch16, adc2

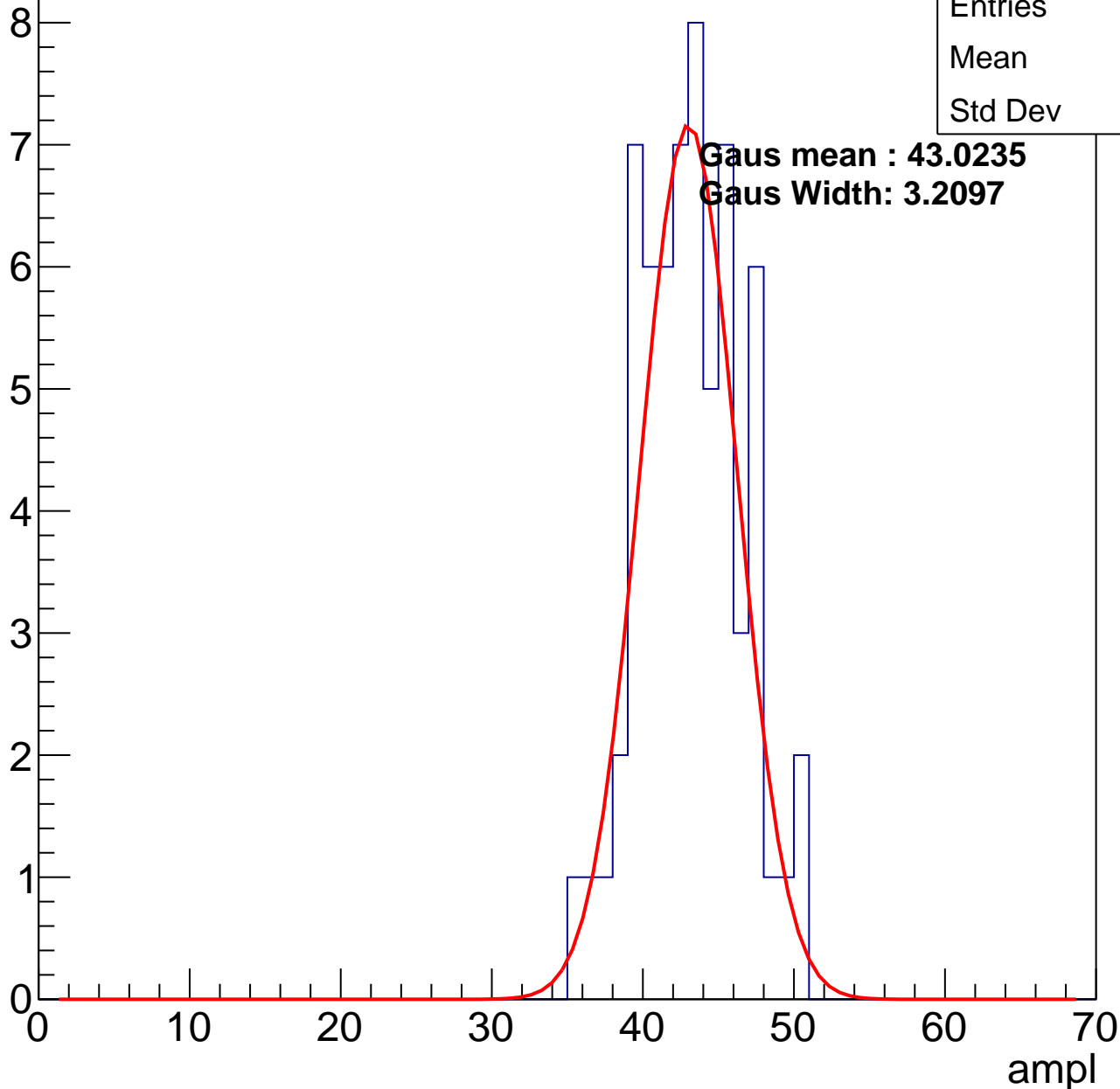
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.7
Std Dev	3.32

**Gaus mean : 43.0235**

**Gaus Width: 3.2097**

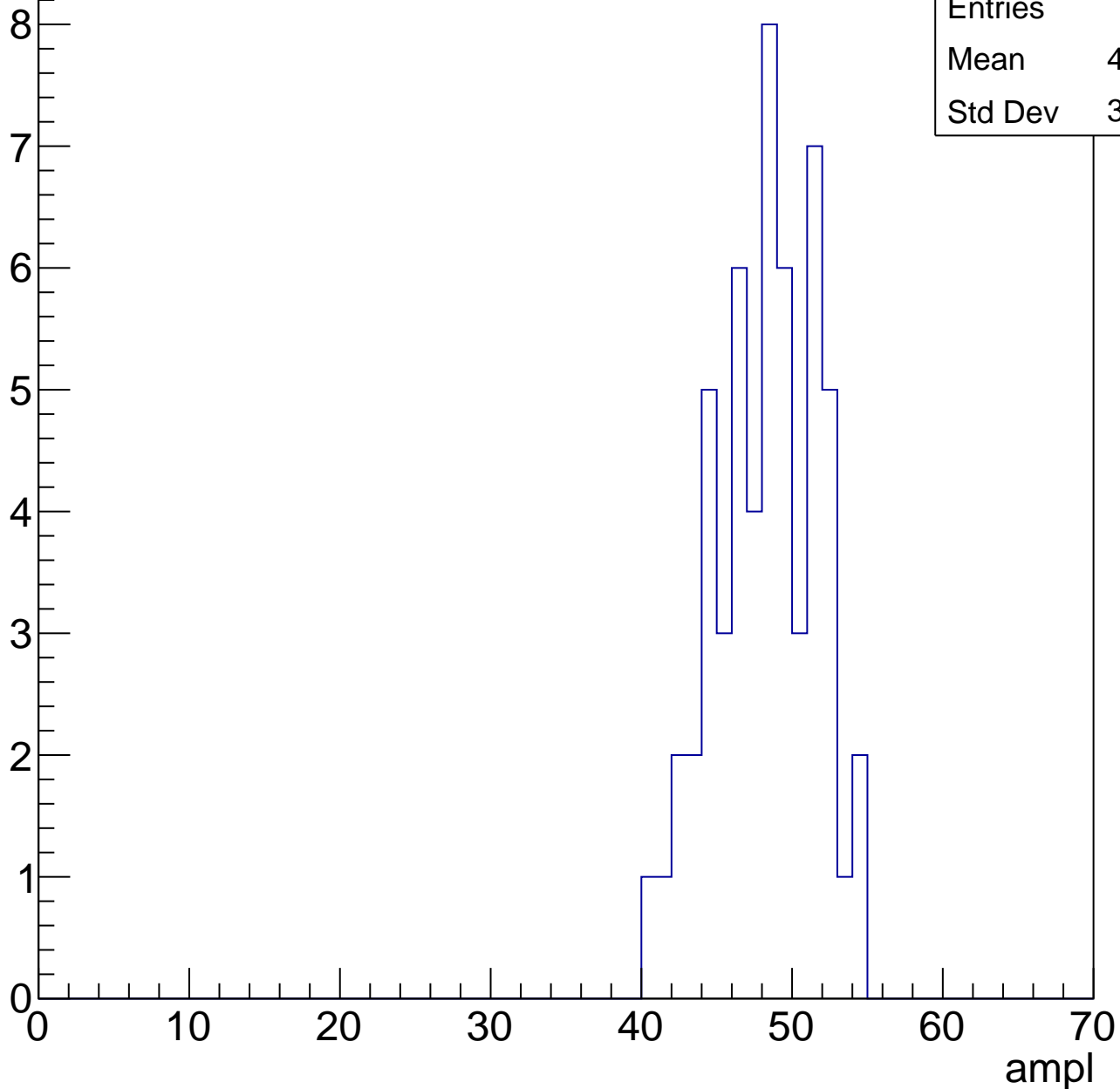


# B0L102S, U17-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	47.79
Std Dev	3.342



# B0L102S, U17-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	85
Mean	55.25
Std Dev	3.633

Entry

10

8

6

4

2

0

0

10

20

30

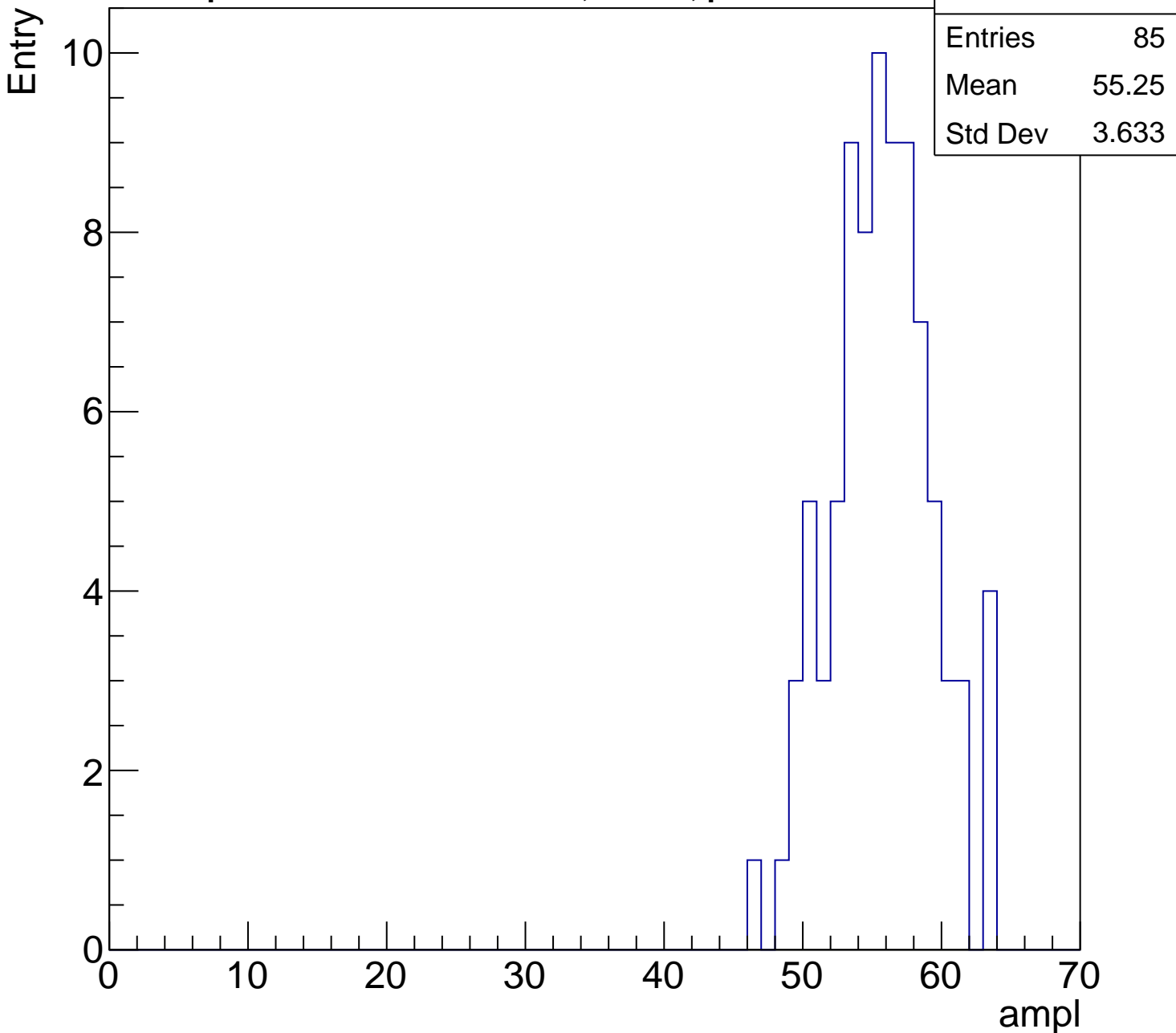
40

50

60

ampl

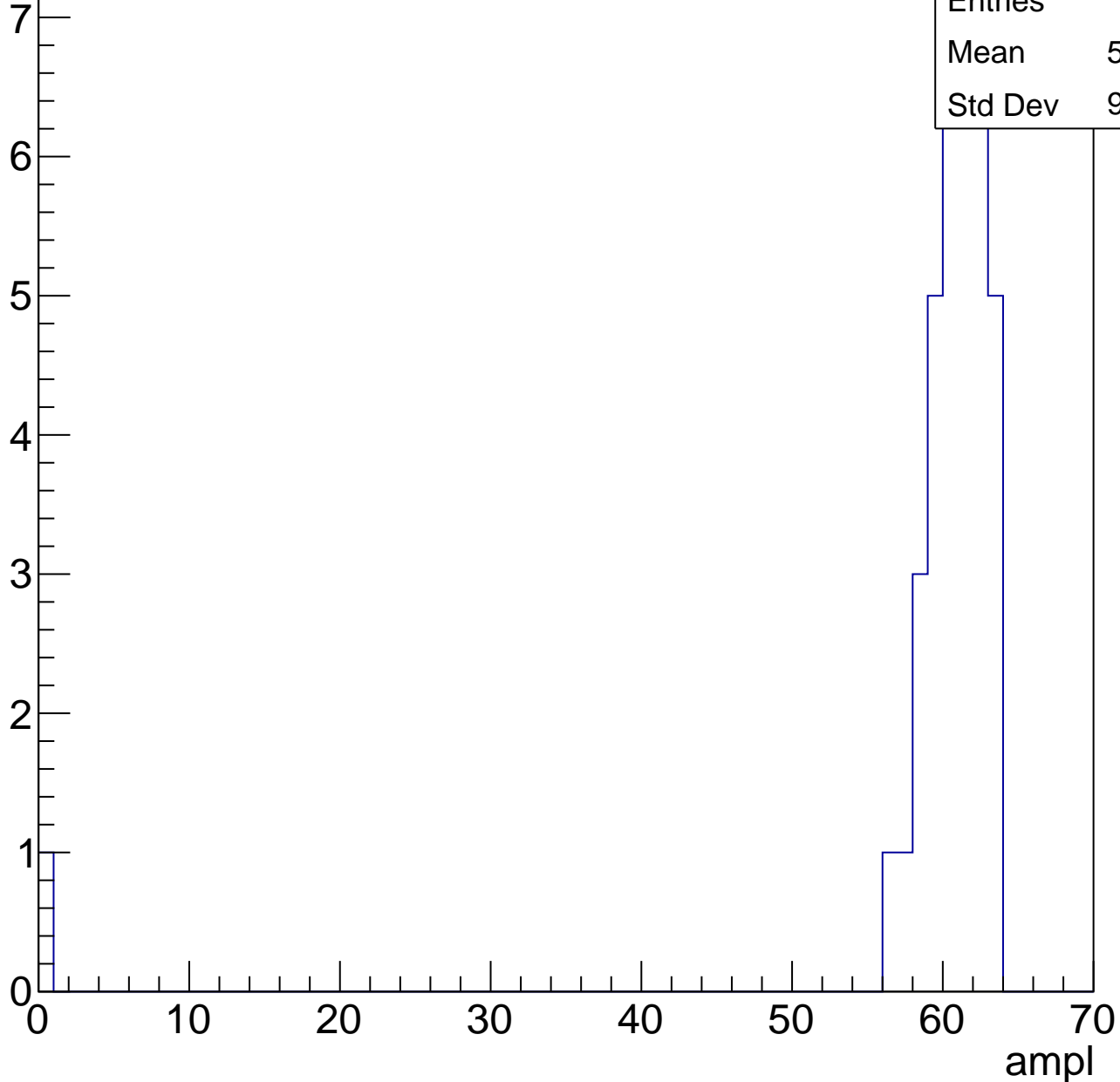
70



# B0L102S, U17-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch17, adc0

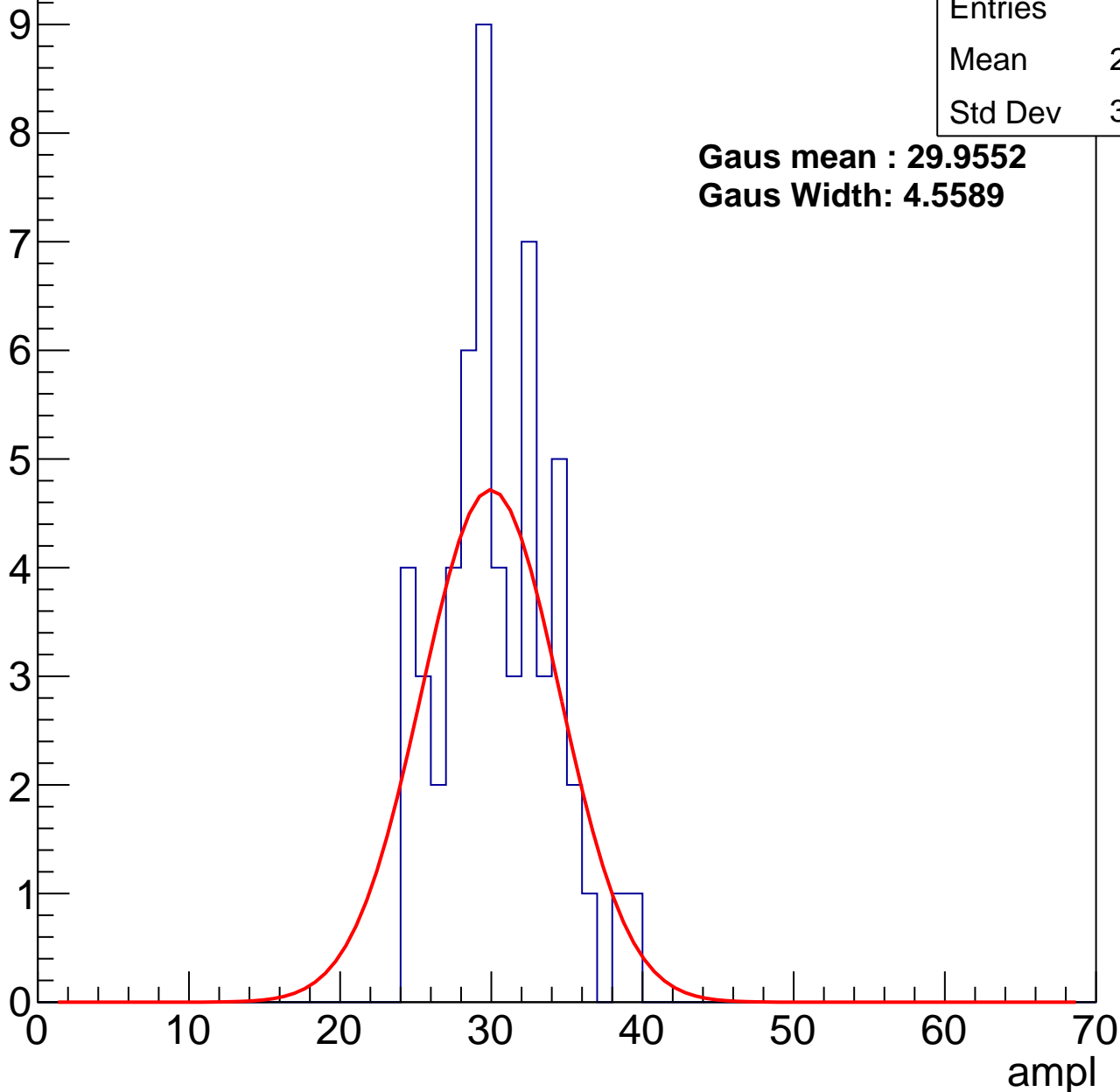
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	29.98
Std Dev	3.539

**Gaus mean : 29.9552**

**Gaus Width: 4.5589**



# B0L102S, U17-ch17, adc1

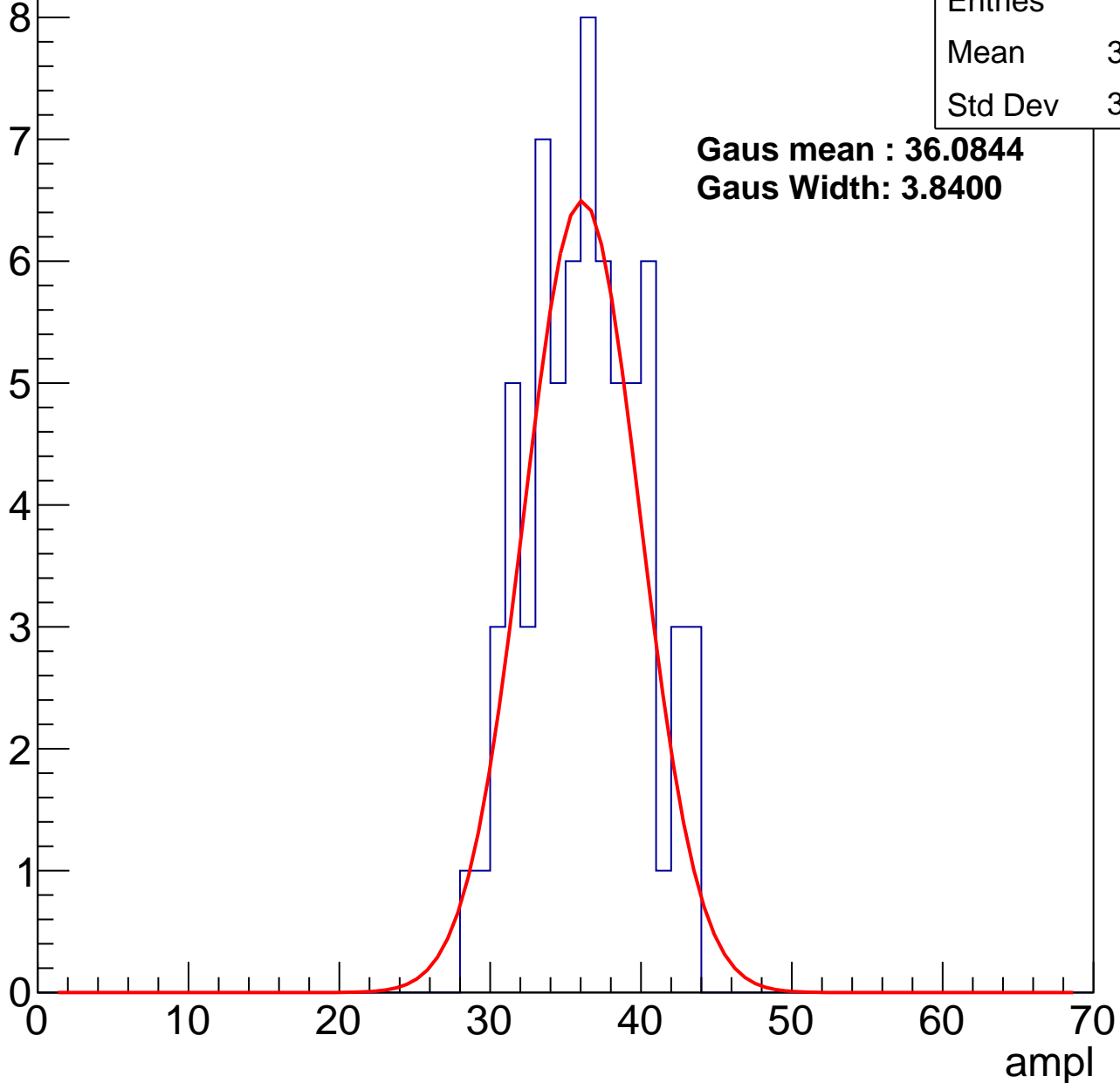
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	35.88
Std Dev	3.696

**Gaus mean : 36.0844**

**Gaus Width: 3.8400**



# B0L102S, U17-ch17, adc2

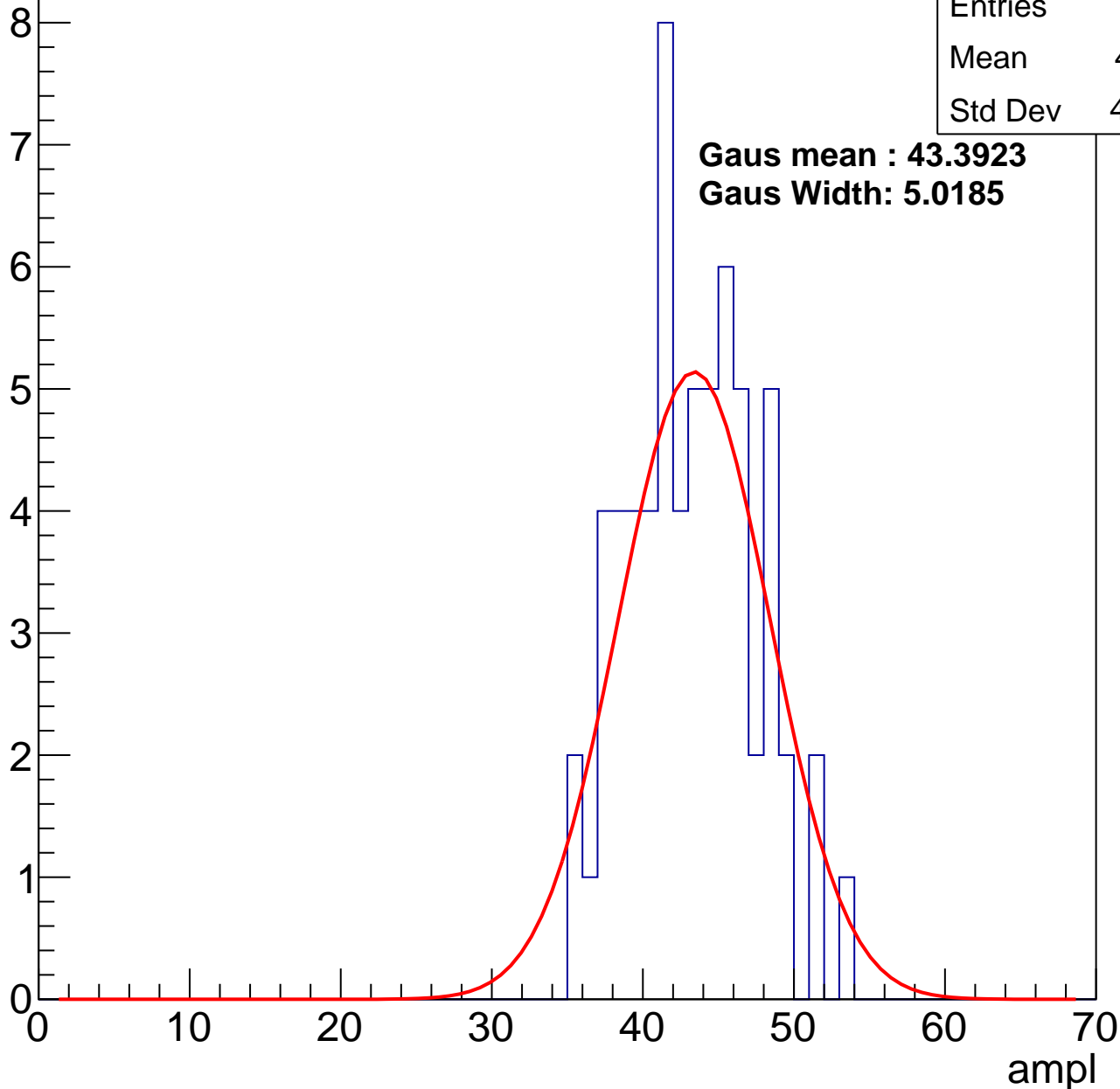
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.81
Std Dev	4.119

**Gaus mean : 43.3923**

**Gaus Width: 5.0185**

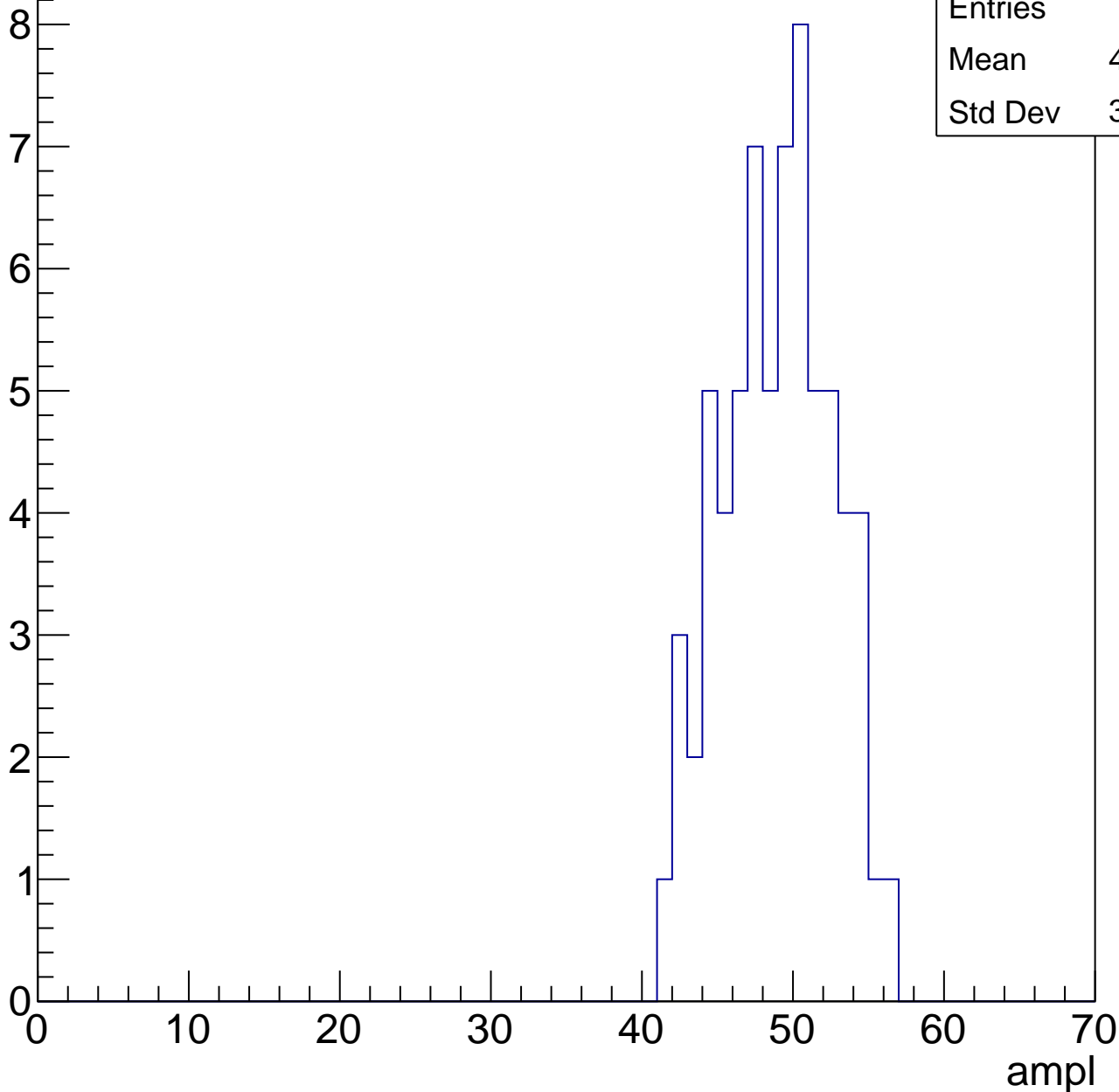


# B0L102S, U17-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	48.49
Std Dev	3.572



# B0L102S, U17-ch17, adc4

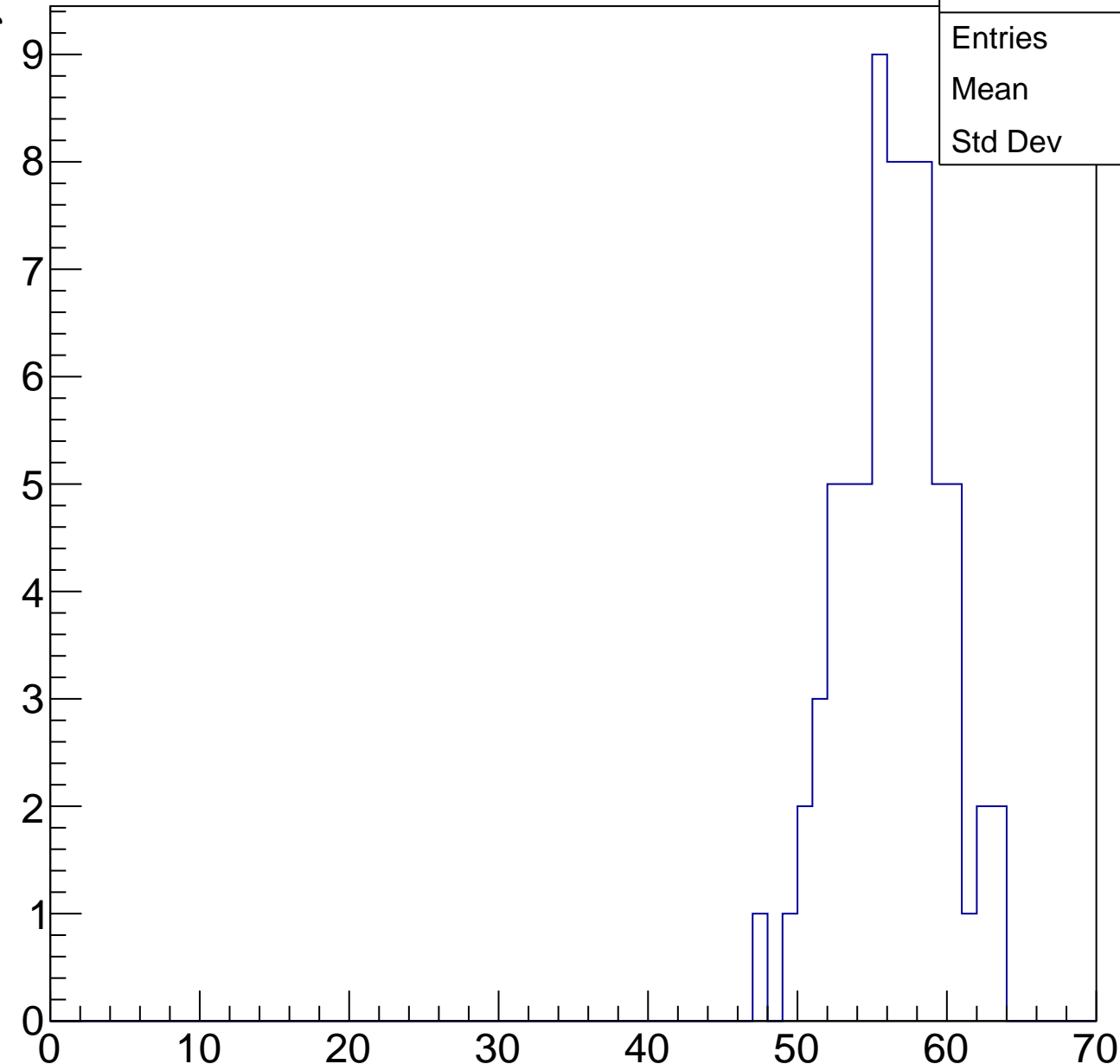
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	70
Mean	55.9
Std Dev	3.36

ampl

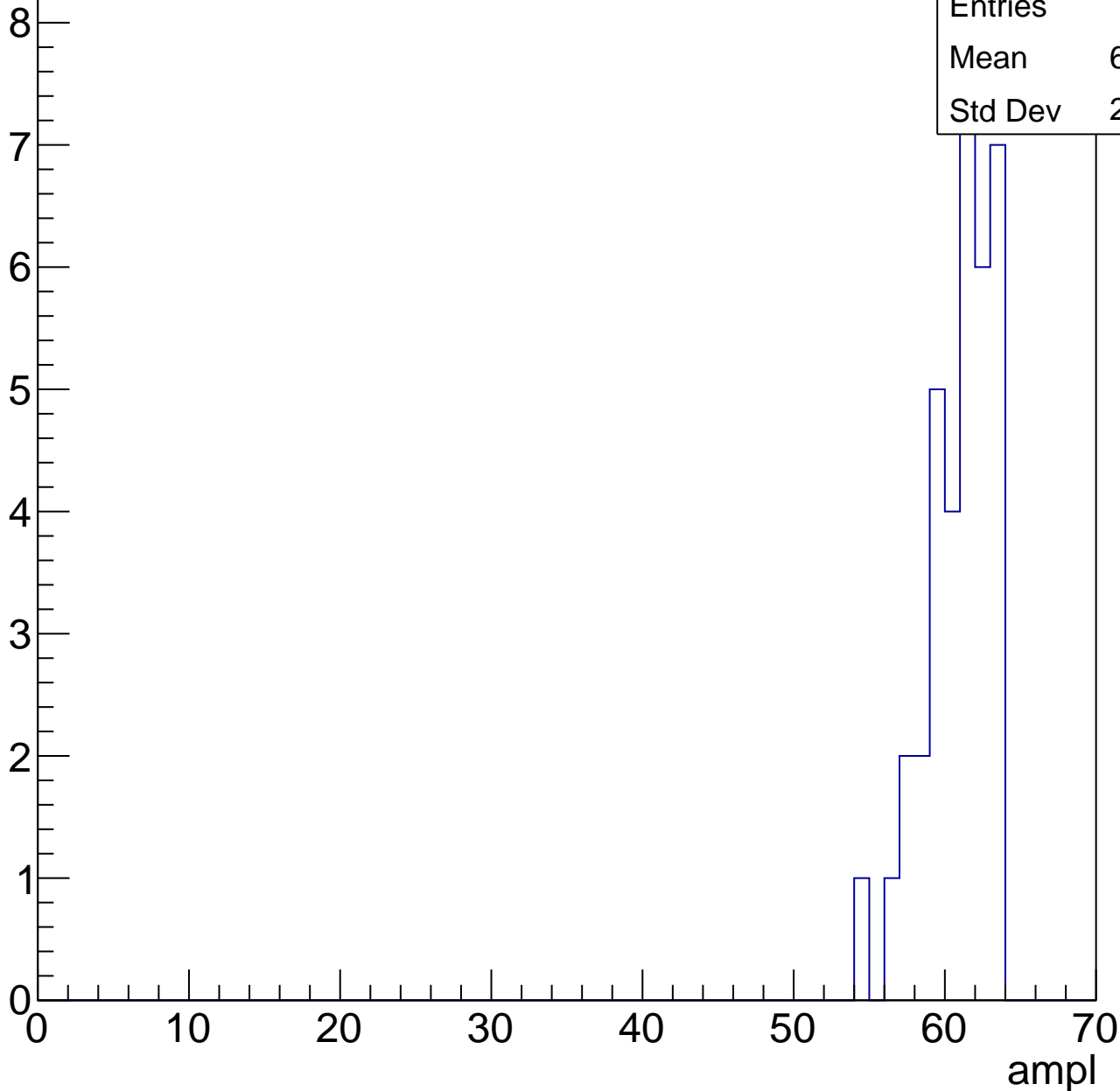


# B0L102S, U17-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	36
Mean	60.44
Std Dev	2.179



# B0L102S, U17-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

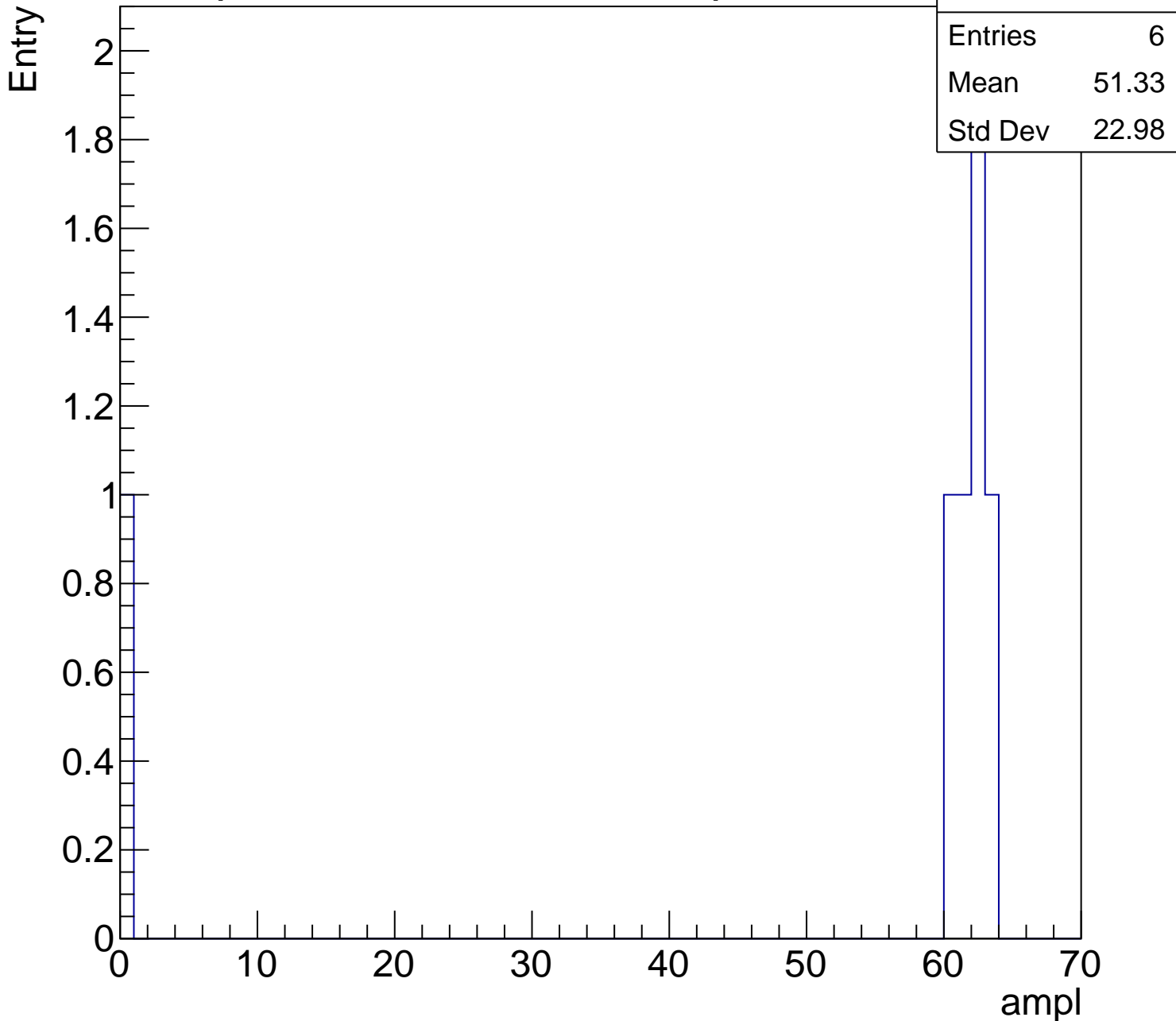
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

ampl





# B0L102S, U17-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch18, adc0

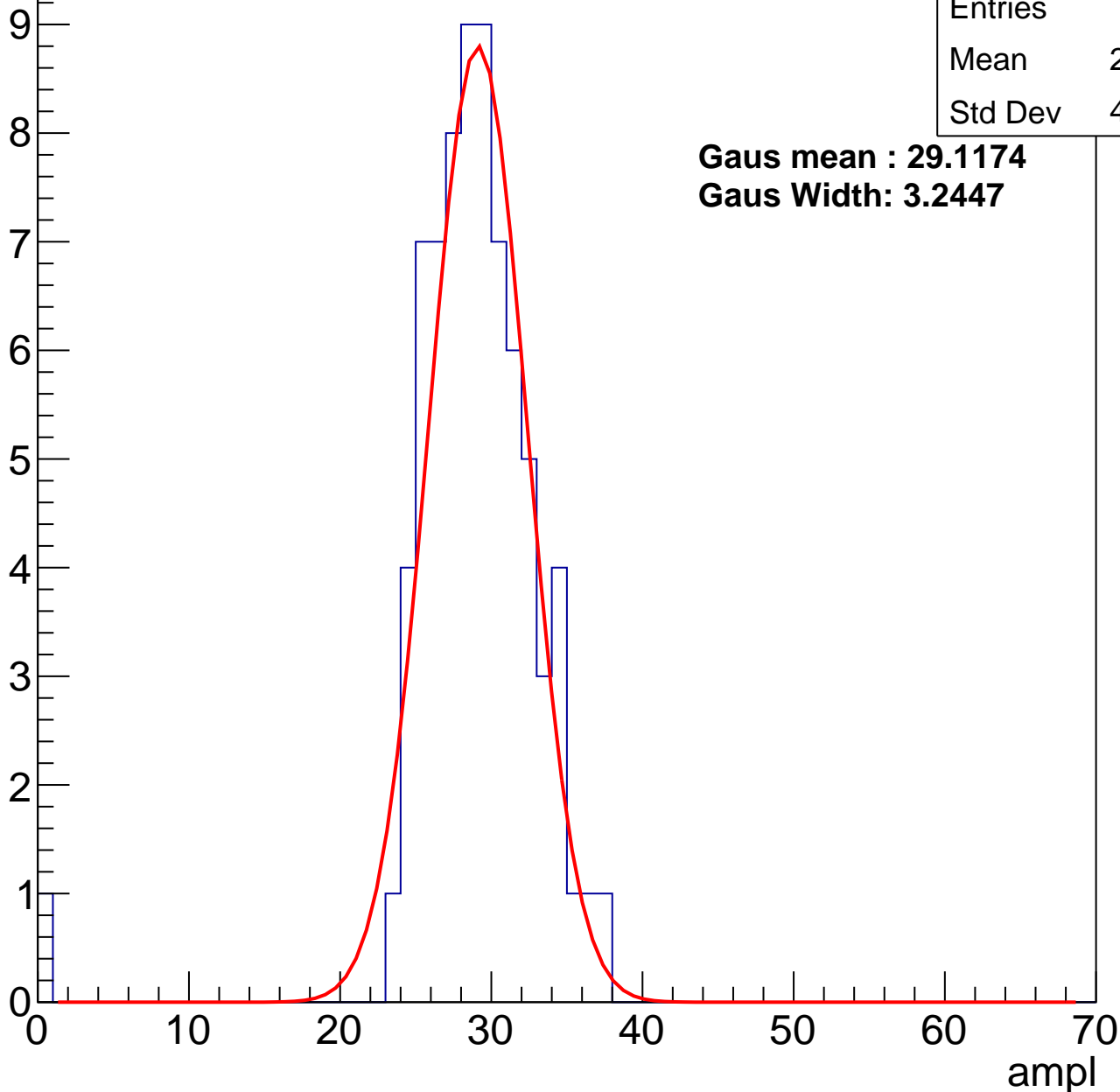
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	28.43
Std Dev	4.565

**Gaus mean : 29.1174**

**Gaus Width: 3.2447**



# B0L102S, U17-ch18, adc1

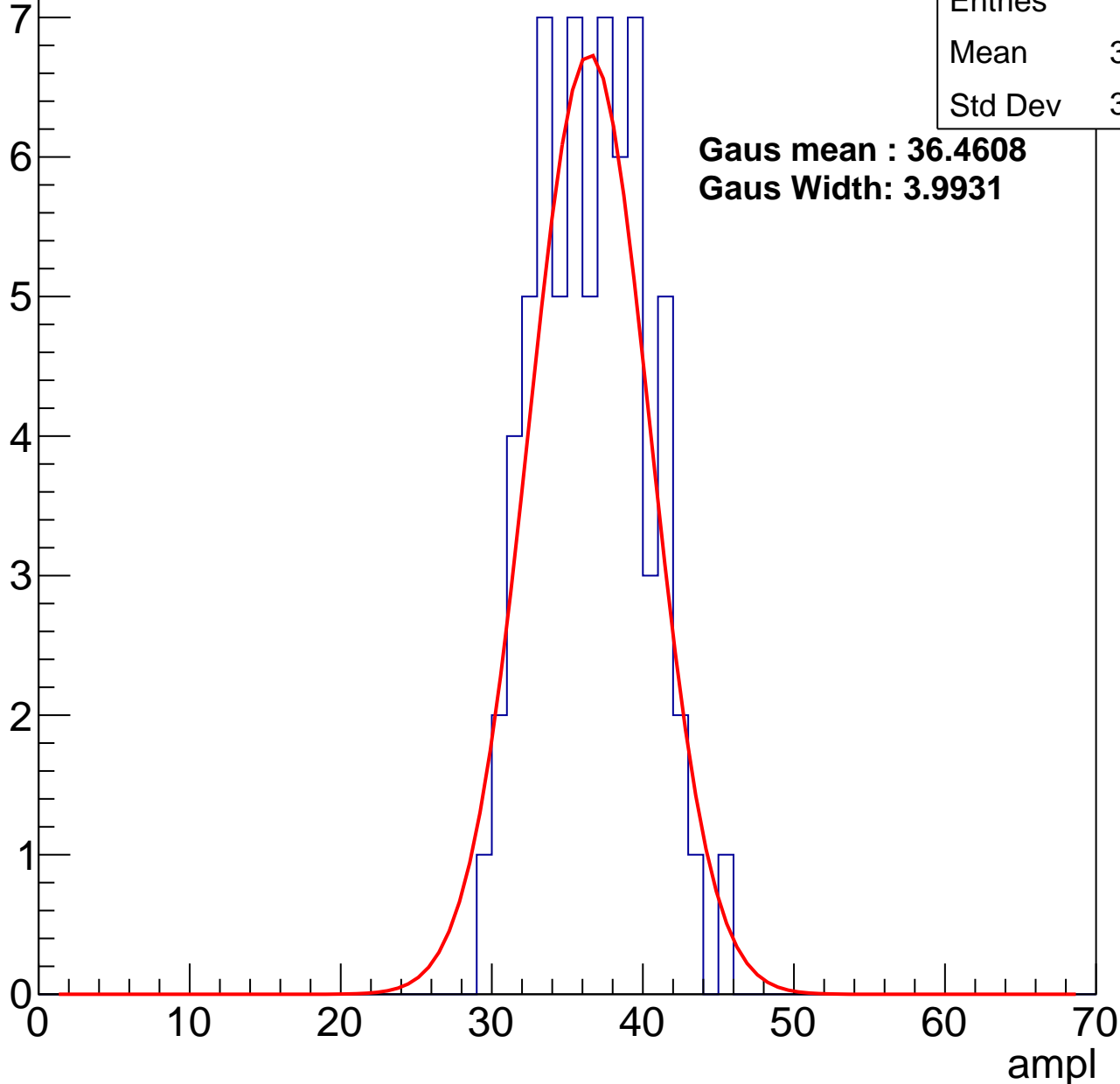
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.12
Std Dev	3.554

**Gaus mean : 36.4608**

**Gaus Width: 3.9931**



# B0L102S, U17-ch18, adc2

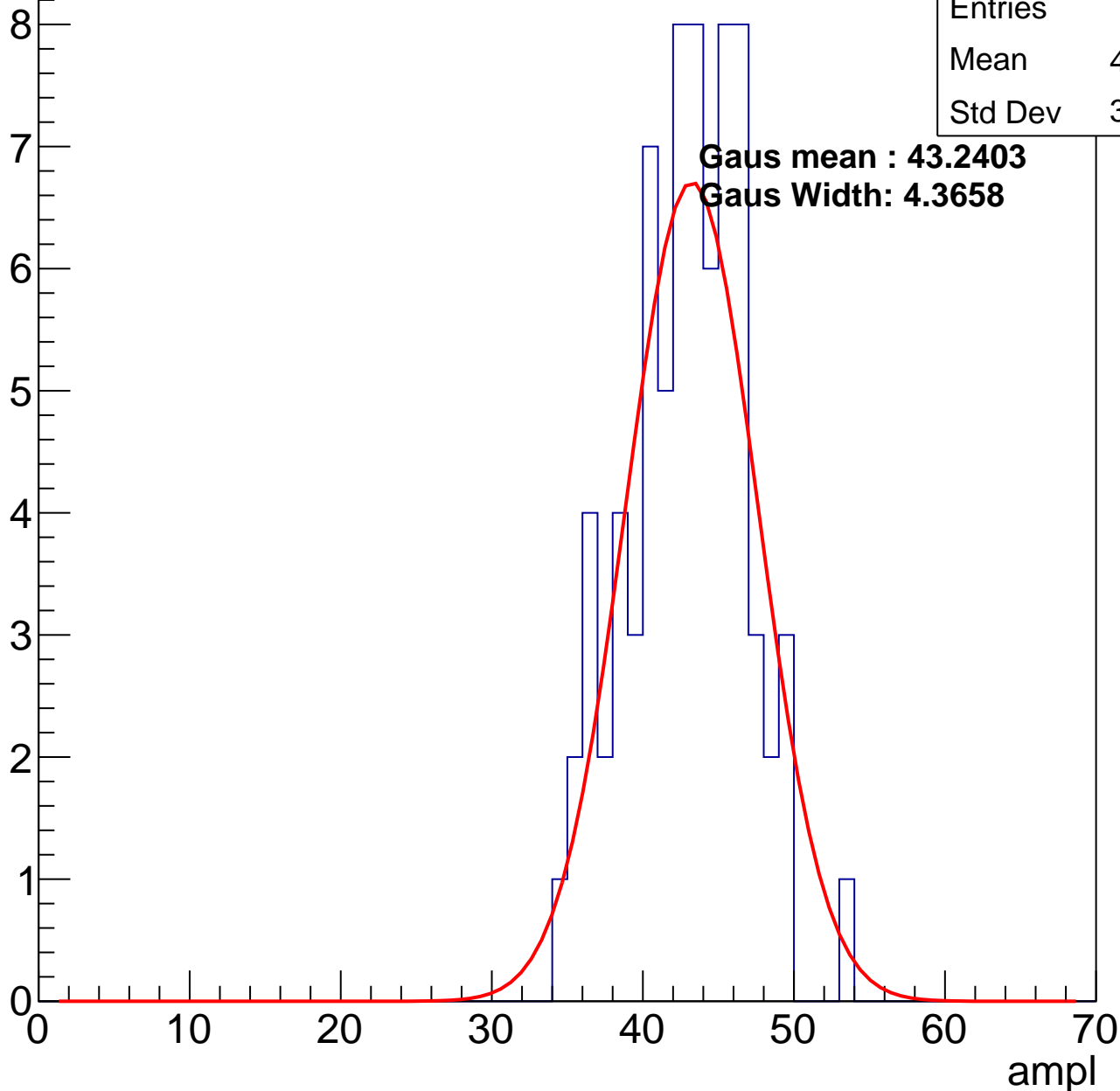
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	42.47
Std Dev	3.848

**Gaus mean : 43.2403**

**Gaus Width: 4.3658**

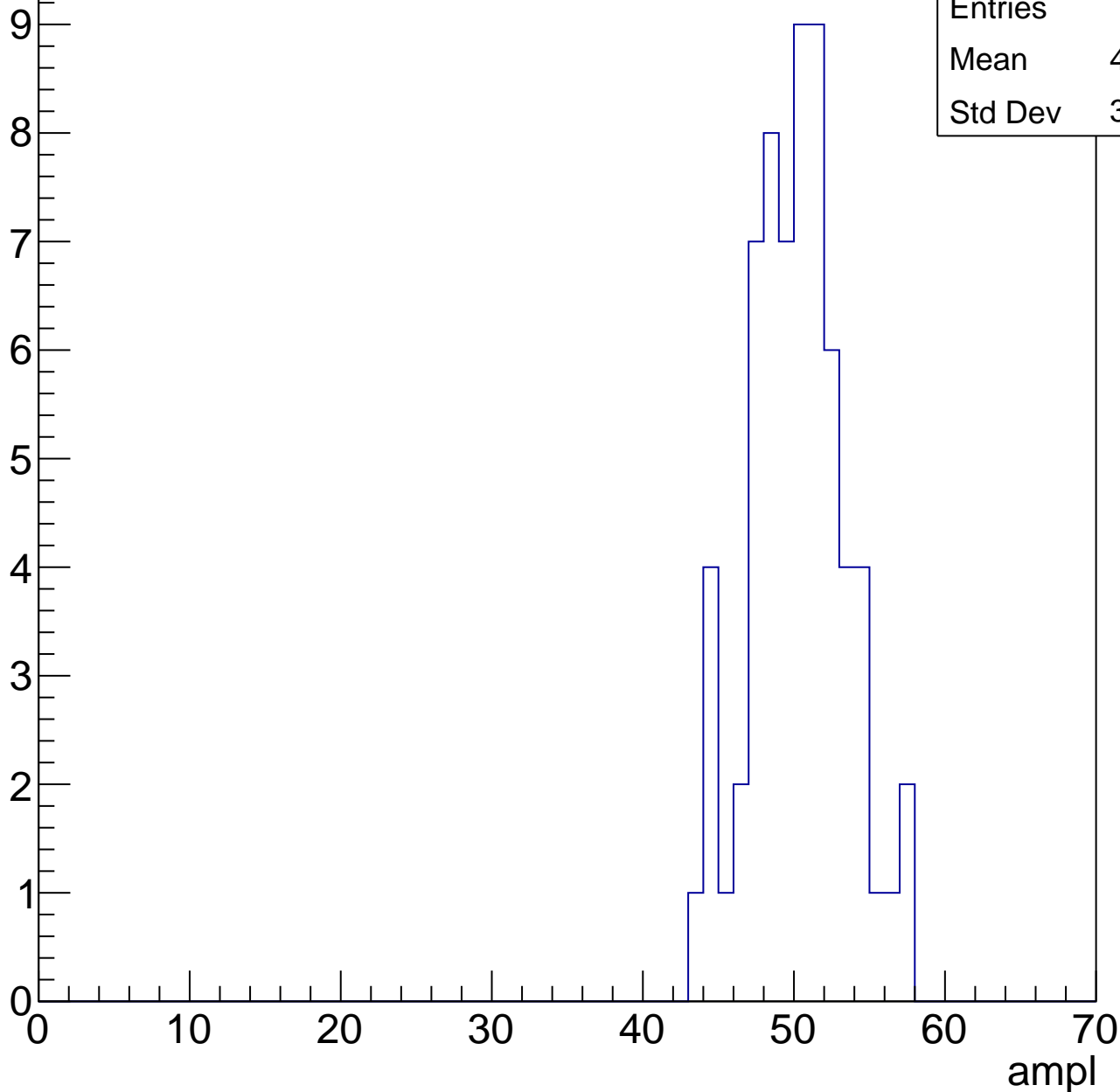


# B0L102S, U17-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	49.79
Std Dev	3.117

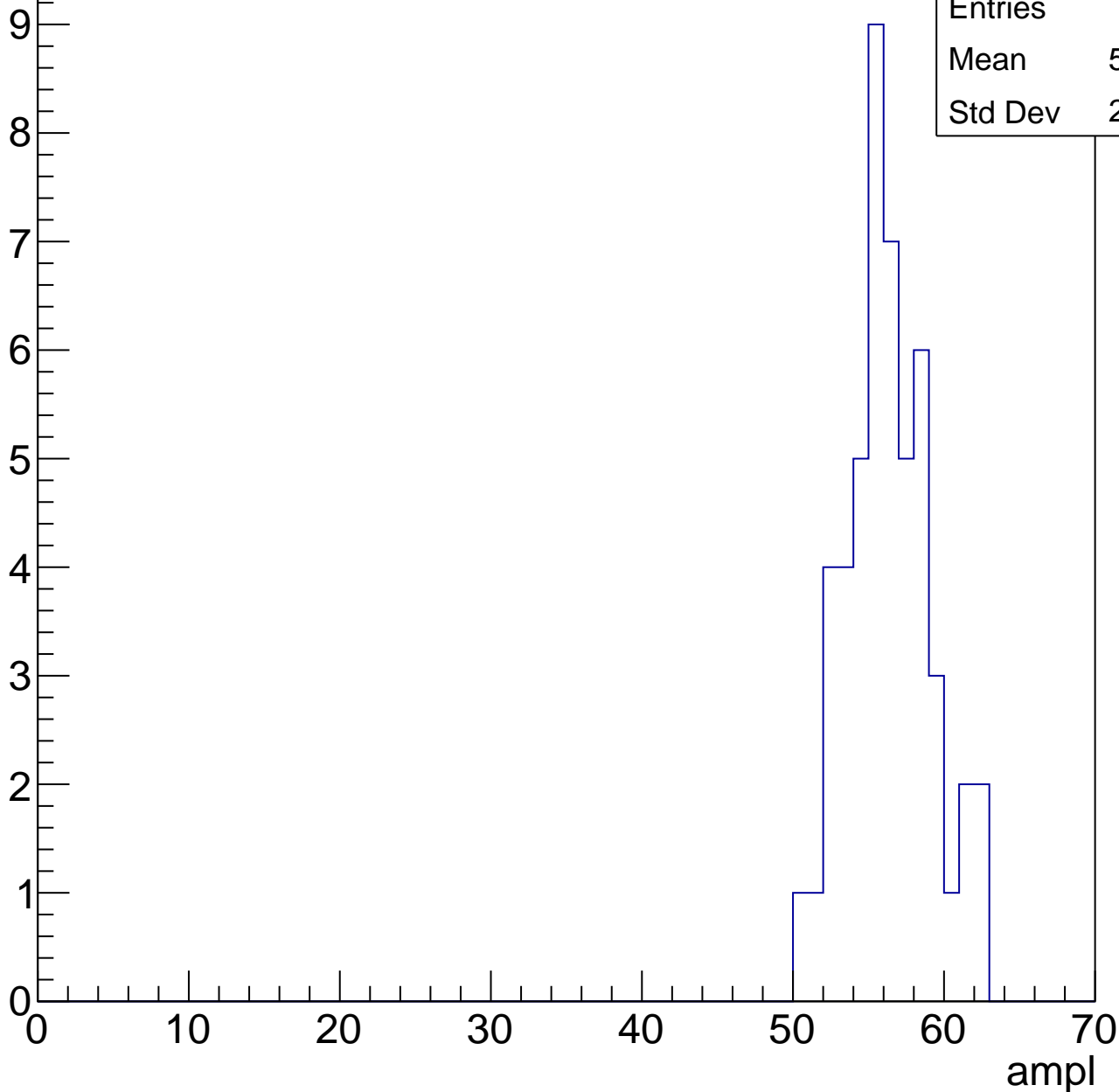


# B0L102S, U17-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	55.88
Std Dev	2.769

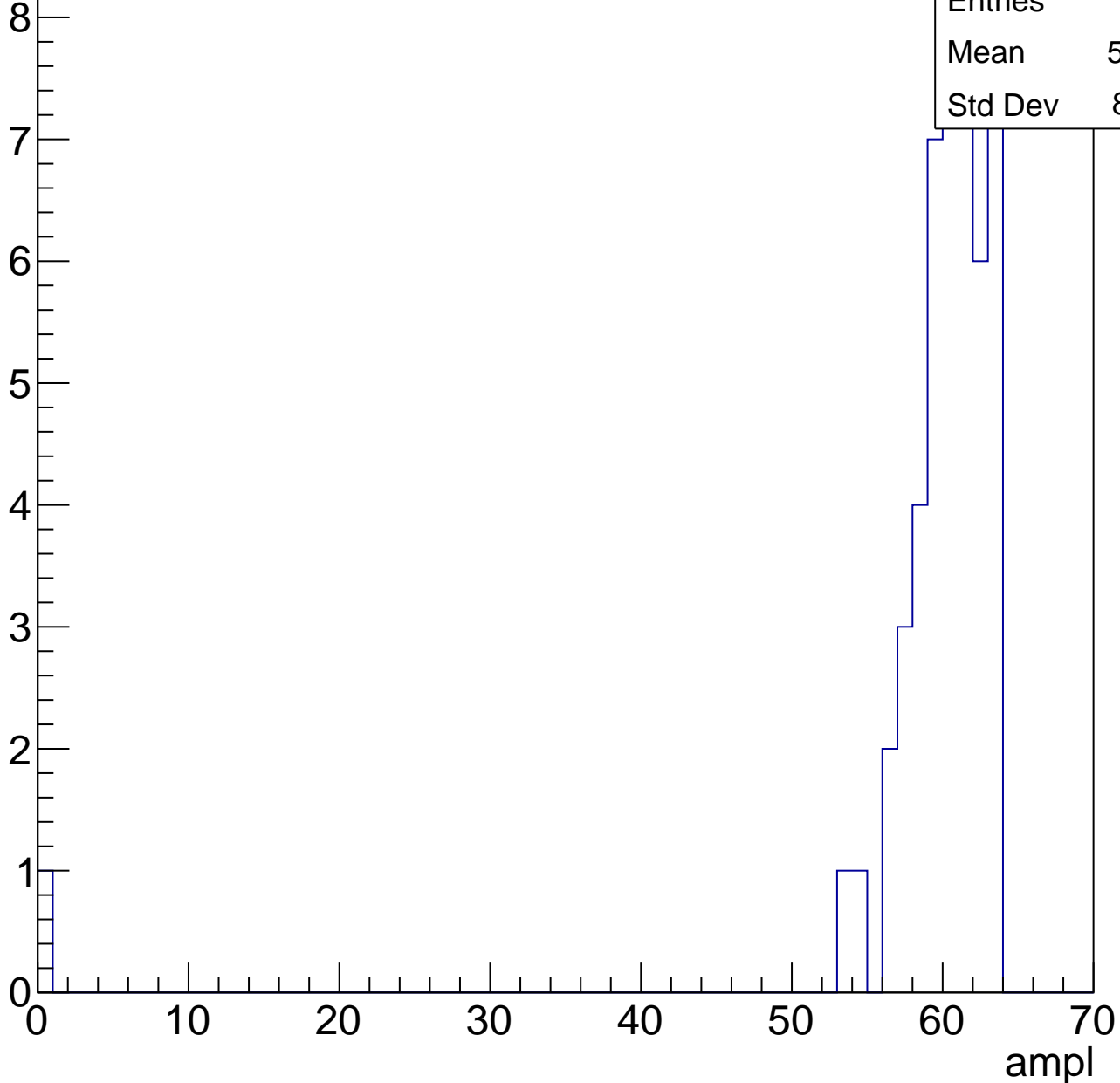


# B0L102S, U17-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	58.76
Std Dev	8.801



# B0L102S, U17-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	62
Mean	29.27
Std Dev	4.843

**Gaus mean : 31.0790**

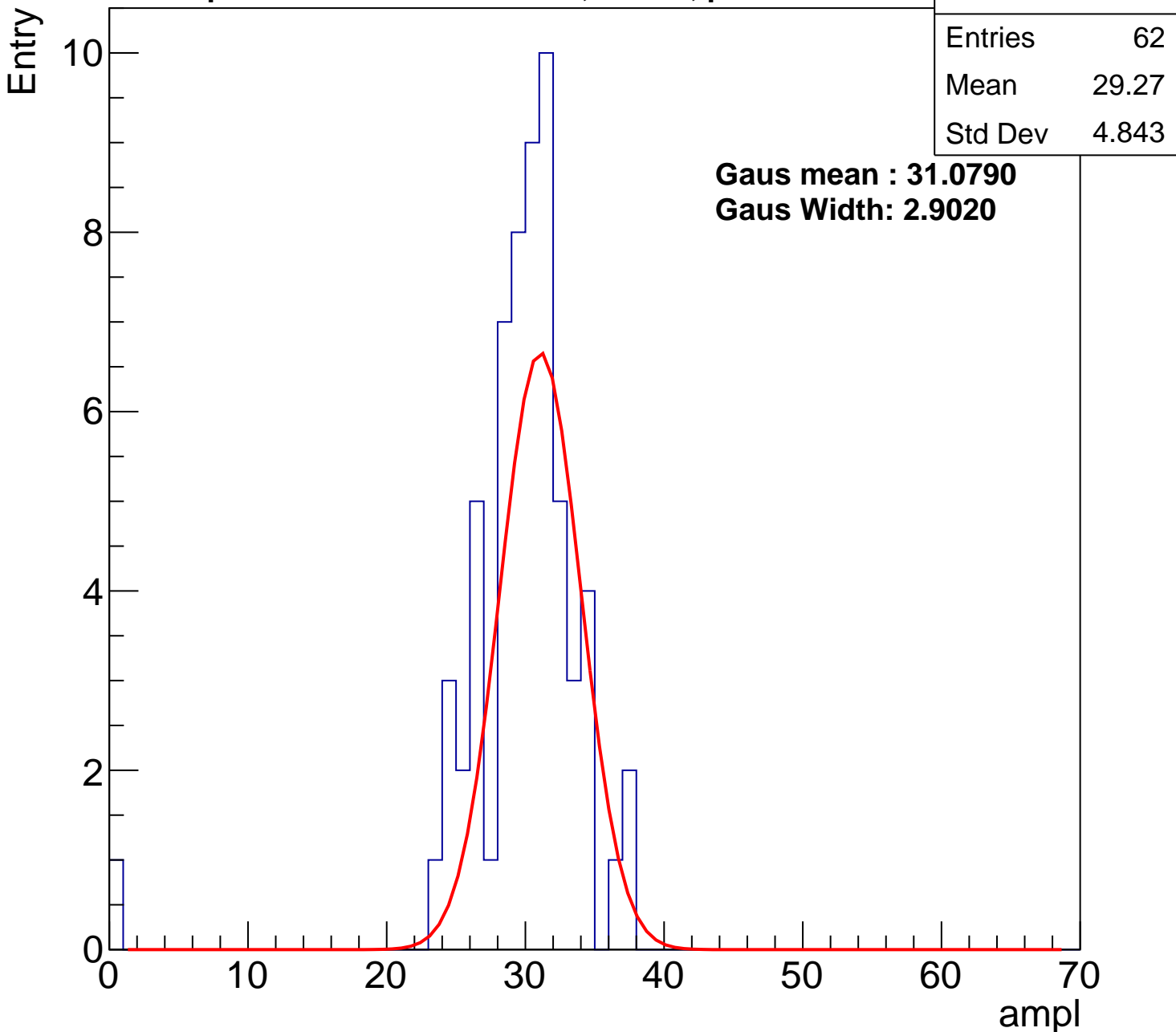
**Gaus Width: 2.9020**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch19, adc1

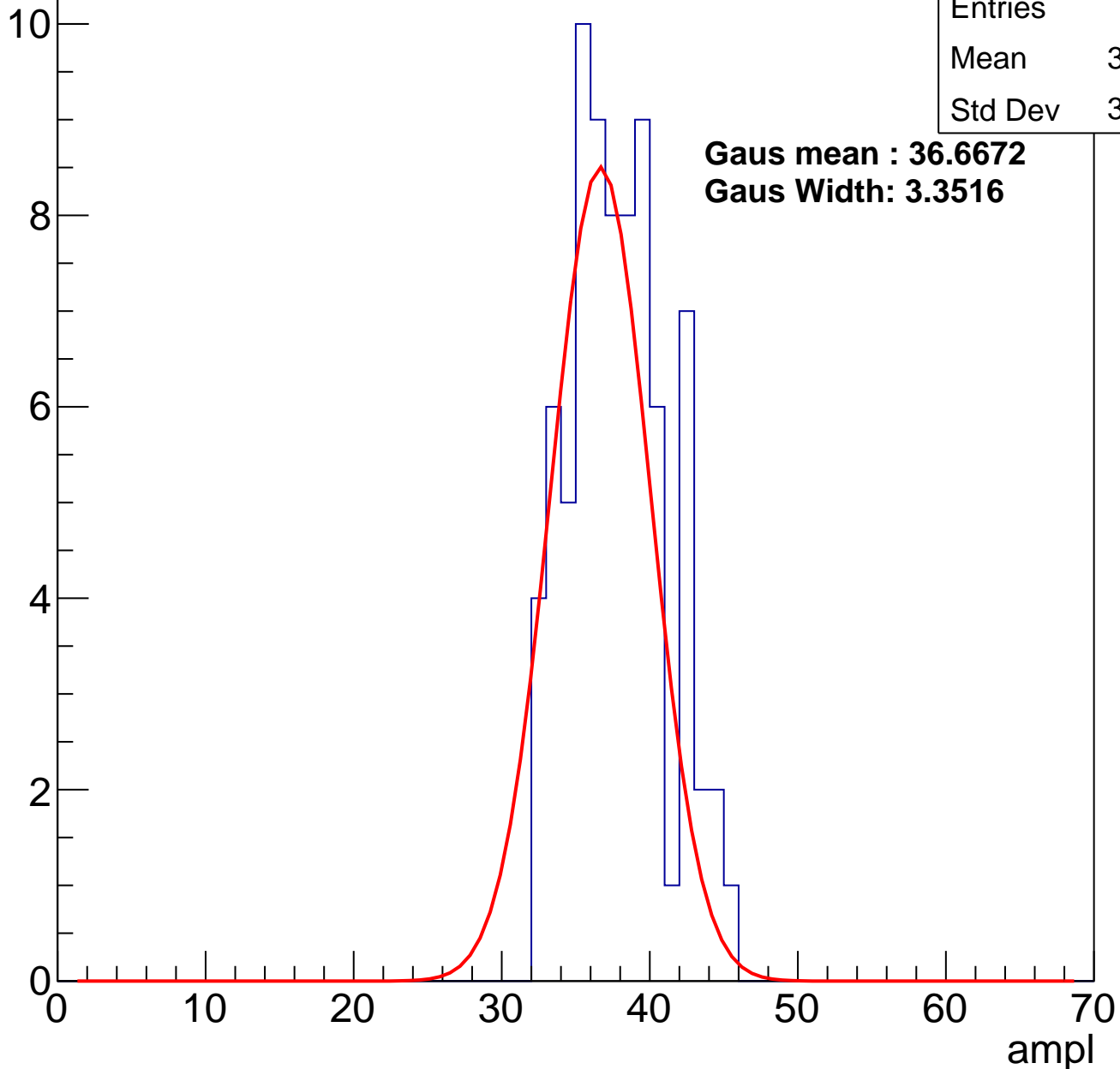
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	78
Mean	37.37
Std Dev	3.207

**Gaus mean : 36.6672**

**Gaus Width: 3.3516**

Entry



# B0L102S, U17-ch19, adc2

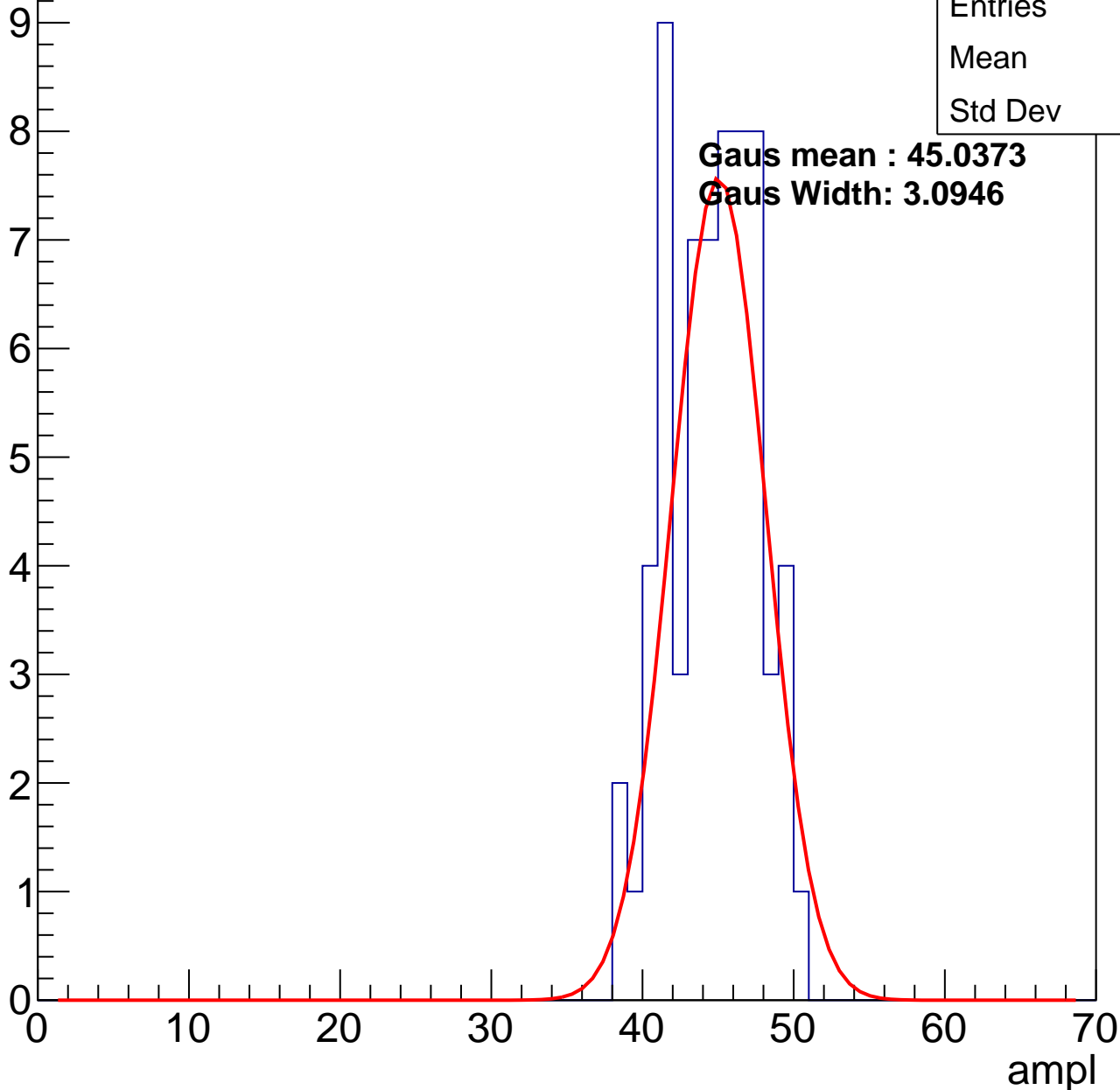
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	44.2
Std Dev	2.92

**Gaus mean : 45.0373**

**Gaus Width: 3.0946**

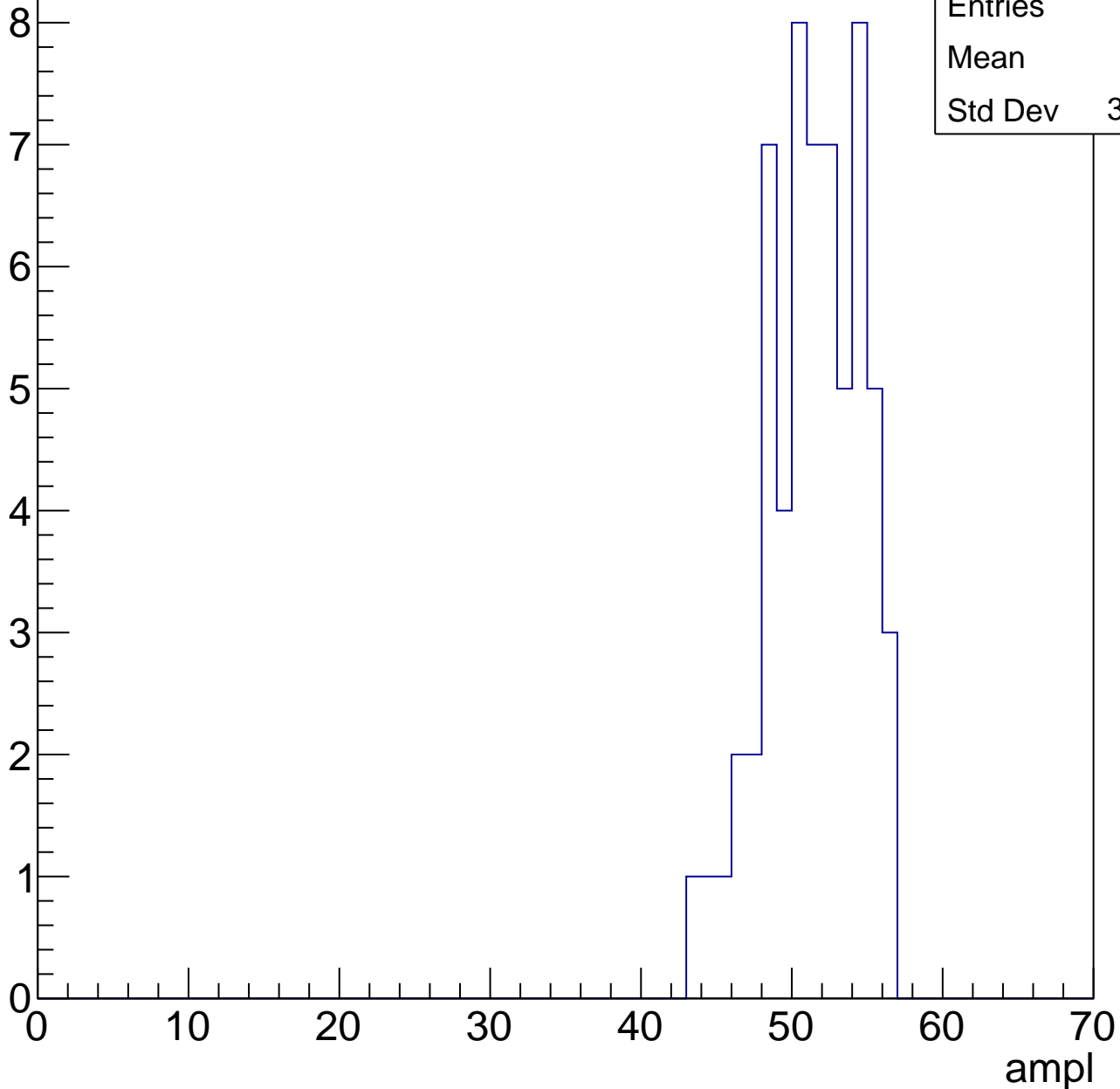


# B0L102S, U17-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	51
Std Dev	3.062

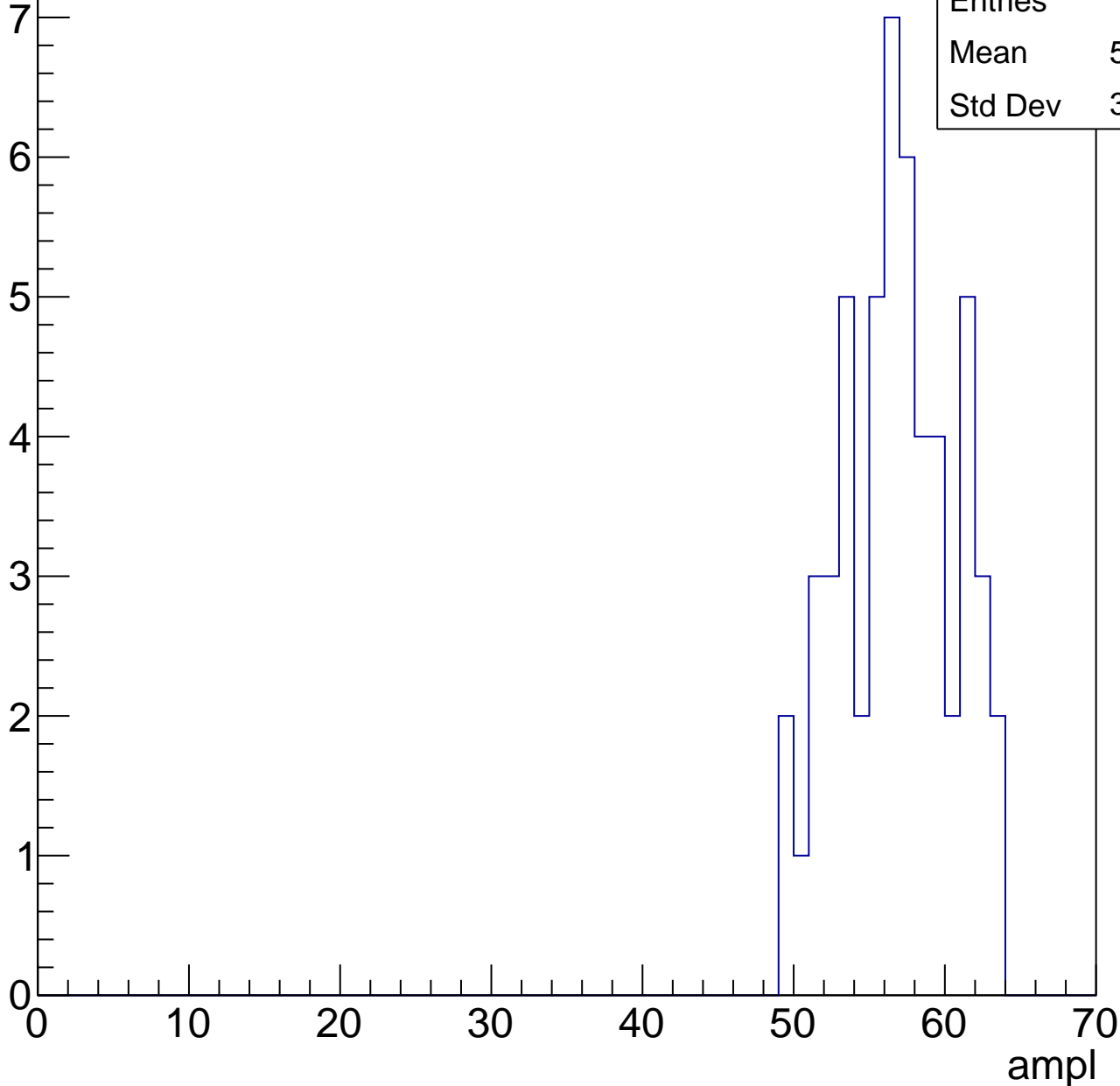


# B0L102S, U17-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	56.37
Std Dev	3.673

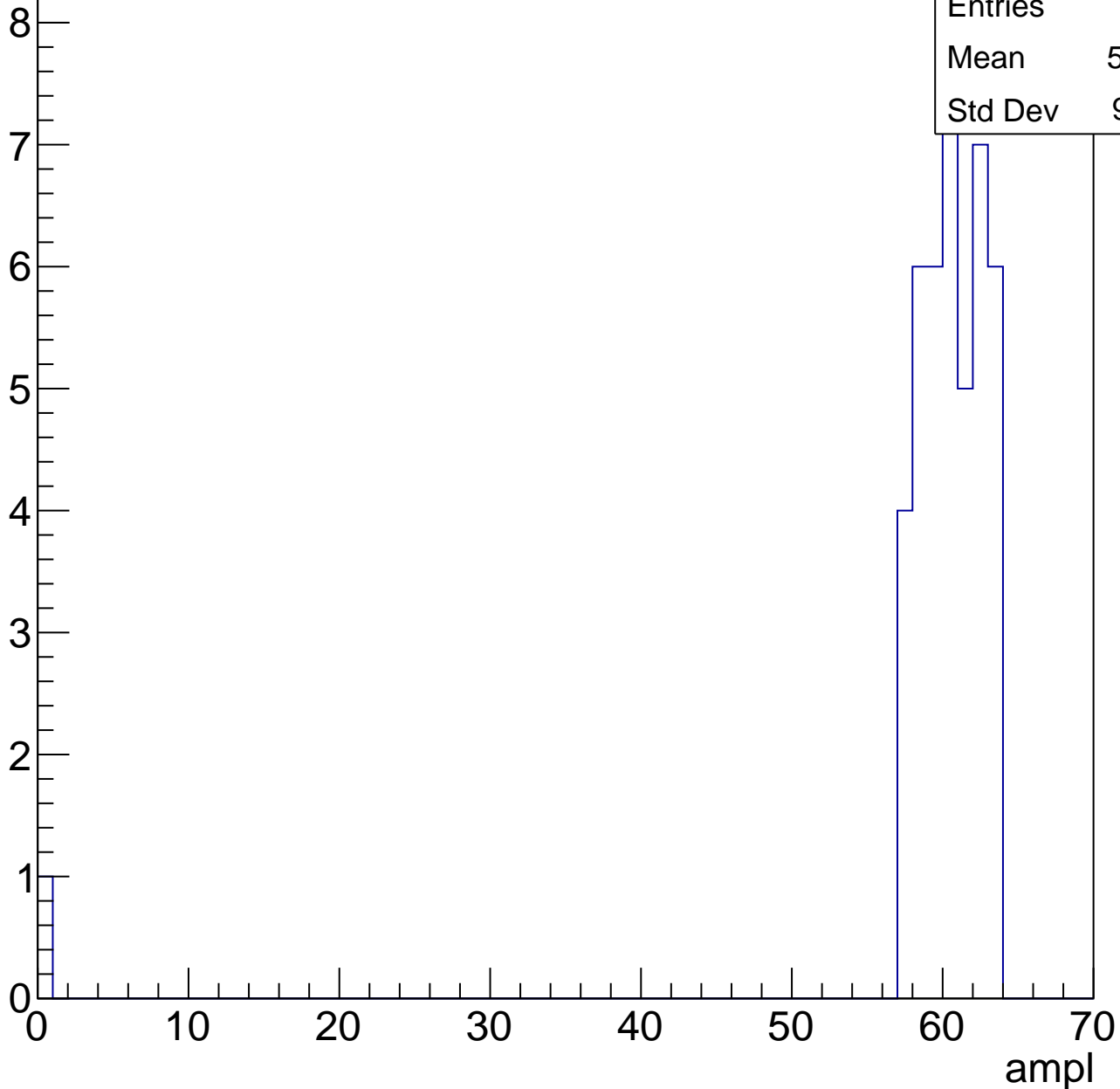


# B0L102S, U17-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	43
Mean	58.77
Std Dev	9.261



# B0L102S, U17-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U17-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch20, adc0

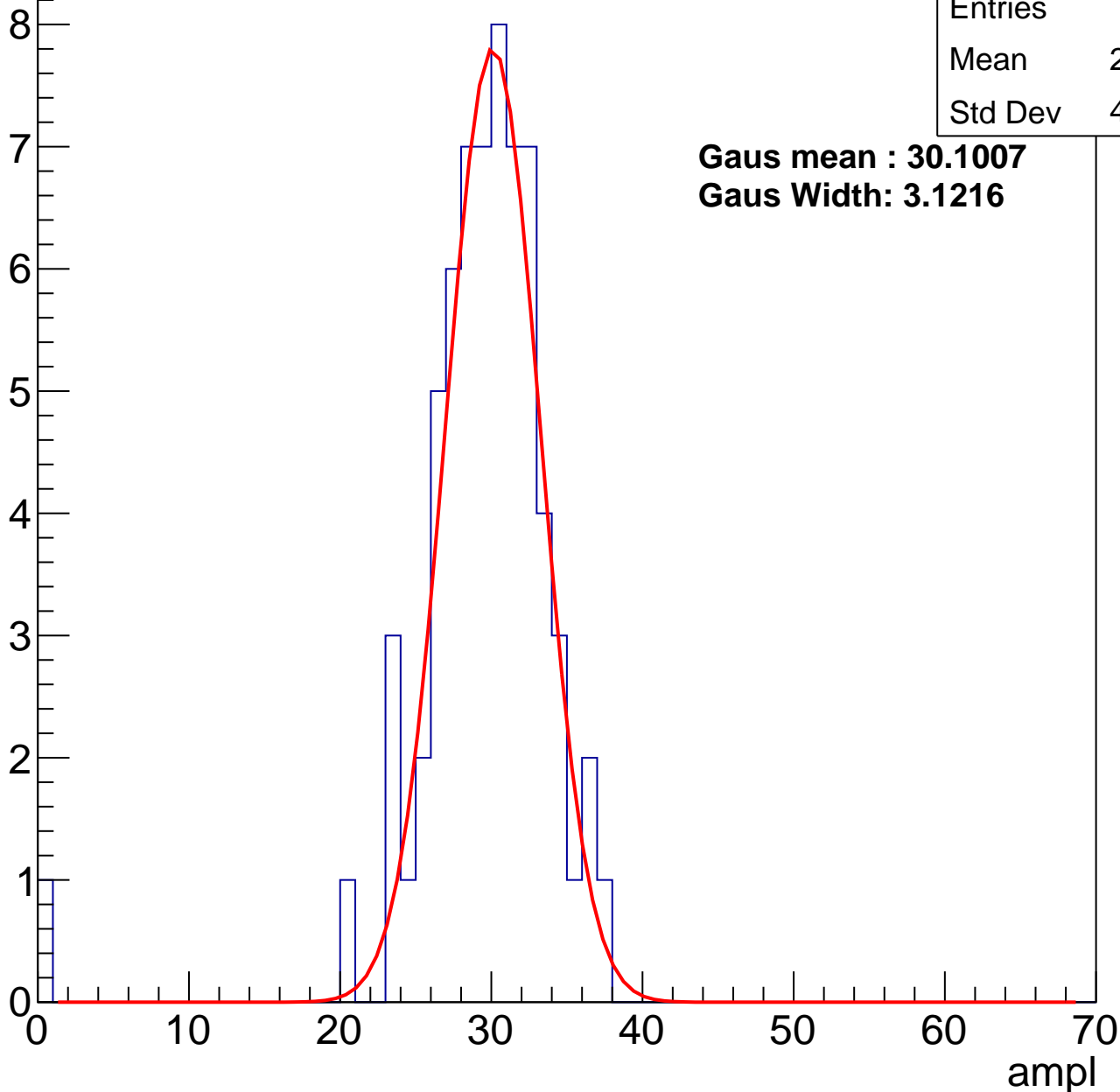
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	28.98
Std Dev	4.922

**Gaus mean : 30.1007**

**Gaus Width: 3.1216**



# B0L102S, U17-ch20, adc1

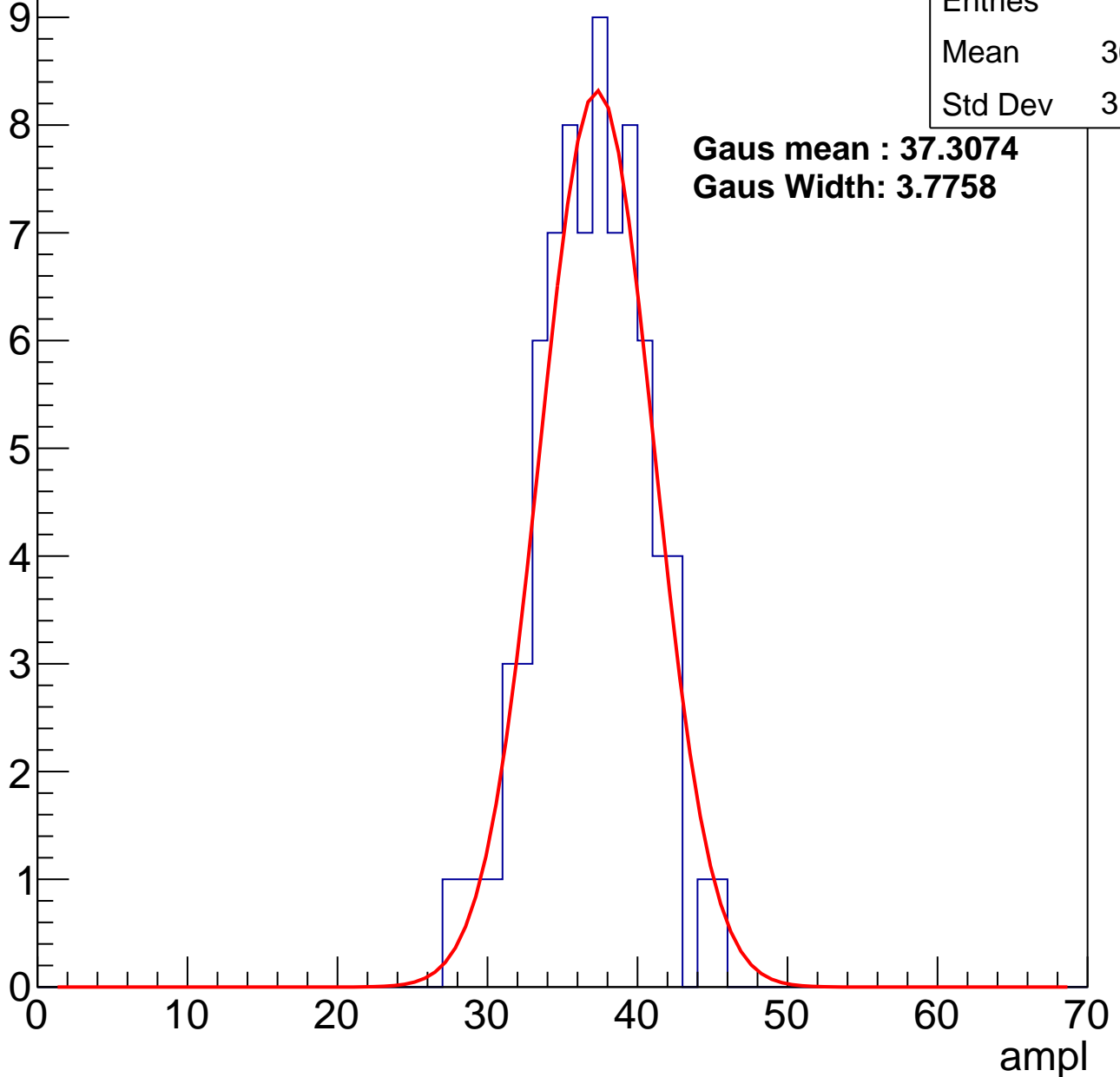
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	36.45
Std Dev	3.618

**Gaus mean : 37.3074**

**Gaus Width: 3.7758**



# B0L102S, U17-ch20, adc2

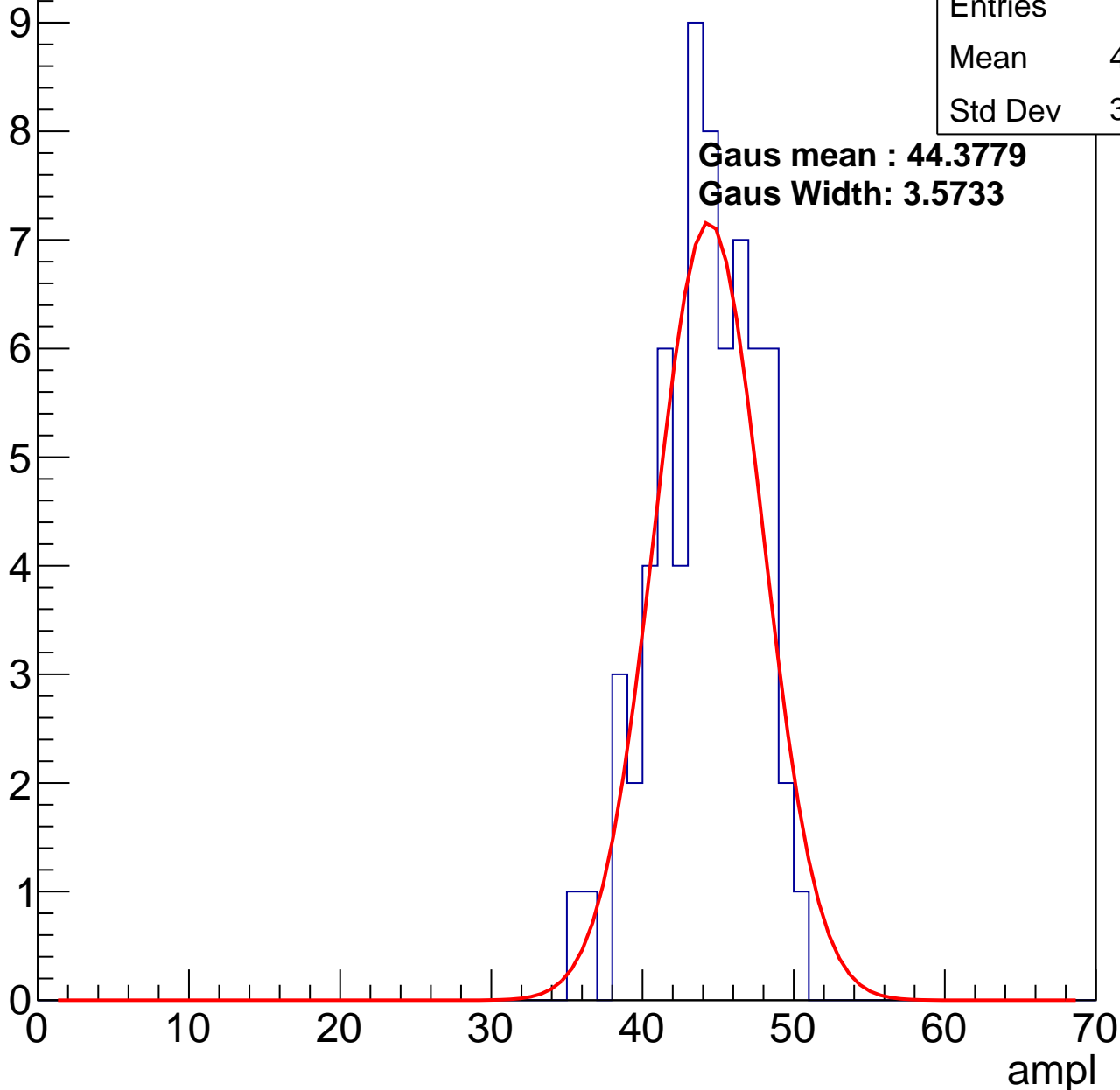
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	43.73
Std Dev	3.282

**Gaus mean : 44.3779**

**Gaus Width: 3.5733**

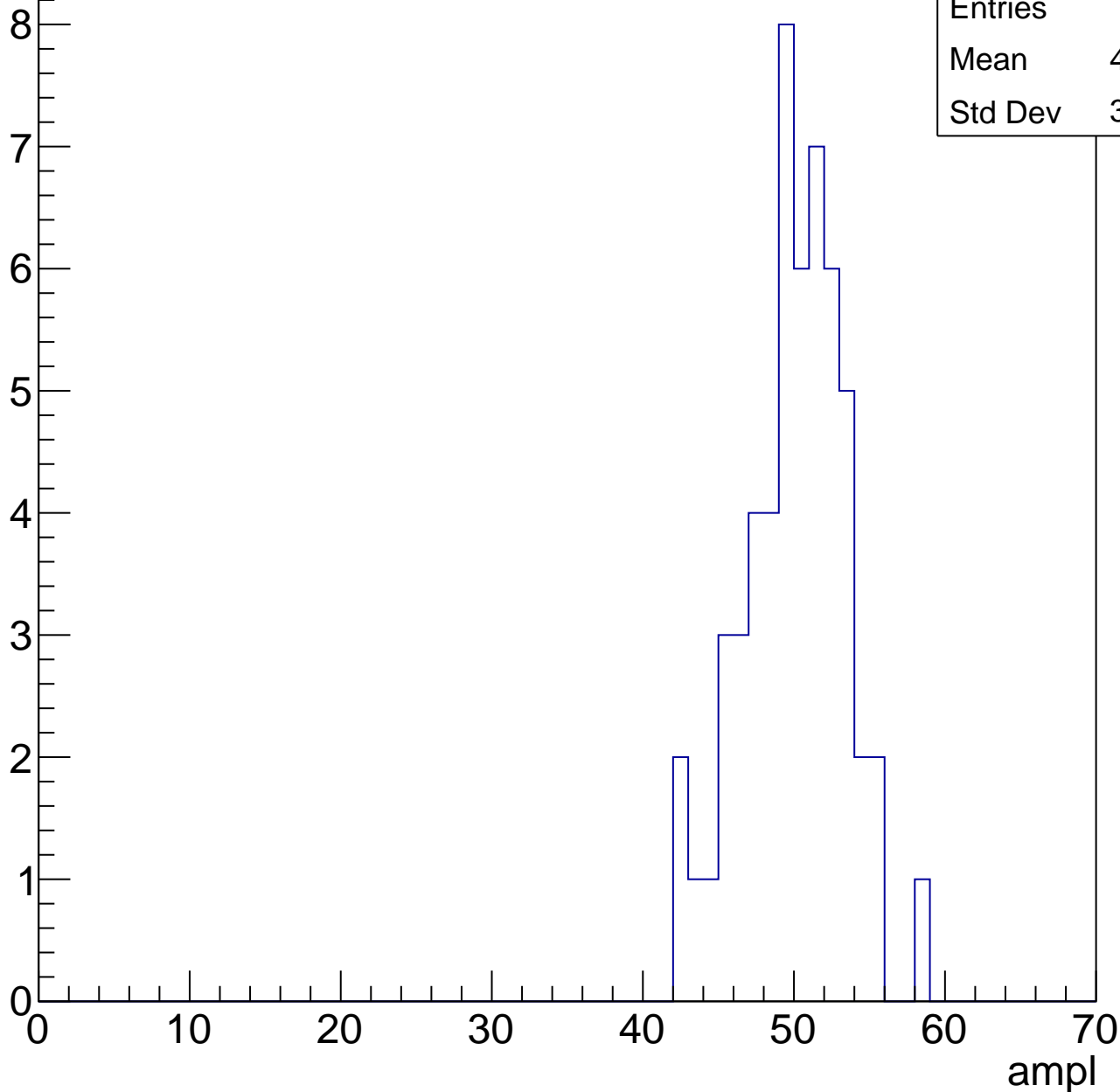


# B0L102S, U17-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	49.56
Std Dev	3.324

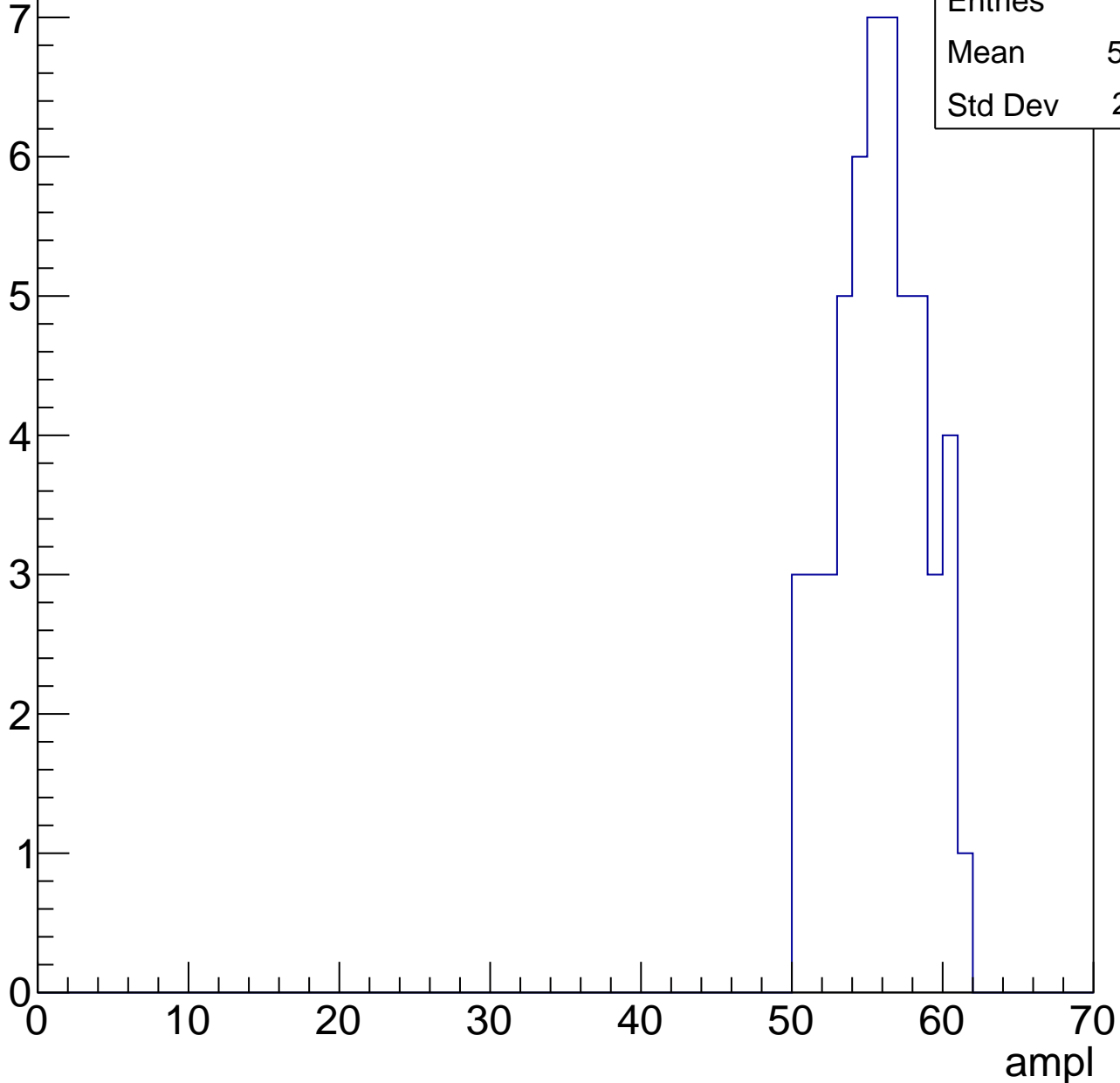


# B0L102S, U17-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55.35
Std Dev	2.861

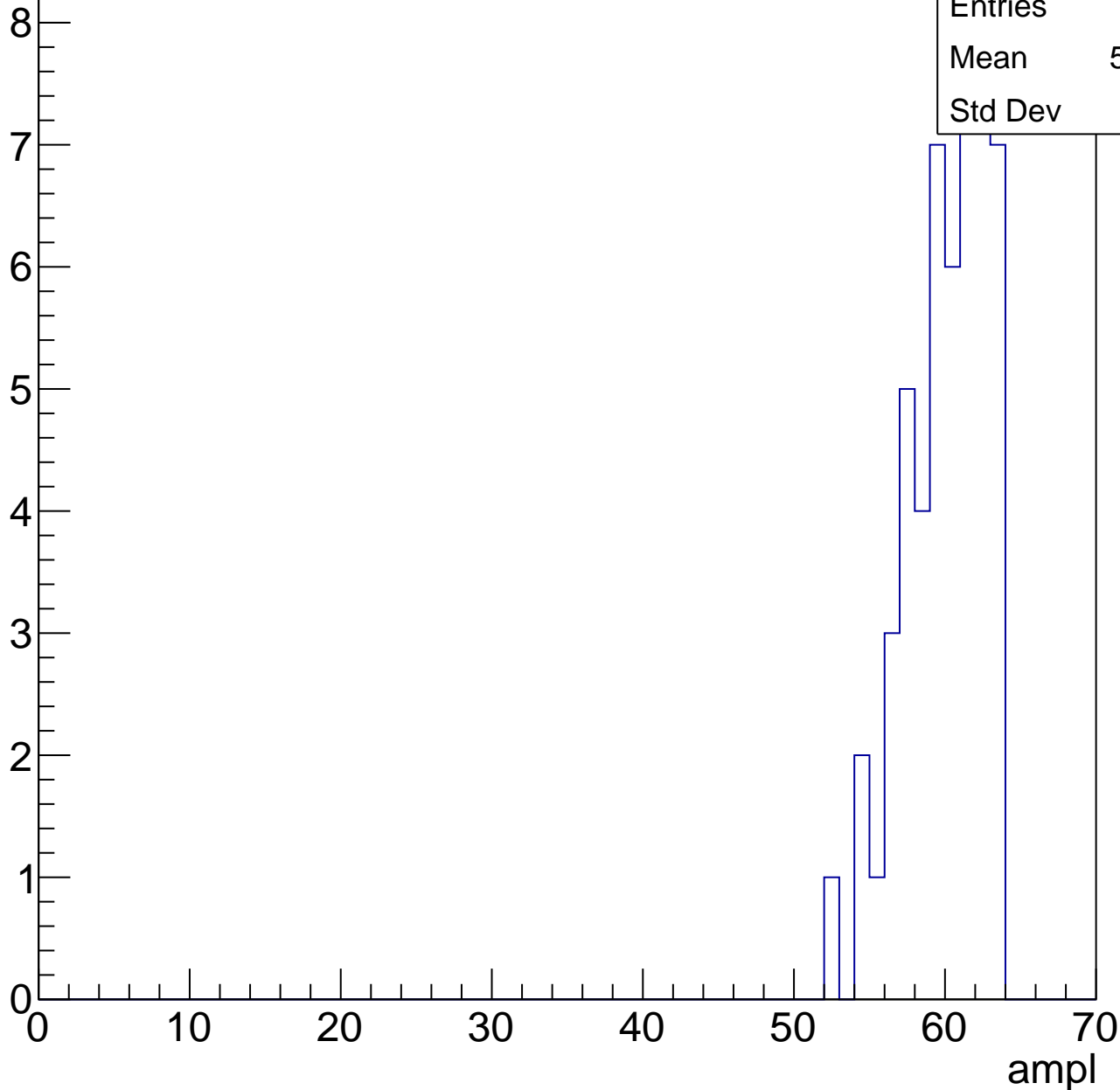


# B0L102S, U17-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	59.58
Std Dev	2.67



# B0L102S, U17-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch21, adc0

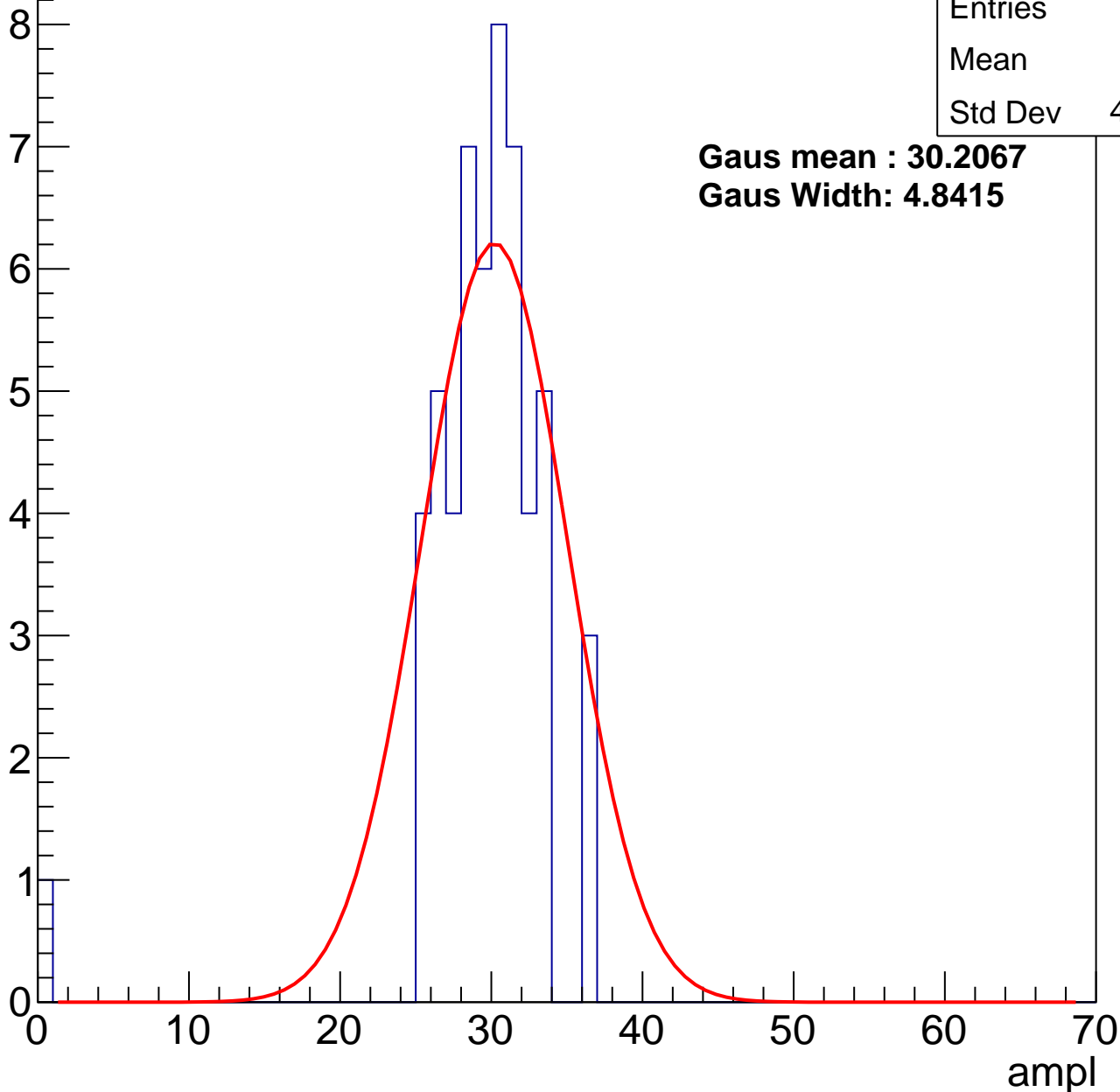
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	29
Std Dev	4.853

**Gaus mean : 30.2067**

**Gaus Width: 4.8415**



# B0L102S, U17-ch21, adc1

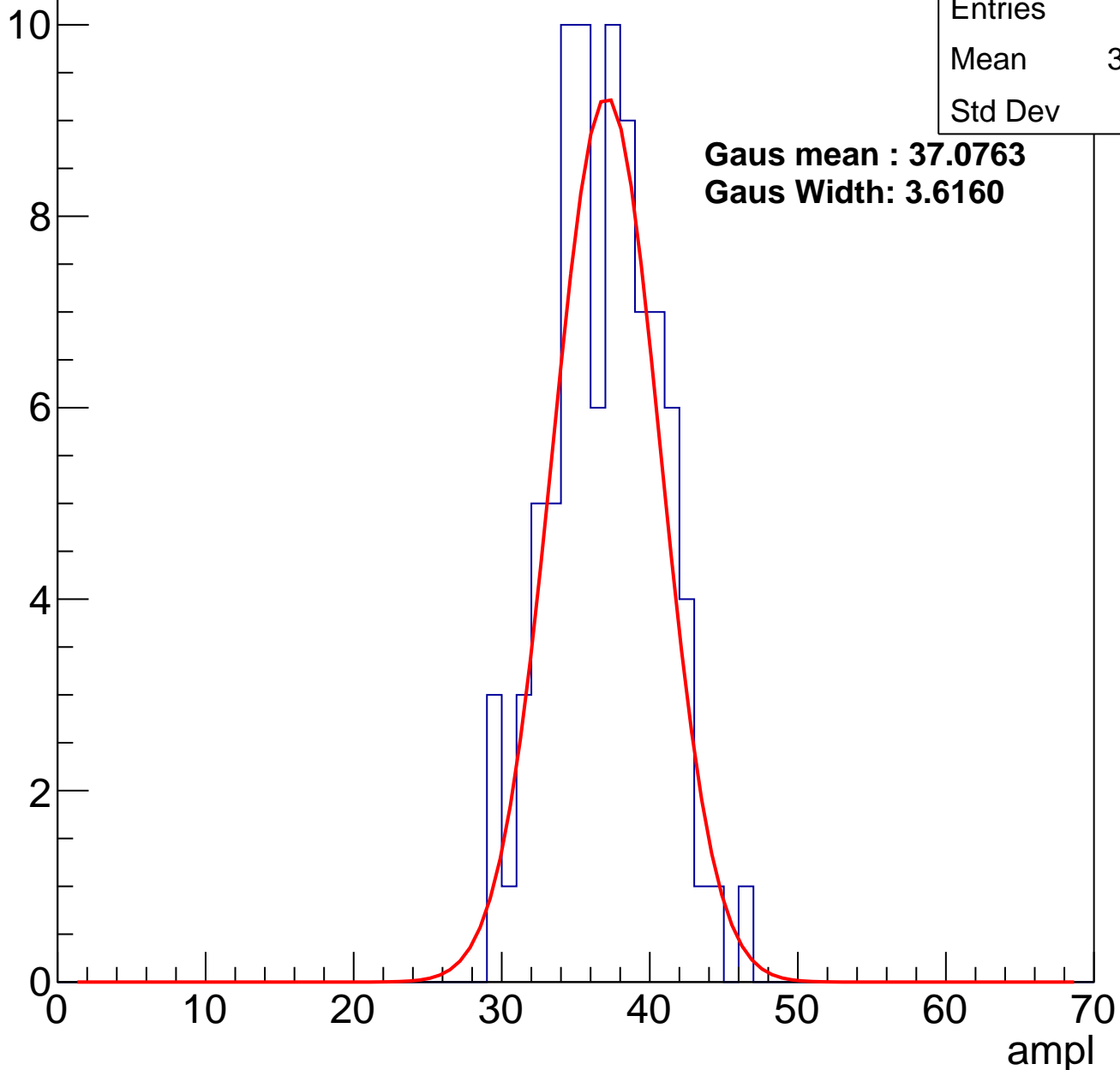
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	89
Mean	36.55
Std Dev	3.57

**Gaus mean : 37.0763**

**Gaus Width: 3.6160**

Entry

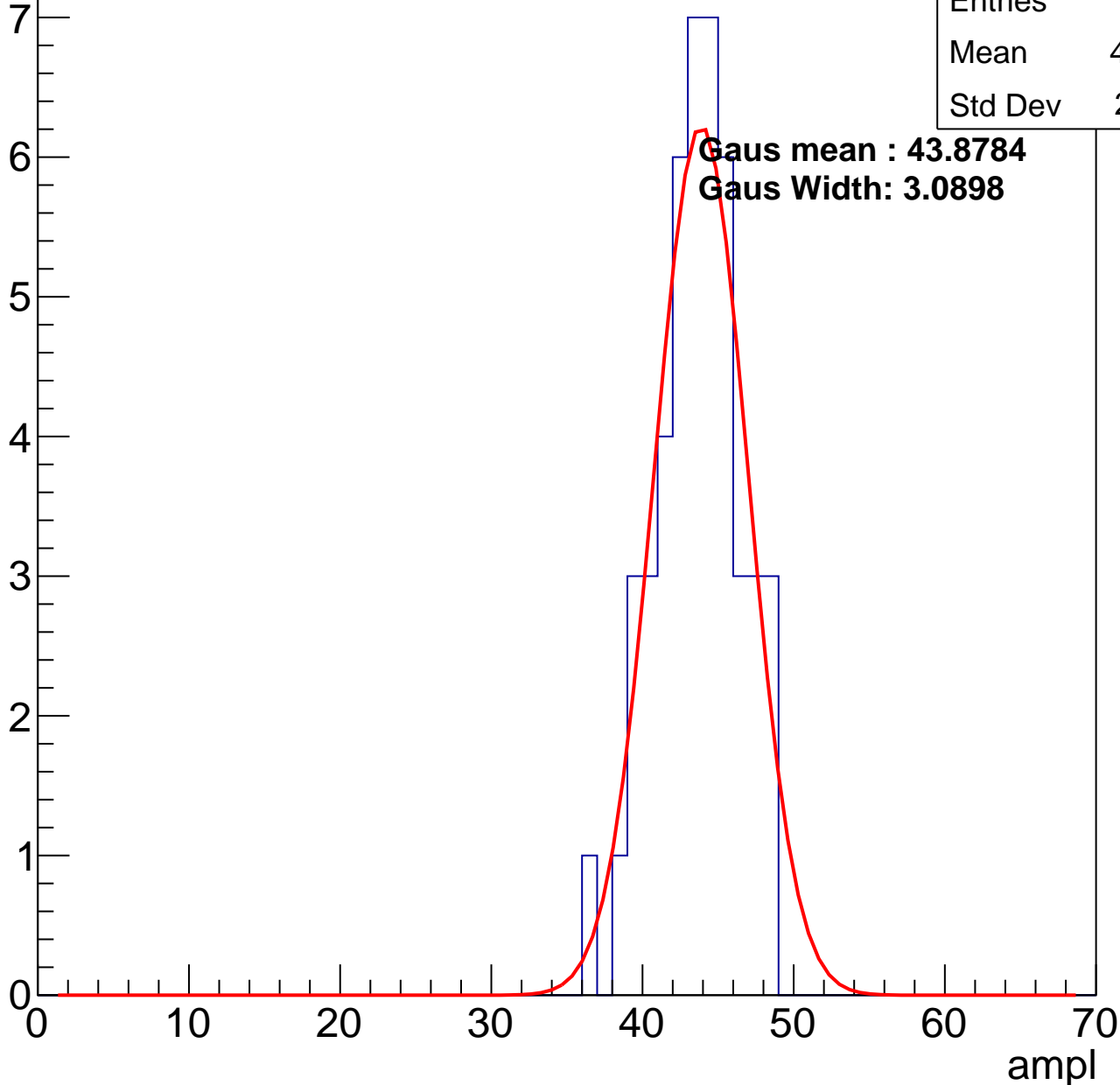


# B0L102S, U17-ch21, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	43.17
Std Dev	2.731



# B0L102S, U17-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	62
Mean	48.97
Std Dev	2.924

Entry

10

8

6

4

2

0

0

10

20

30

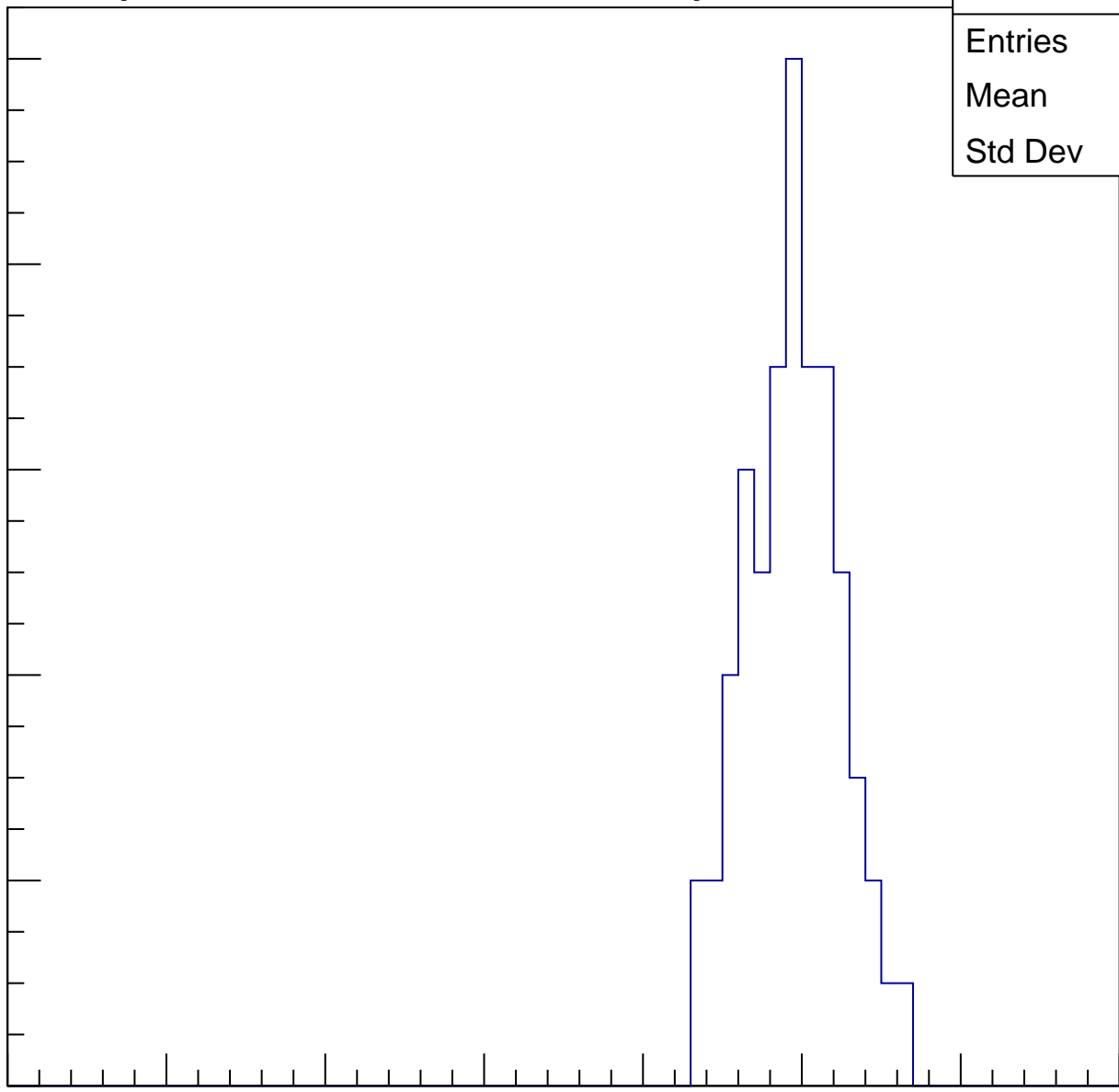
40

50

60

70

ampl

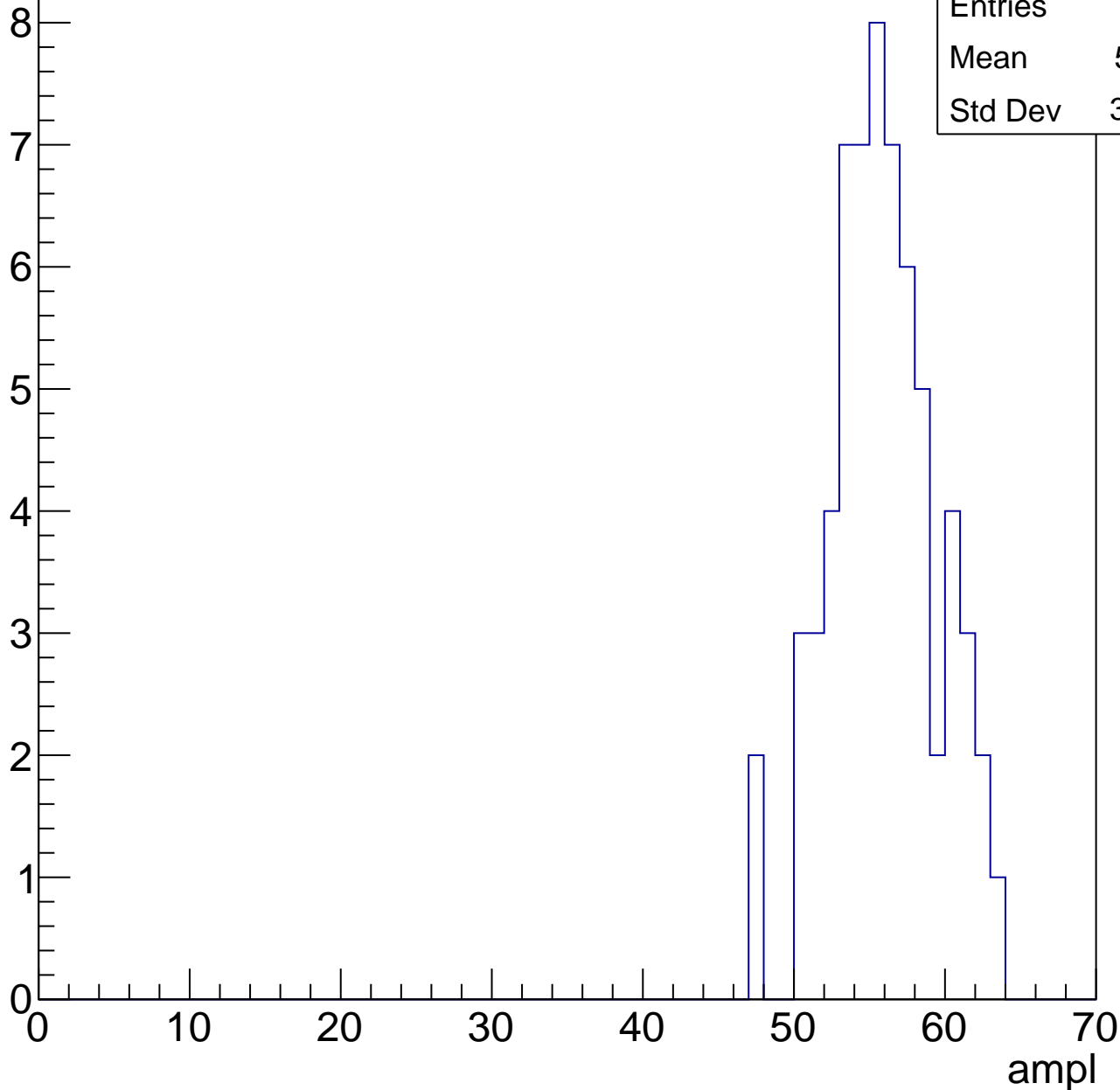


# B0L102S, U17-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	55.41
Std Dev	3.512

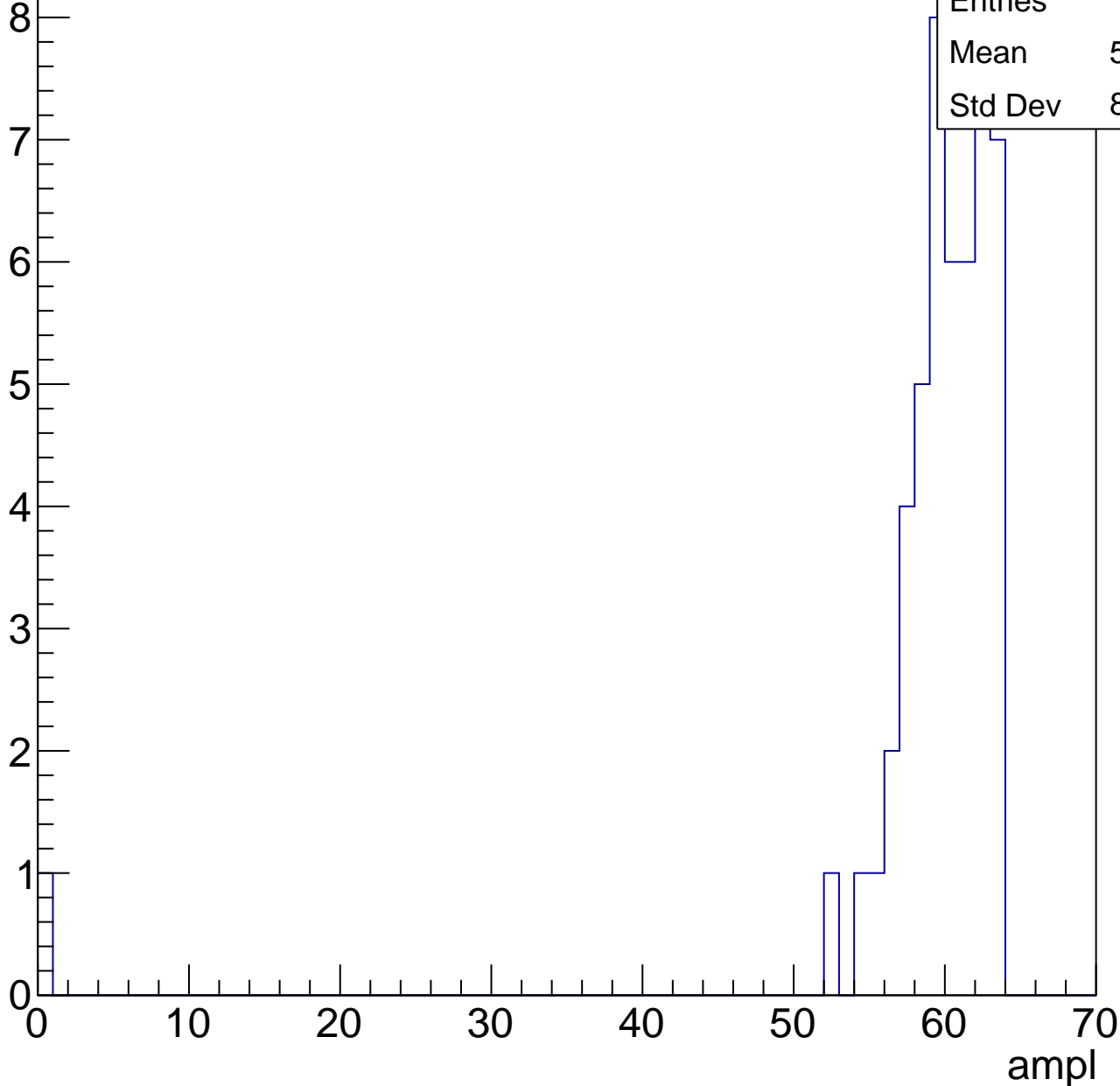


# B0L102S, U17-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.52
Std Dev	8.732



# B0L102S, U17-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl





# B0L102S, U17-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	95
Mean	28.55
Std Dev	5.982

**Gaus mean : 29.4323**

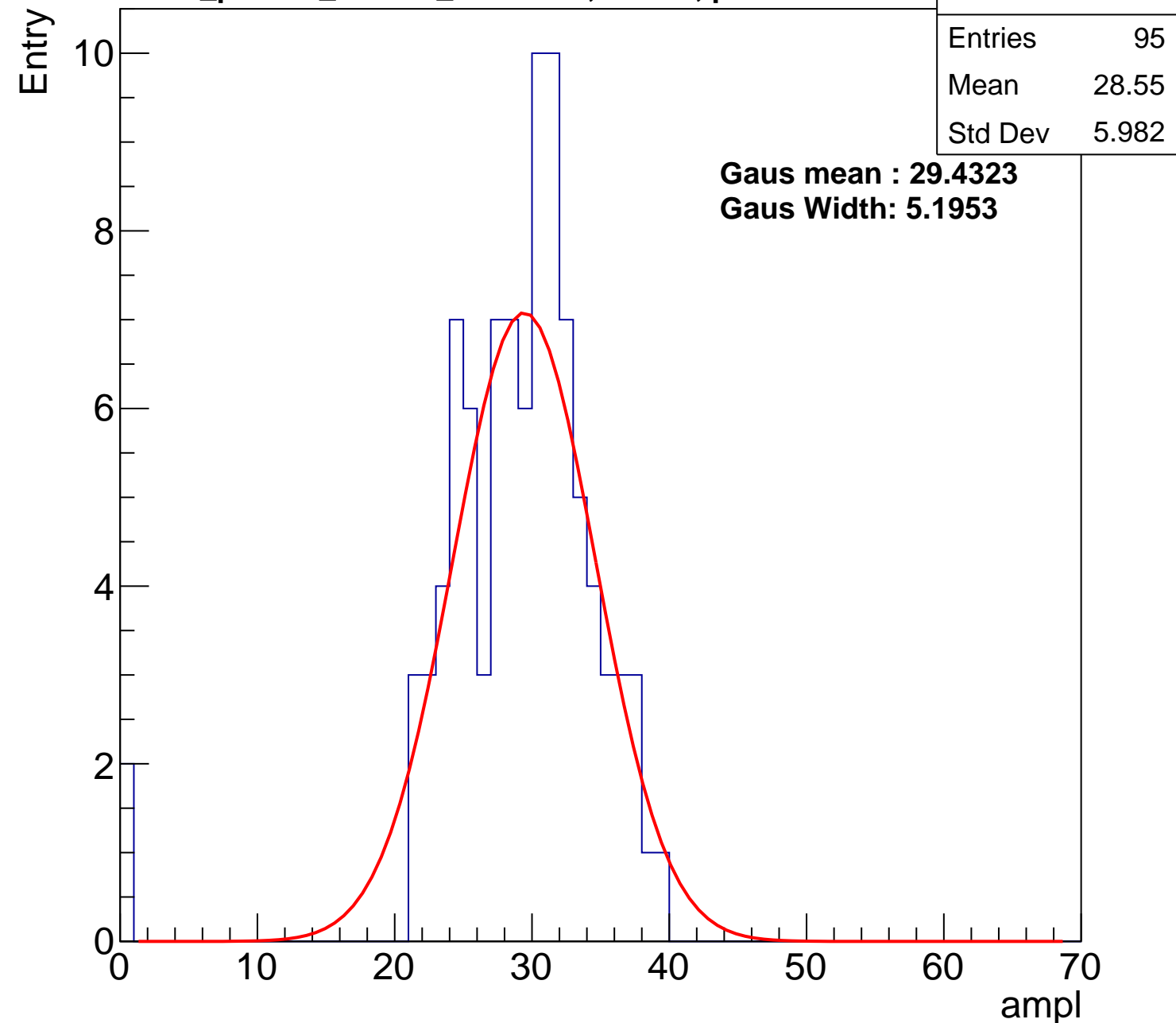
**Gaus Width: 5.1953**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch22, adc1

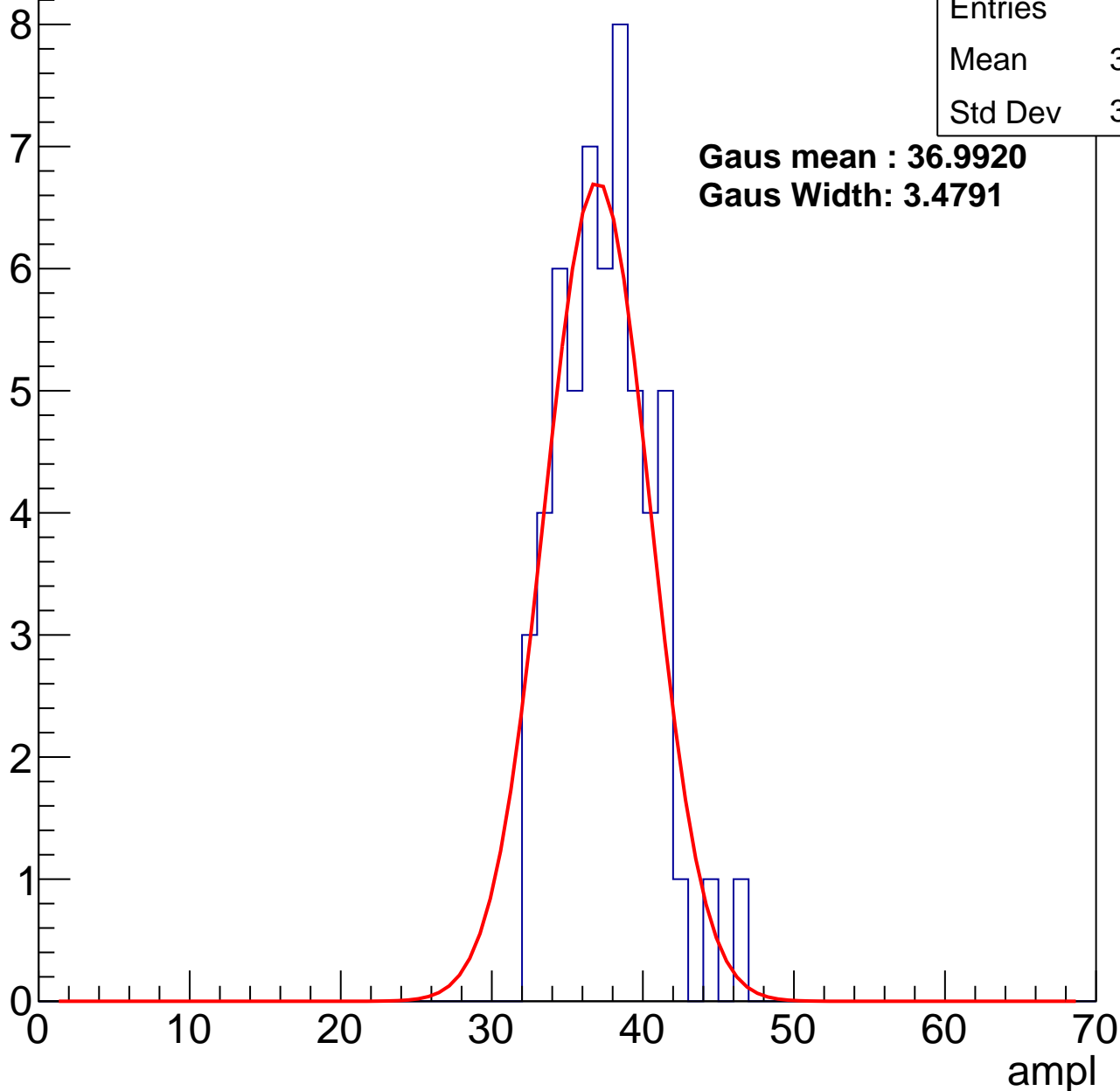
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	37.09
Std Dev	3.043

**Gaus mean : 36.9920**

**Gaus Width: 3.4791**



# B0L102S, U17-ch22, adc2

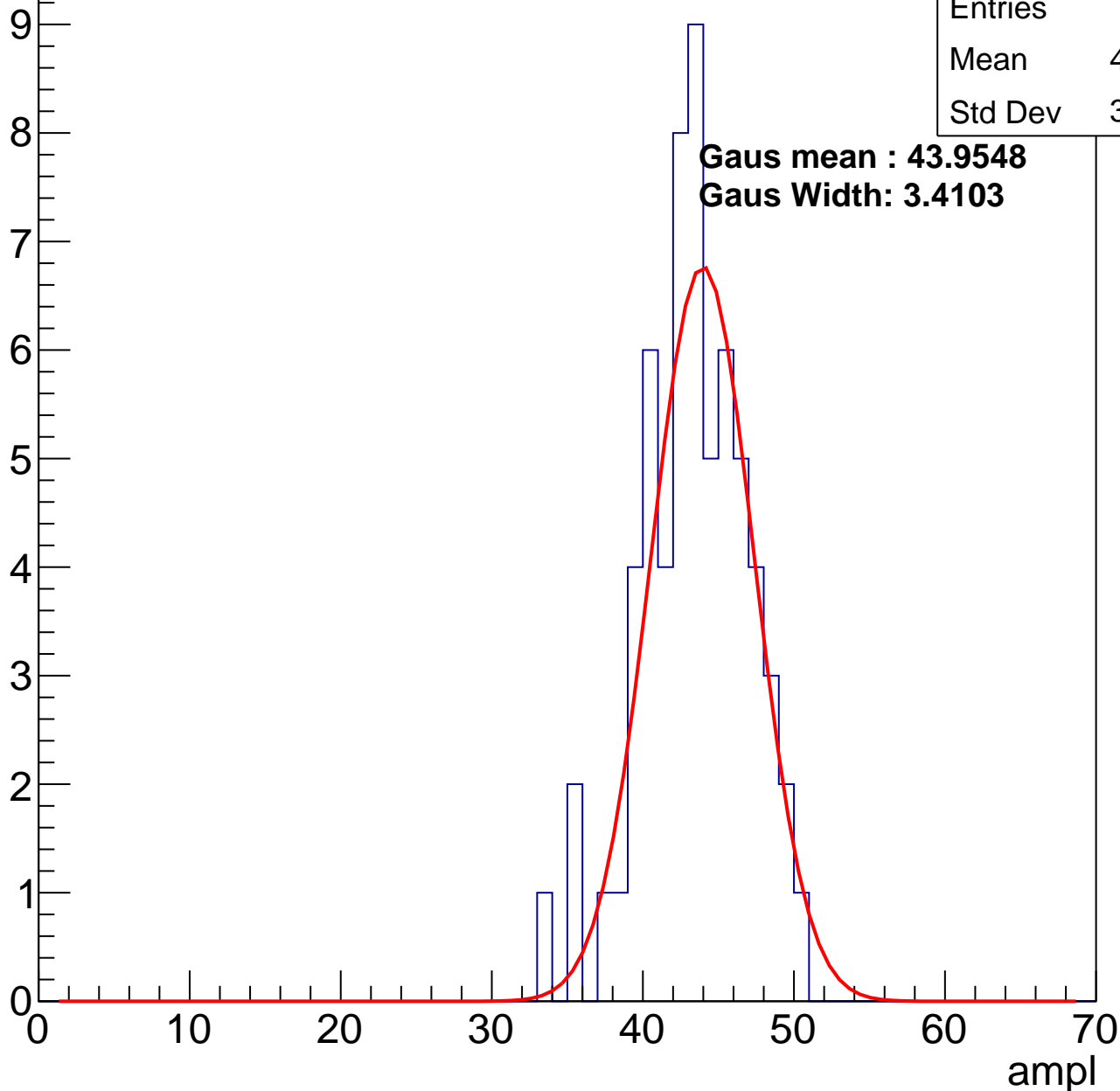
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.92
Std Dev	3.512

**Gaus mean : 43.9548**

**Gaus Width: 3.4103**

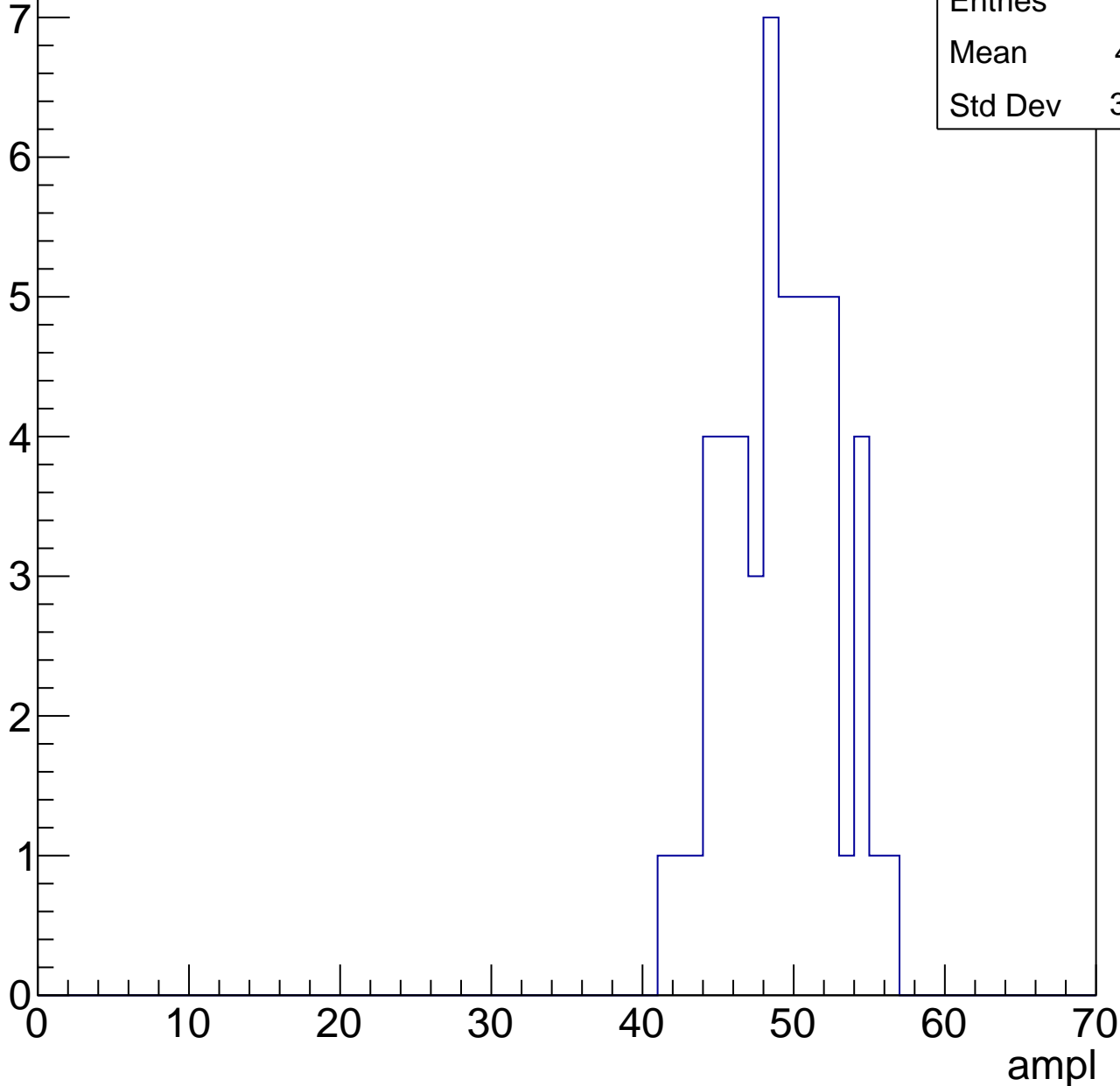


# B0L102S, U17-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	48.71
Std Dev	3.494

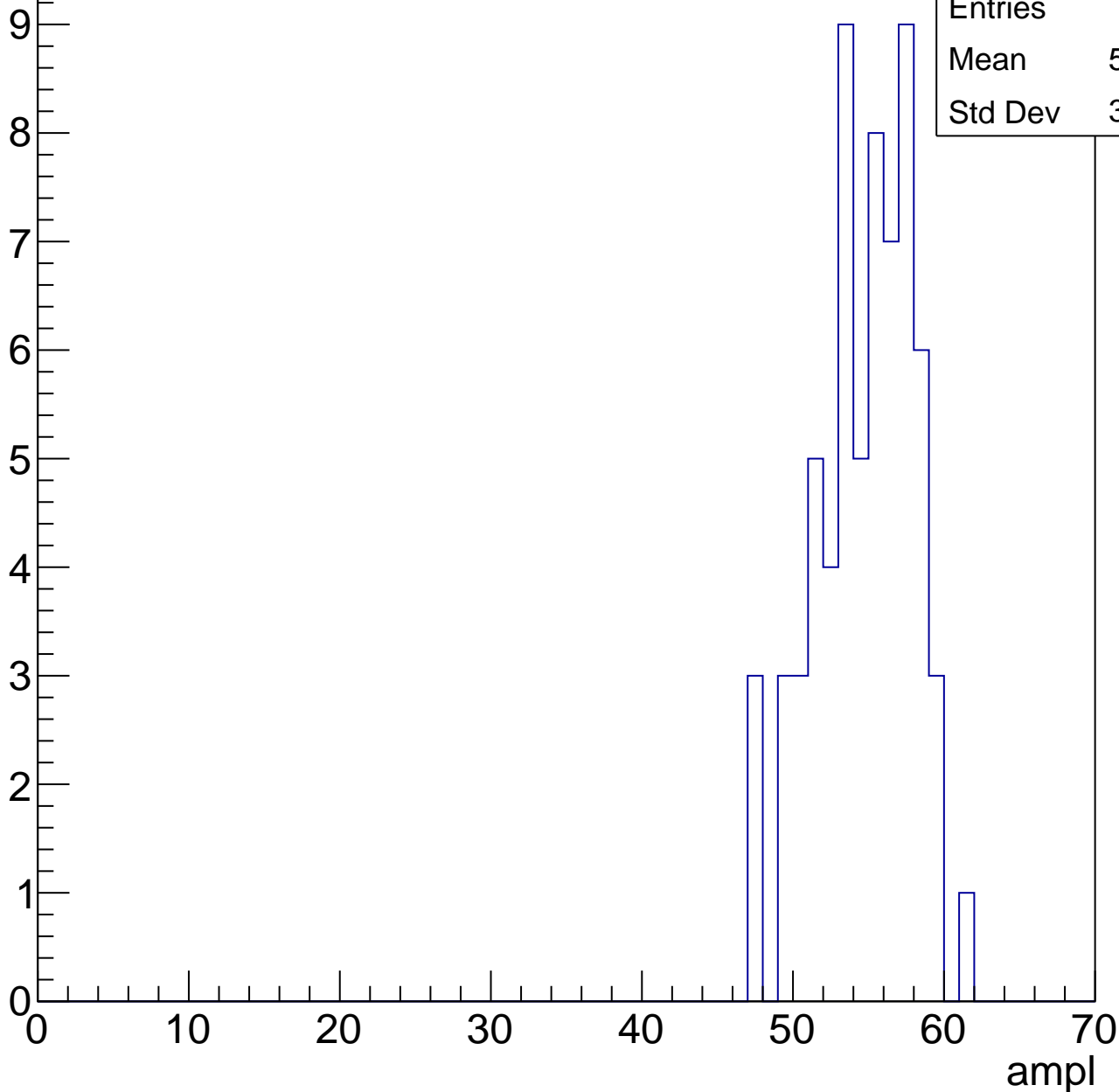


# B0L102S, U17-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	54.23
Std Dev	3.195



# B0L102S, U17-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

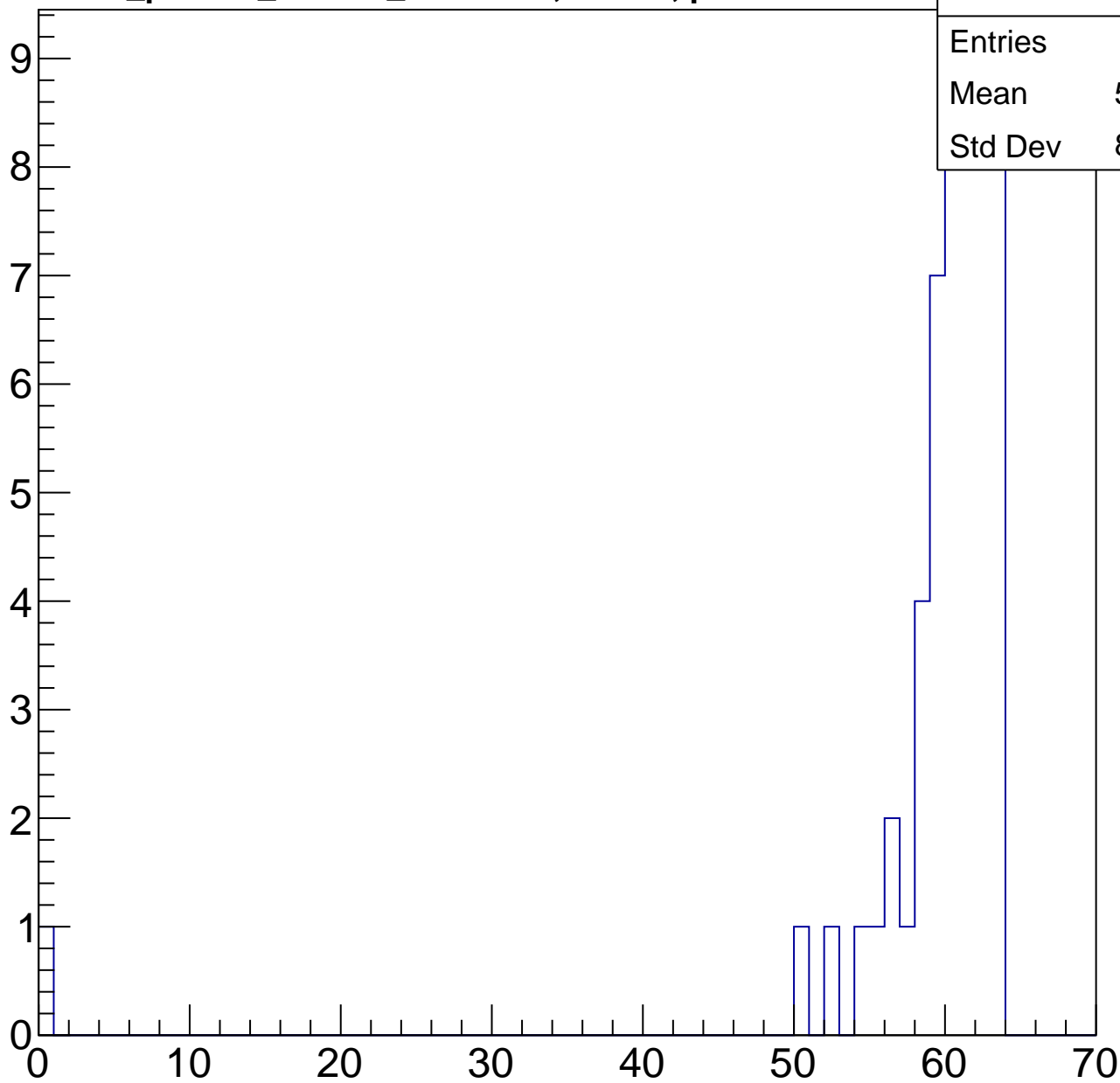
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.81
Std Dev	8.531

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch23, adc0

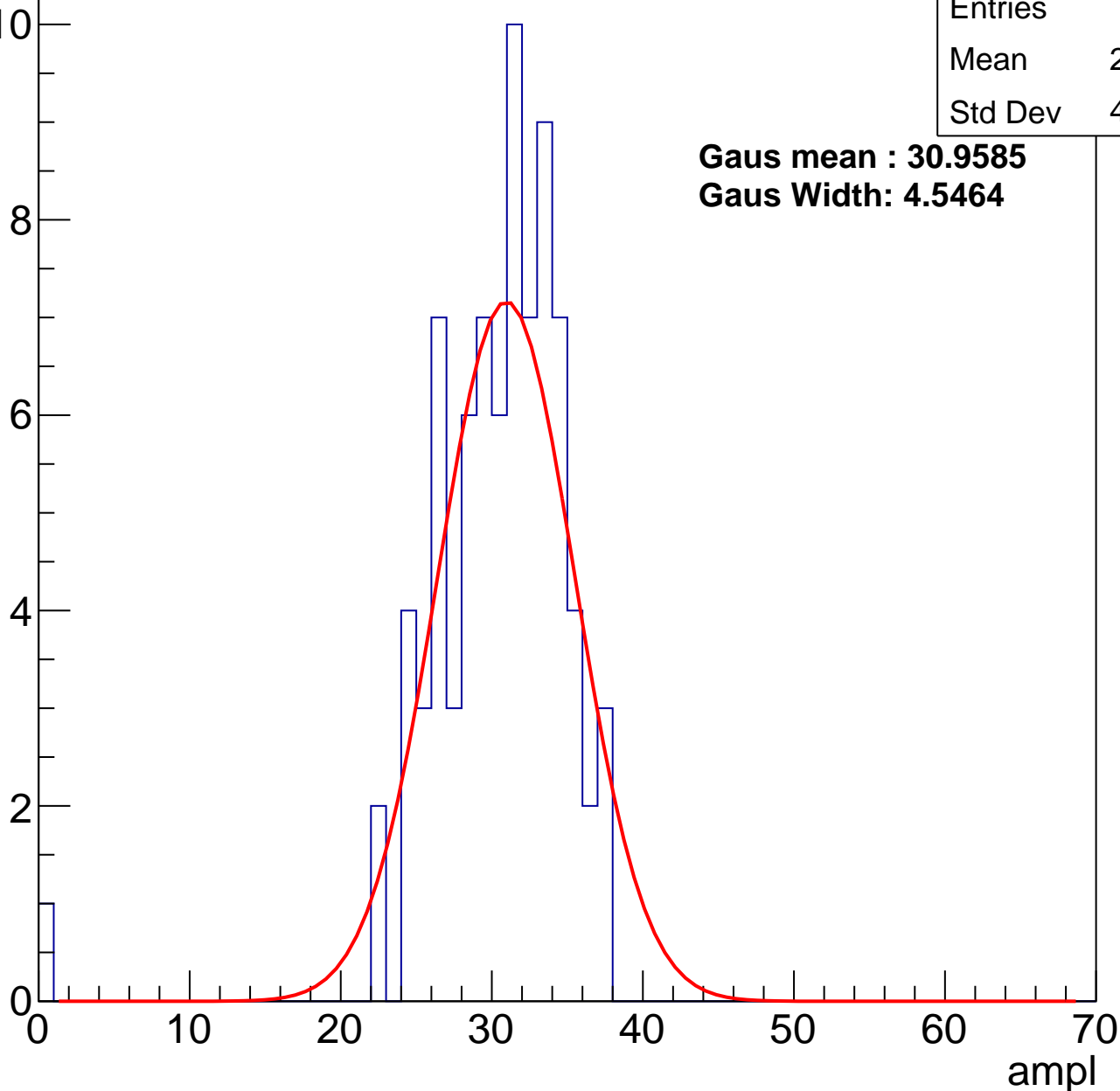
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.89
Std Dev	4.927

**Gaus mean : 30.9585**

**Gaus Width: 4.5464**



# B0L102S, U17-ch23, adc1

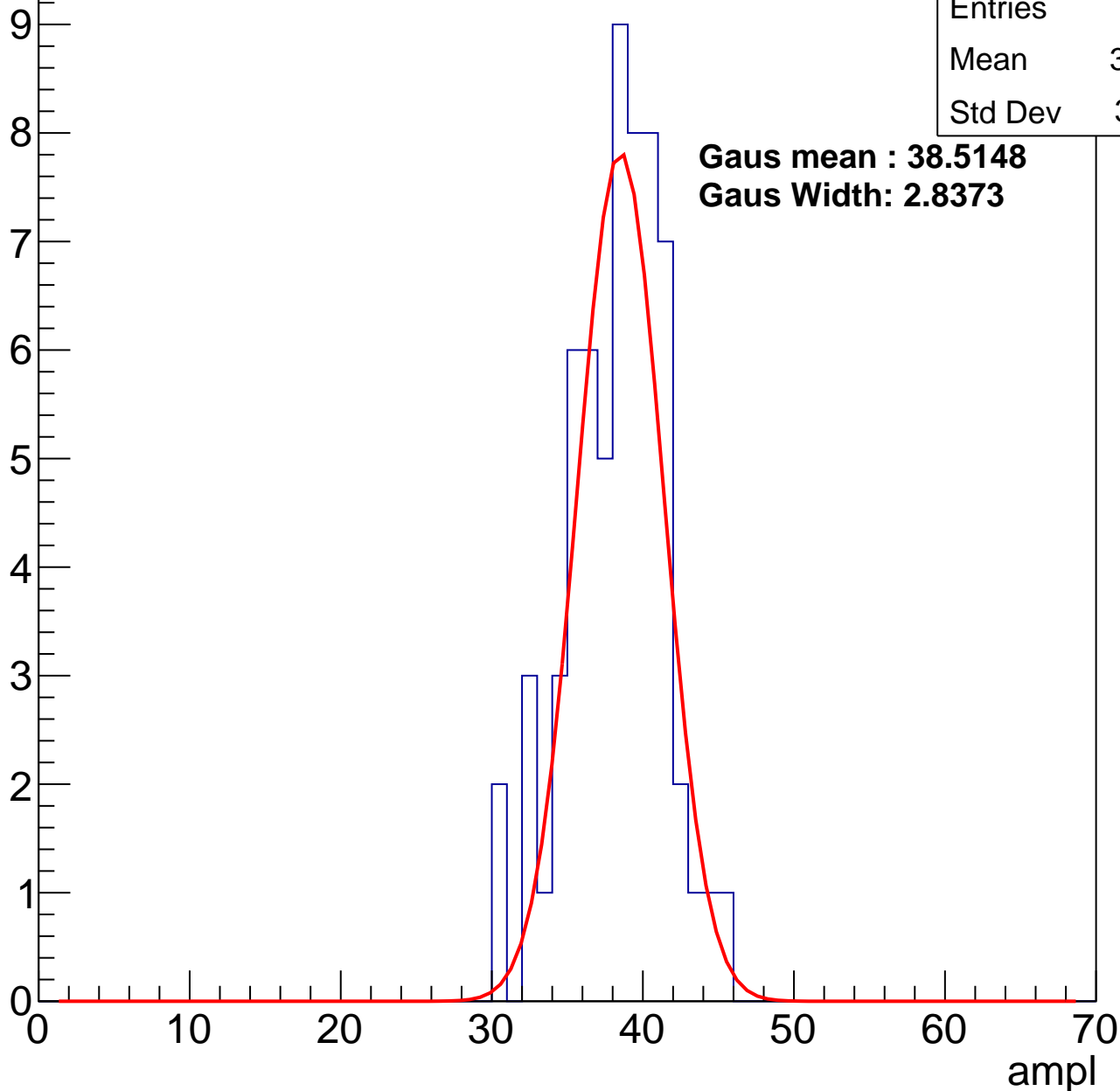
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	37.76
Std Dev	3.171

**Gaus mean : 38.5148**

**Gaus Width: 2.8373**



# B0L102S, U17-ch23, adc2

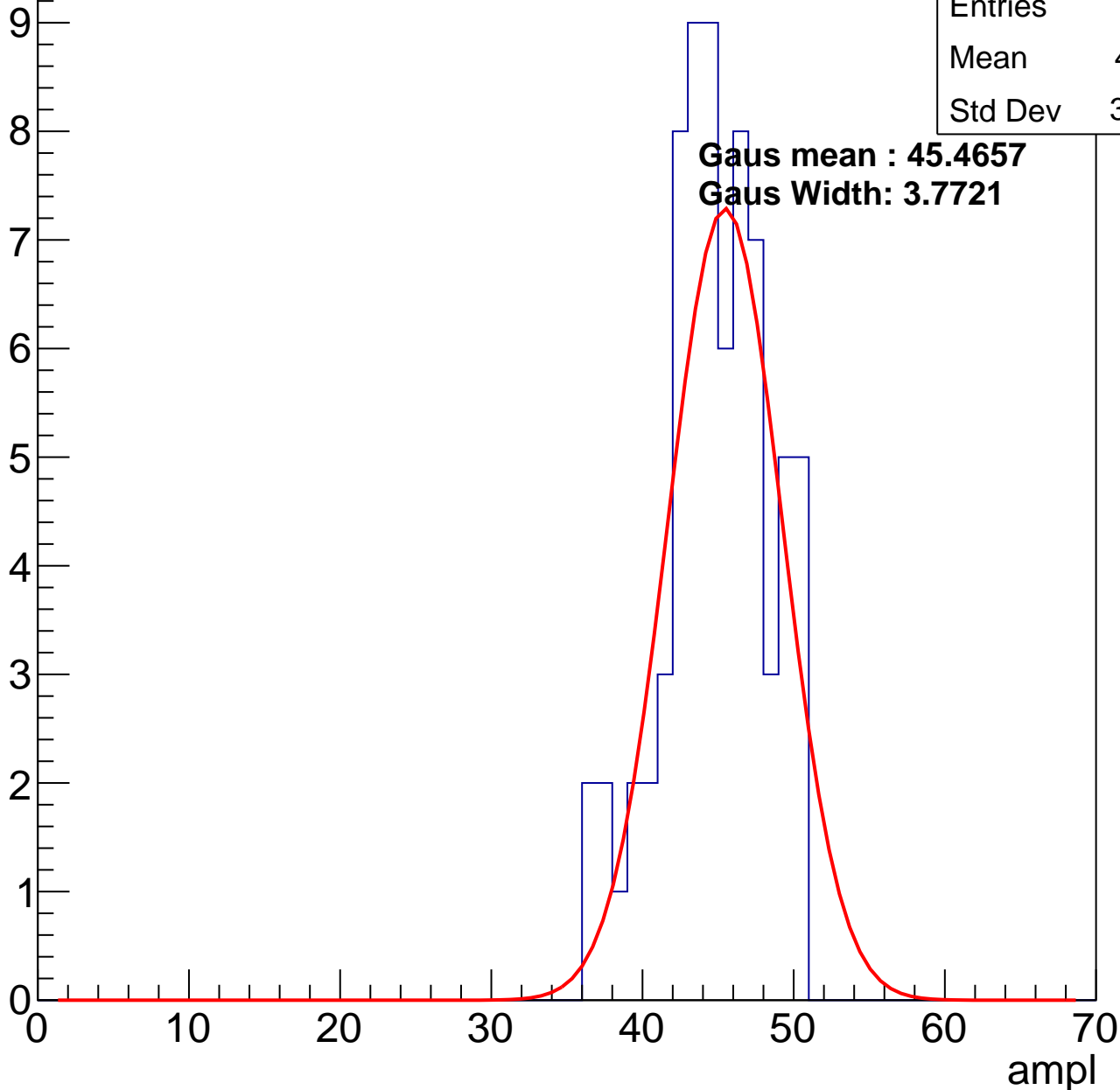
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	44.31
Std Dev	3.455

**Gaus mean : 45.4657**

**Gaus Width: 3.7721**

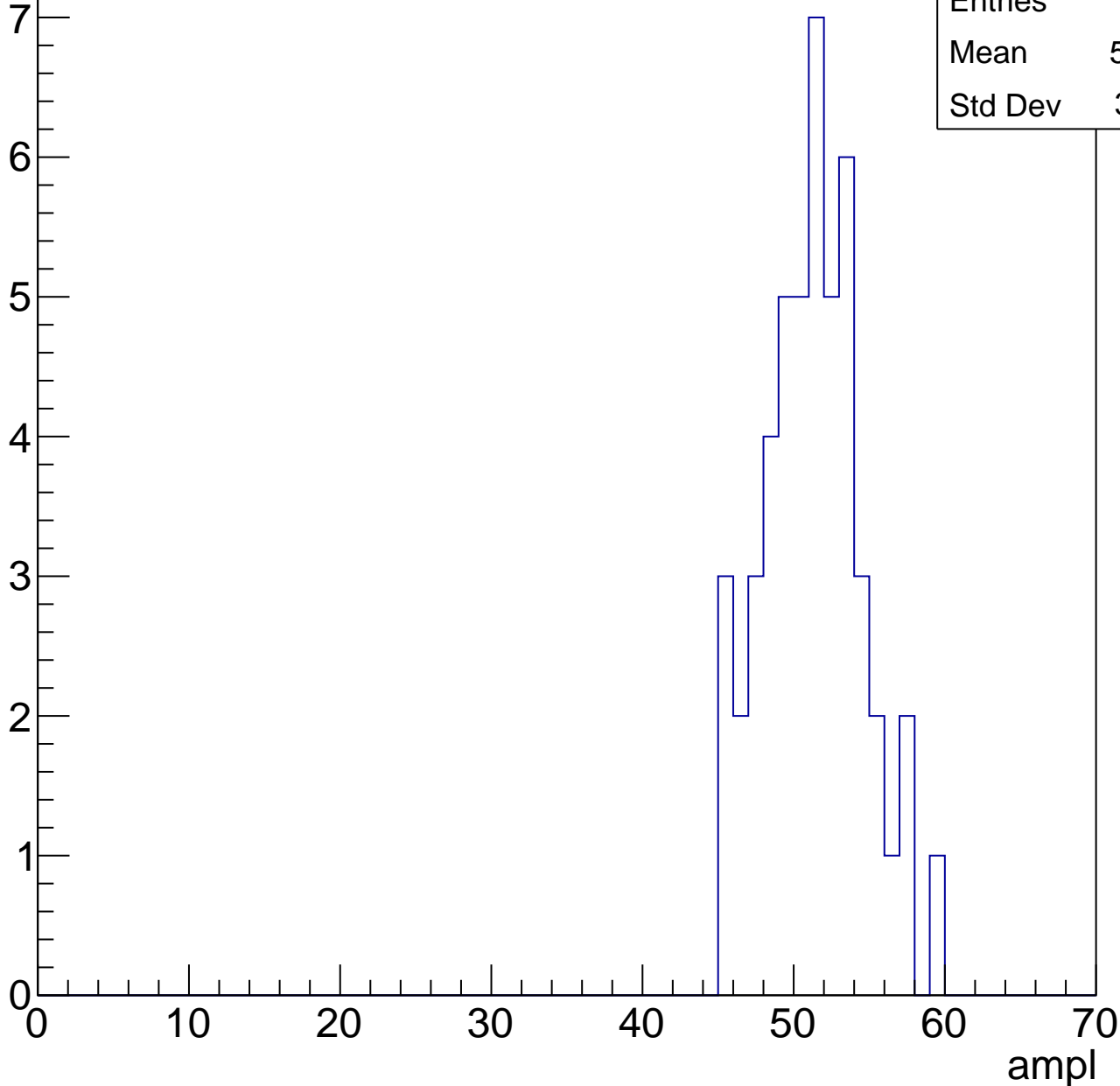


# B0L102S, U17-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	50.84
Std Dev	3.241



# B0L102S, U17-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	77
Mean	56.61
Std Dev	3.665

Entry

10

8

6

4

2

0

0

10

20

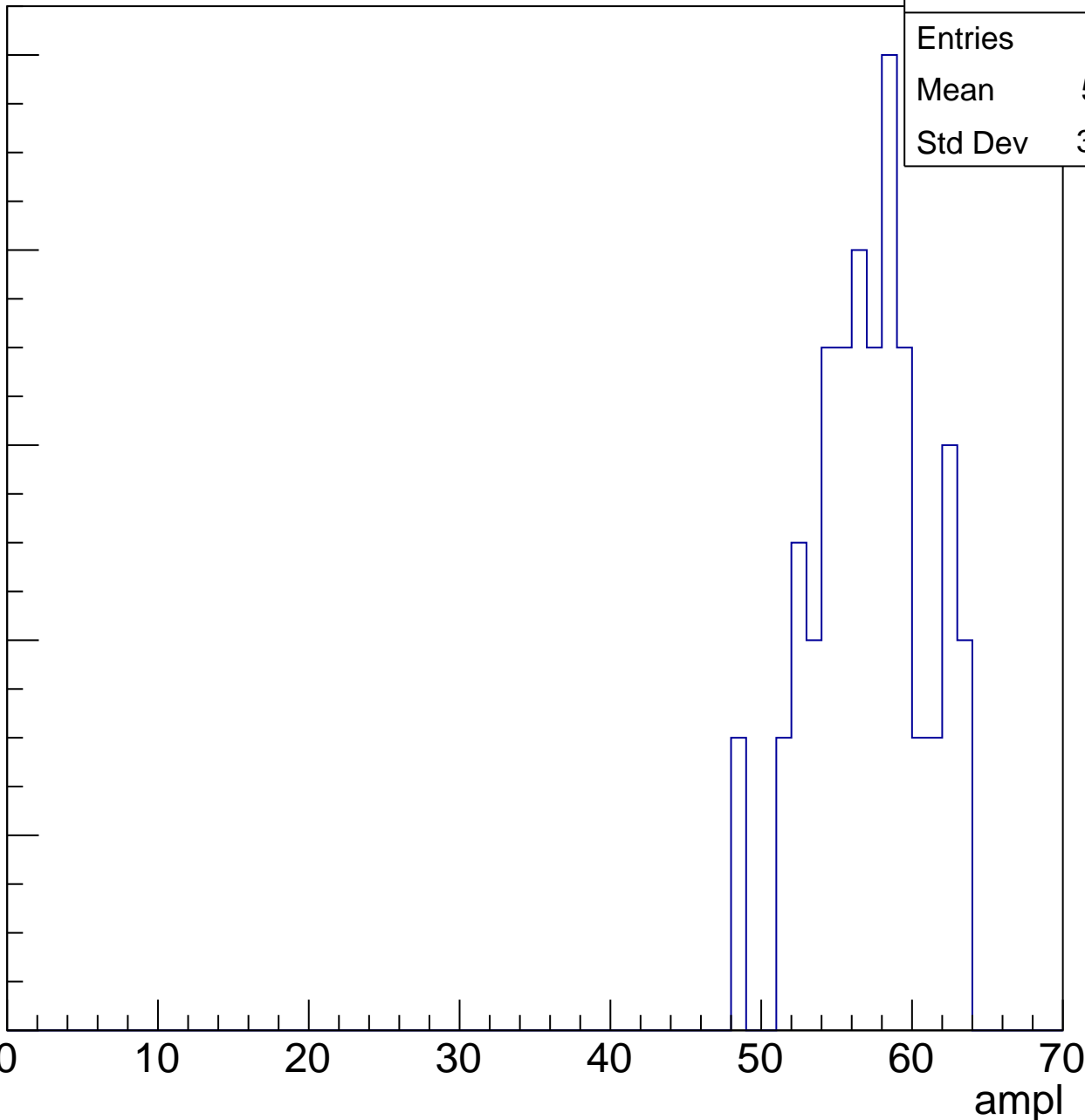
30

40

50

60

ampl

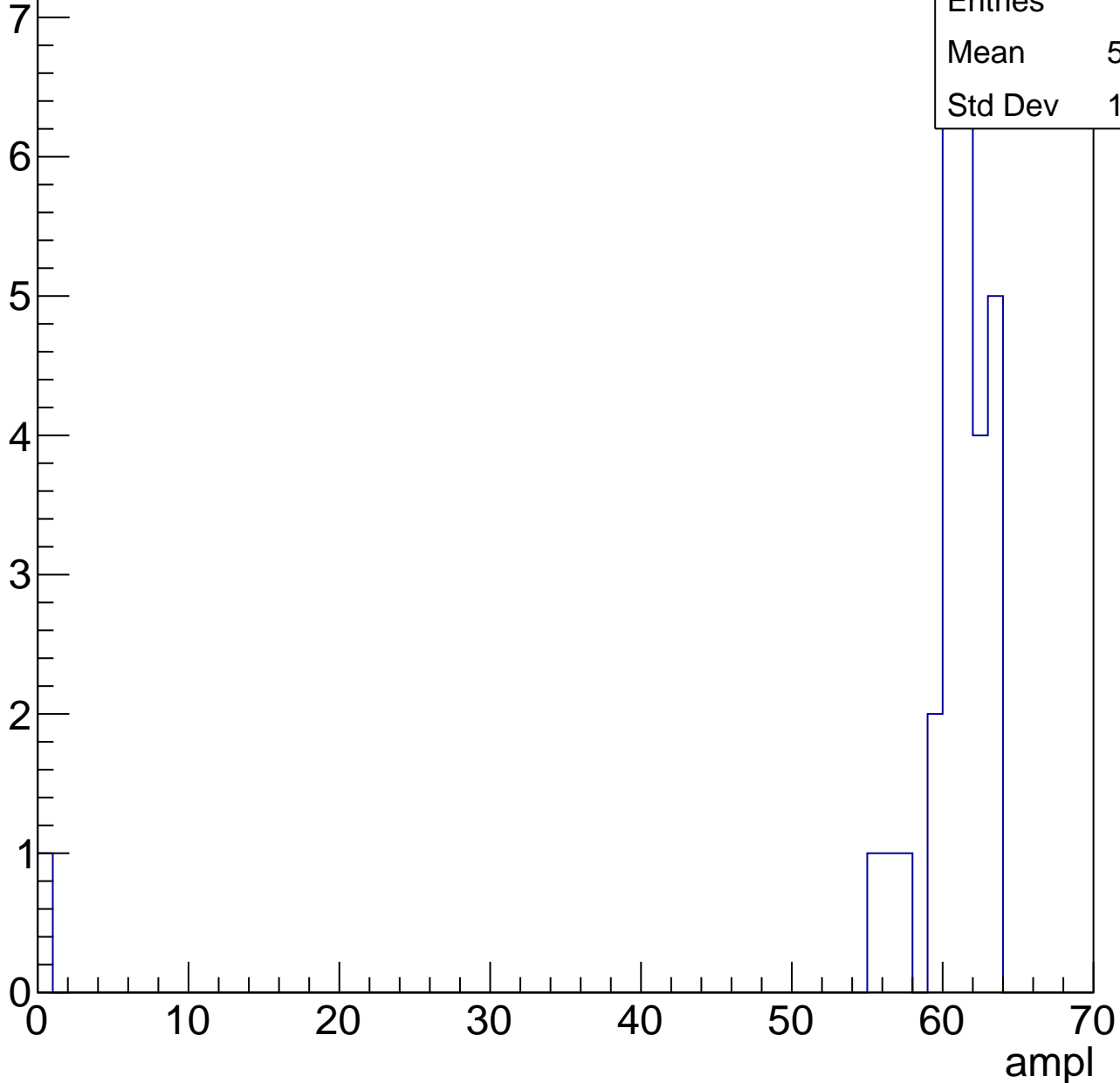


# B0L102S, U17-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	29
Mean	58.48
Std Dev	11.22



# B0L102S, U17-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch24, adc0

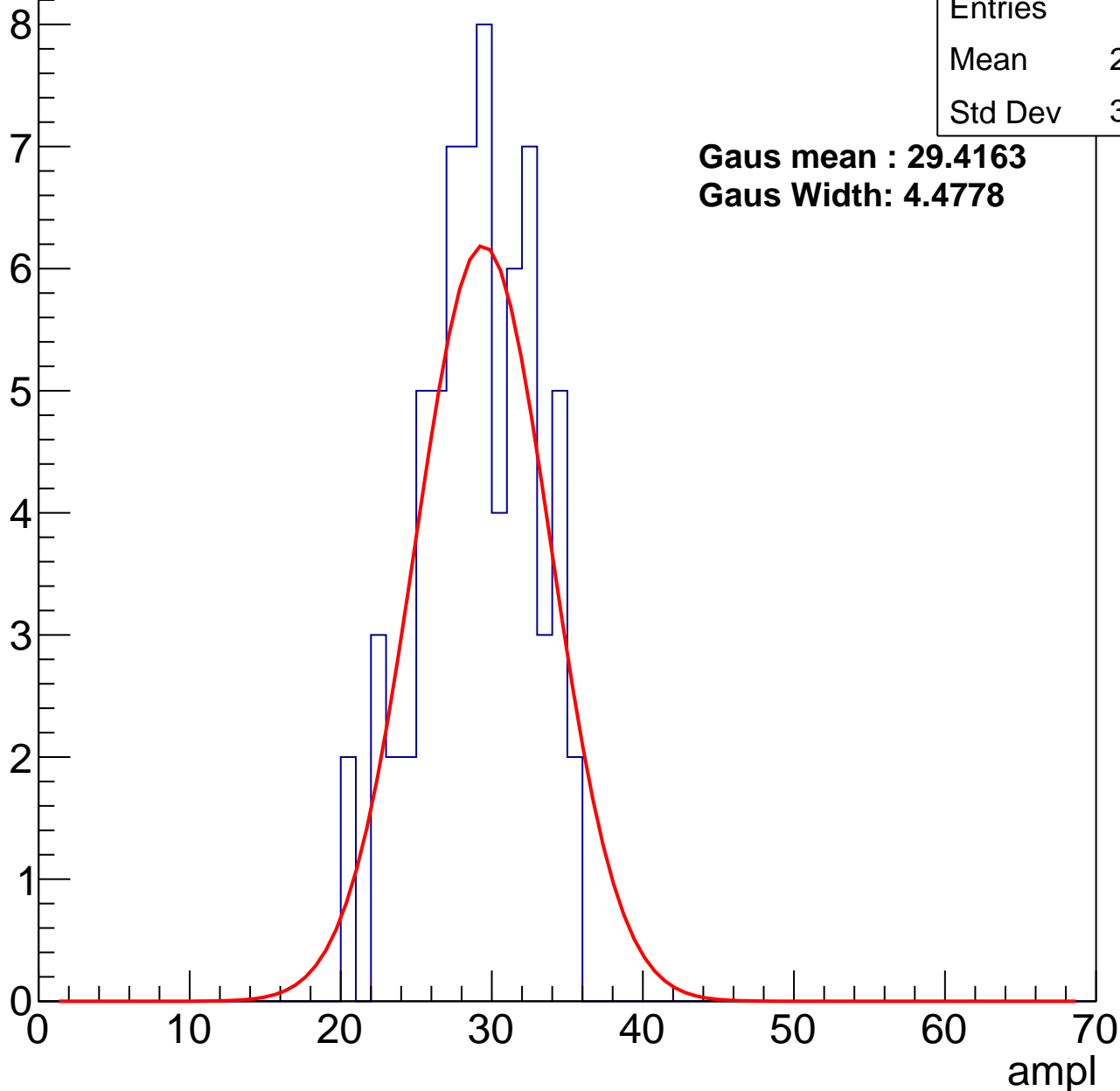
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.54
Std Dev	3.664

**Gaus mean : 29.4163**

**Gaus Width: 4.4778**



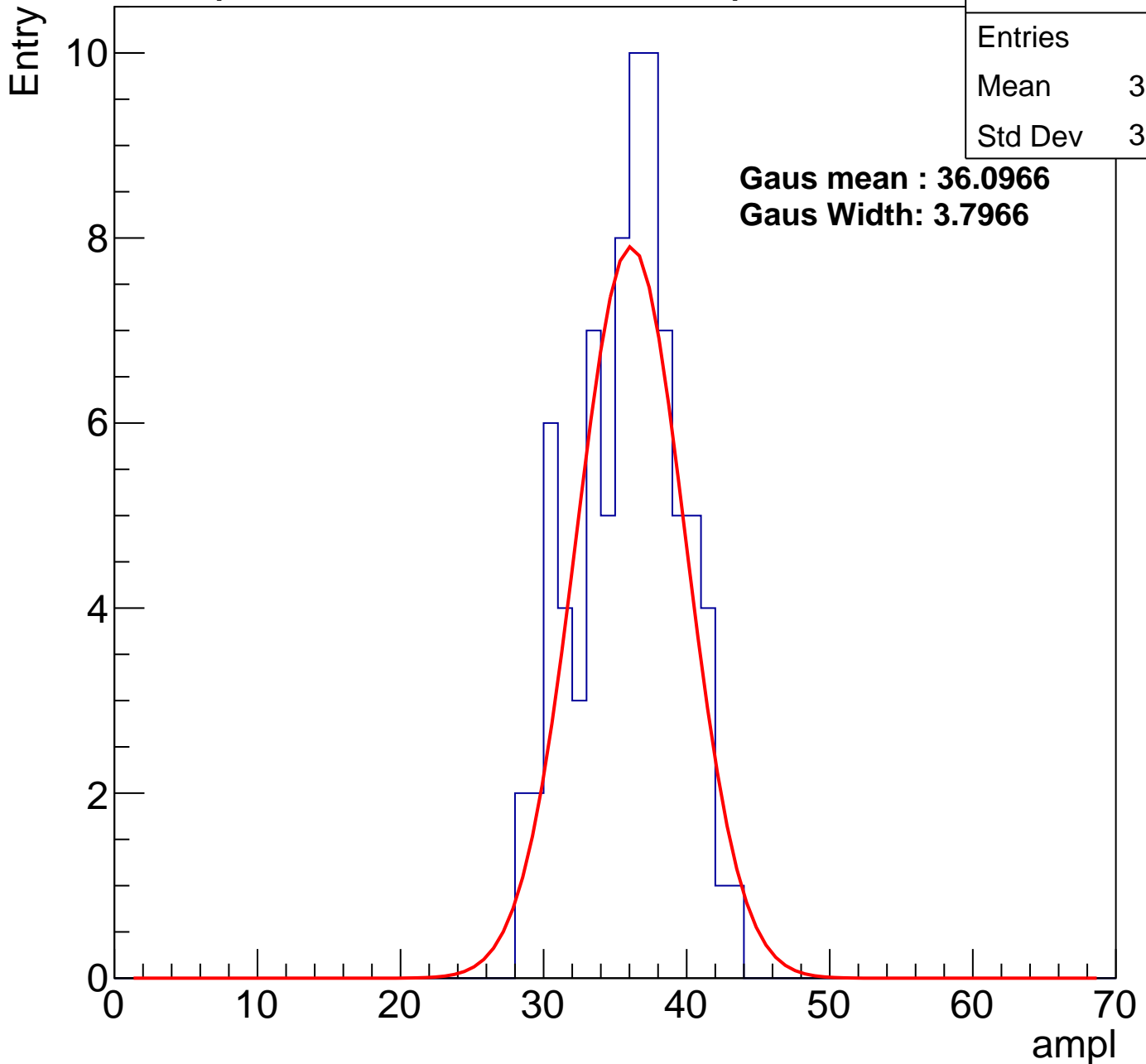
# B0L102S, U17-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	35.44
Std Dev	3.542

**Gaus mean : 36.0966**

**Gaus Width: 3.7966**



# B0L102S, U17-ch24, adc2

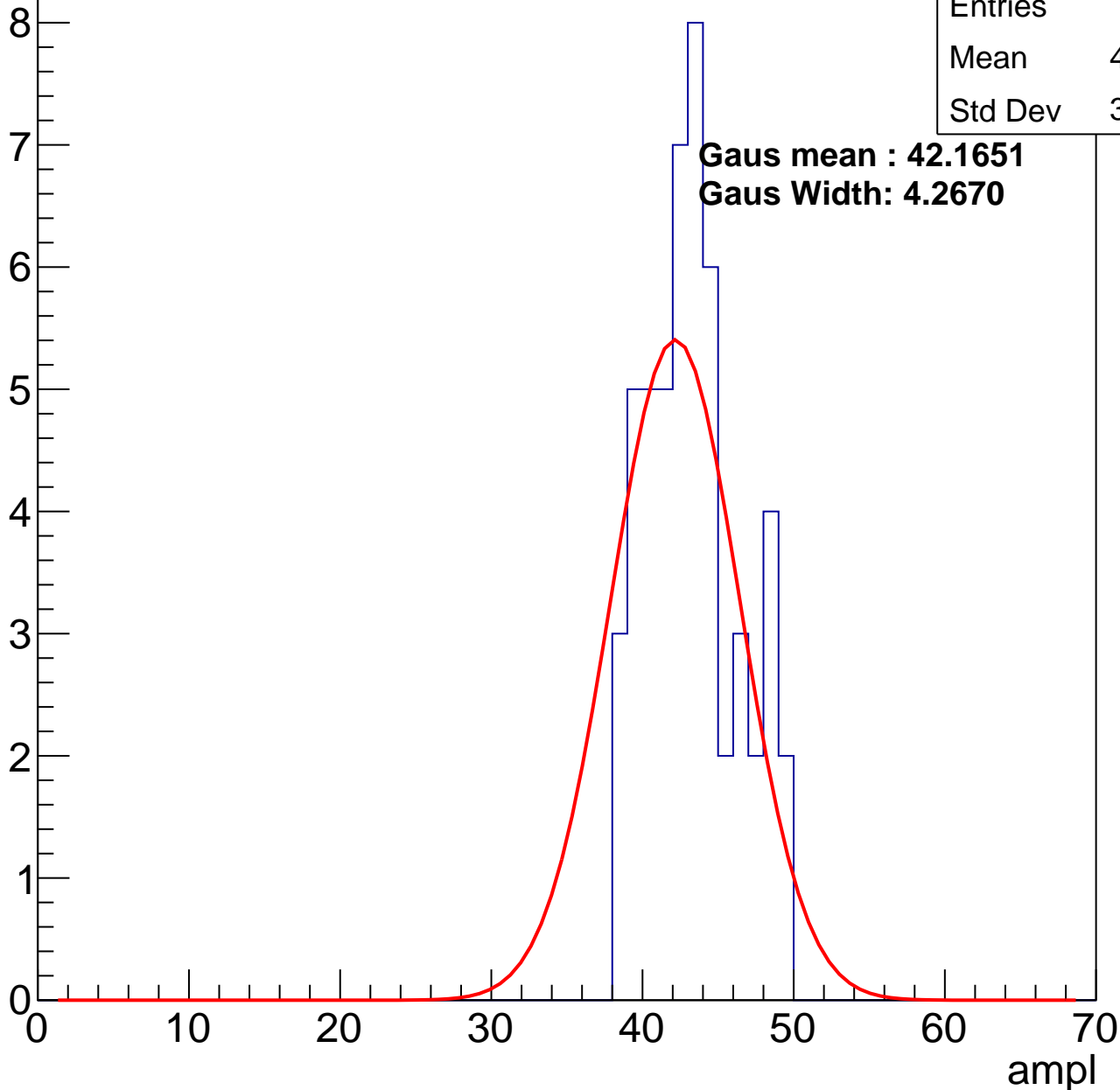
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	42.85
Std Dev	3.009

**Gaus mean : 42.1651**

**Gaus Width: 4.2670**

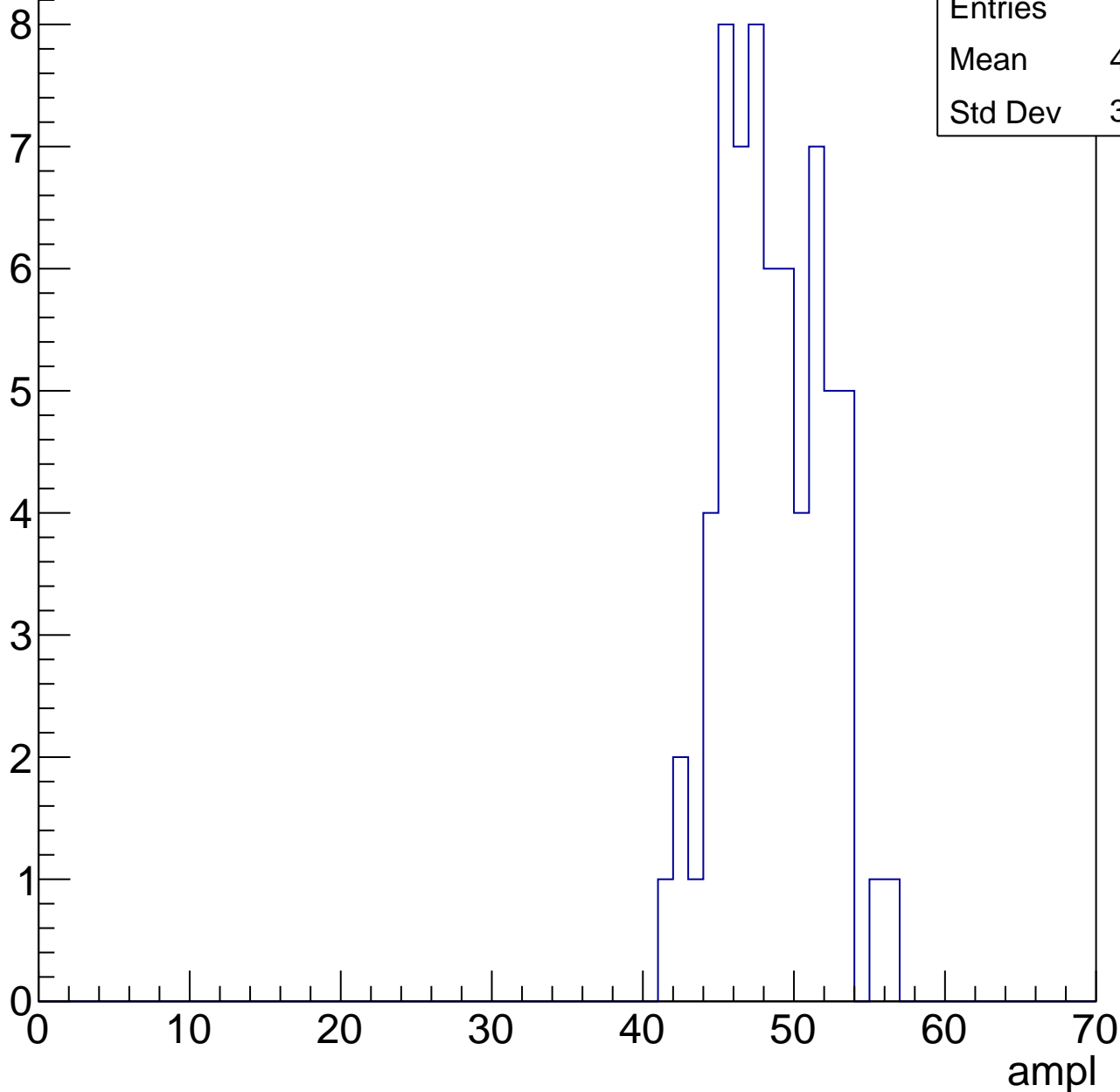


# B0L102S, U17-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.14
Std Dev	3.302

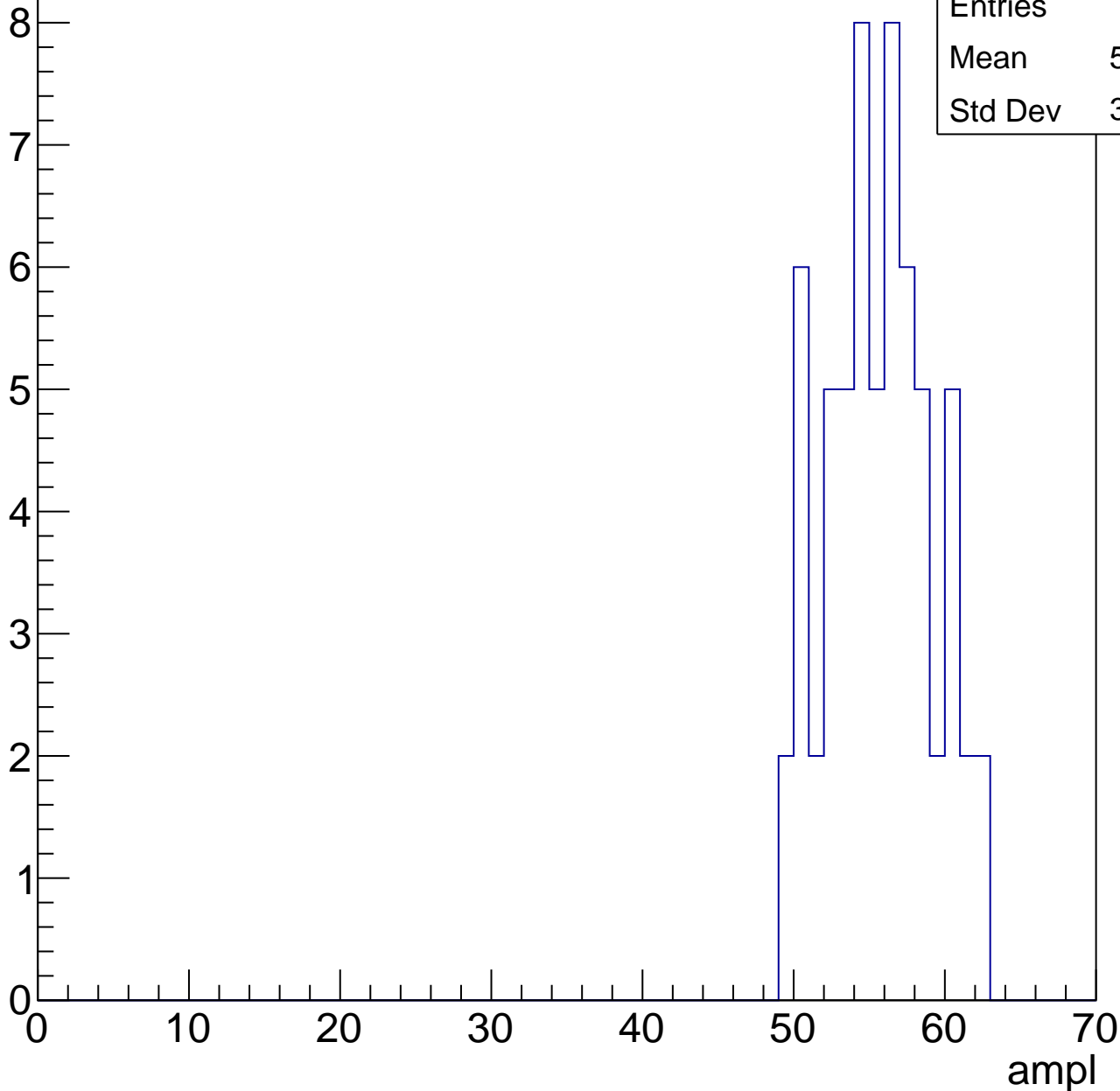


# B0L102S, U17-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.17
Std Dev	3.402

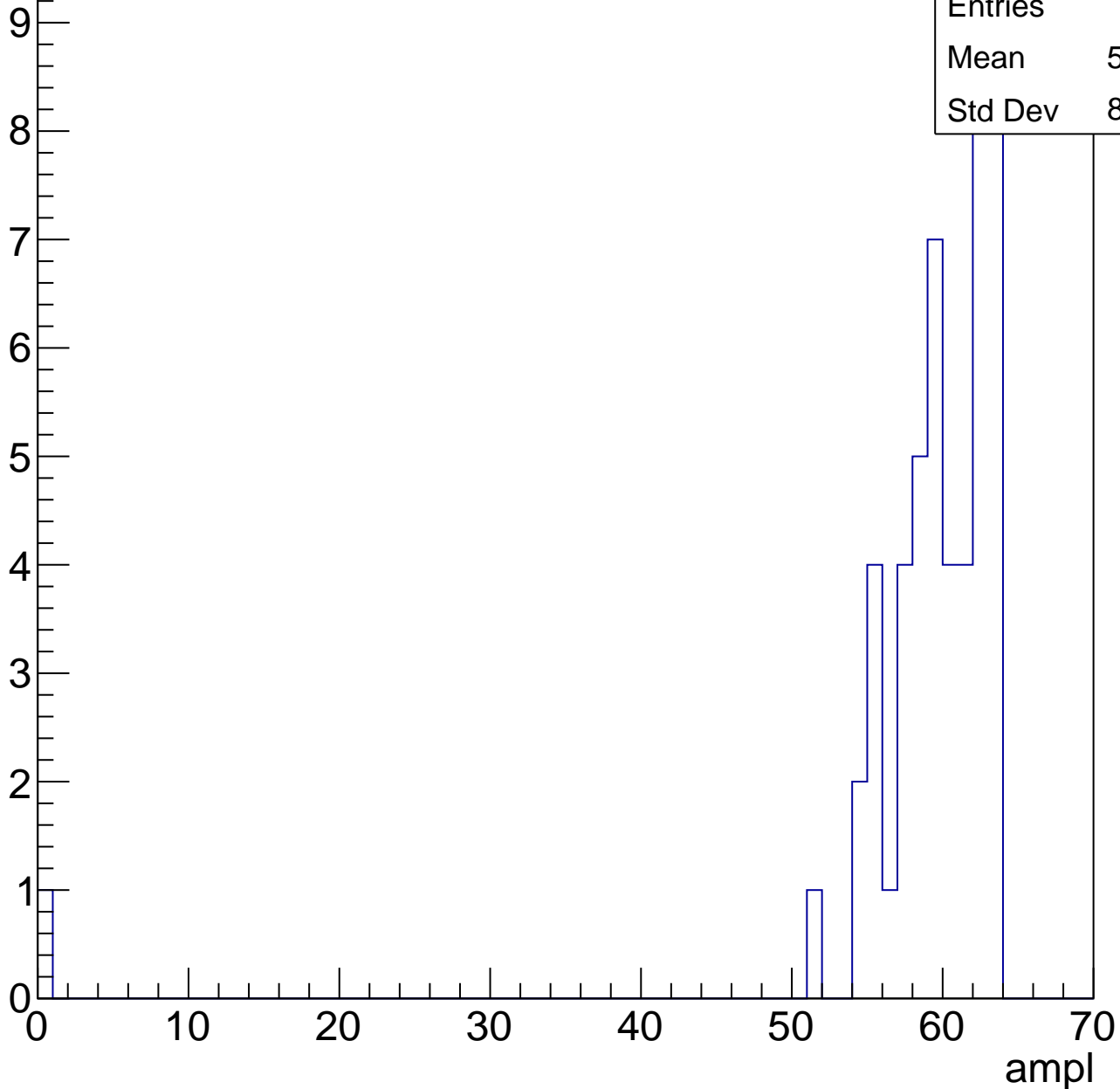


# B0L102S, U17-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

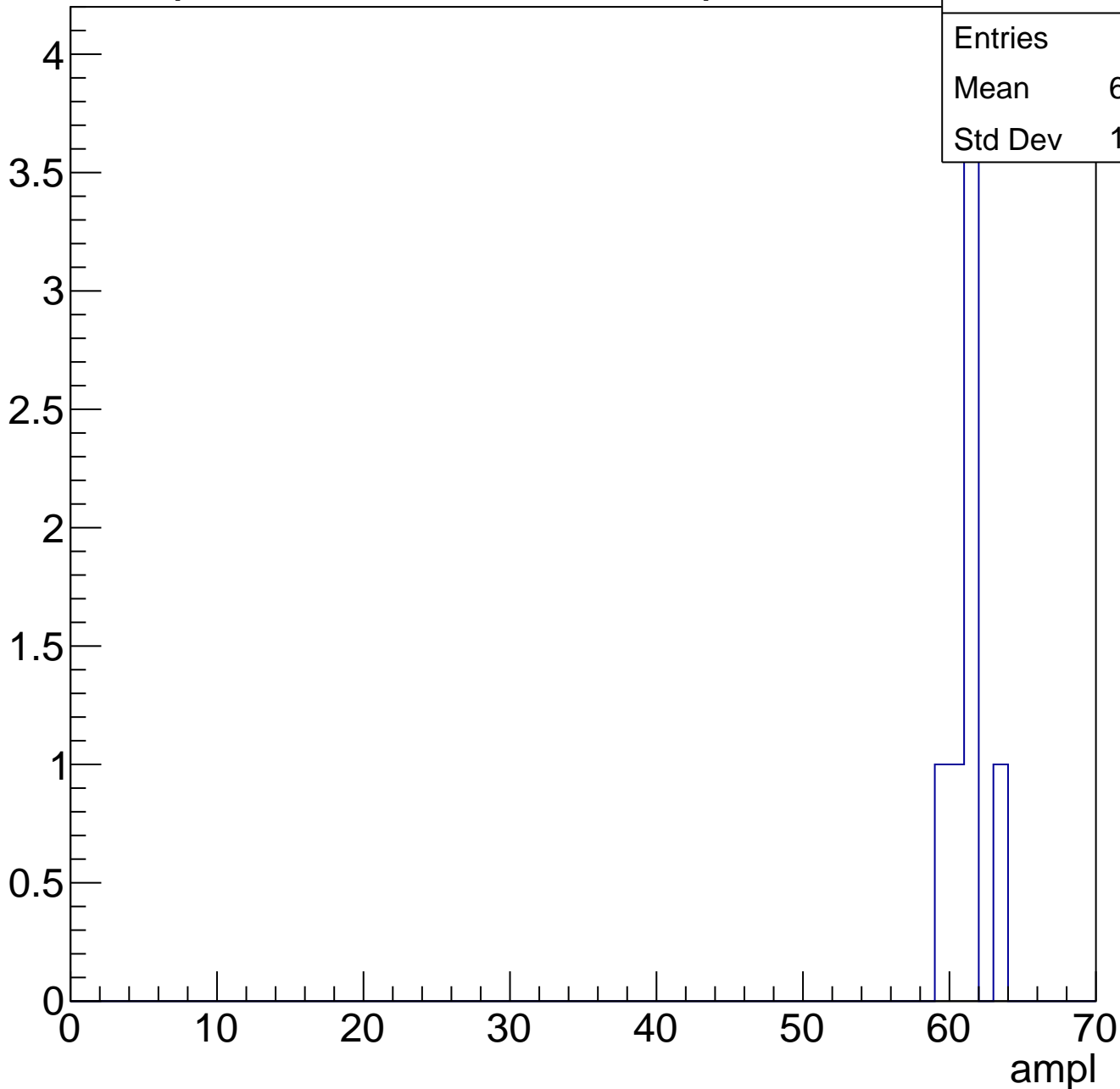
Entries	50
Mean	58.26
Std Dev	8.829



# B0L102S, U17-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	89
Mean	28.02
Std Dev	6.414

**Gaus mean : 29.7543**

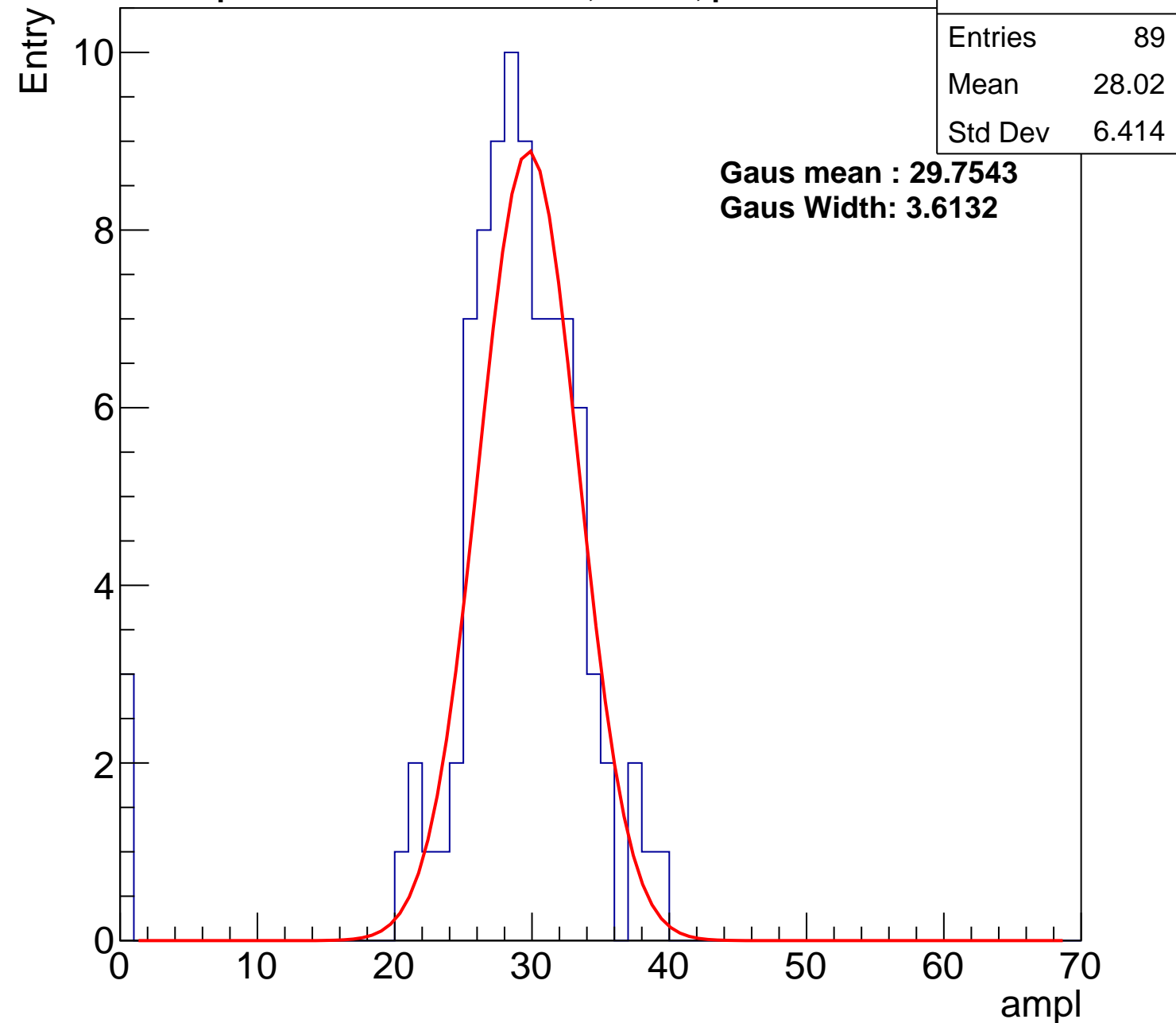
**Gaus Width: 3.6132**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch25, adc1

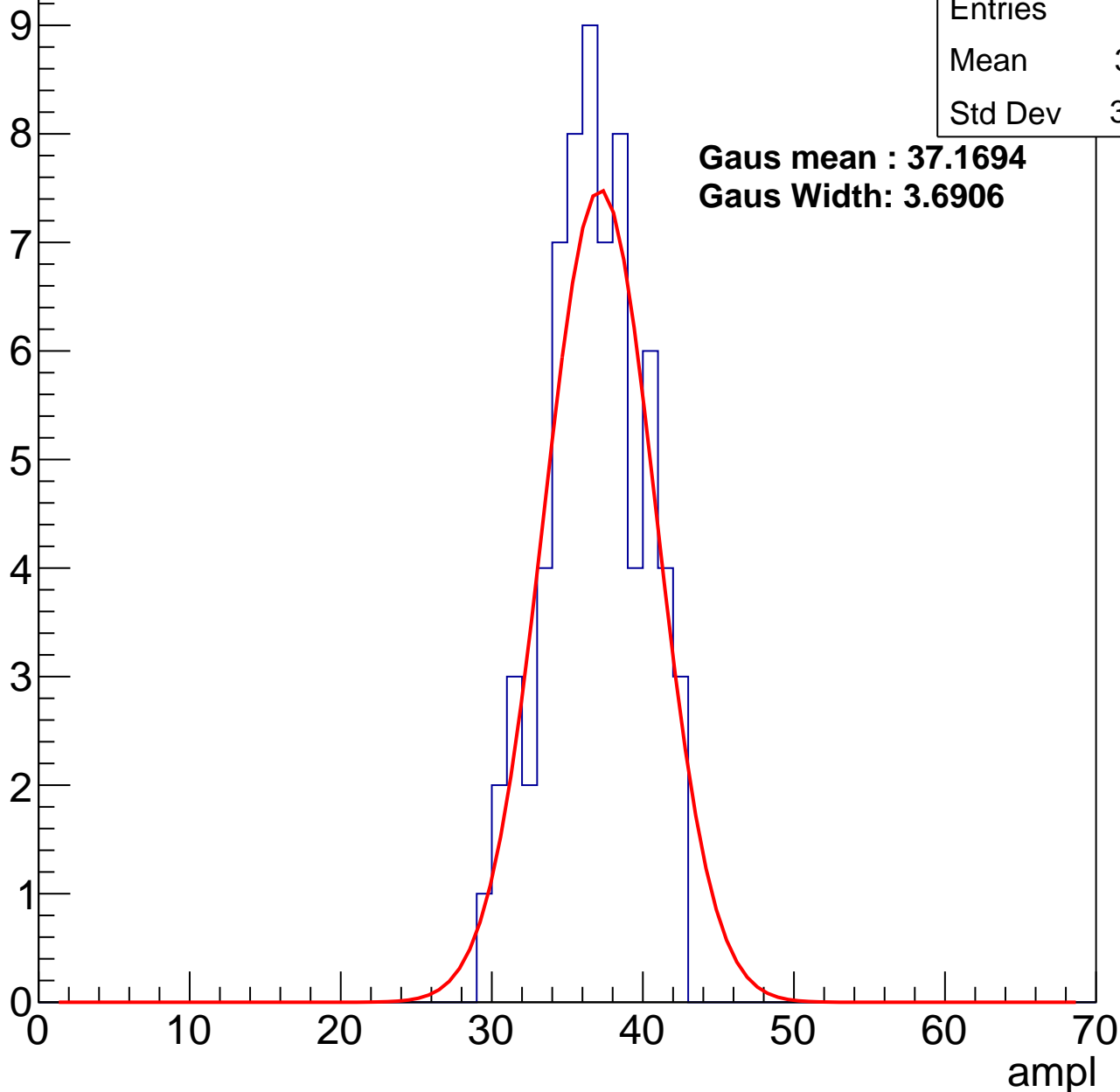
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.31
Std Dev	3.145

**Gaus mean : 37.1694**

**Gaus Width: 3.6906**



# B0L102S, U17-ch25, adc2

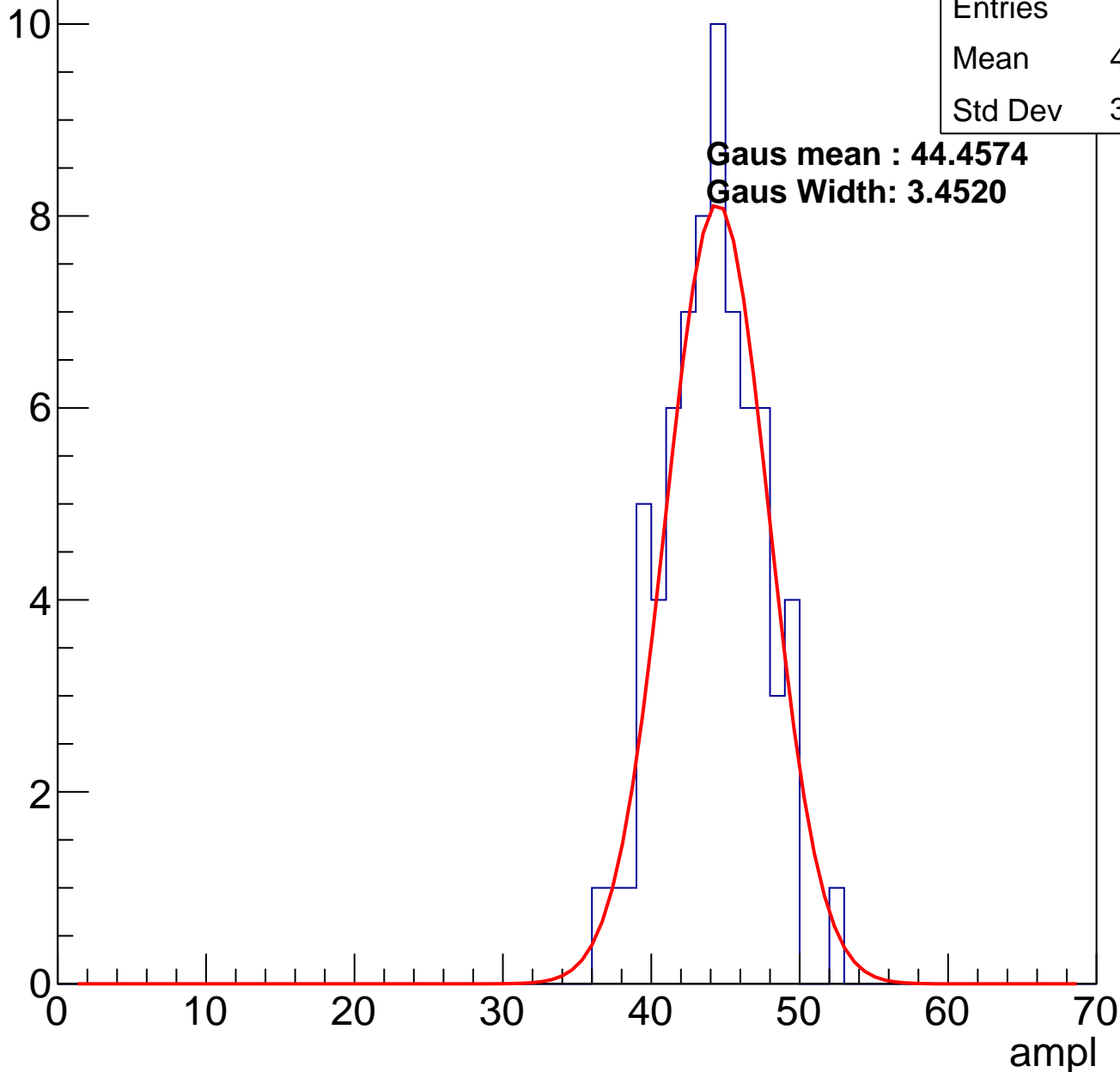
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	43.64
Std Dev	3.198

**Gaus mean : 44.4574**

**Gaus Width: 3.4520**

Entry

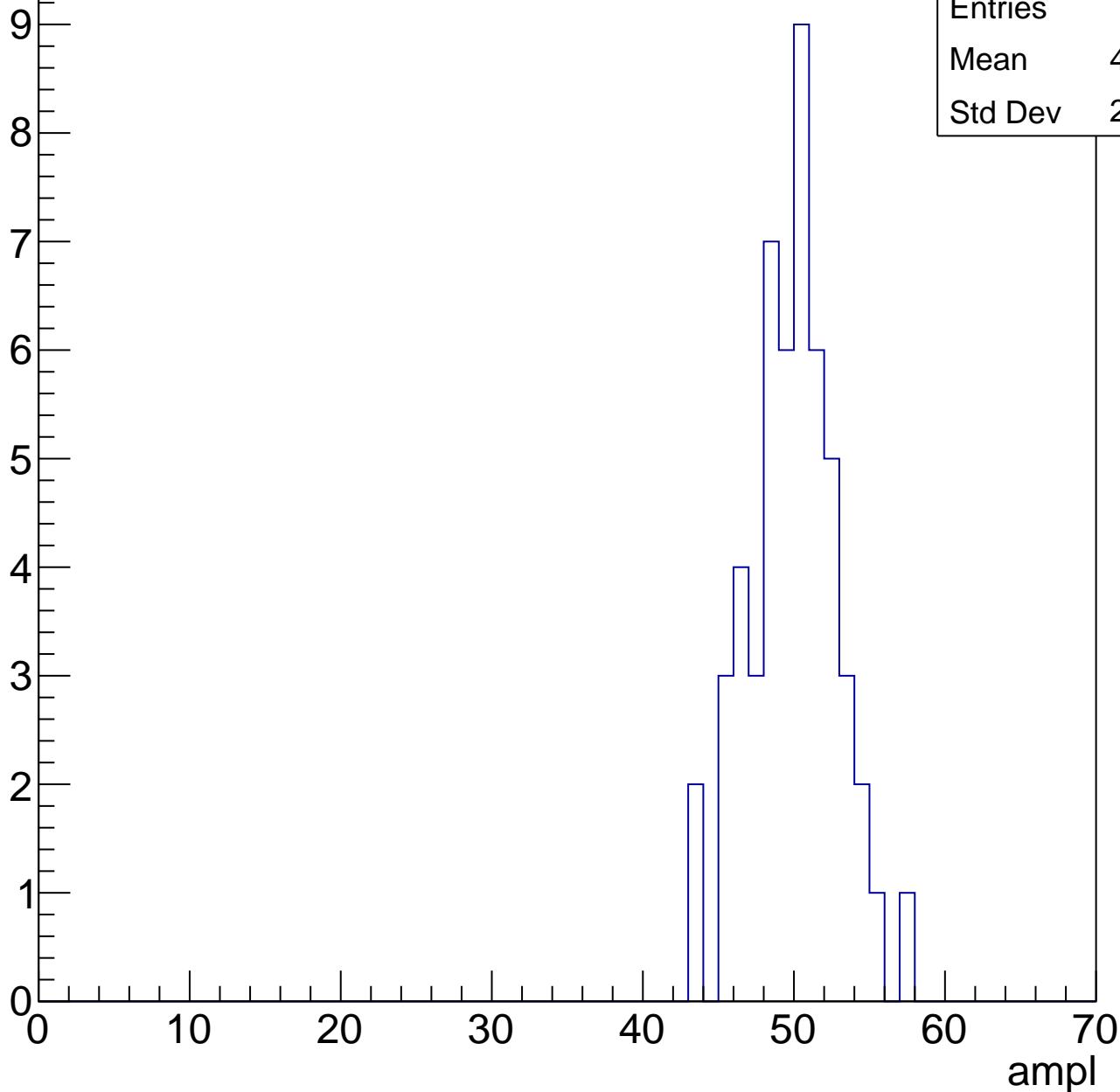


# B0L102S, U17-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	49.44
Std Dev	2.912

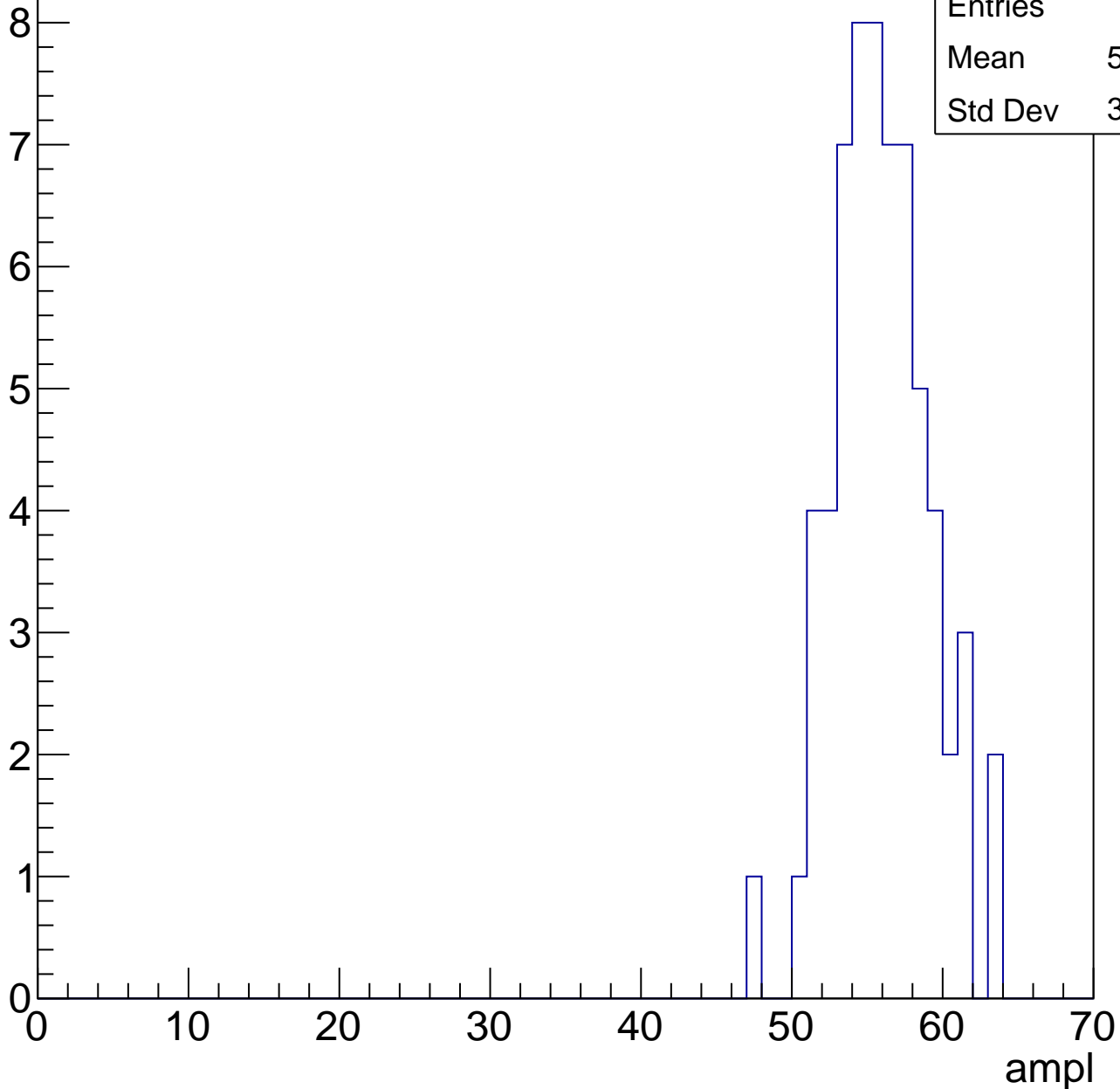


# B0L102S, U17-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.52
Std Dev	3.182

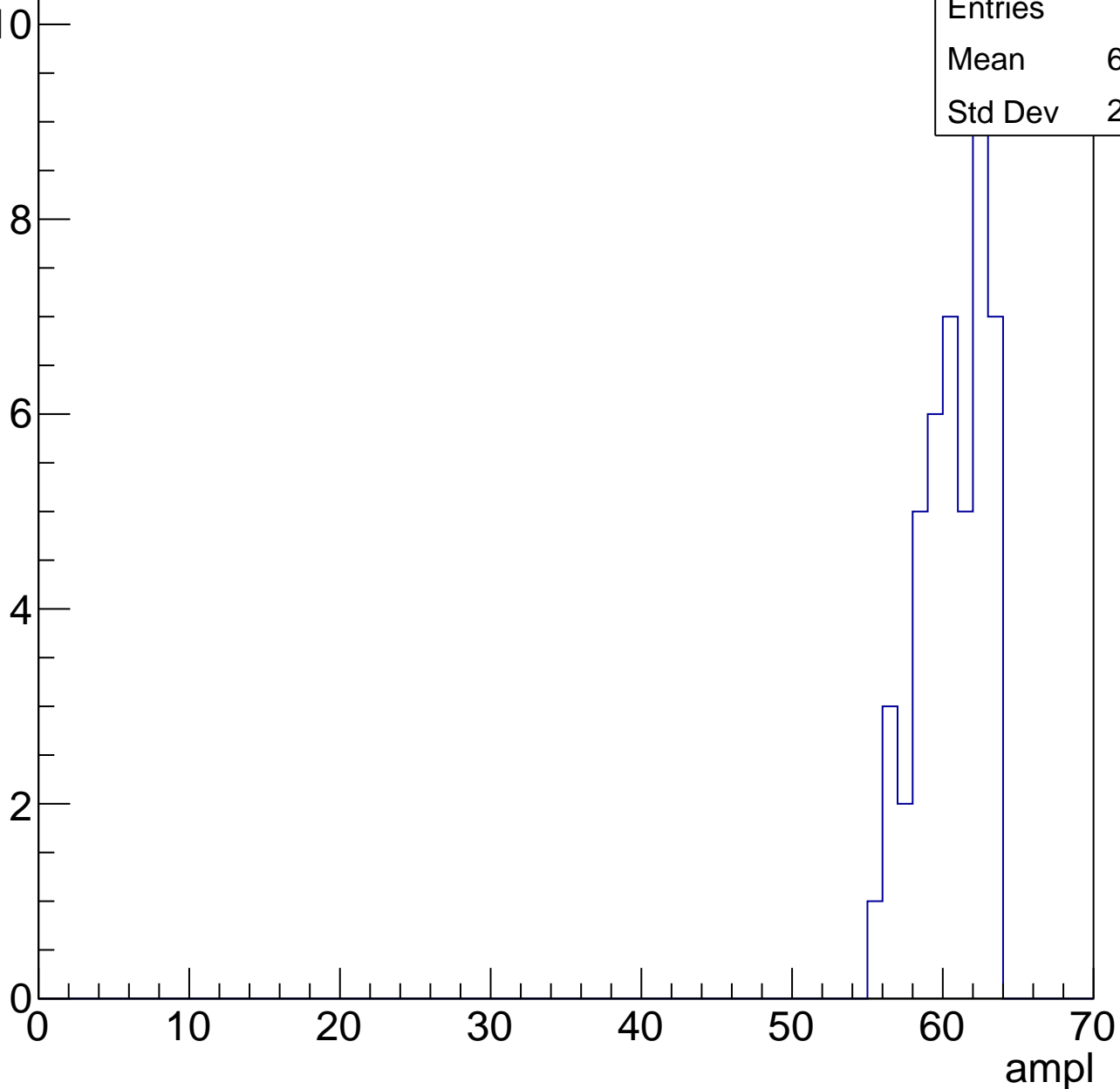


# B0L102S, U17-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

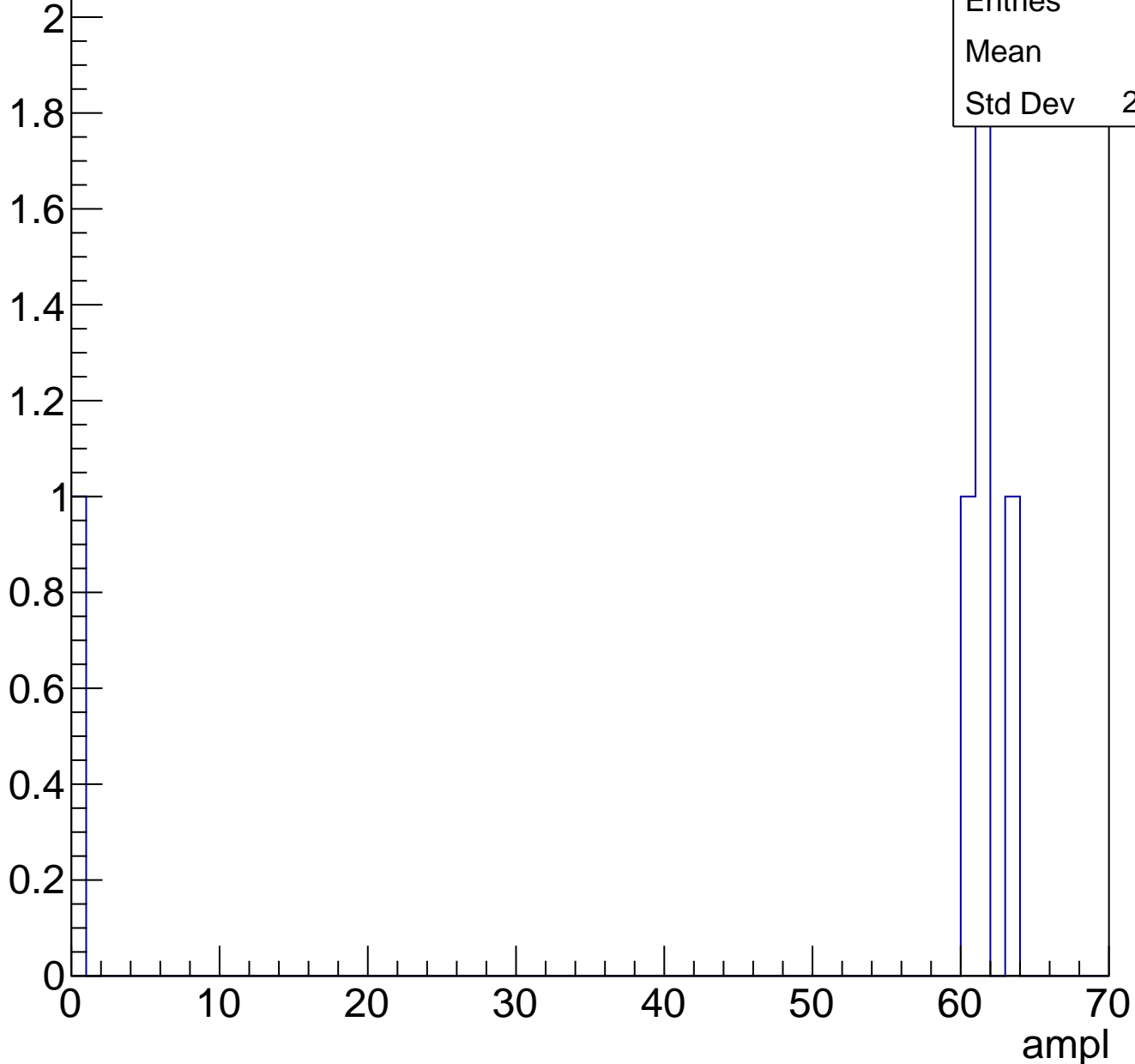
Entries	46
Mean	60.15
Std Dev	2.206



# B0L102S, U17-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	5
Mean	49
Std Dev	24.52



# B0L102S, U17-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch26, adc0

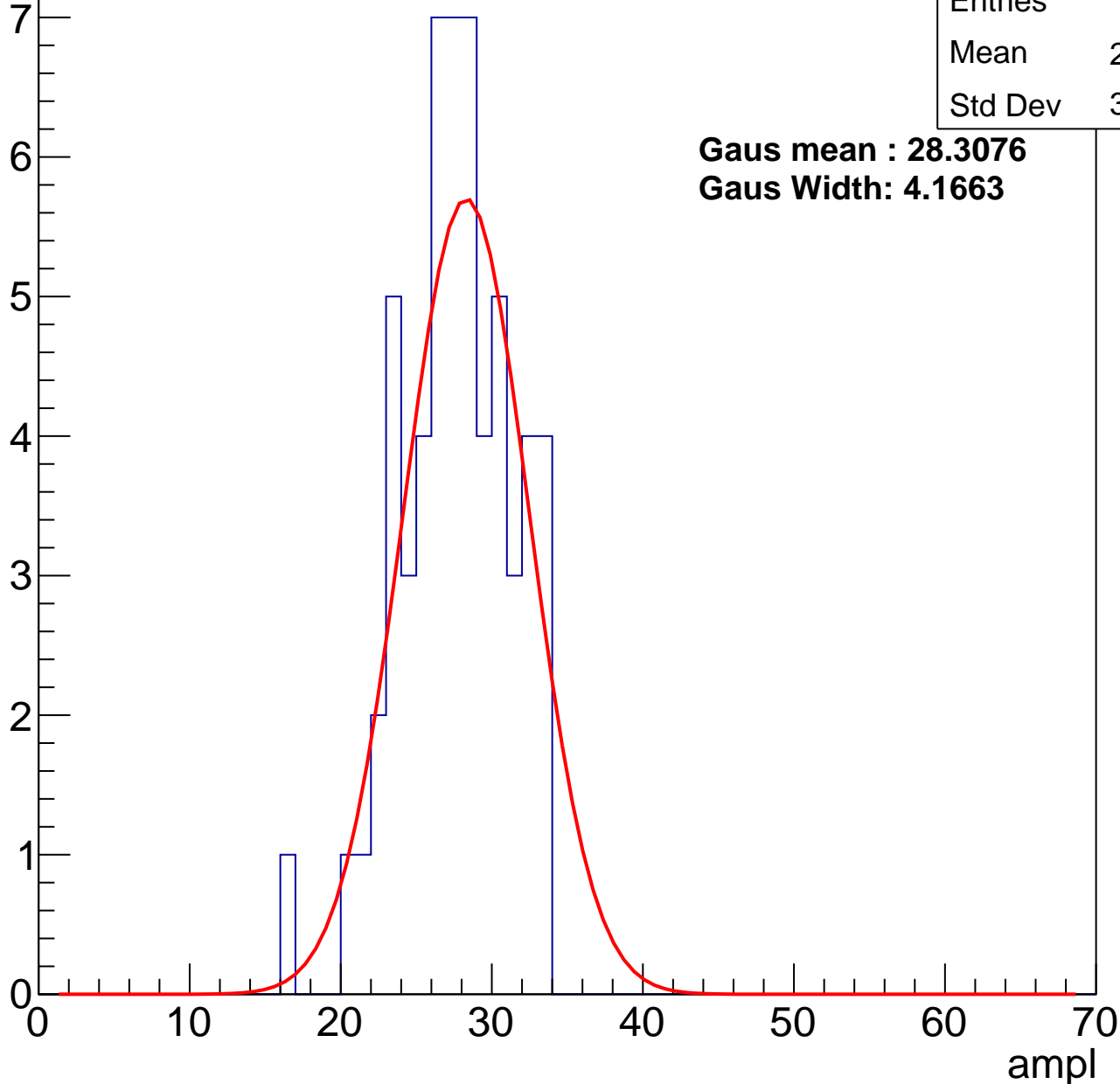
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	27.14
Std Dev	3.584

**Gaus mean : 28.3076**

**Gaus Width: 4.1663**



# B0L102S, U17-ch26, adc1

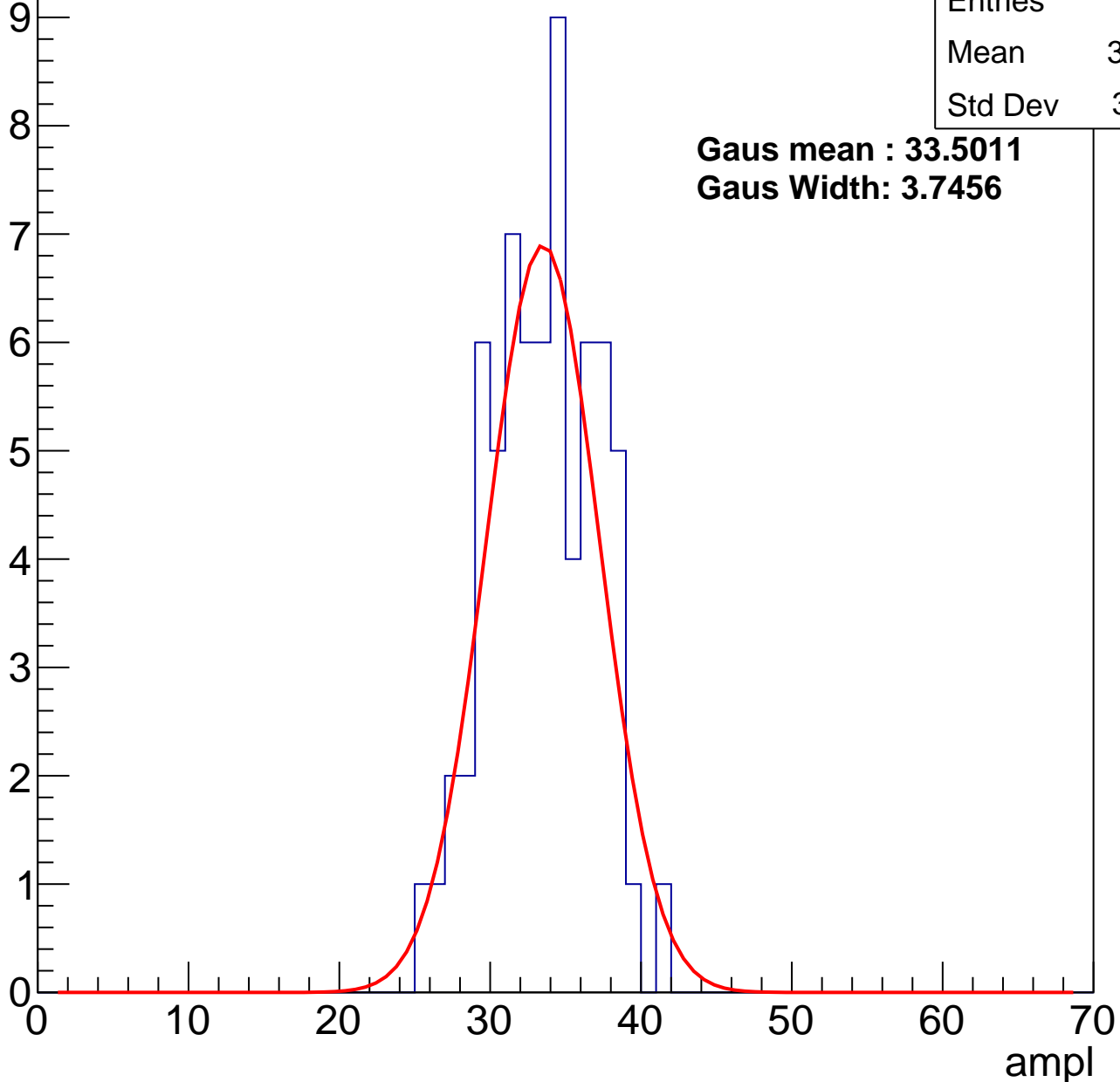
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	33.03
Std Dev	3.451

**Gaus mean : 33.5011**

**Gaus Width: 3.7456**



# B0L102S, U17-ch26, adc2

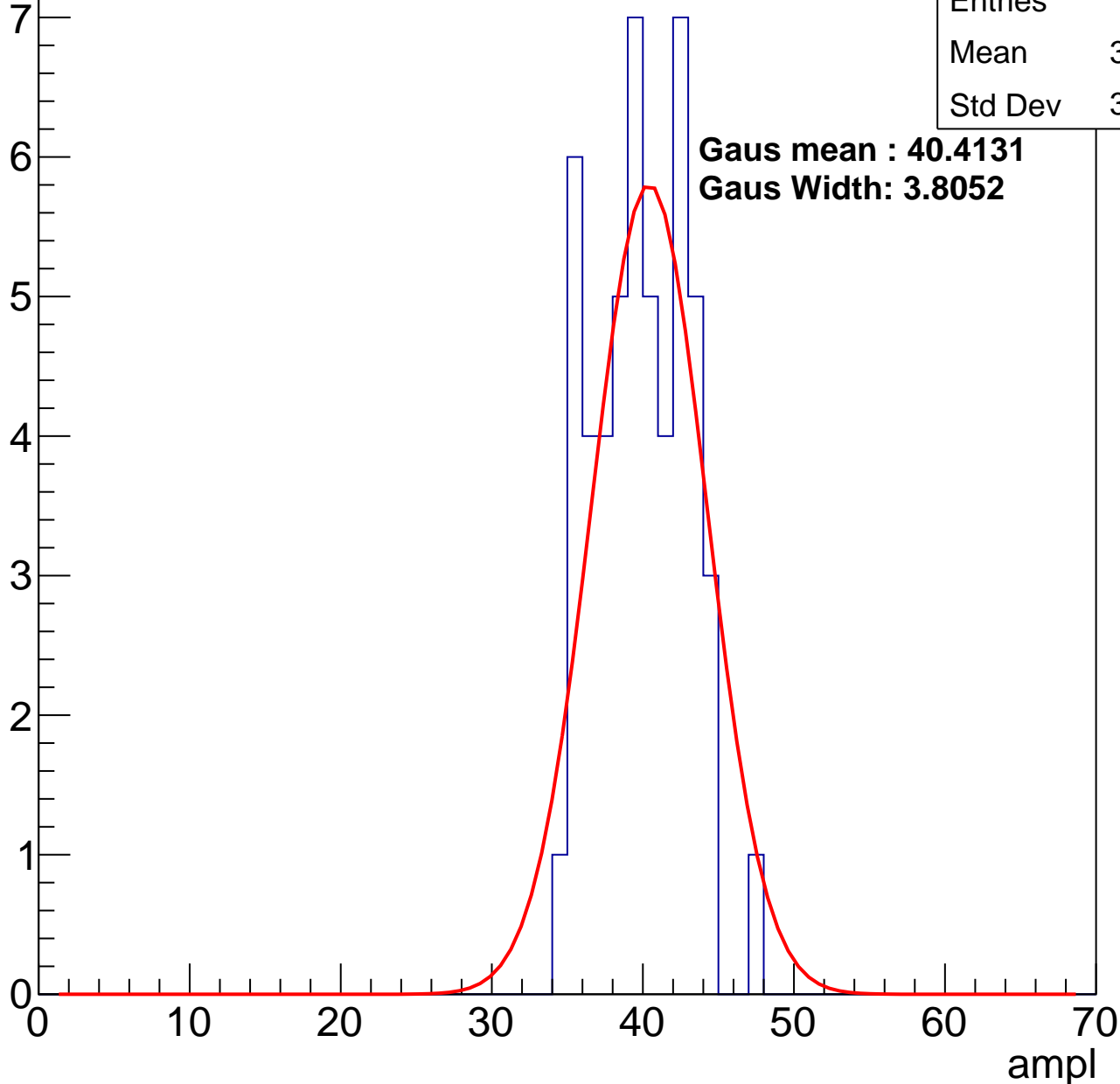
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	39.44
Std Dev	3.009

**Gaus mean : 40.4131**

**Gaus Width: 3.8052**



# B0L102S, U17-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

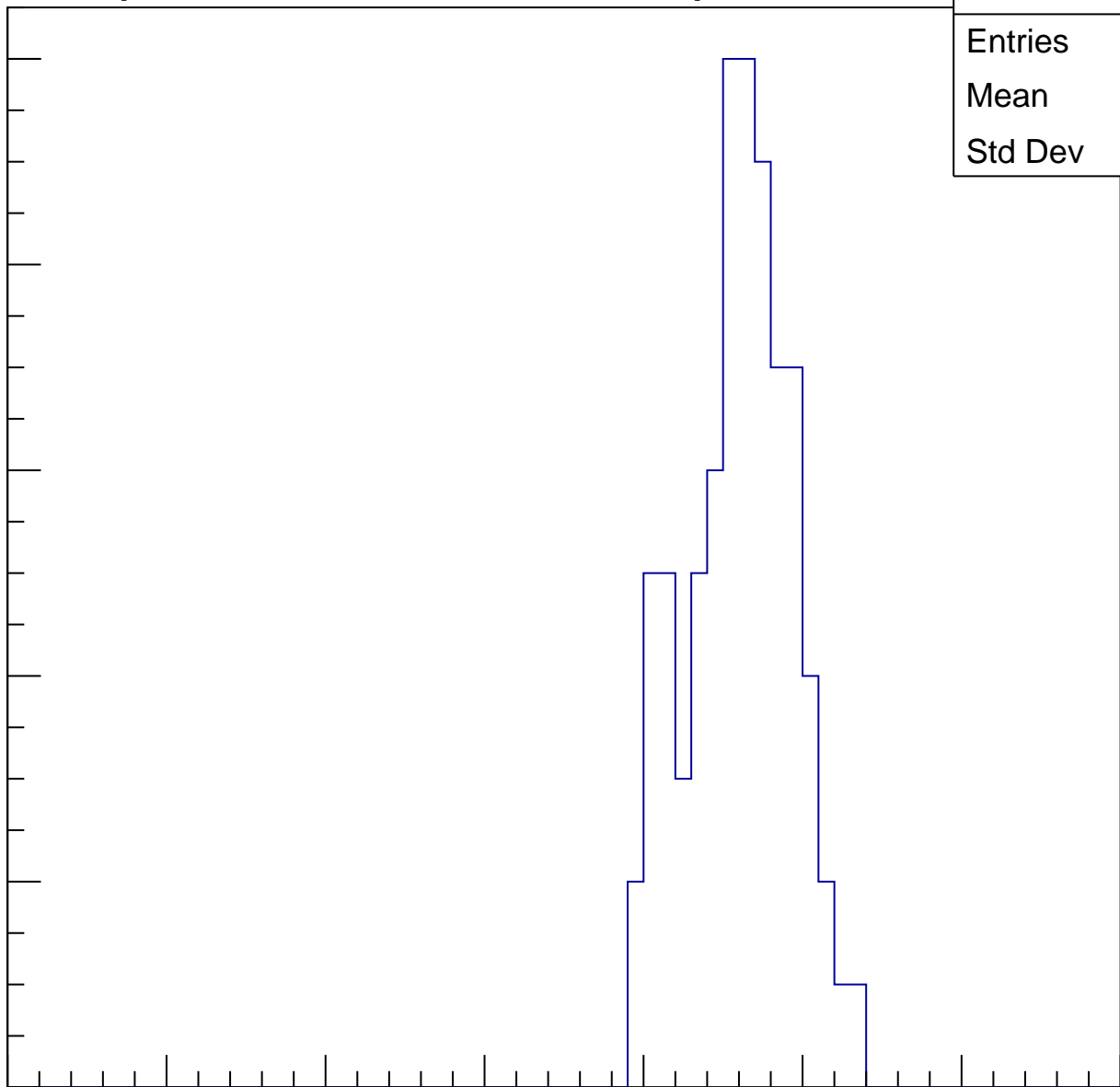
Entries	77
Mean	45.55
Std Dev	3.25

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

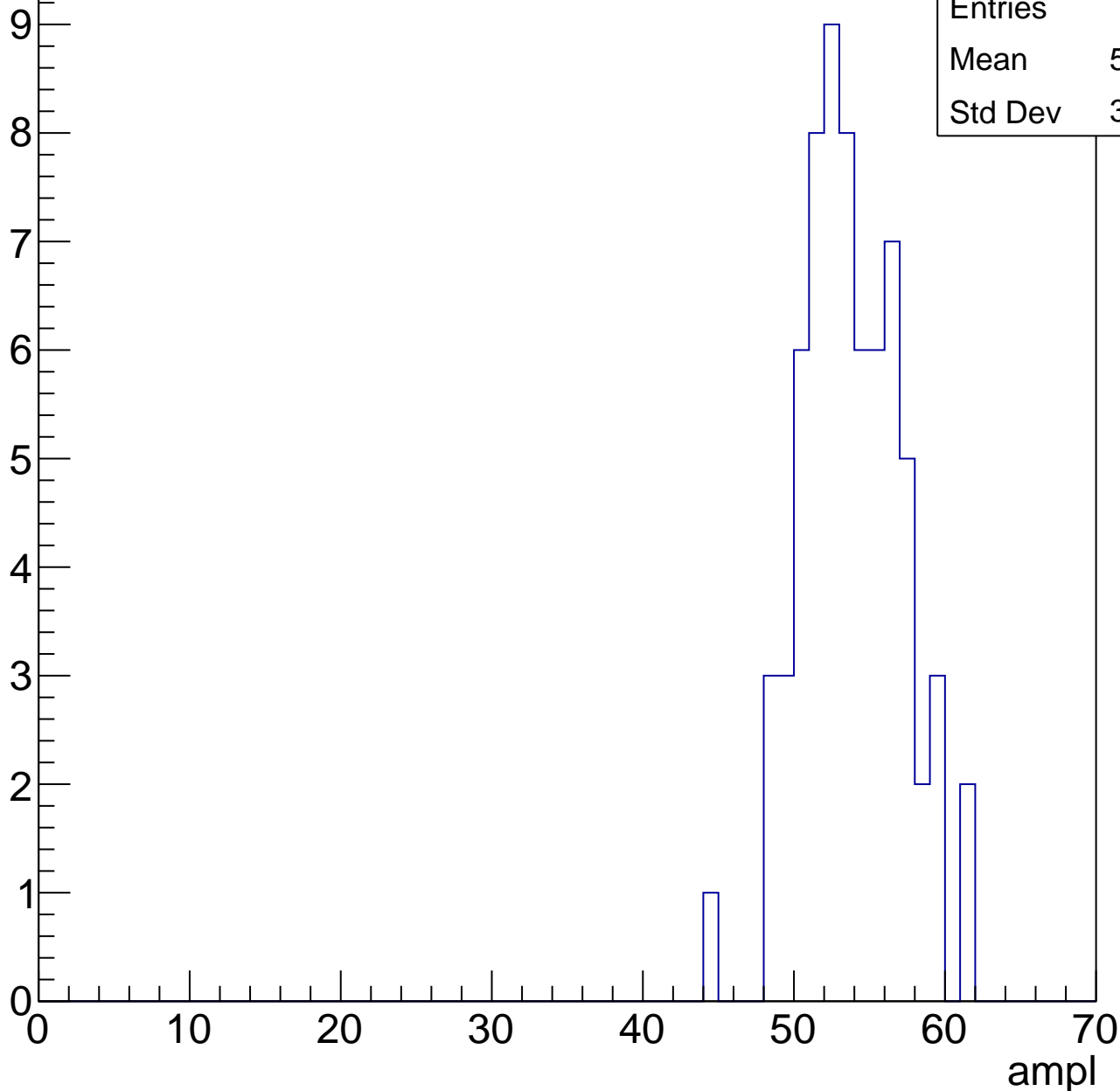


# B0L102S, U17-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	53.35
Std Dev	3.296

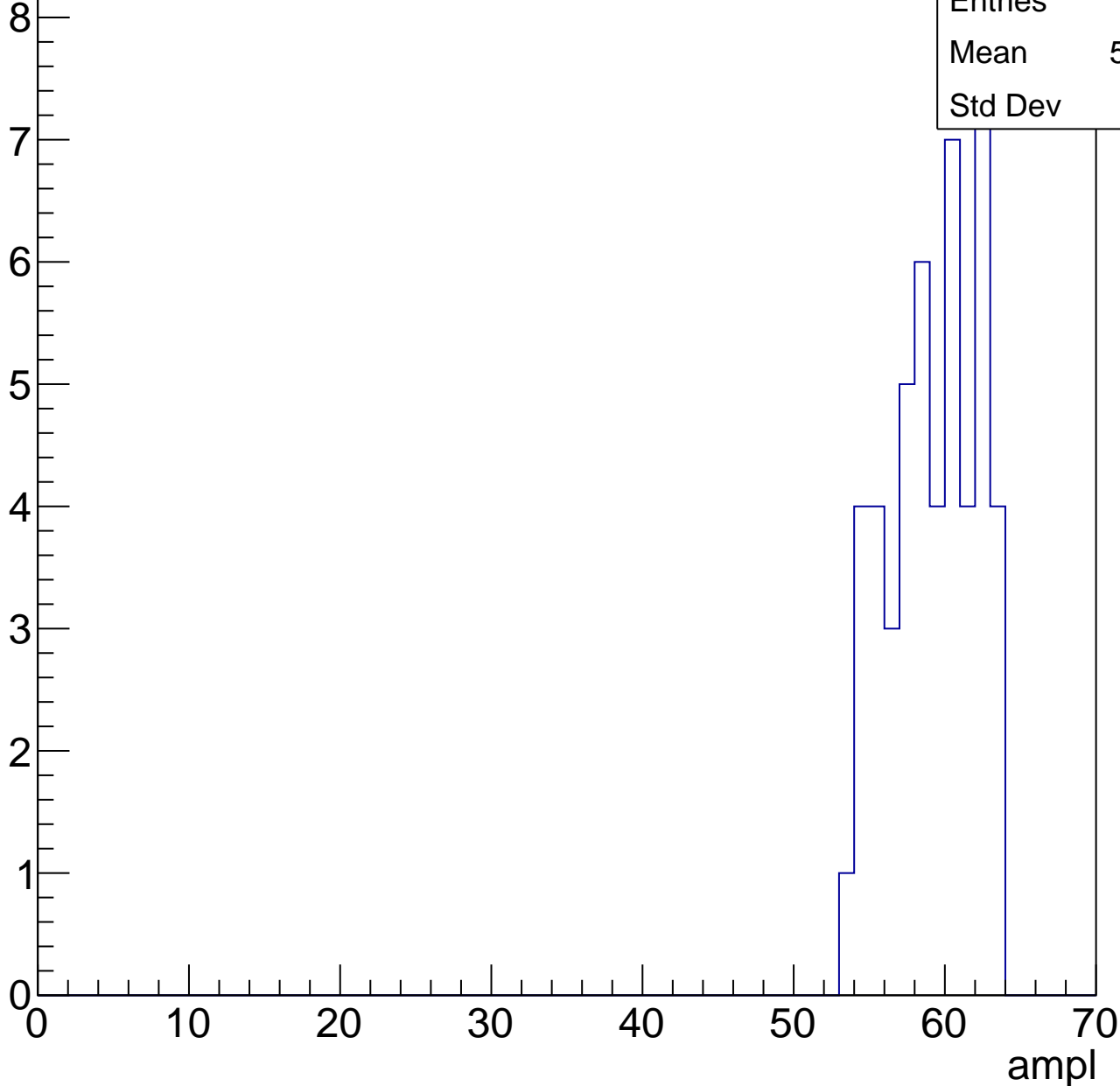


# B0L102S, U17-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.76
Std Dev	2.86

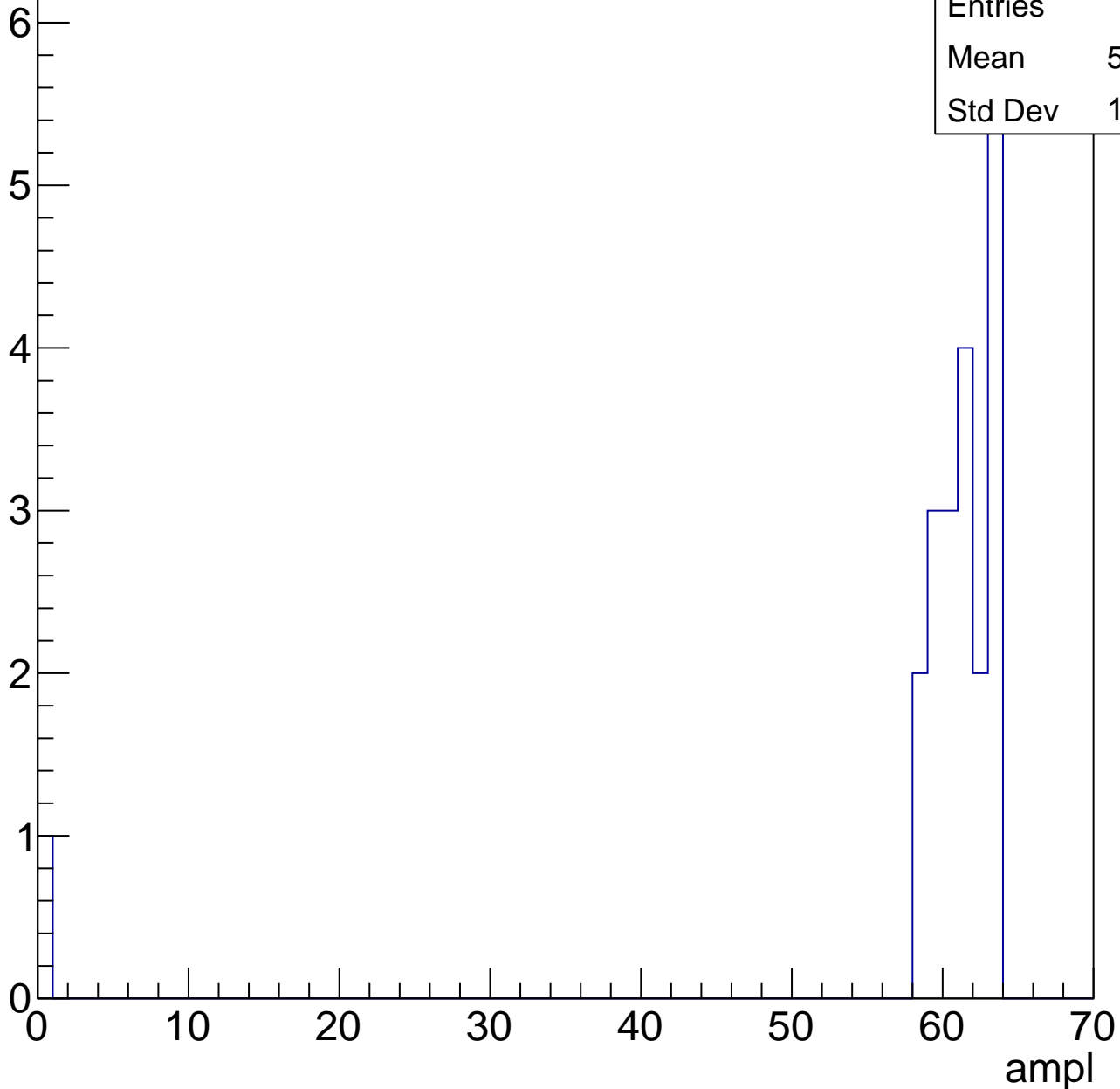


# B0L102S, U17-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	21
Mean	58.05
Std Dev	13.09





# B0L102S, U17-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch27, adc0

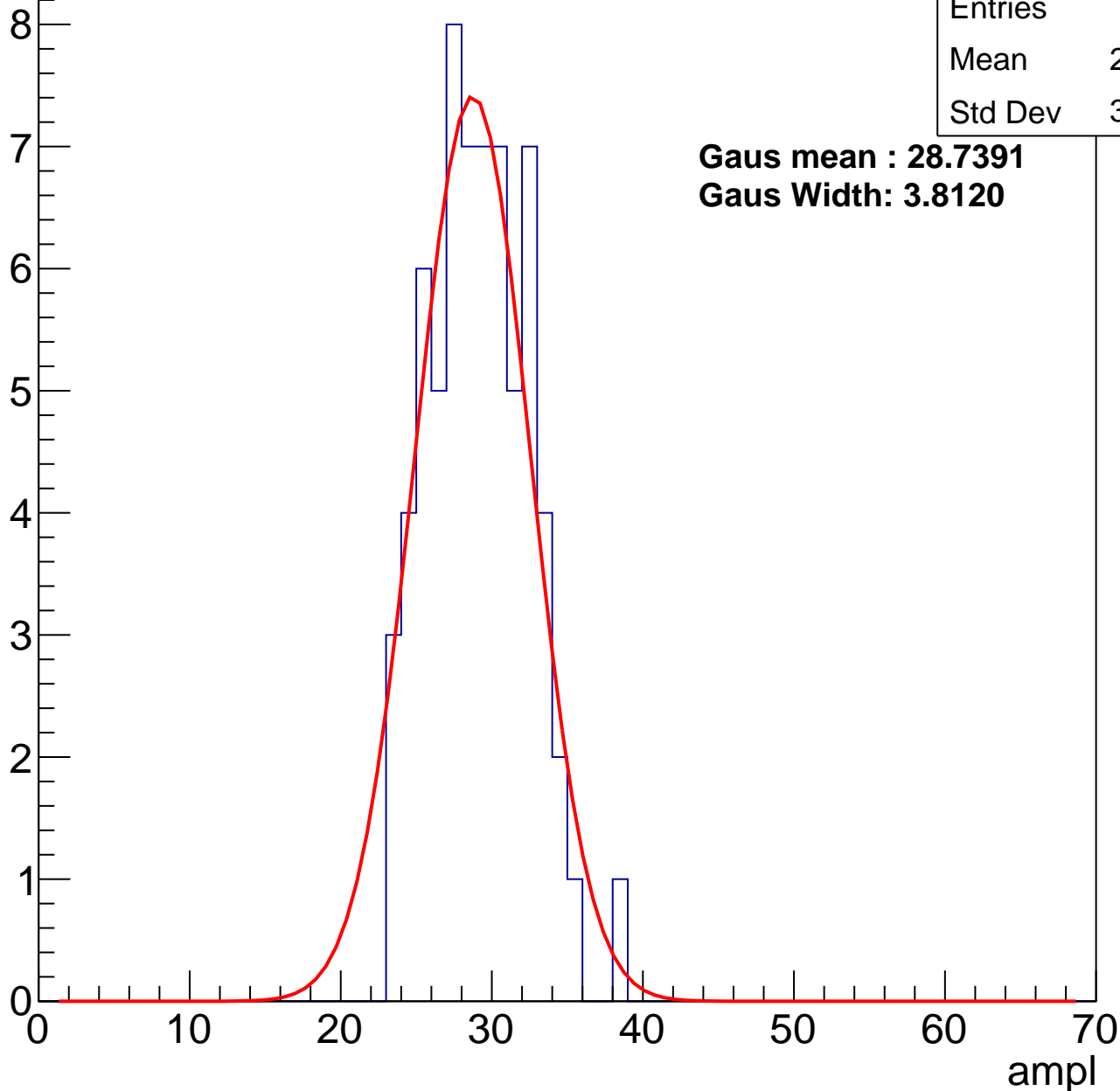
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	28.69
Std Dev	3.238

**Gaus mean : 28.7391**

**Gaus Width: 3.8120**



# B0L102S, U17-ch27, adc1

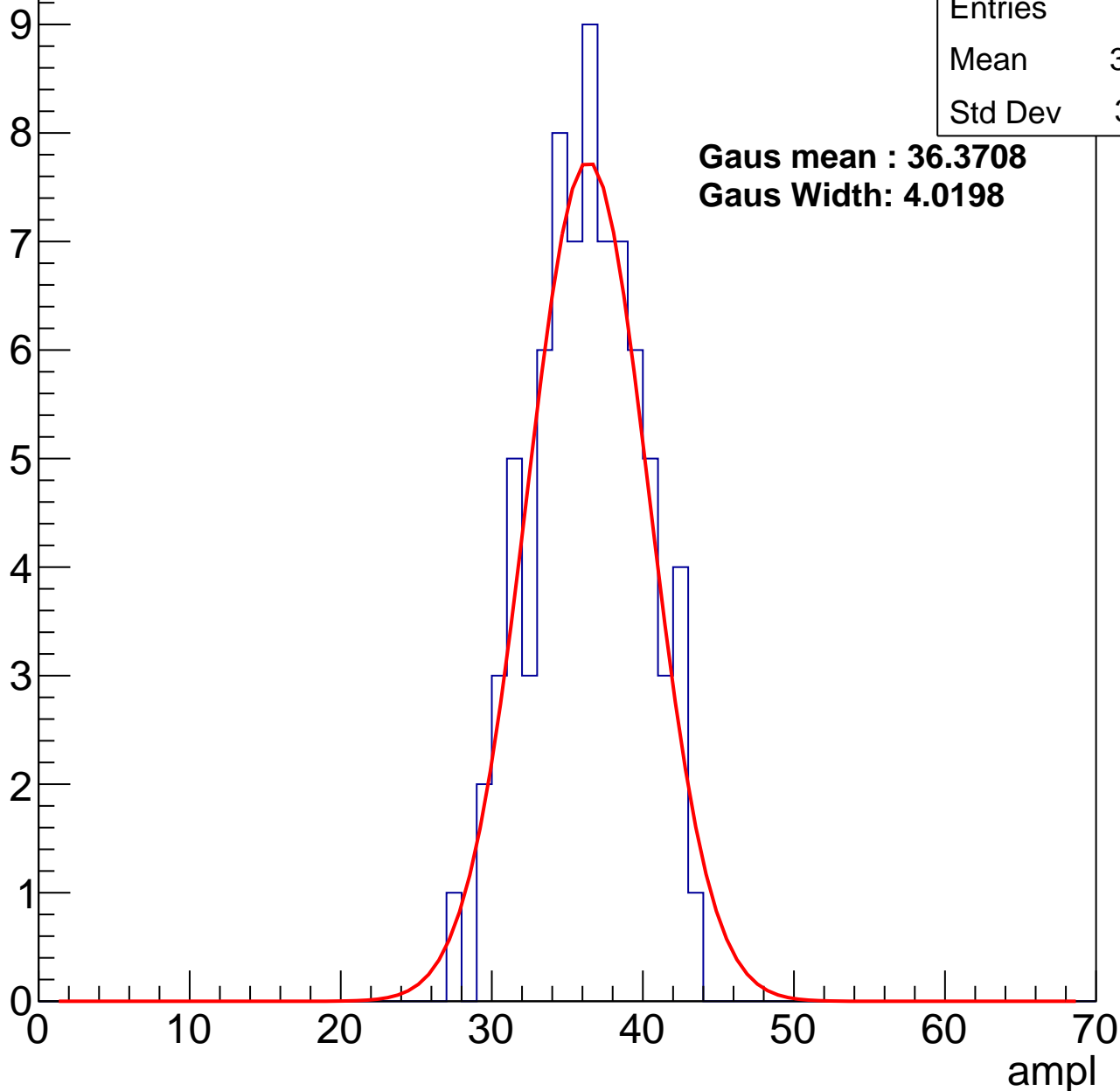
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.82
Std Dev	3.581

**Gaus mean : 36.3708**

**Gaus Width: 4.0198**



# B0L102S, U17-ch27, adc2

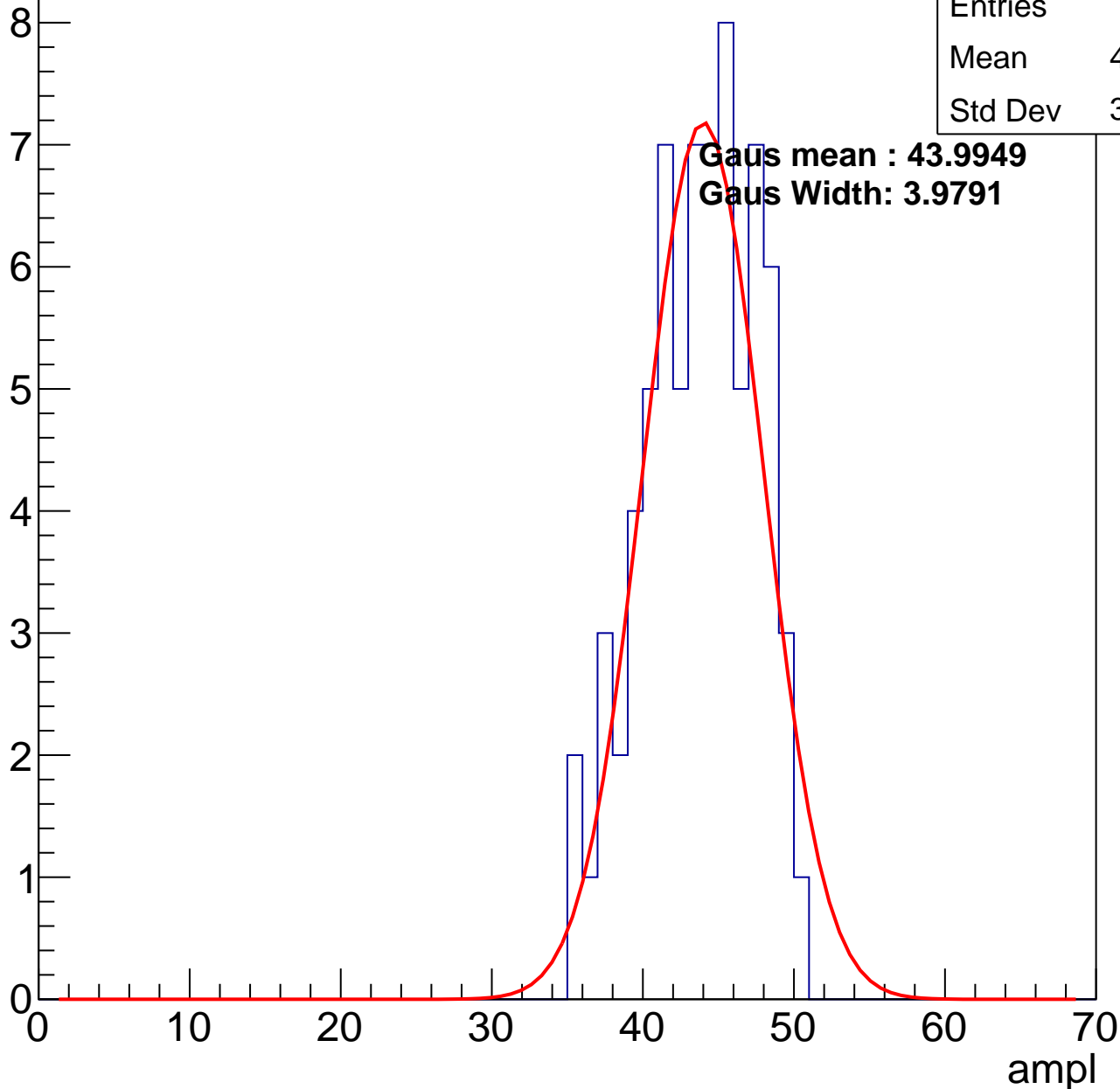
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	43.27
Std Dev	3.654

**Gaus mean : 43.9949**

**Gaus Width: 3.9791**

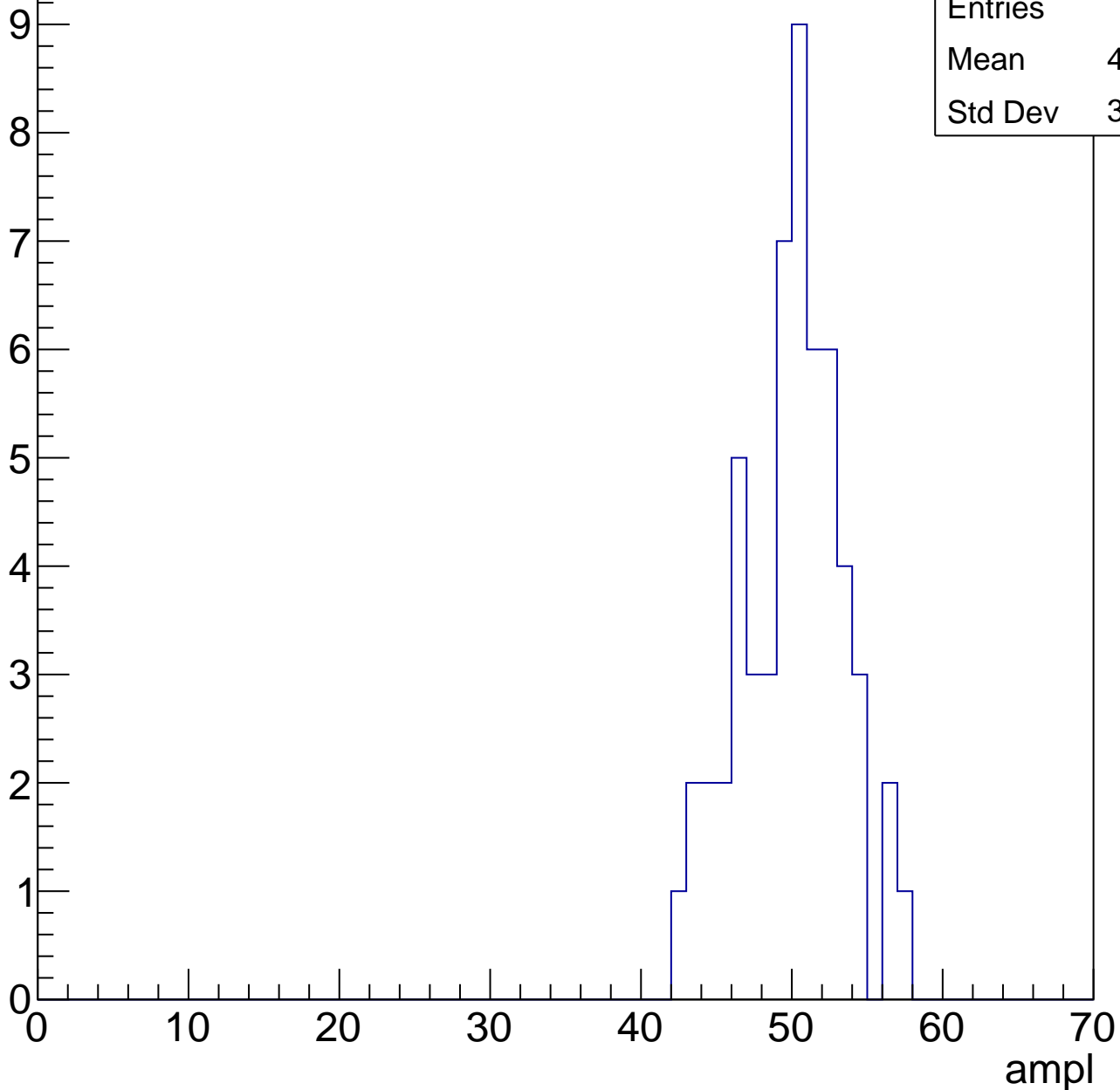


# B0L102S, U17-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	49.55
Std Dev	3.364

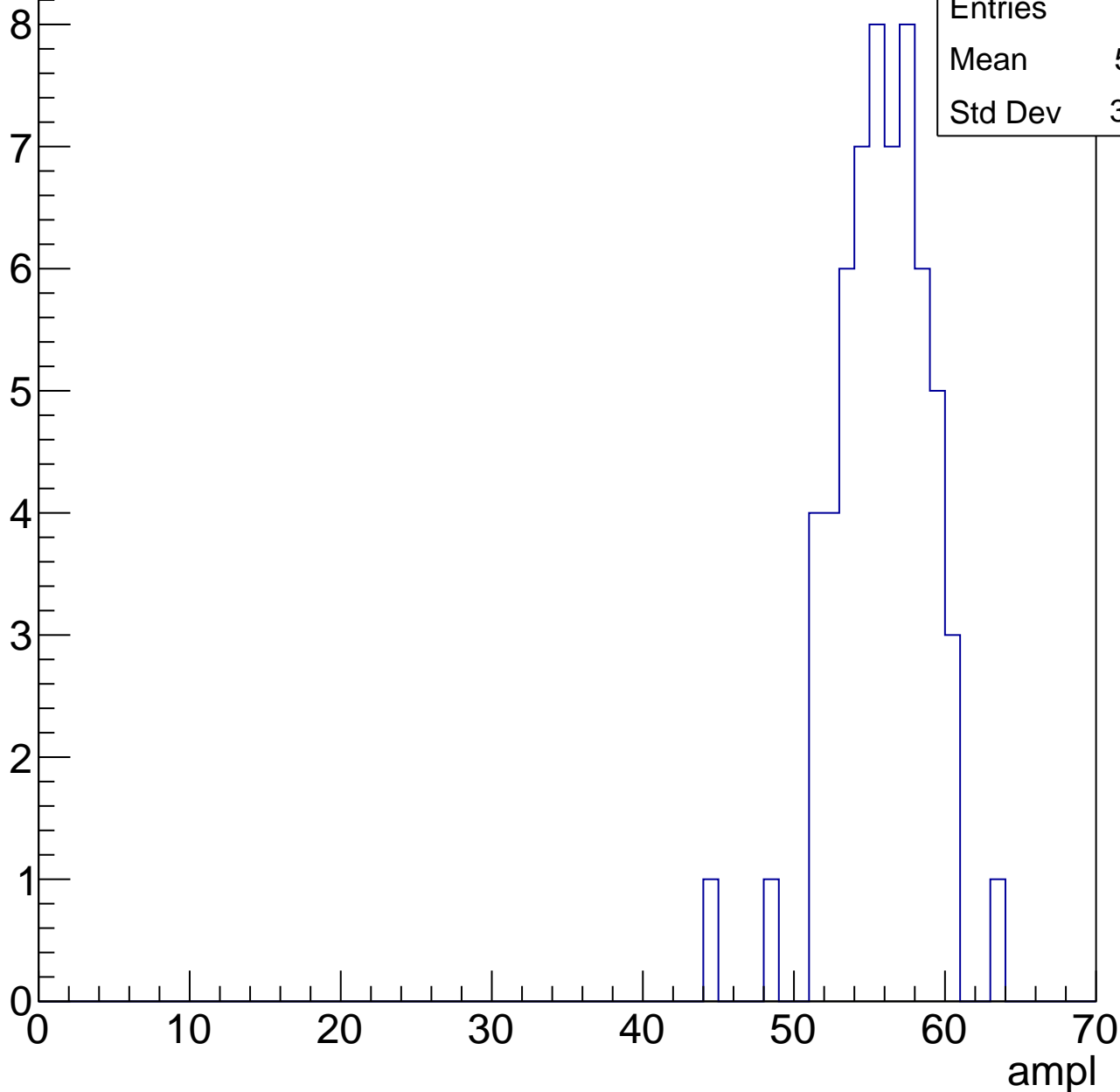


# B0L102S, U17-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

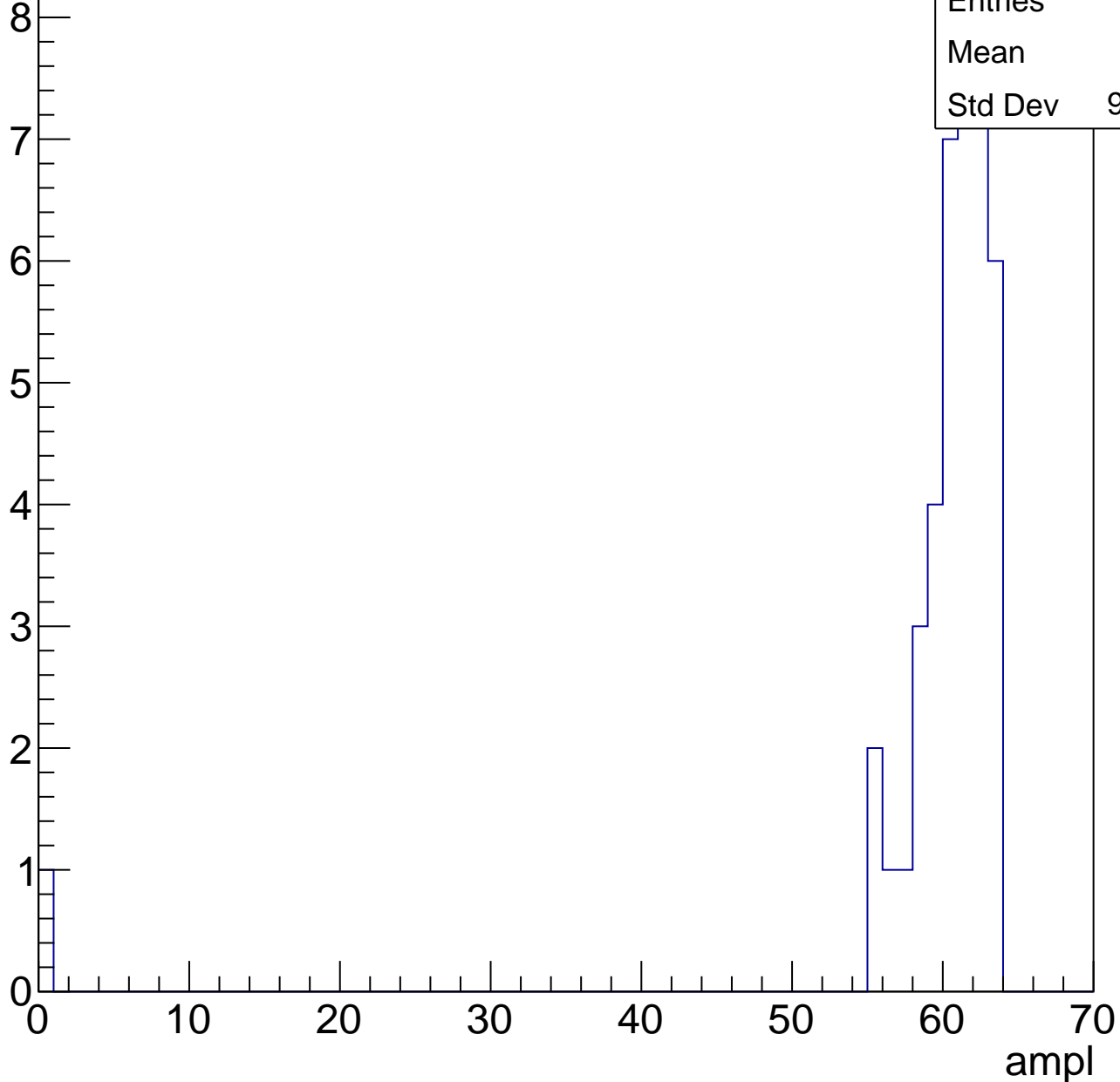
Entries	61
Mean	55.31
Std Dev	3.155



# B0L102S, U17-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

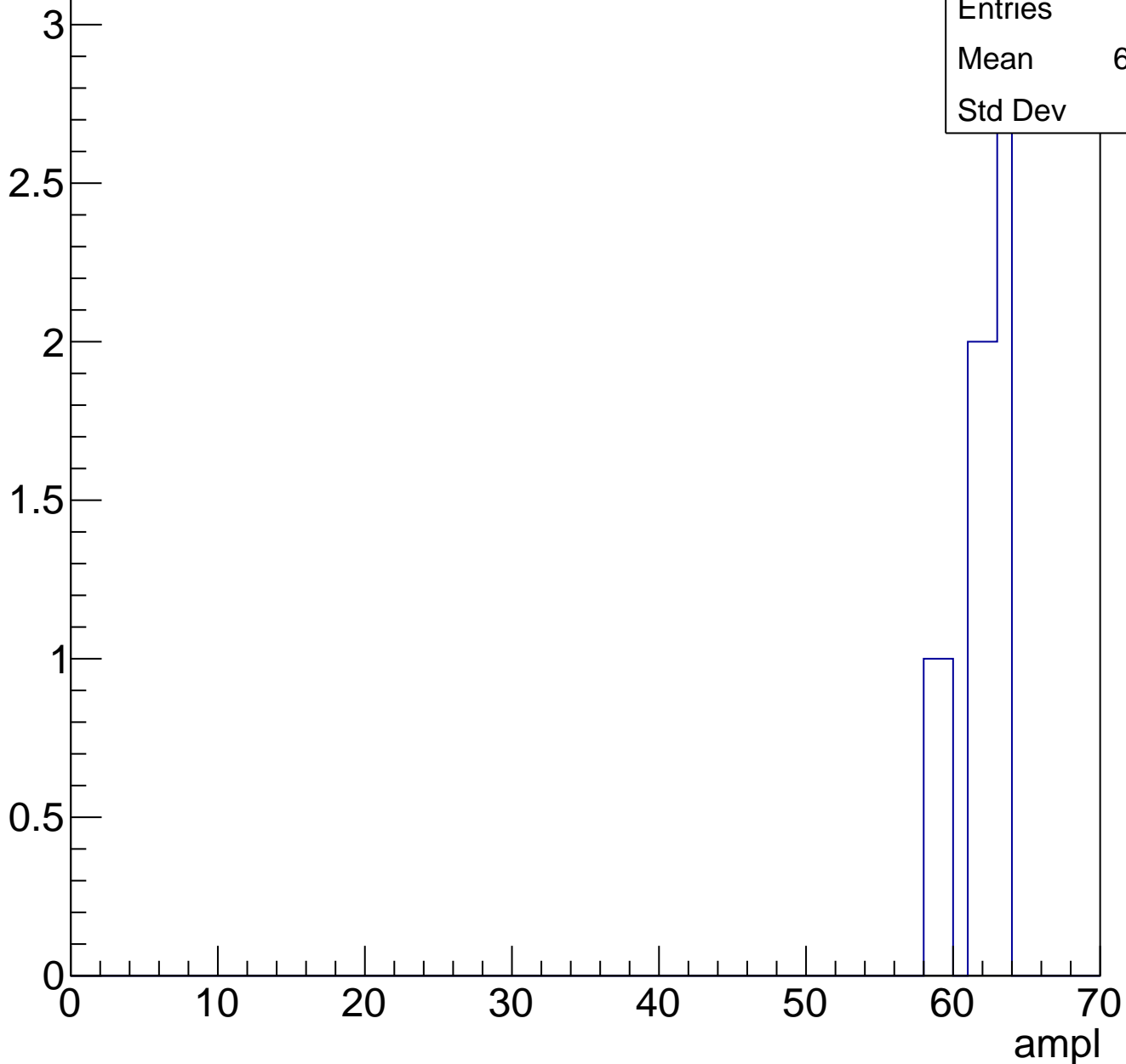
Entry



# B0L102S, U17-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch28, adc0

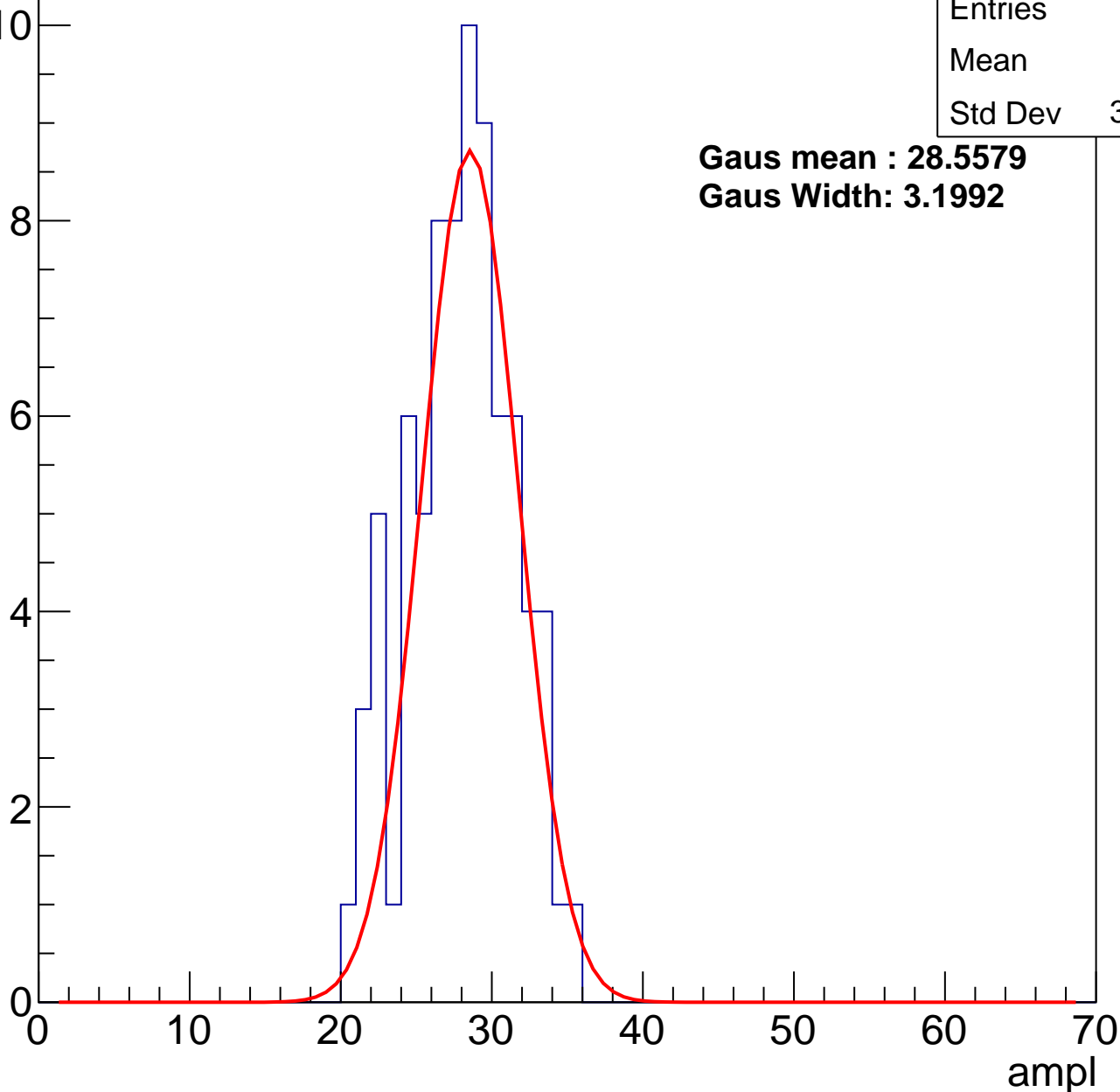
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	27.5
Std Dev	3.422

**Gaus mean : 28.5579**

**Gaus Width: 3.1992**



# B0L102S, U17-ch28, adc1

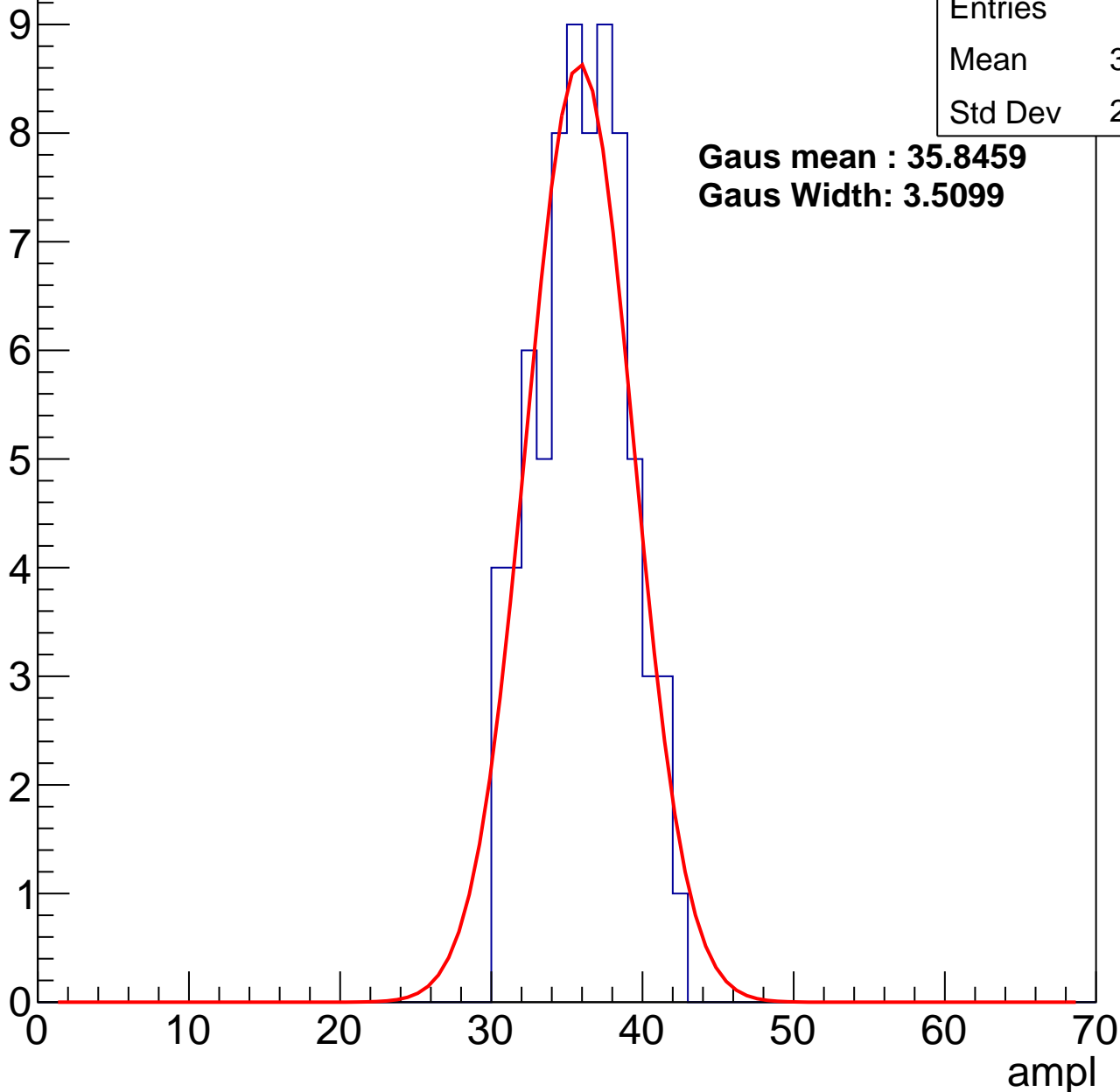
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.52
Std Dev	2.994

**Gaus mean : 35.8459**

**Gaus Width: 3.5099**



# B0L102S, U17-ch28, adc2

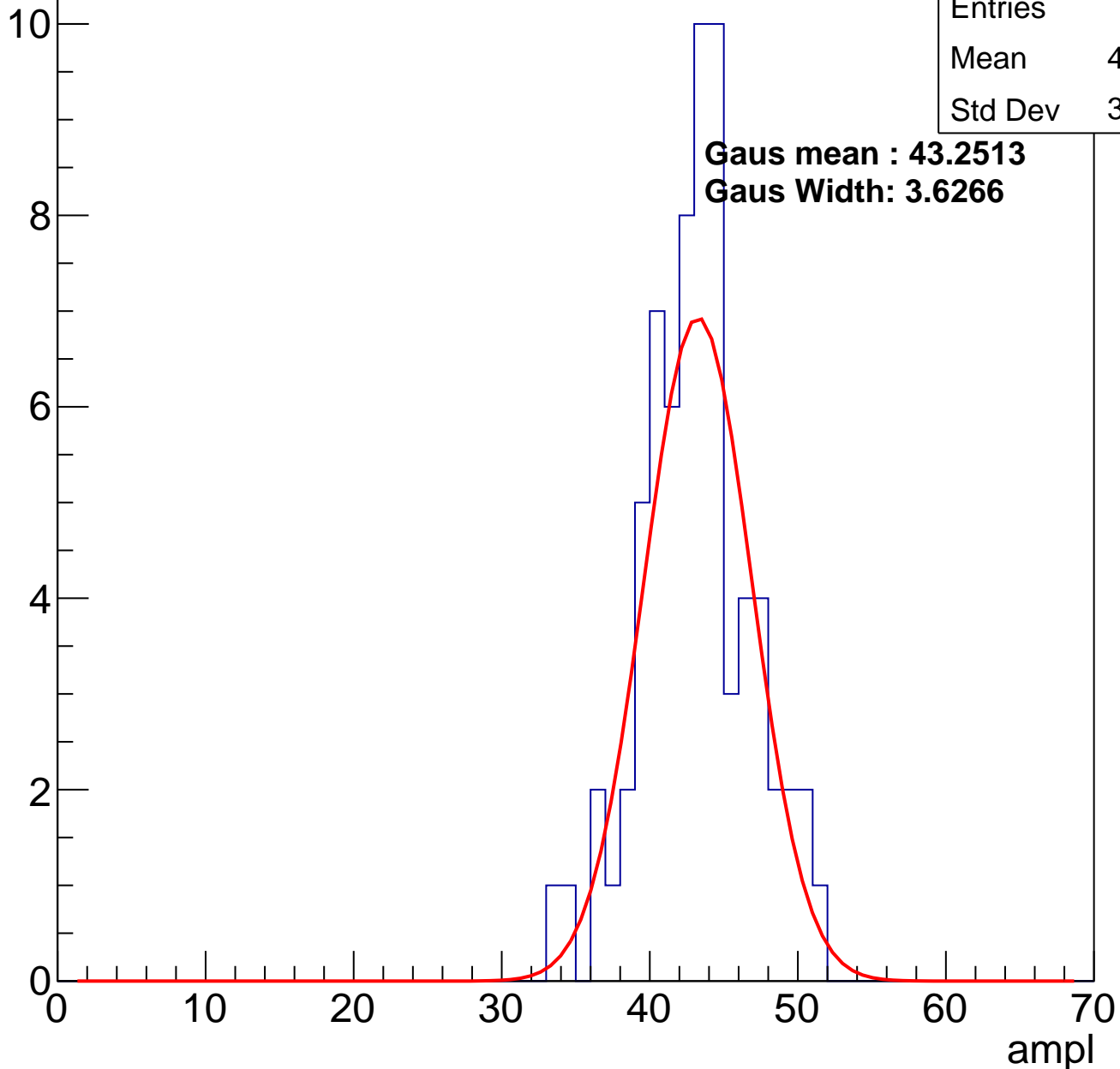
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	71
Mean	42.69
Std Dev	3.637

**Gaus mean : 43.2513**

**Gaus Width: 3.6266**

Entry

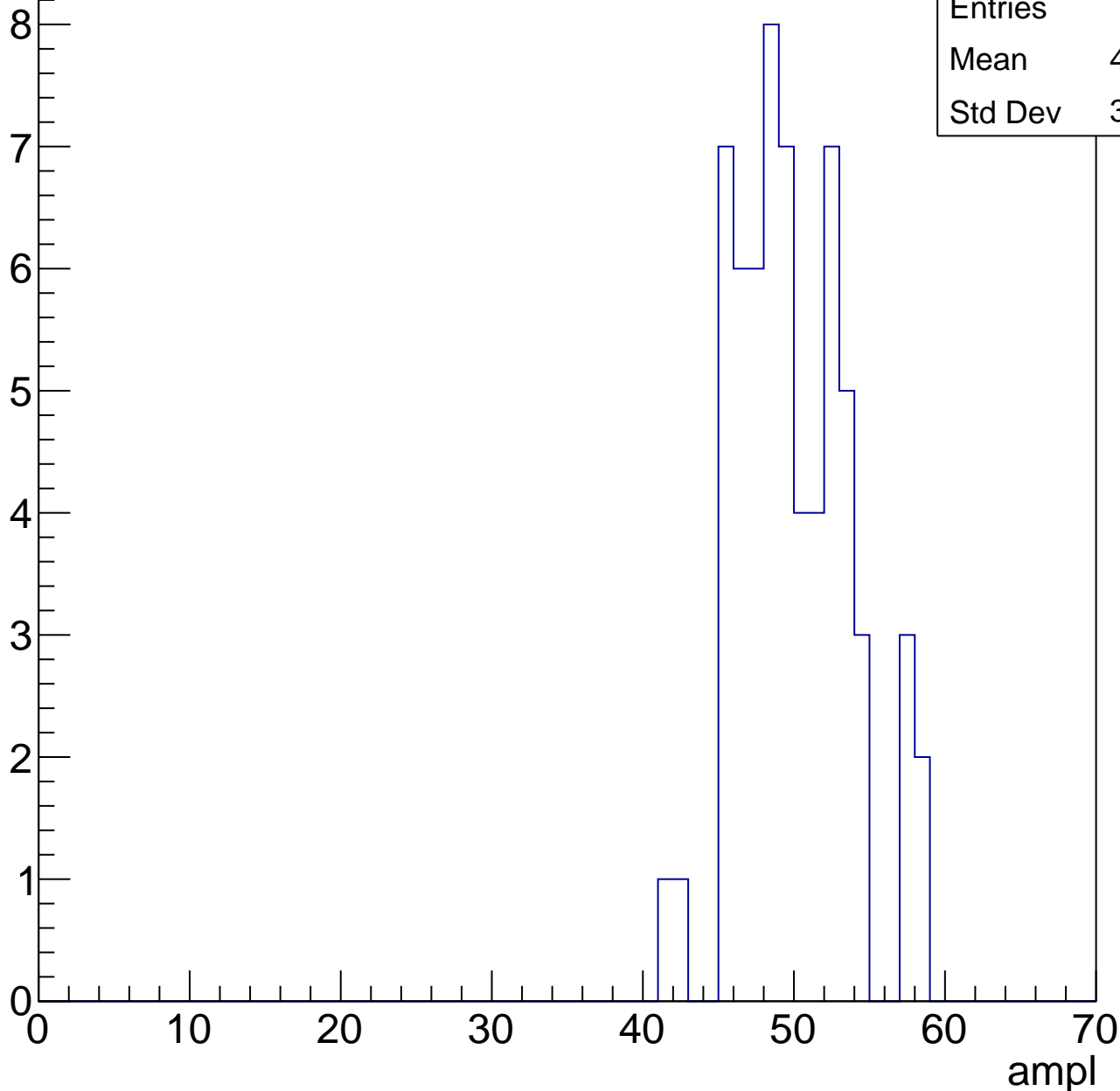


# B0L102S, U17-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	49.45
Std Dev	3.729

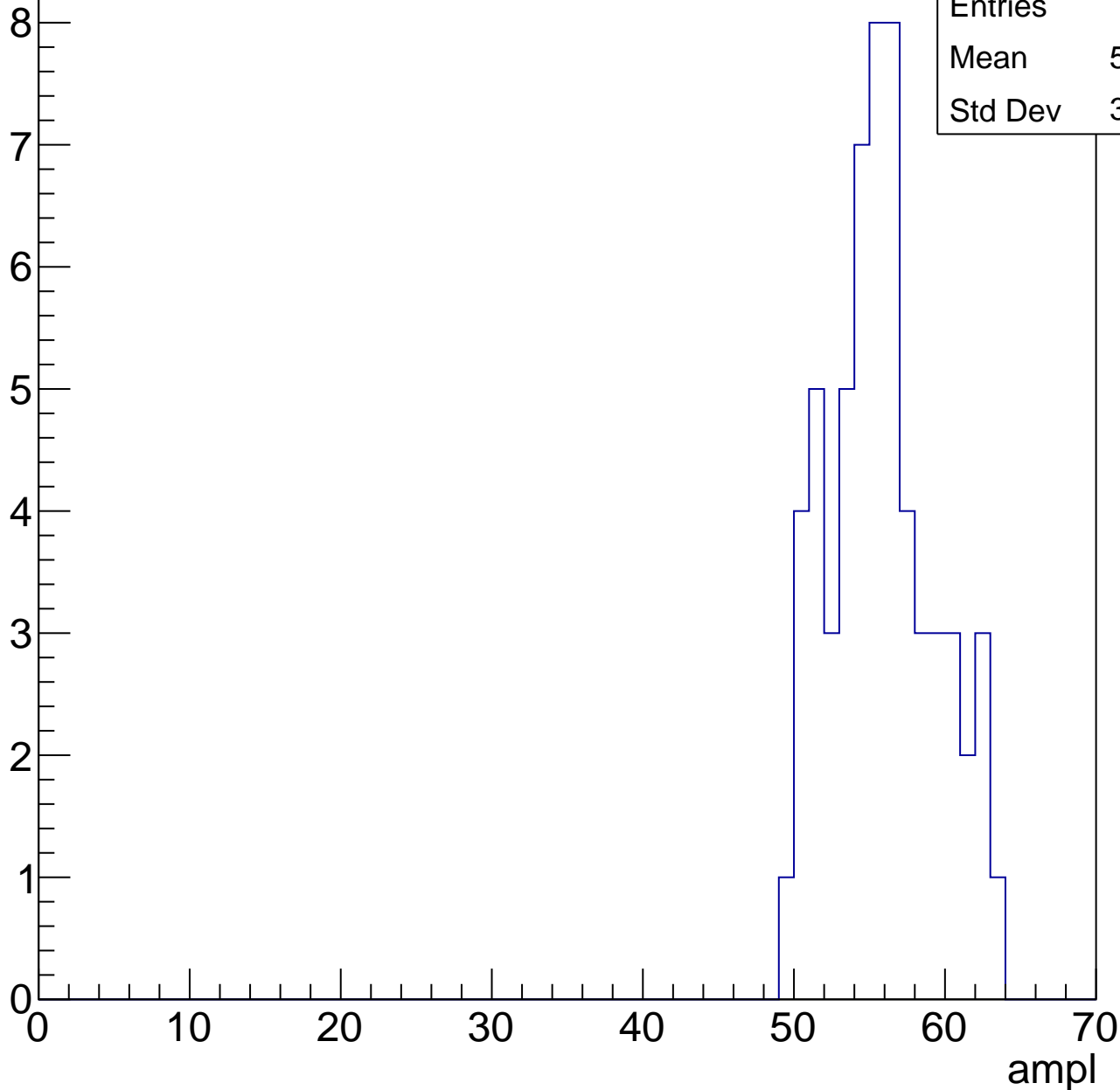


# B0L102S, U17-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	55.35
Std Dev	3.463



# B0L102S, U17-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	59.77
Std Dev	2.229

0

10

20

30

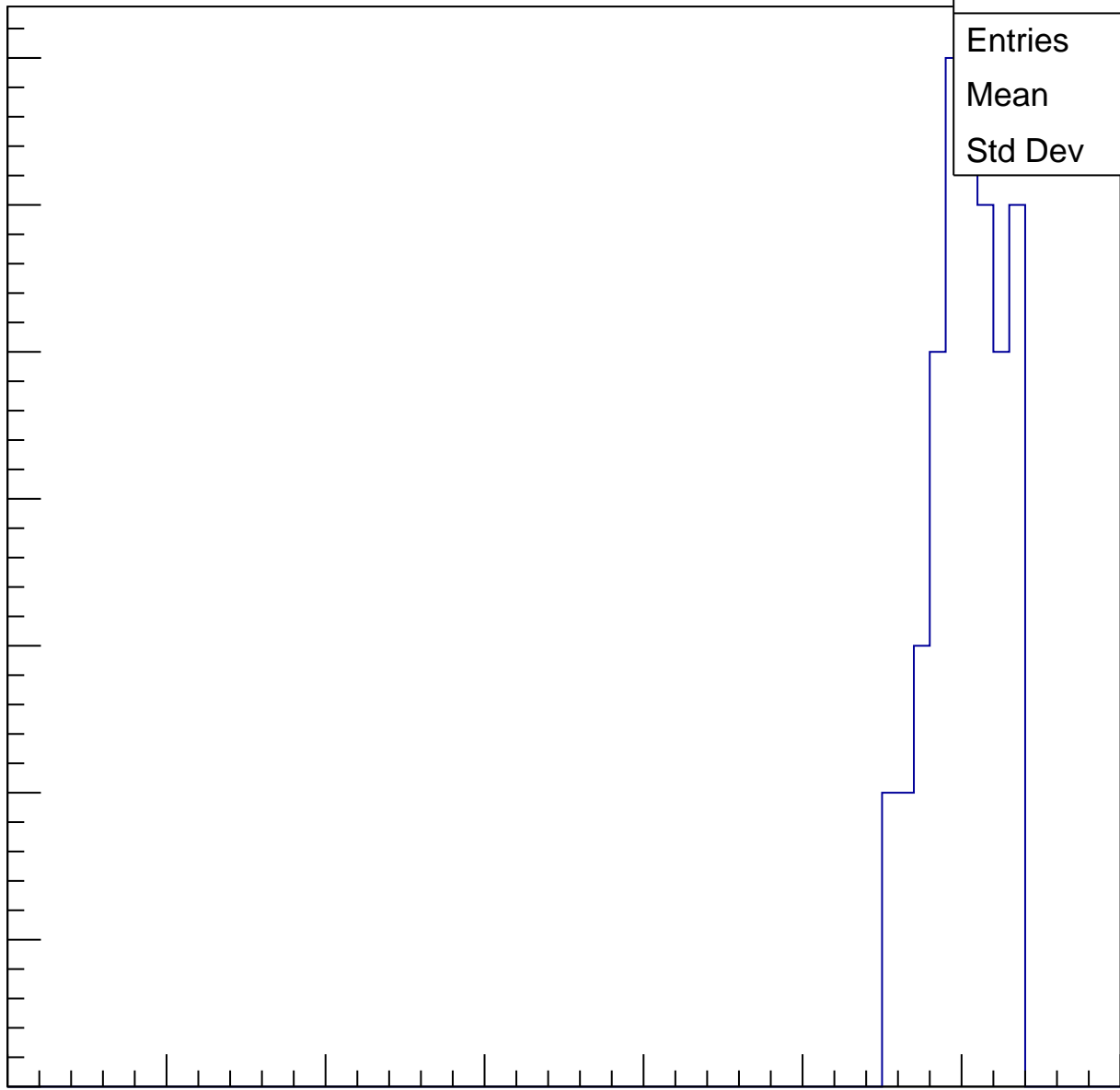
40

50

60

70

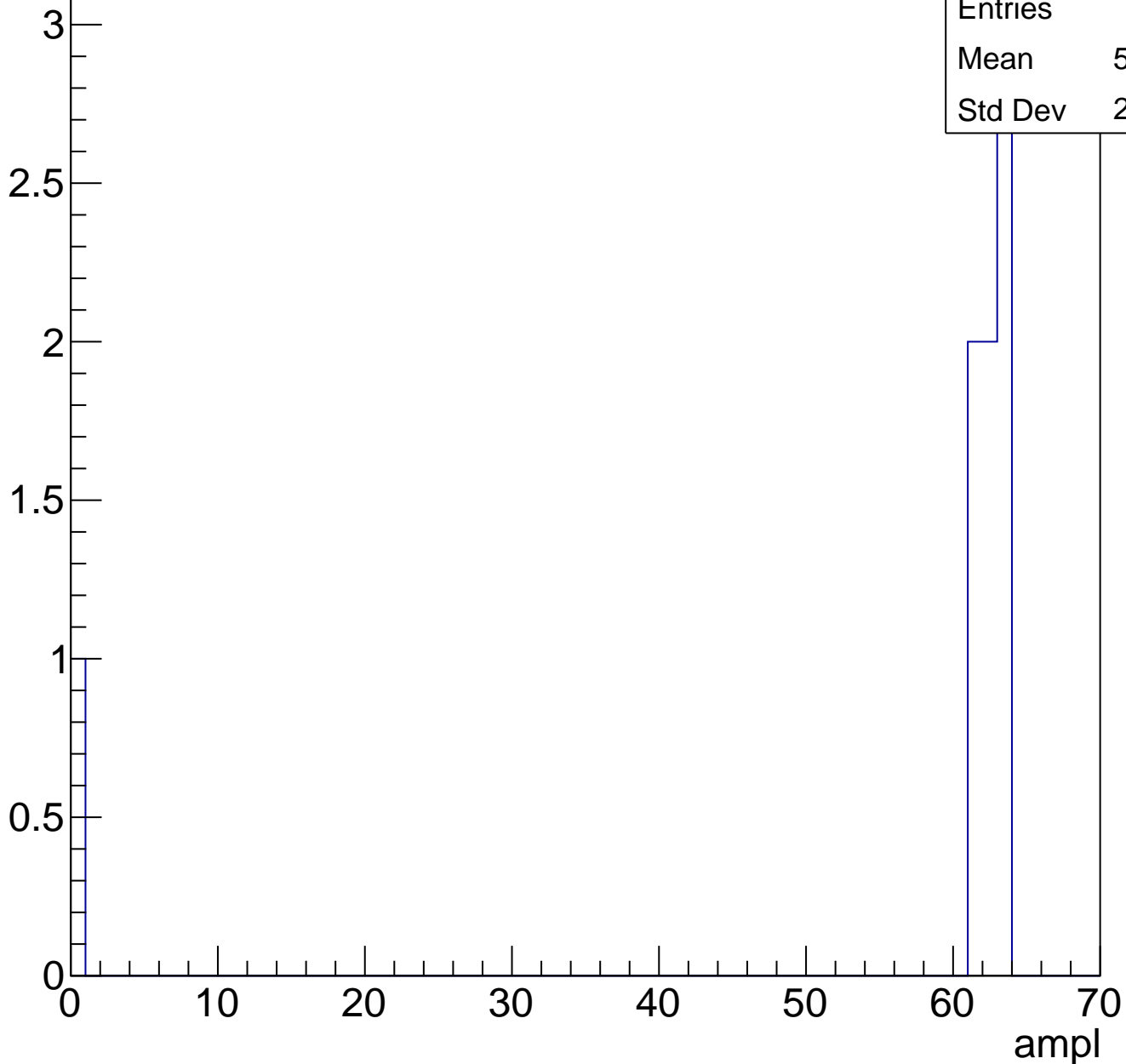
ampl



# B0L102S, U17-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch29, adc0

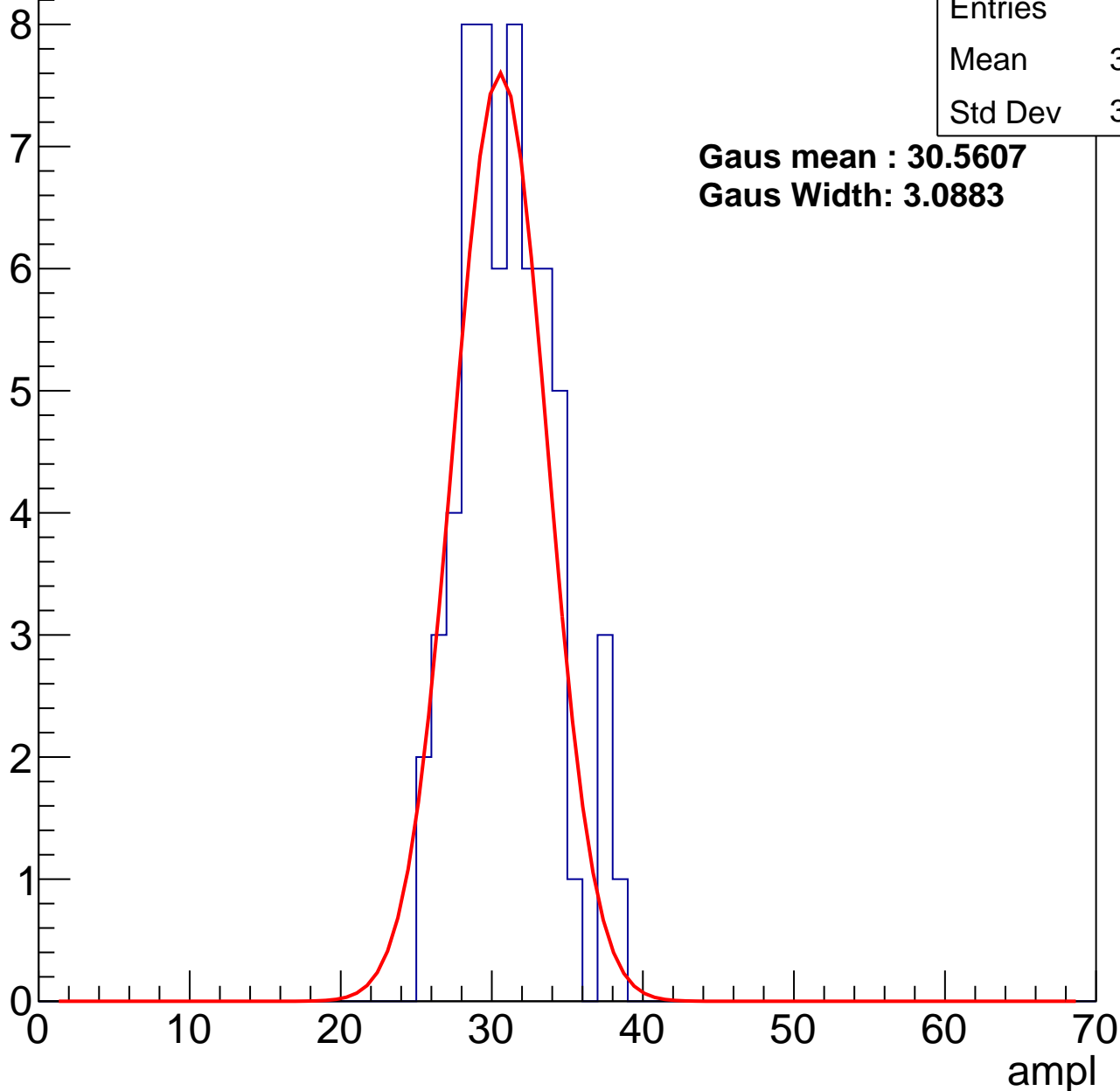
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	30.56
Std Dev	3.022

**Gaus mean : 30.5607**

**Gaus Width: 3.0883**



# B0L102S, U17-ch29, adc1

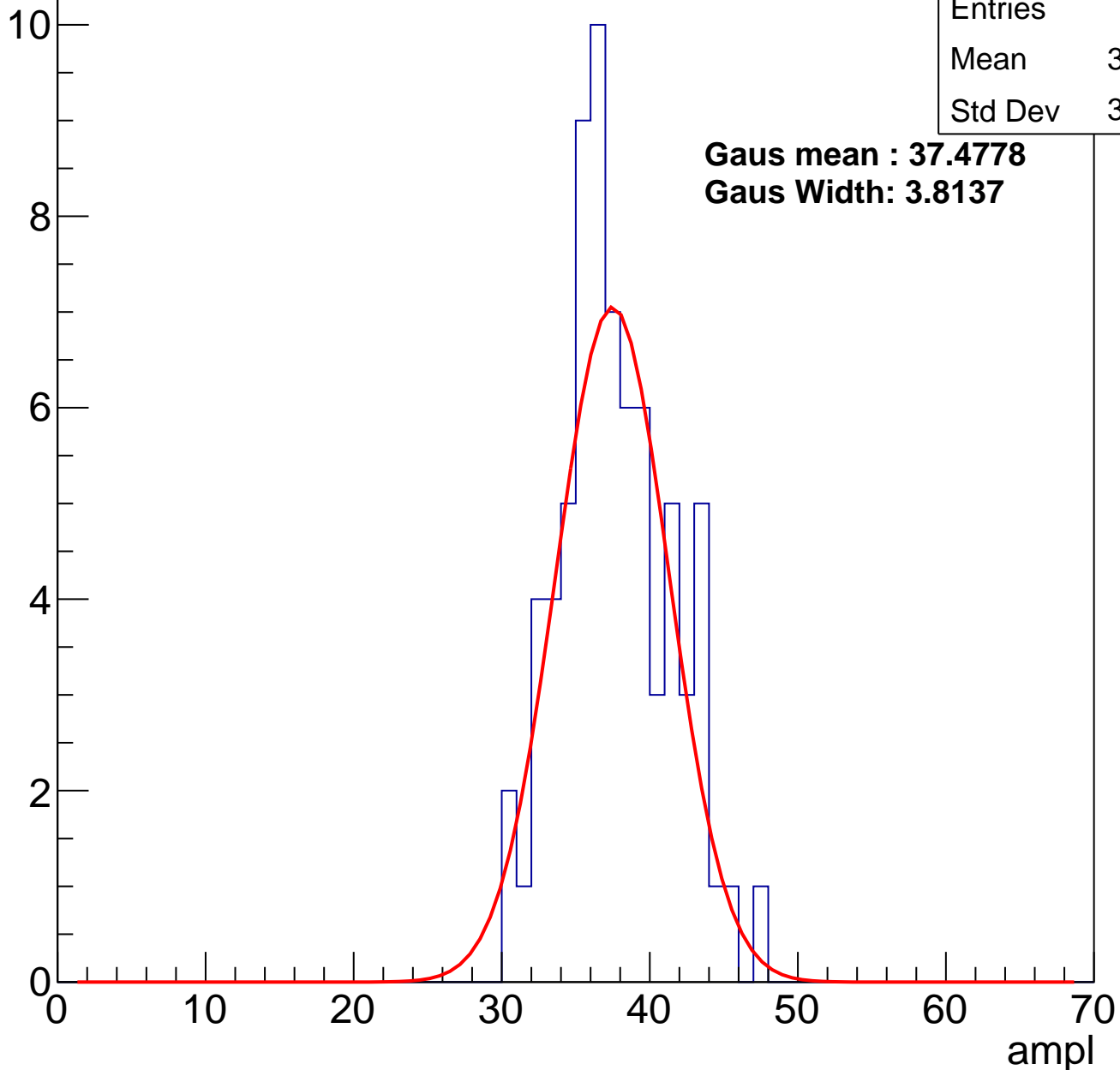
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	37.25
Std Dev	3.682

**Gaus mean : 37.4778**

**Gaus Width: 3.8137**

Entry



# B0L102S, U17-ch29, adc2

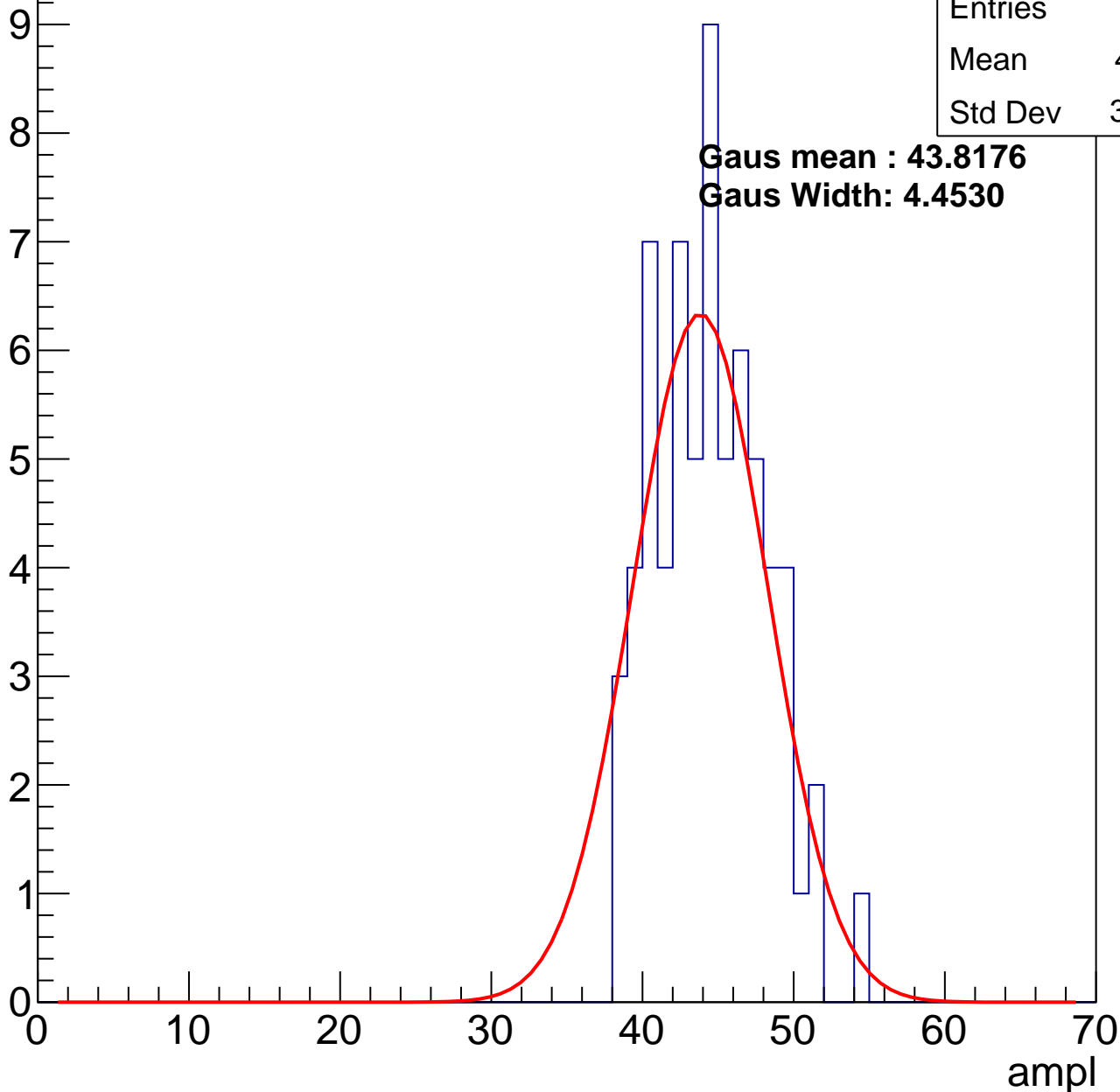
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	44.01
Std Dev	3.585

**Gaus mean : 43.8176**

**Gaus Width: 4.4530**

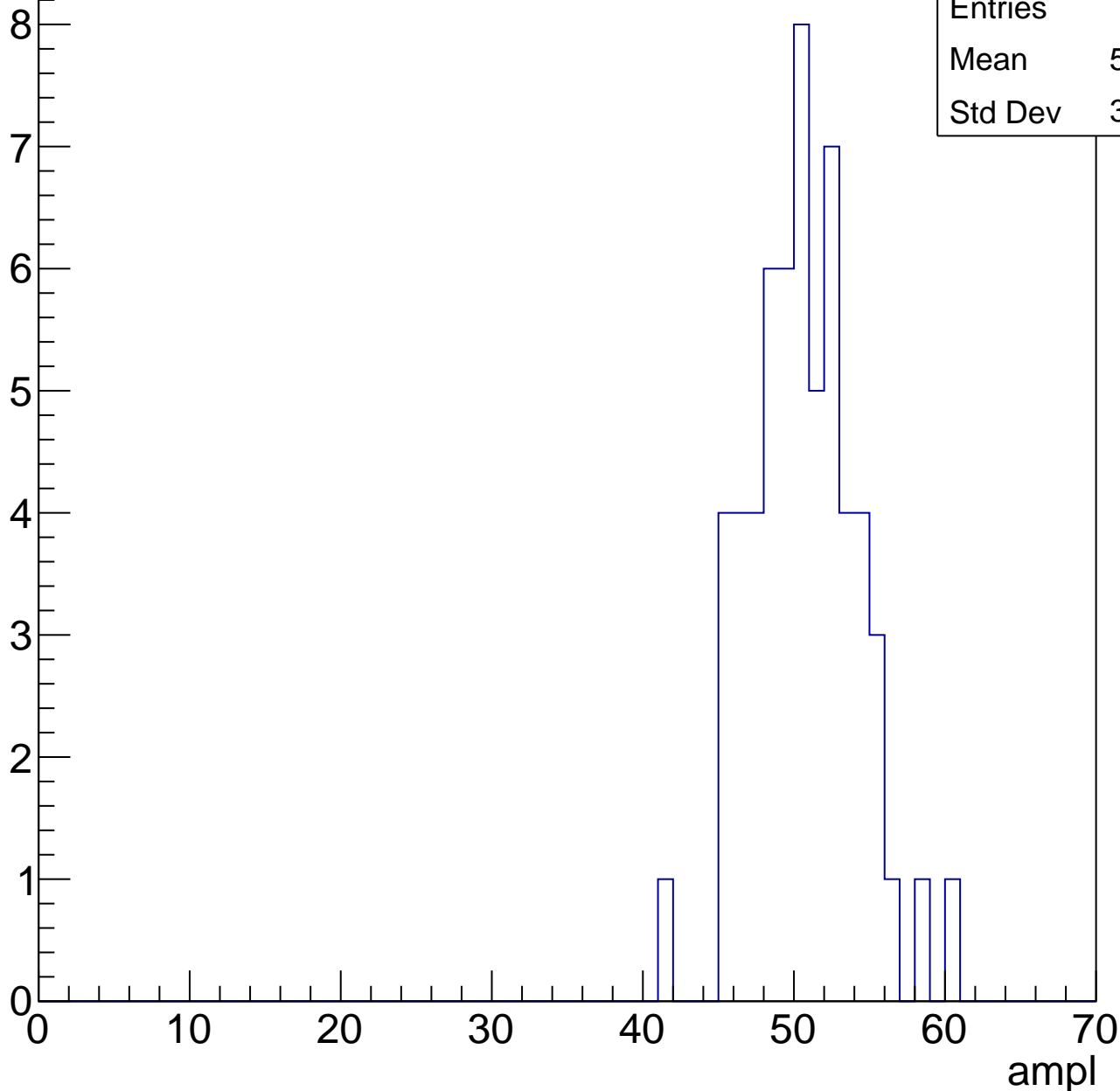


# B0L102S, U17-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

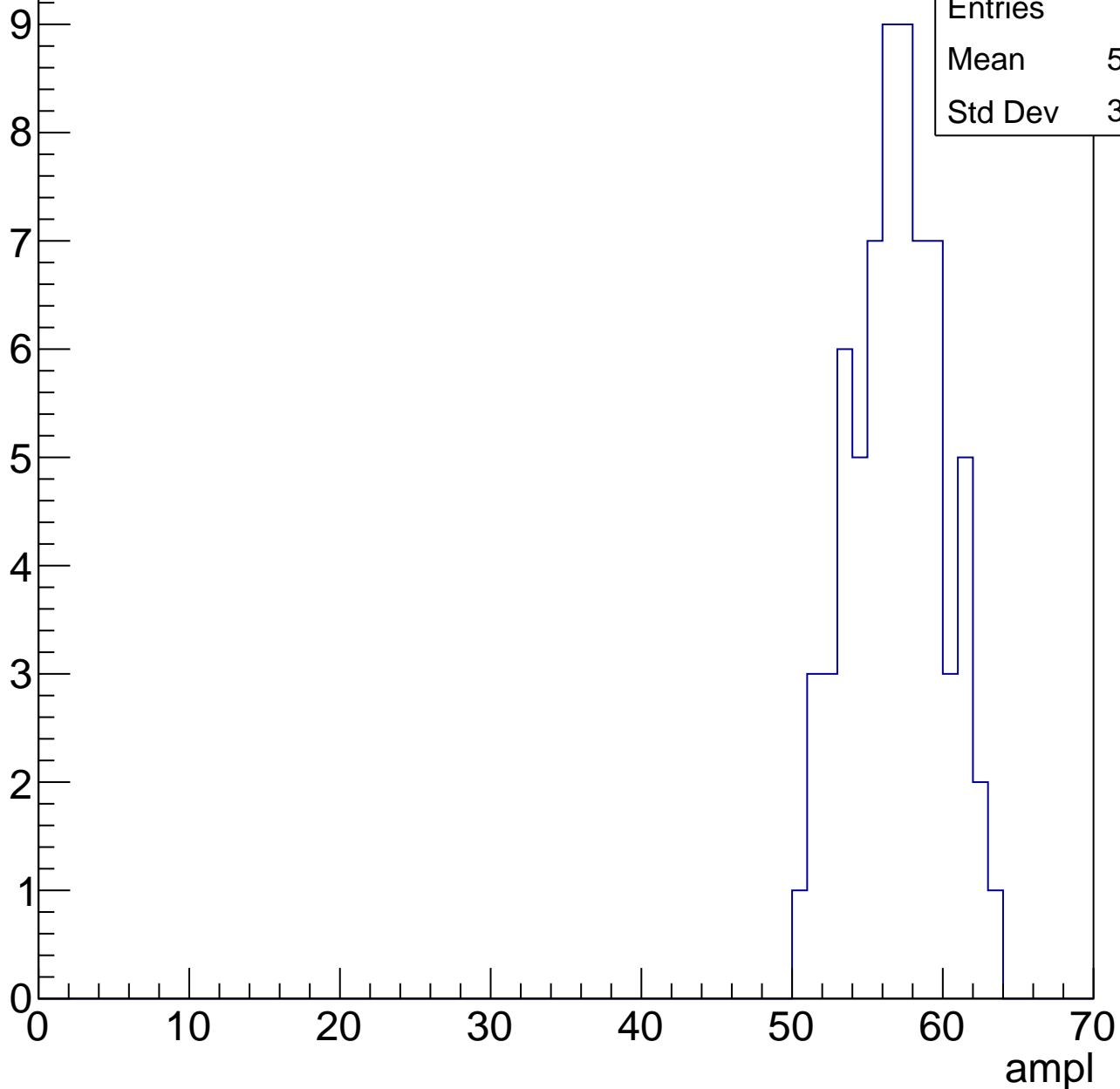
Entries	59
Mean	50.19
Std Dev	3.486



# B0L102S, U17-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

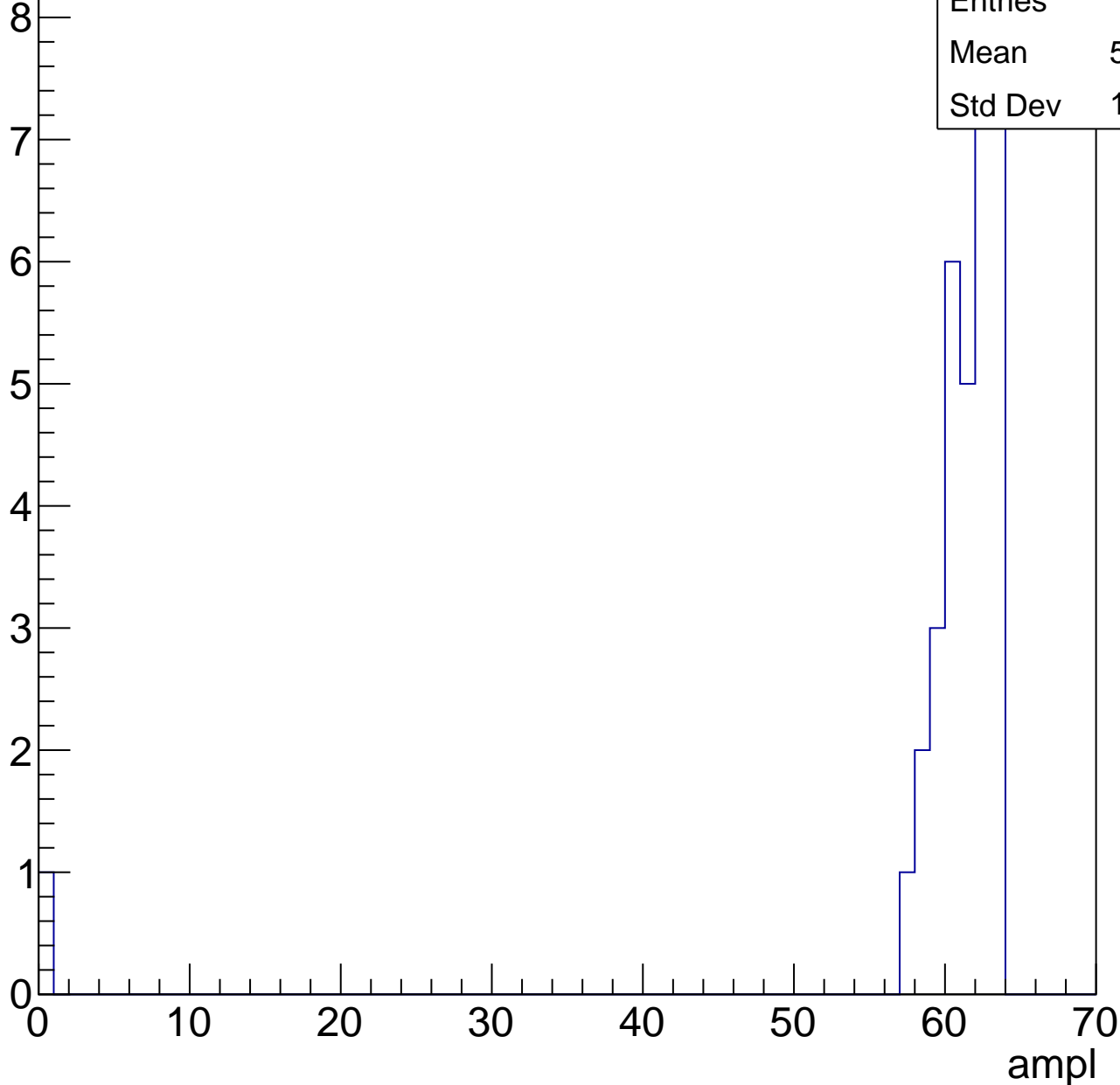


# B0L102S, U17-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	34
Mean	59.26
Std Dev	10.45



# B0L102S, U17-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L102S, U17-ch30, adc0

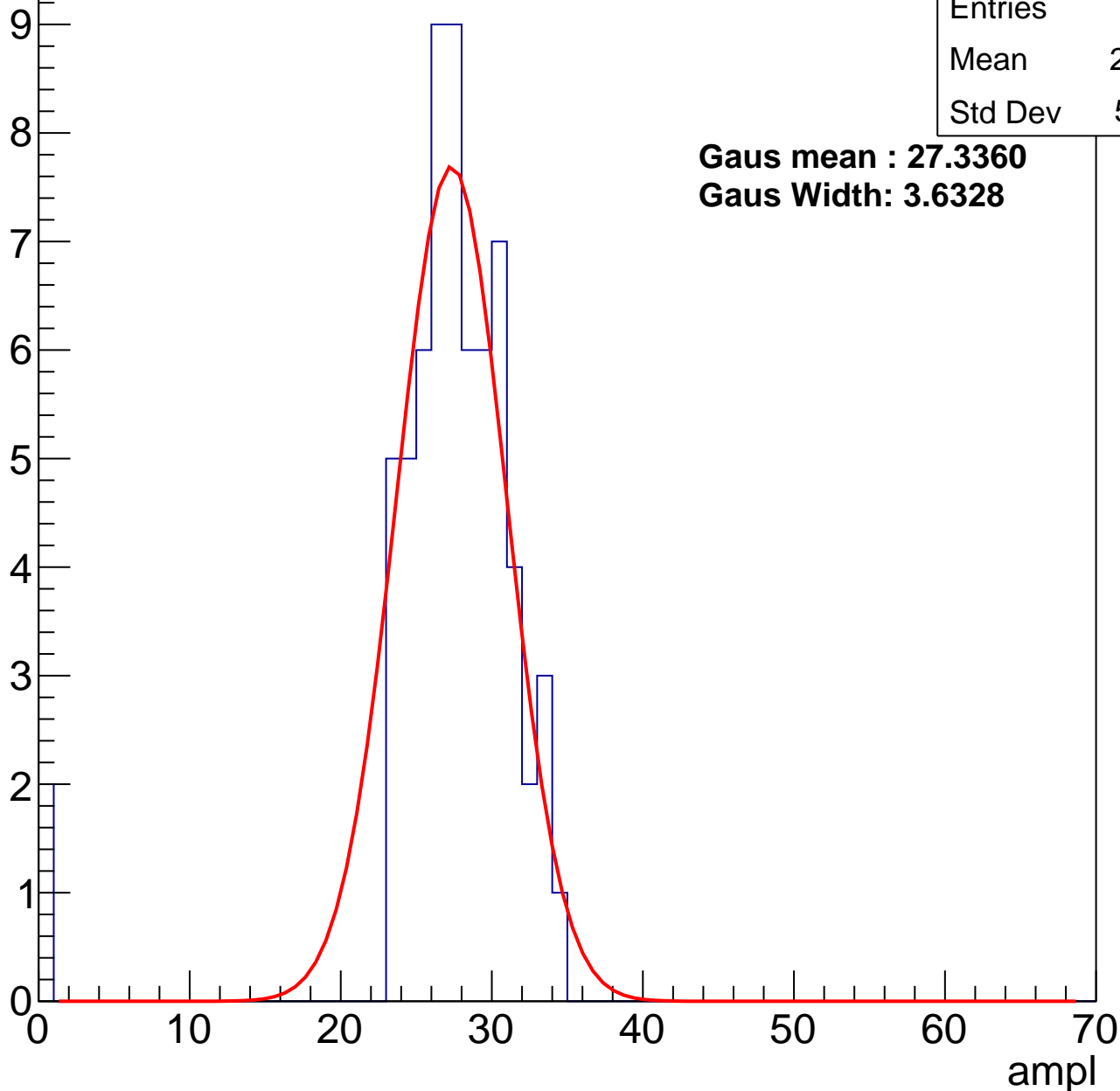
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	26.69
Std Dev	5.511

**Gaus mean : 27.3360**

**Gaus Width: 3.6328**



# B0L102S, U17-ch30, adc1

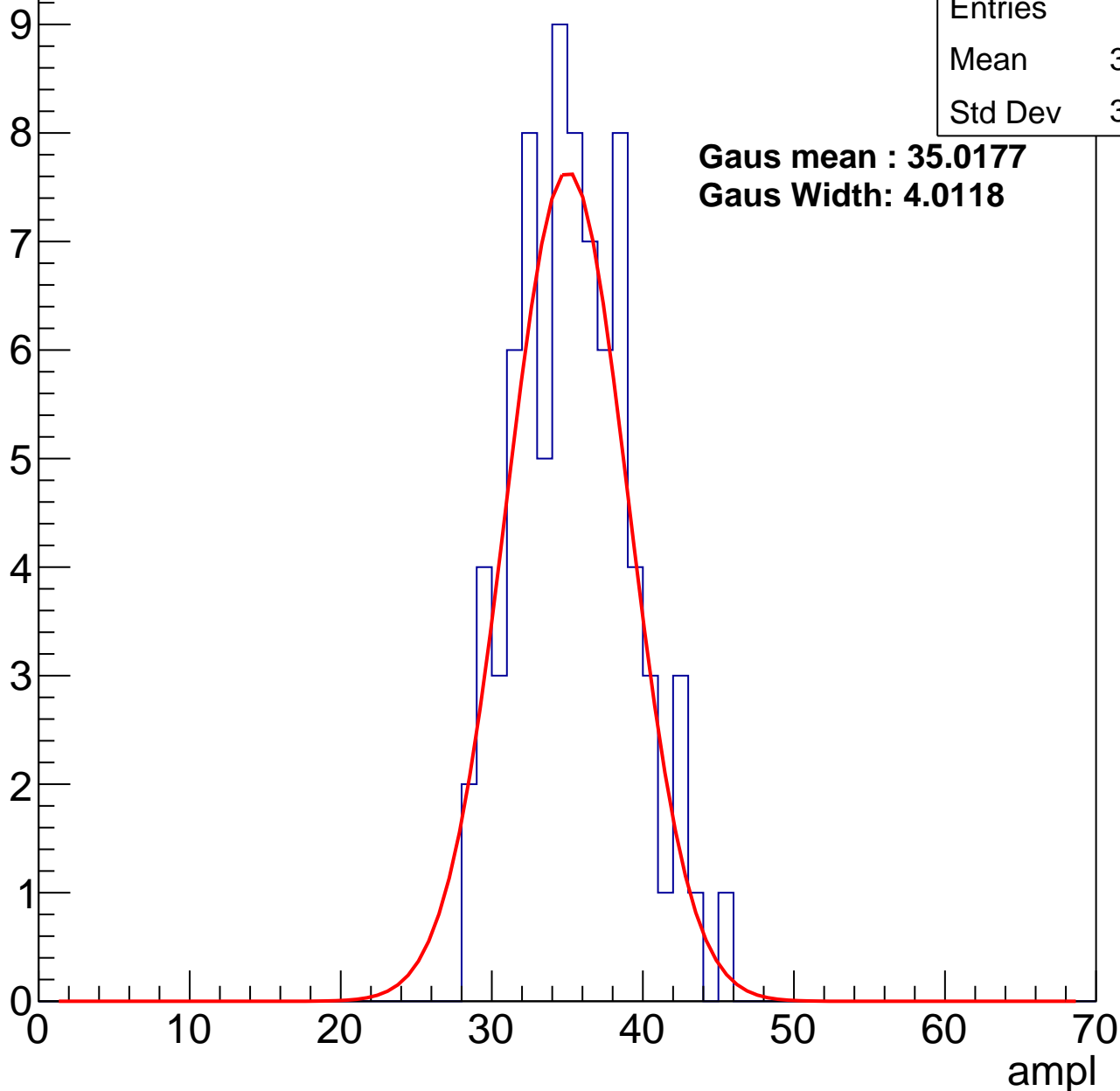
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	34.99
Std Dev	3.737

**Gaus mean : 35.0177**

**Gaus Width: 4.0118**



# B0L102S, U17-ch30, adc2

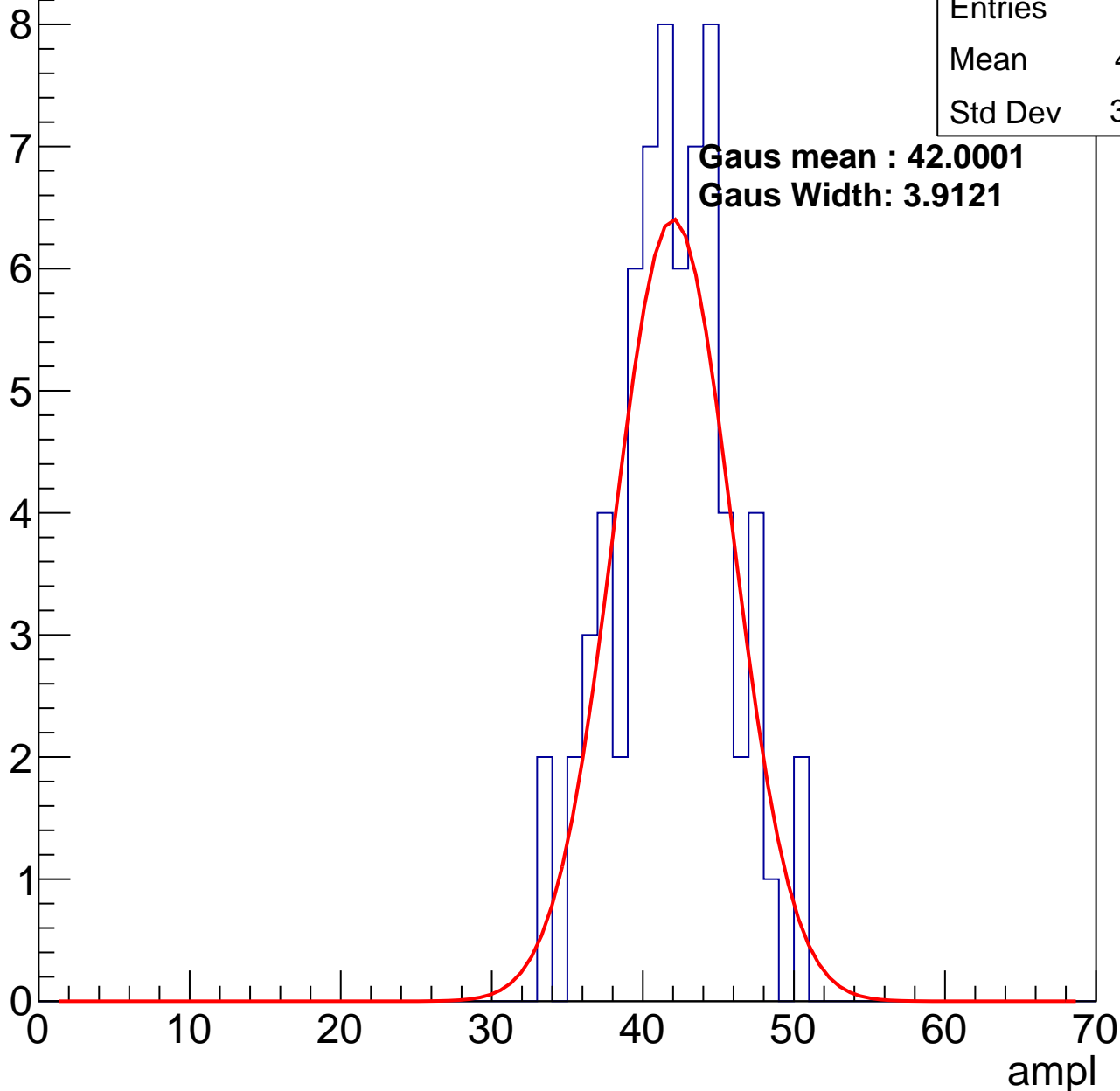
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.51
Std Dev	3.728

**Gaus mean : 42.0001**

**Gaus Width: 3.9121**

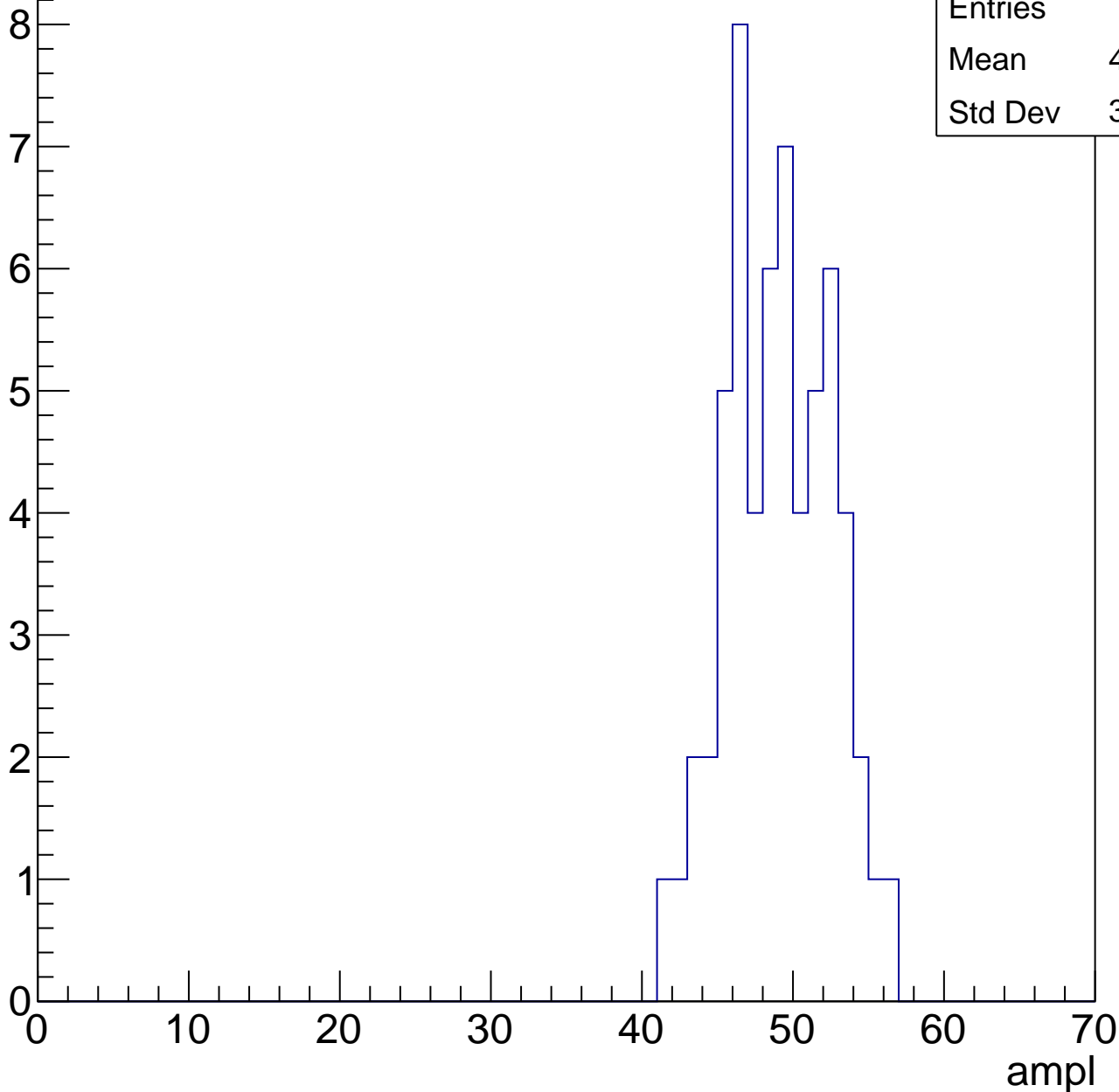


# B0L102S, U17-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	48.59
Std Dev	3.386



# B0L102S, U17-ch30, adc4

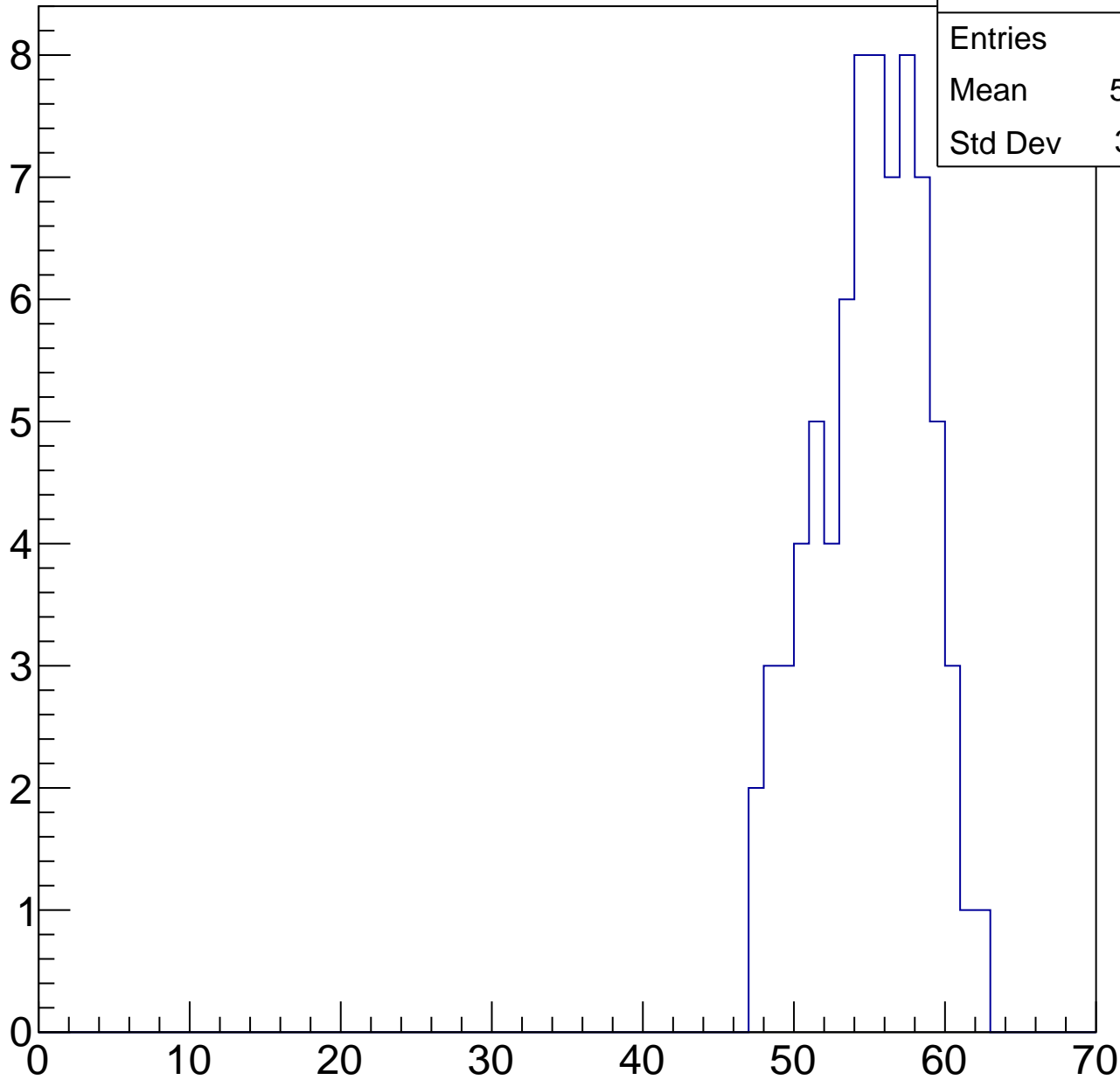
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	75
Mean	54.53
Std Dev	3.571

ampl



# B0L102S, U17-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	34
Mean	59.91
Std Dev	2.063

ampl

0

10

20

30

40

50

60

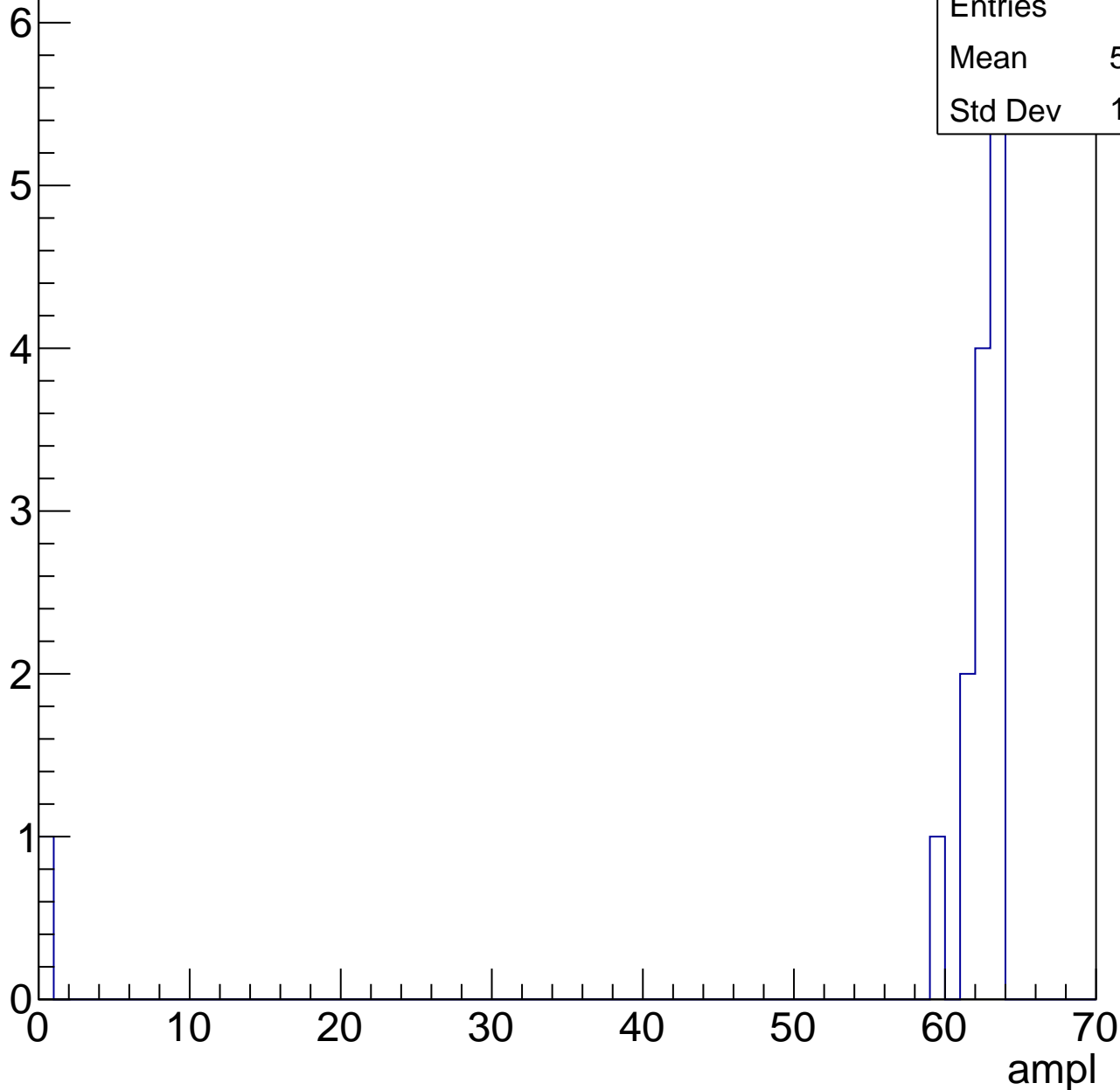
70

# B0L102S, U17-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	57.64
Std Dev	16.03





# B0L102S, U17-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch31, adc0

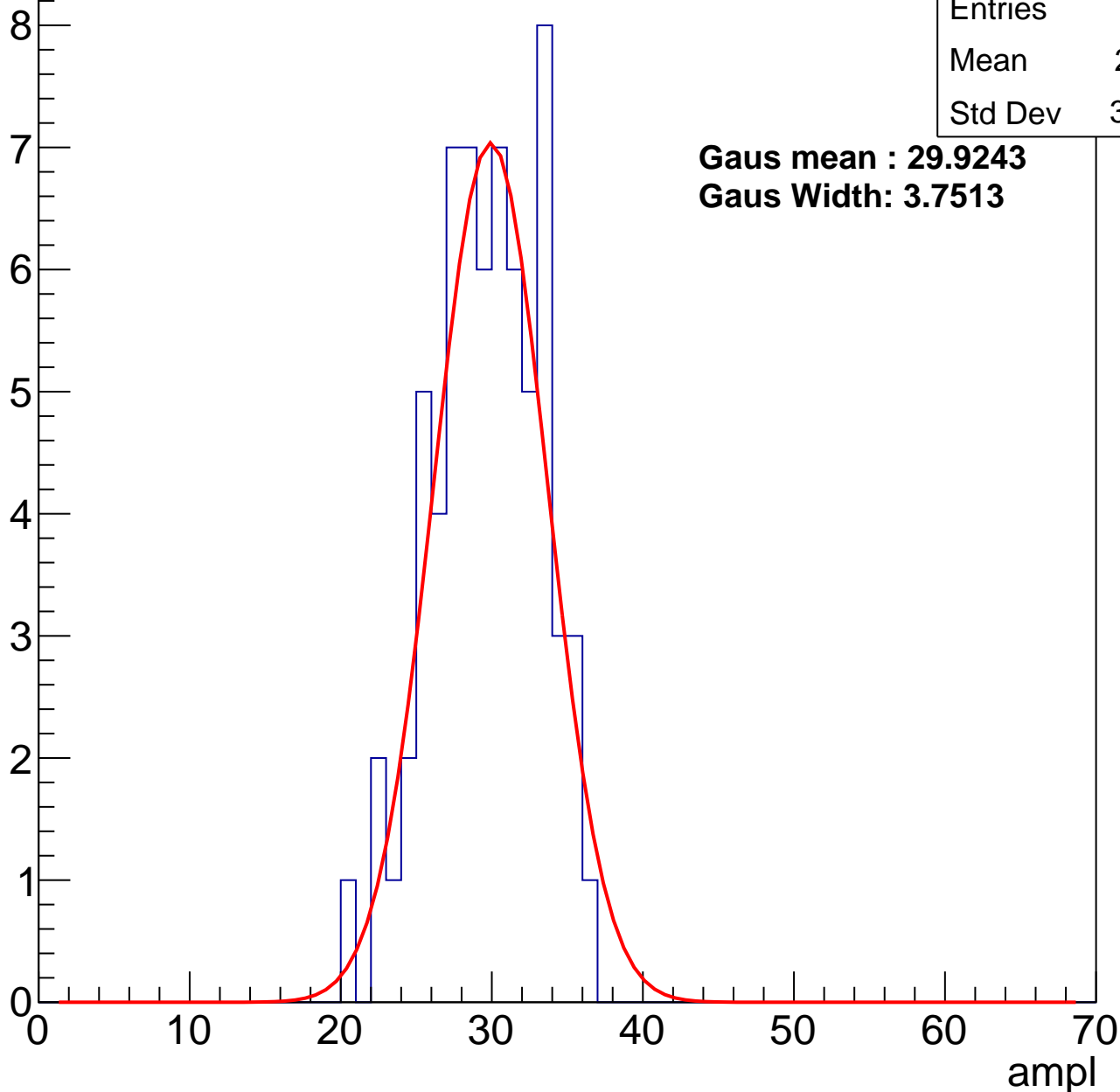
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.21
Std Dev	3.534

**Gaus mean : 29.9243**

**Gaus Width: 3.7513**



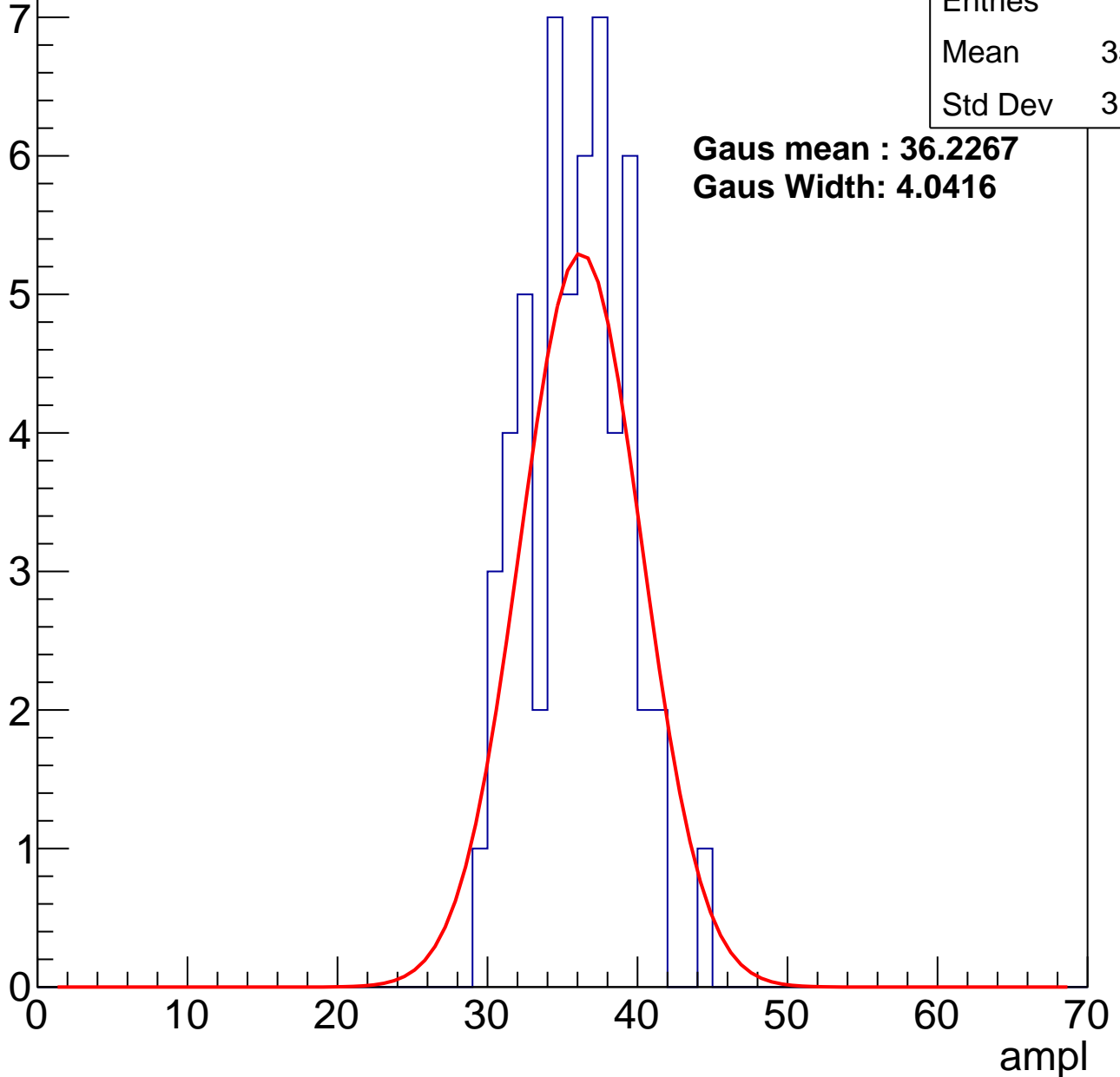
# B0L102S, U17-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	35.44
Std Dev	3.285

**Gaus mean : 36.2267**  
**Gaus Width: 4.0416**



# B0L102S, U17-ch31, adc2

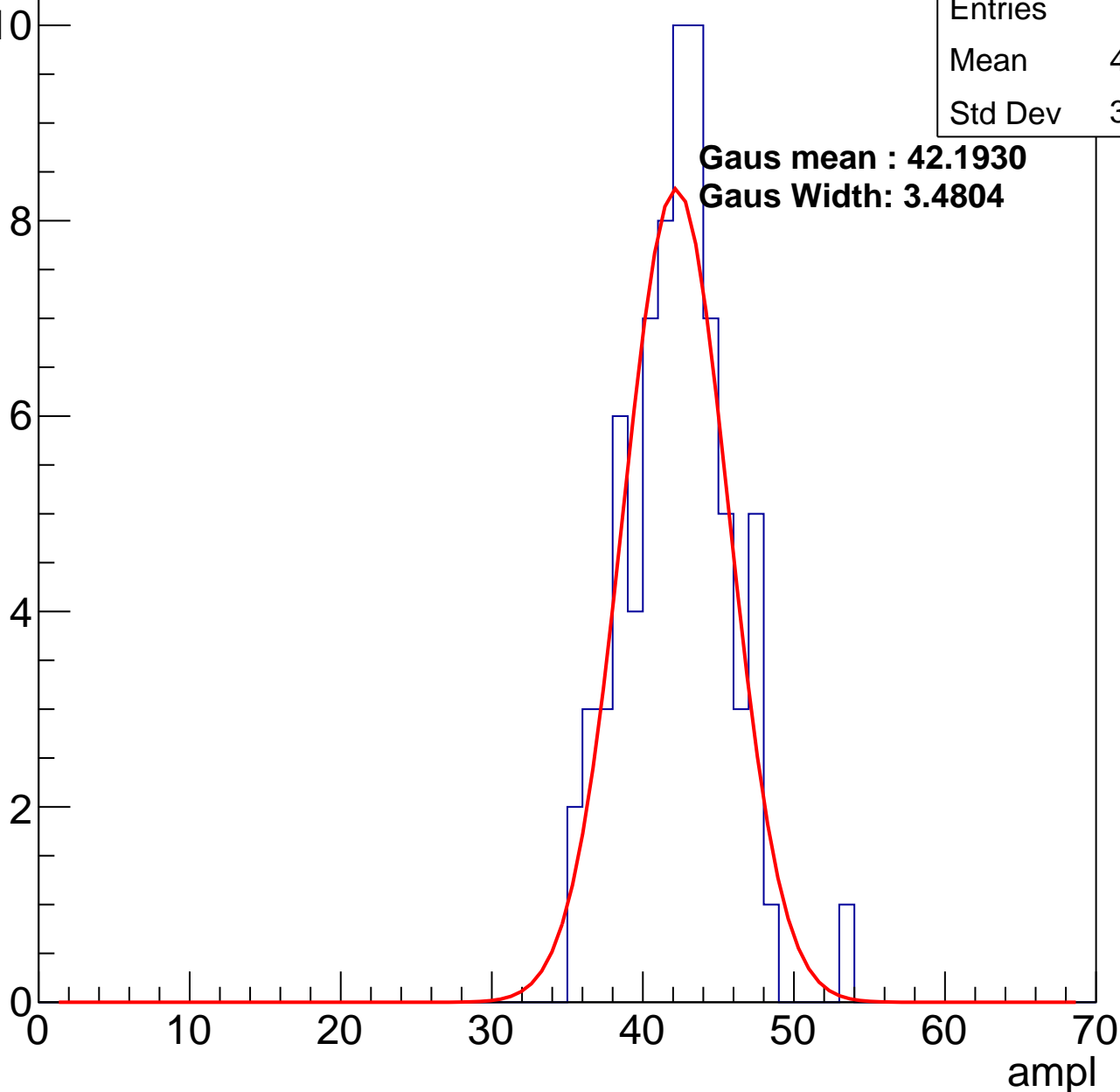
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	41.84
Std Dev	3.386

**Gaus mean : 42.1930**

**Gaus Width: 3.4804**



# B0L102S, U17-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

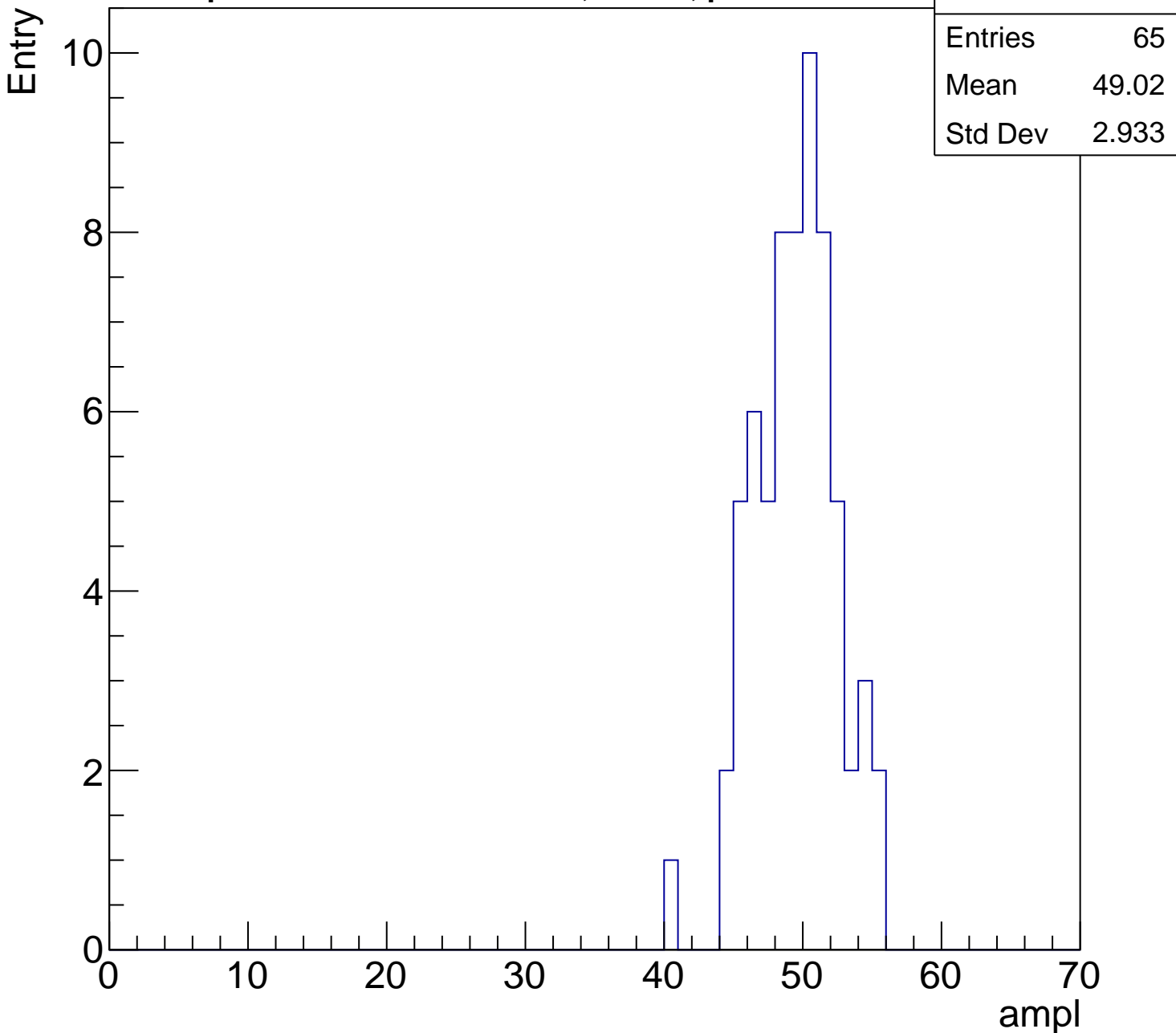
Entries	65
Mean	49.02
Std Dev	2.933

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

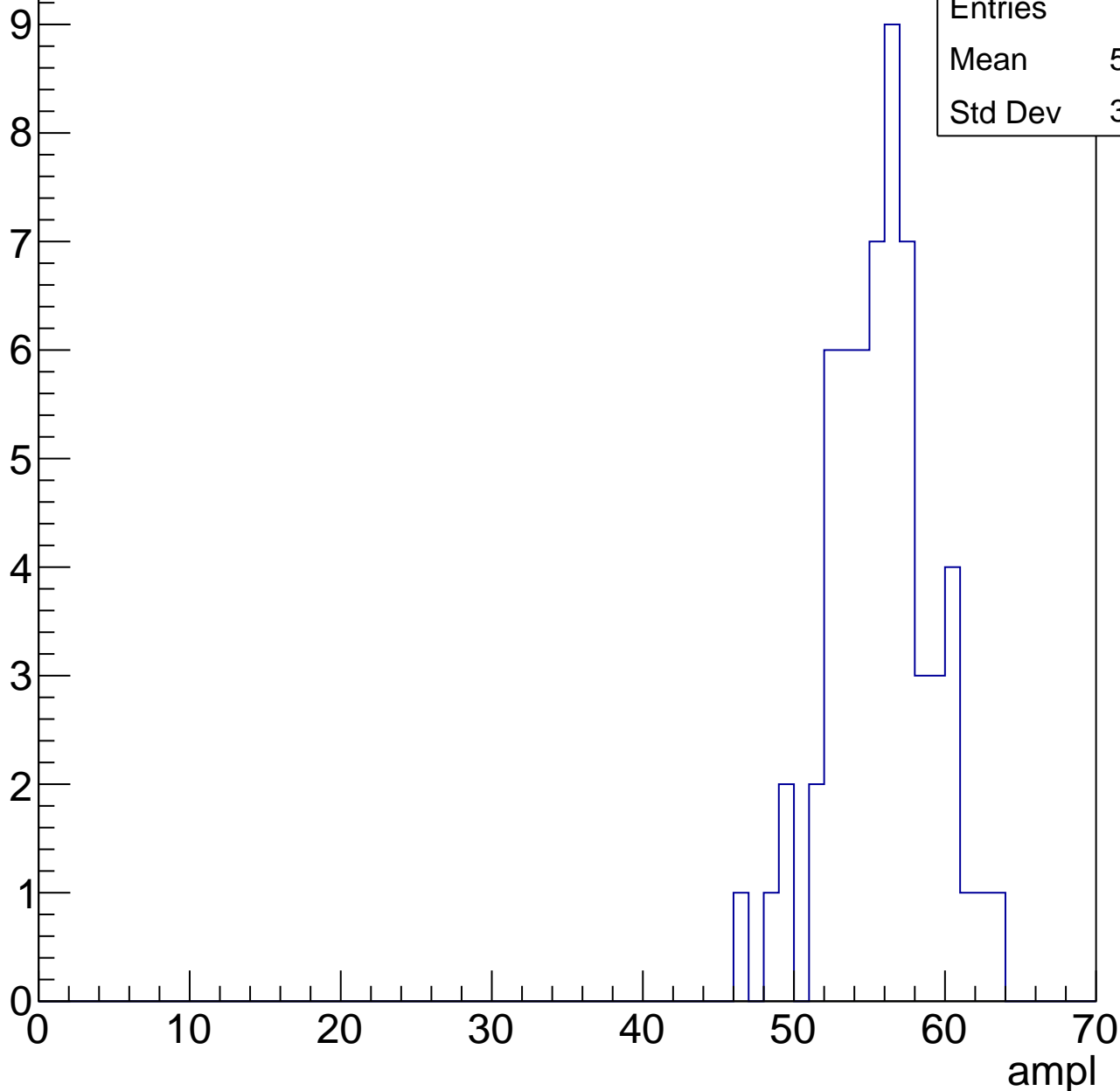


# B0L102S, U17-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

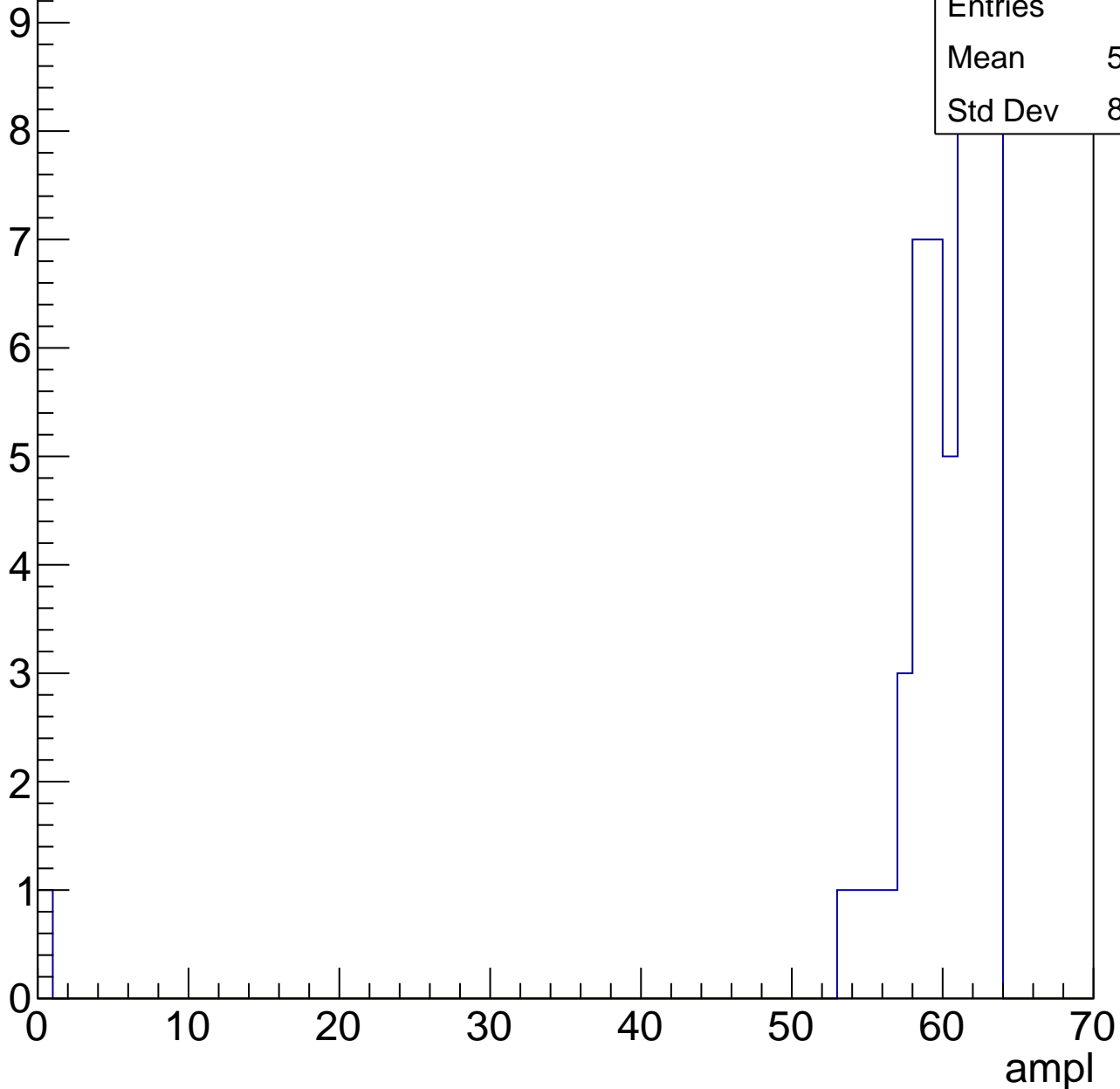
Entries	60
Mean	55.22
Std Dev	3.357



# B0L102S, U17-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	63
Mean	27.83
Std Dev	3.016

**Gaus mean : 28.3586**

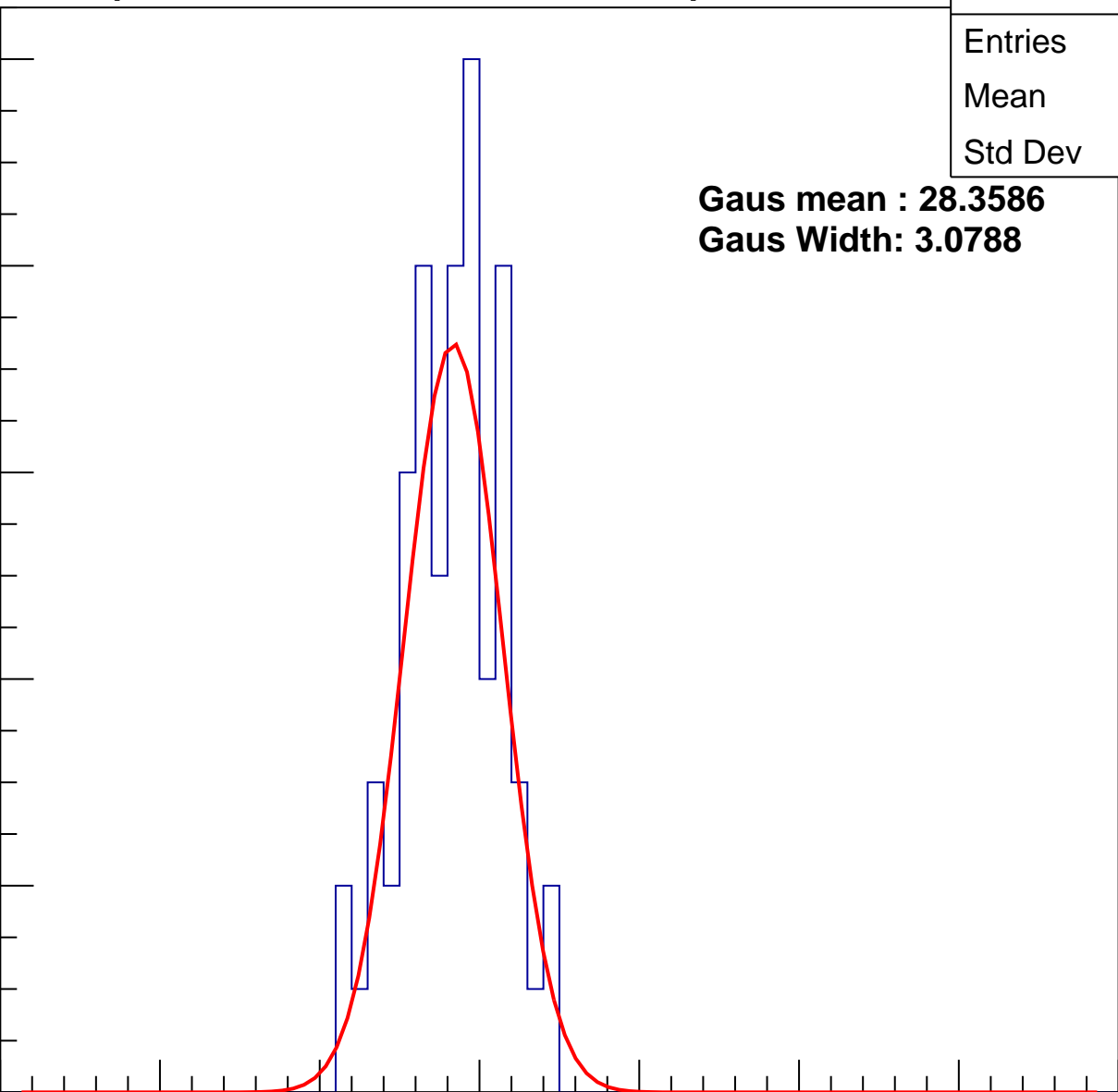
**Gaus Width: 3.0788**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch32, adc1

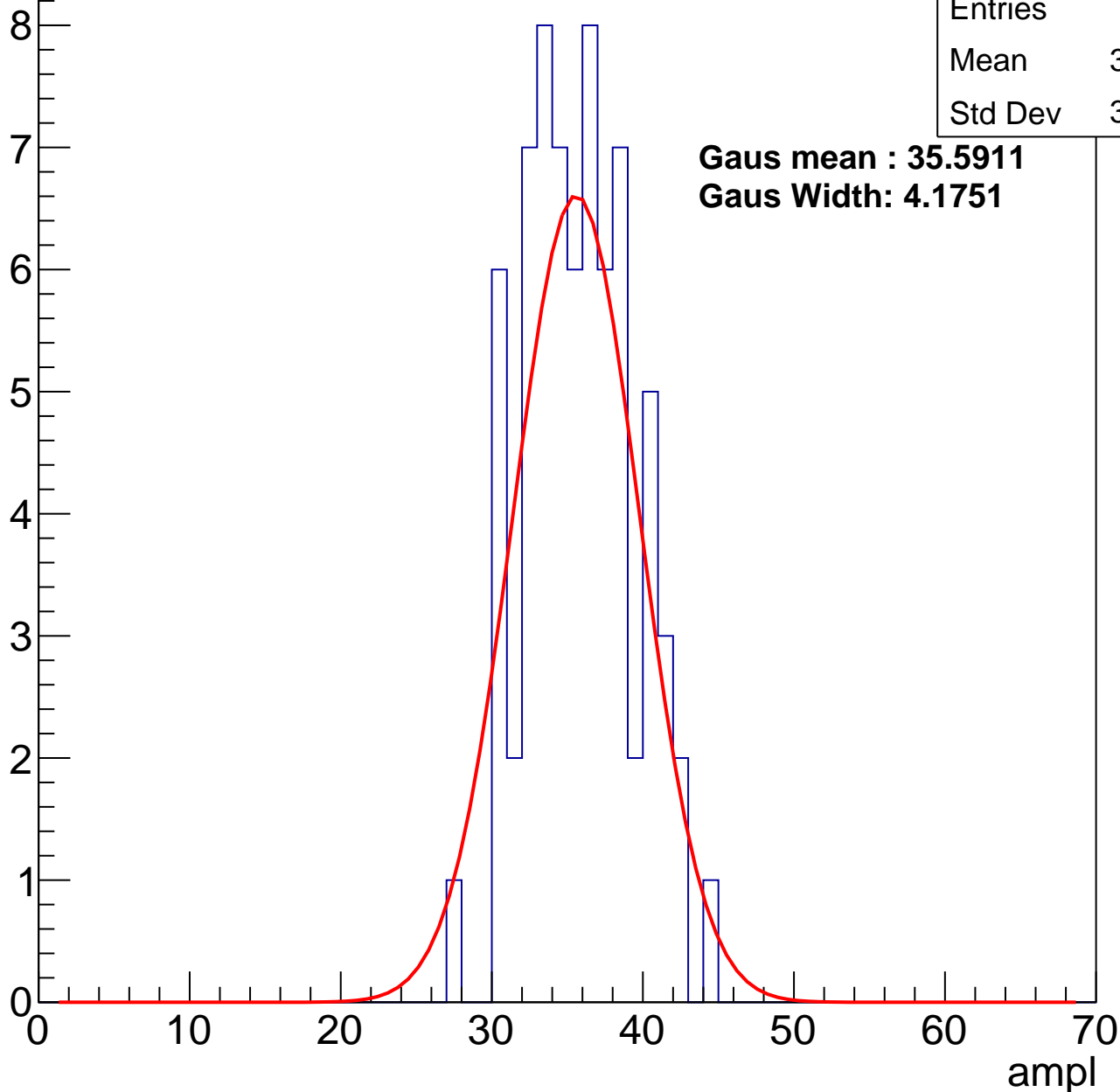
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	35.35
Std Dev	3.517

**Gaus mean : 35.5911**

**Gaus Width: 4.1751**



# B0L102S, U17-ch32, adc2

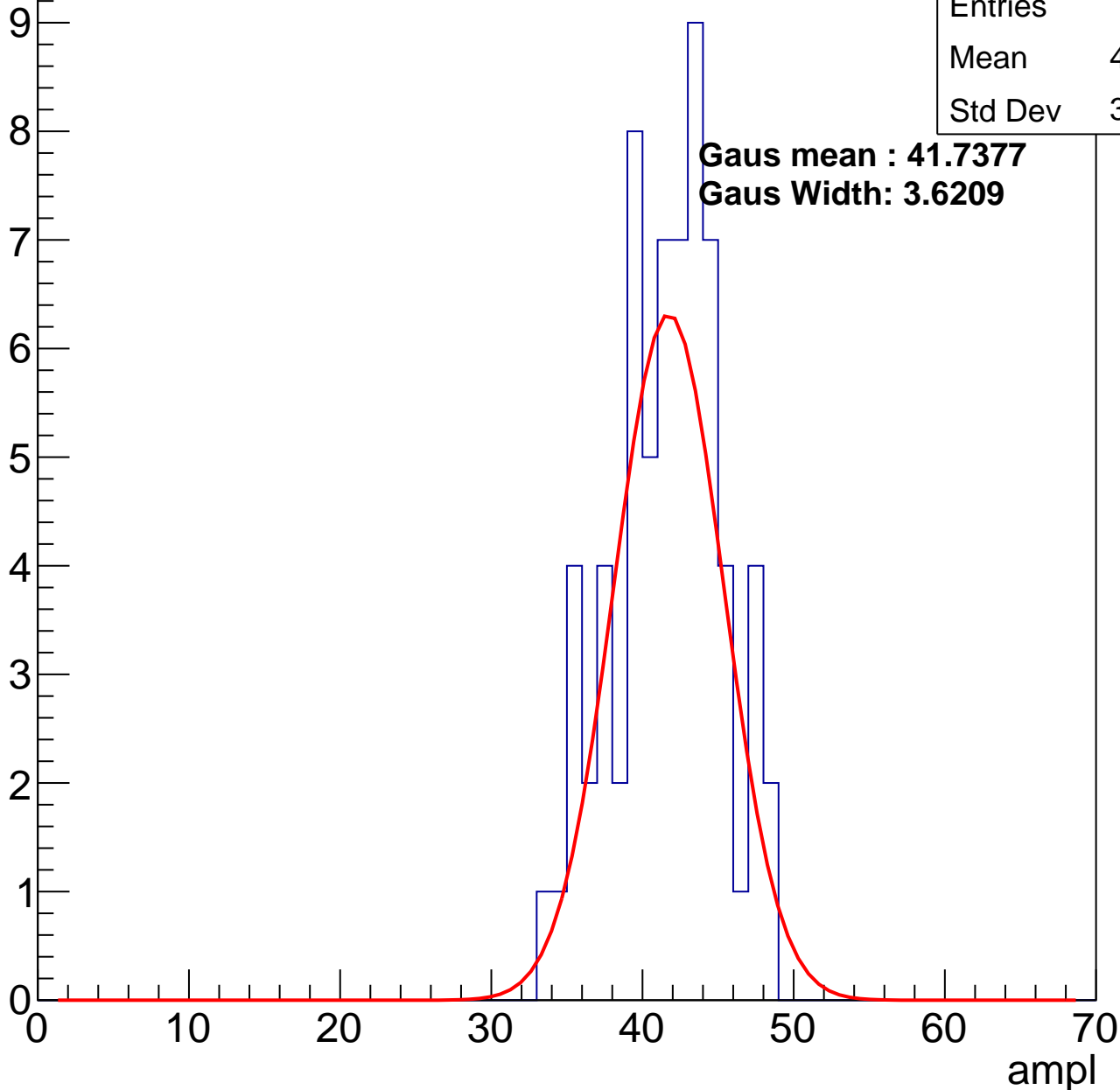
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.19
Std Dev	3.557

**Gaus mean : 41.7377**

**Gaus Width: 3.6209**

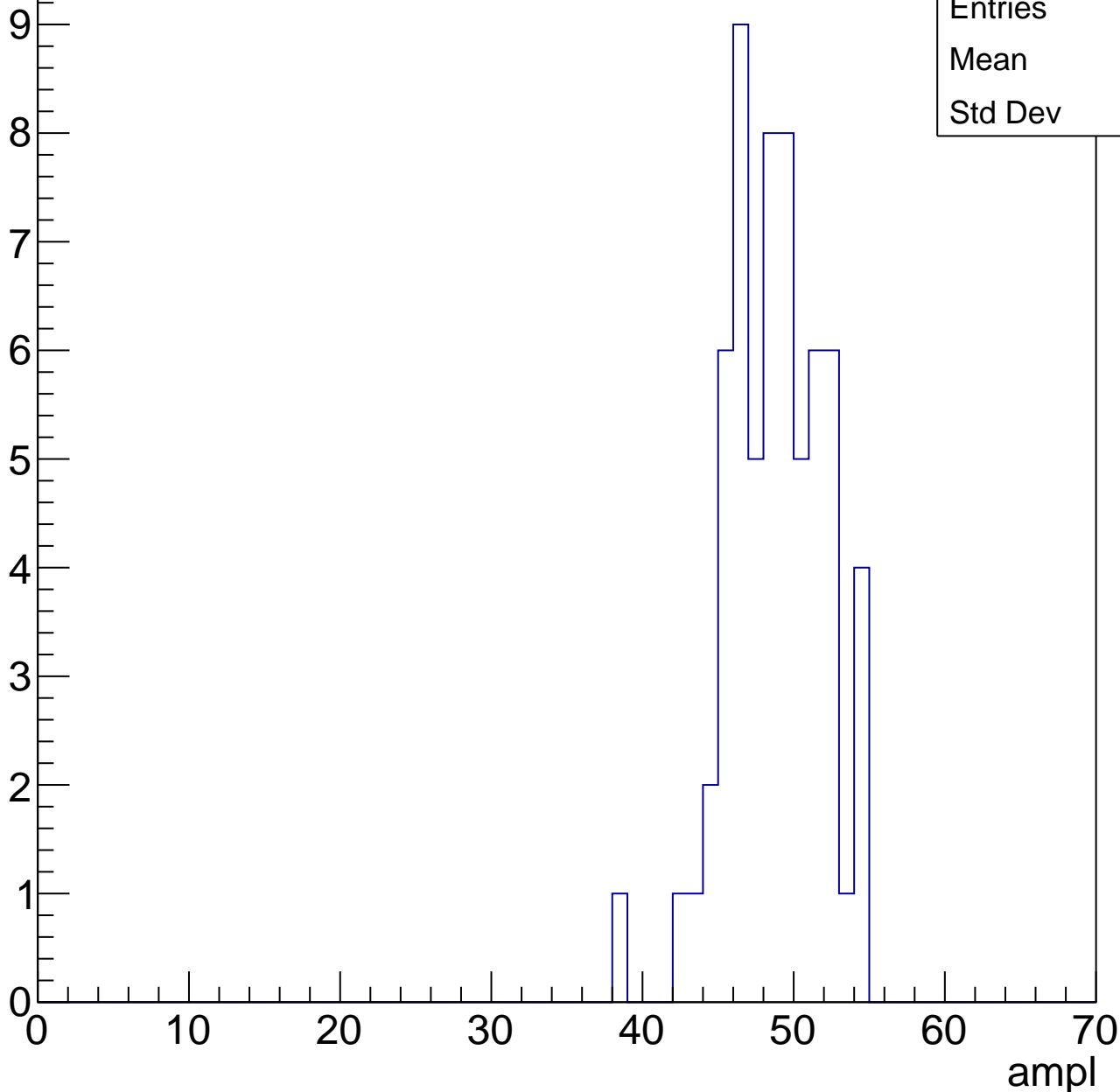


# B0L102S, U17-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	48.3
Std Dev	3.16

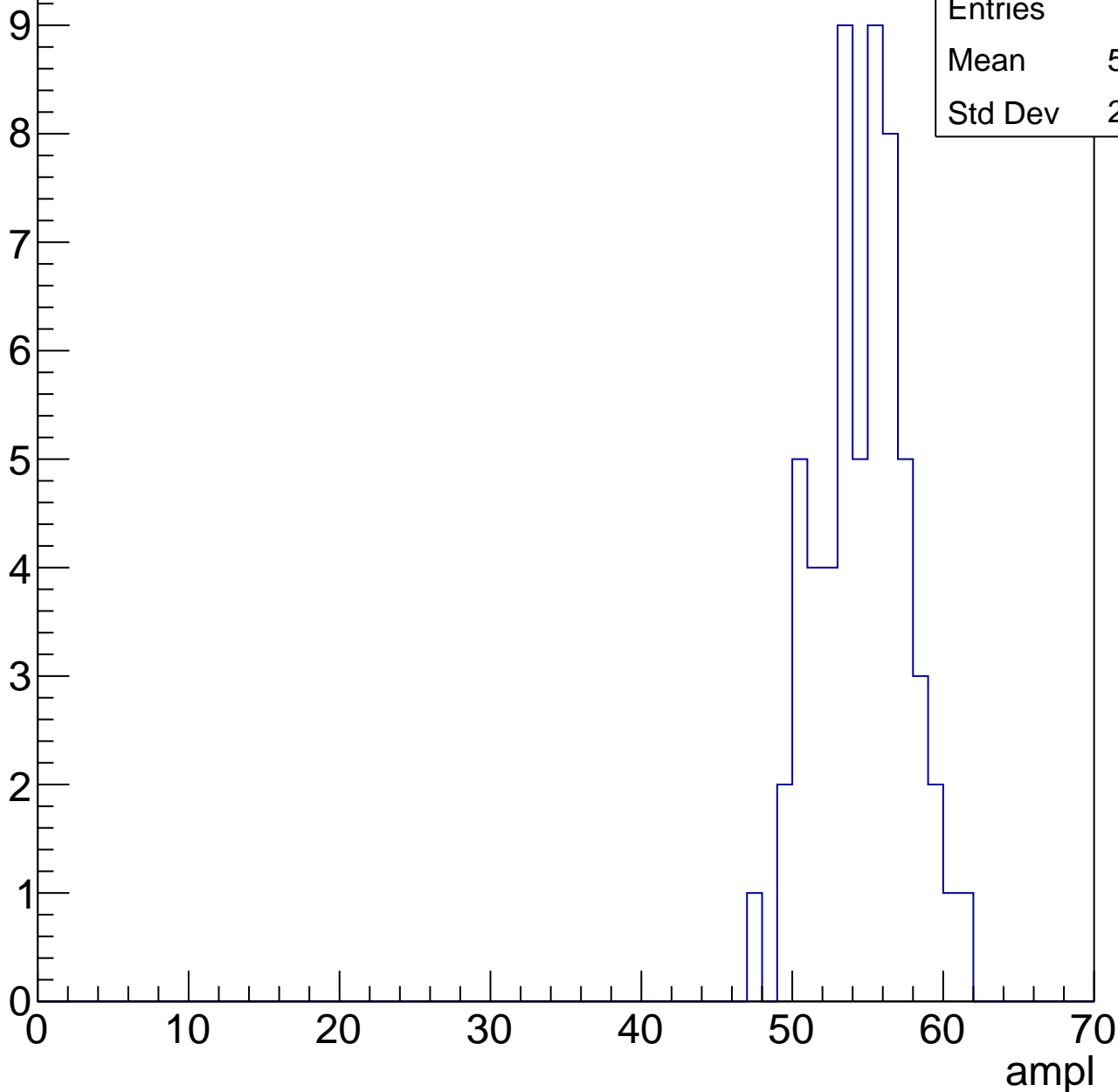


# B0L102S, U17-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.15
Std Dev	2.933



# B0L102S, U17-ch32, adc5

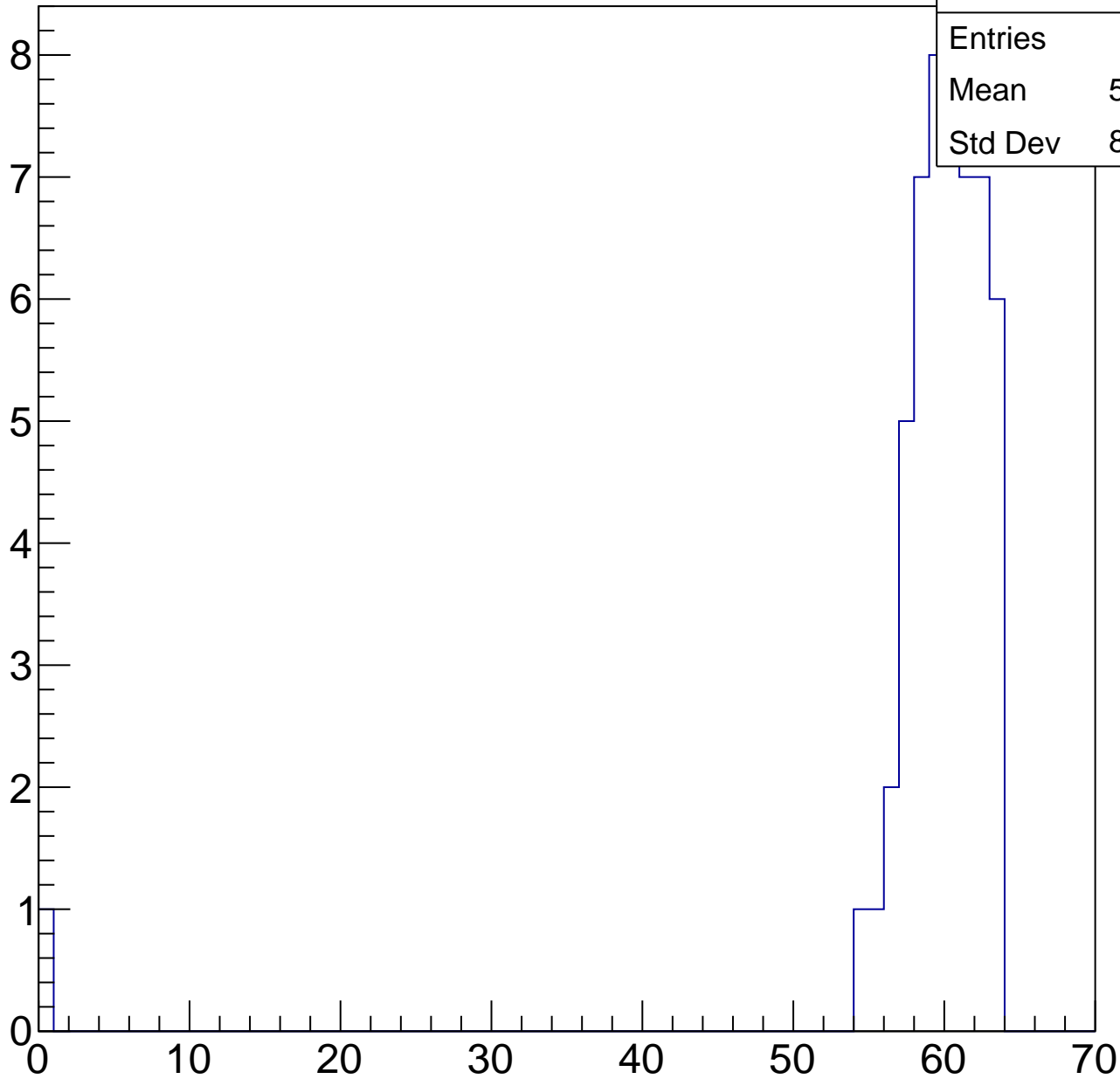
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.55
Std Dev	8.413

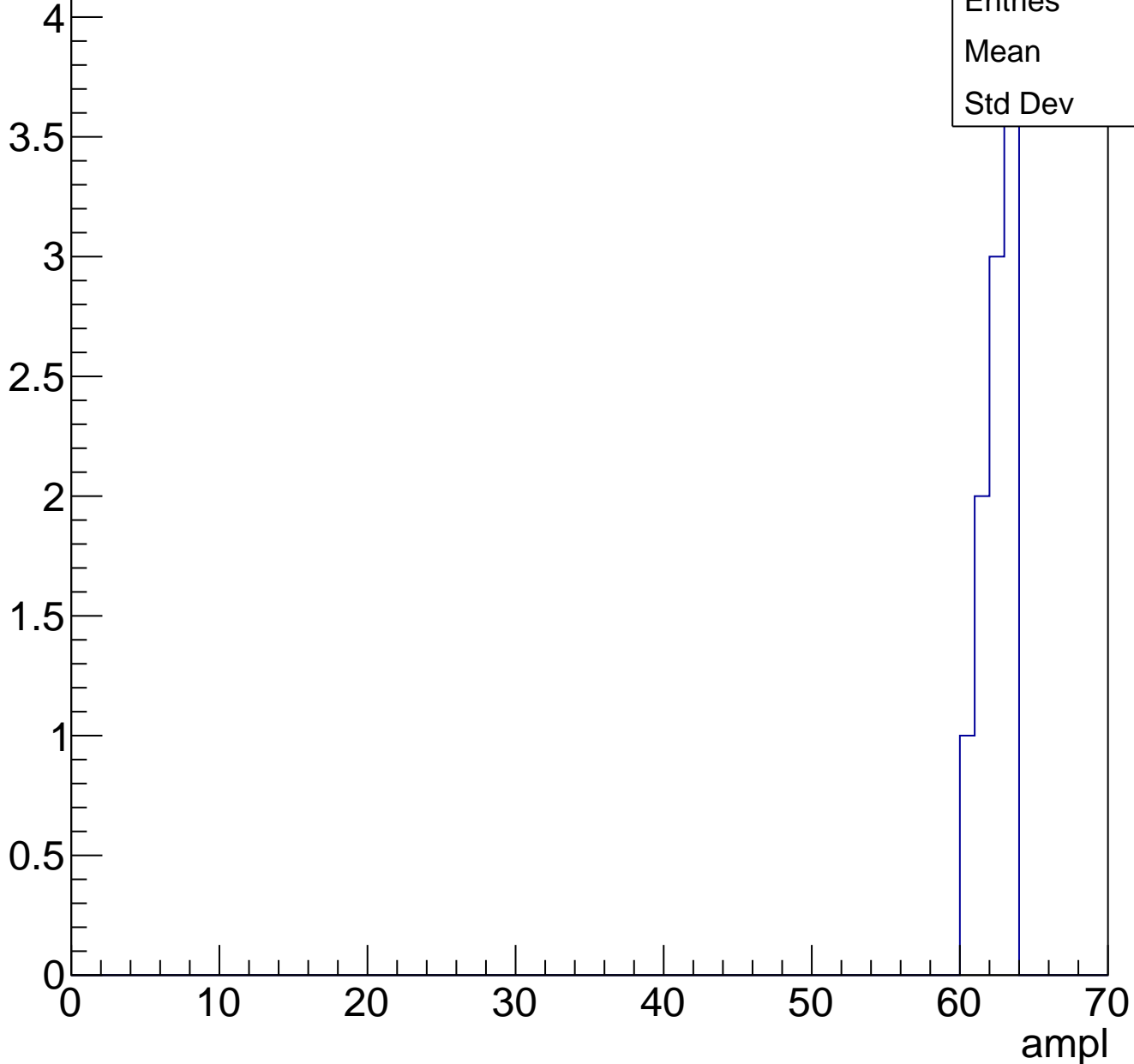
ampl



# B0L102S, U17-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch33, adc0

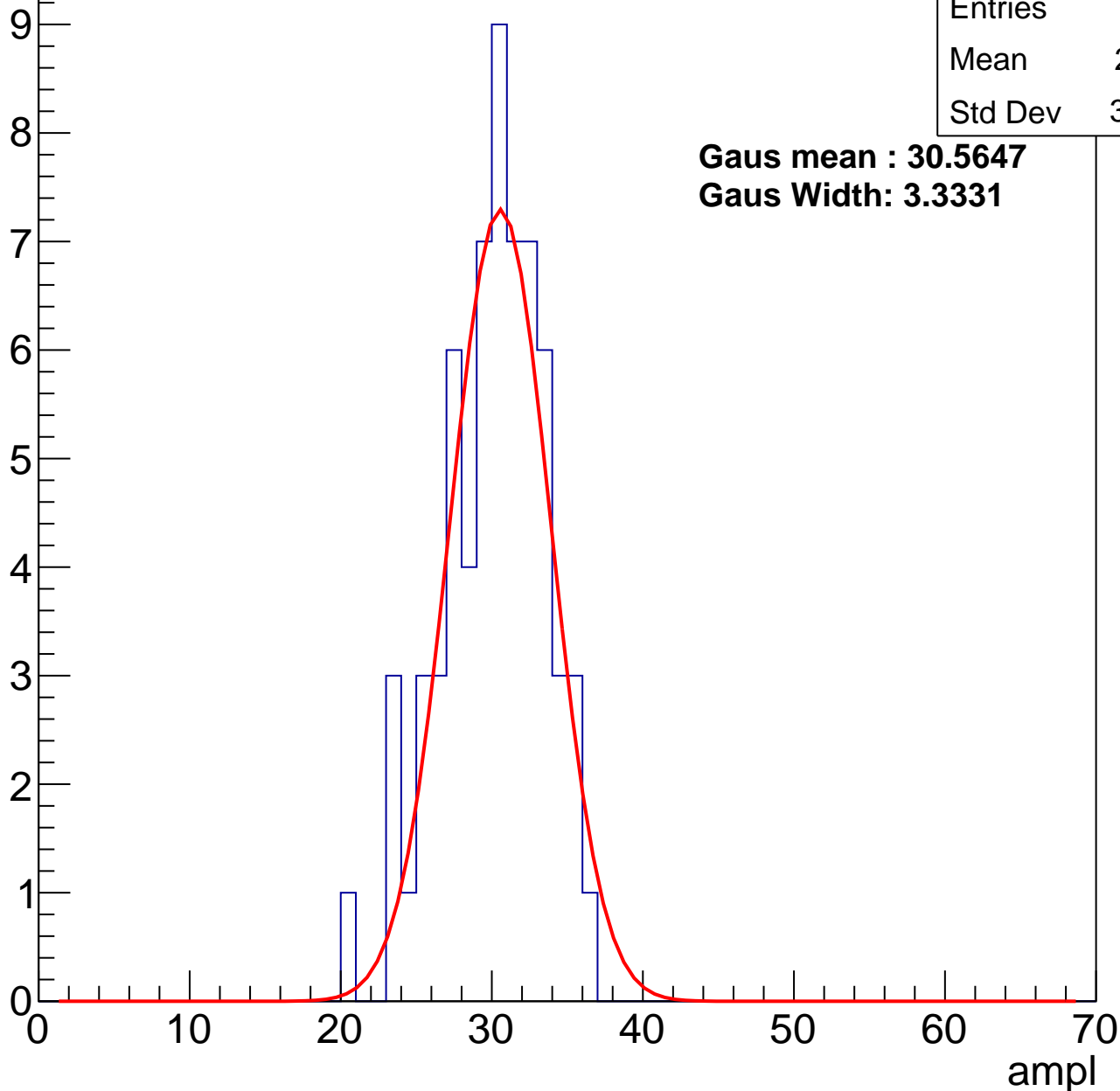
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.61
Std Dev	3.357

**Gaus mean : 30.5647**

**Gaus Width: 3.3331**



# B0L102S, U17-ch33, adc1

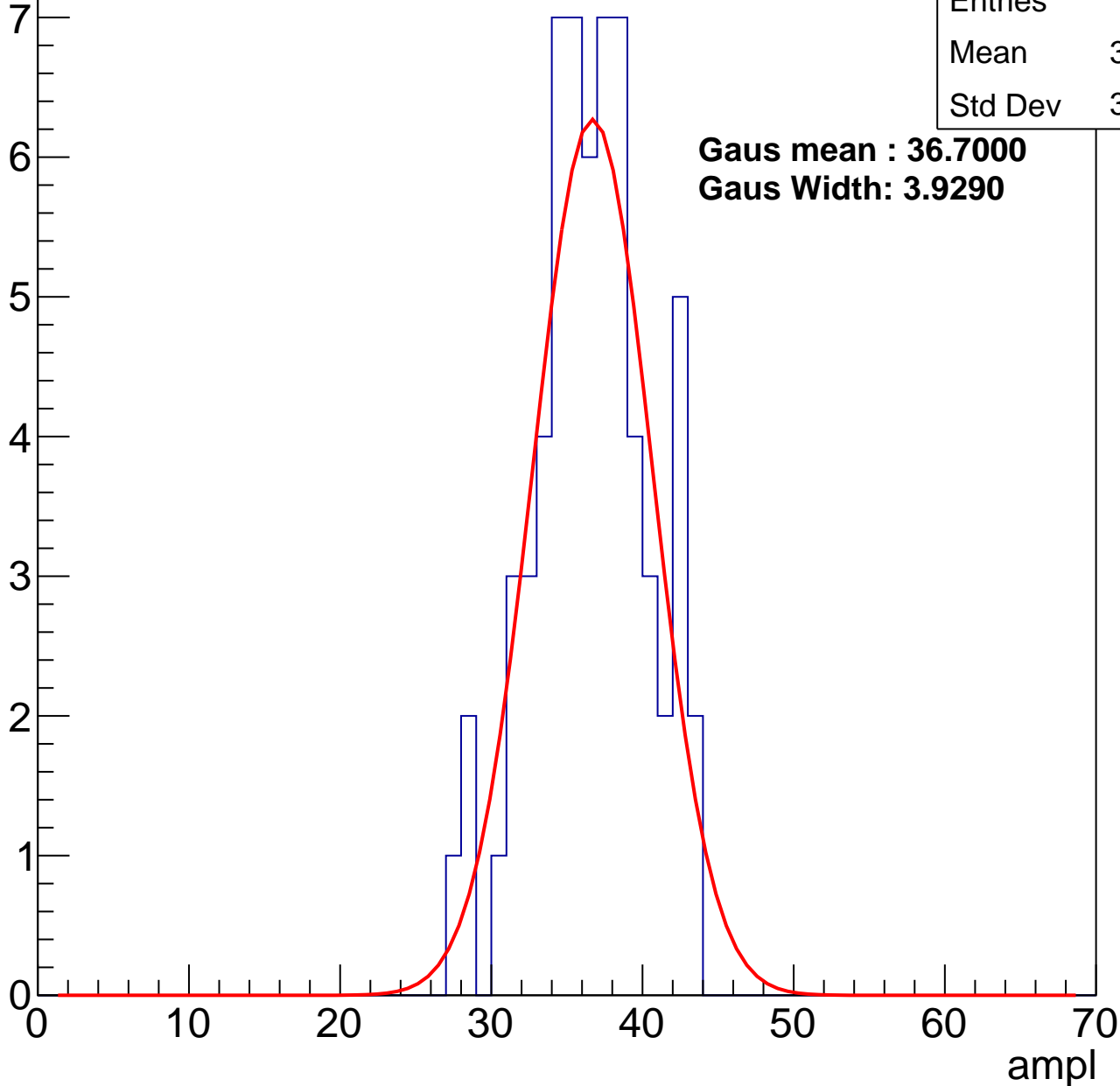
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	36.12
Std Dev	3.719

**Gaus mean : 36.7000**

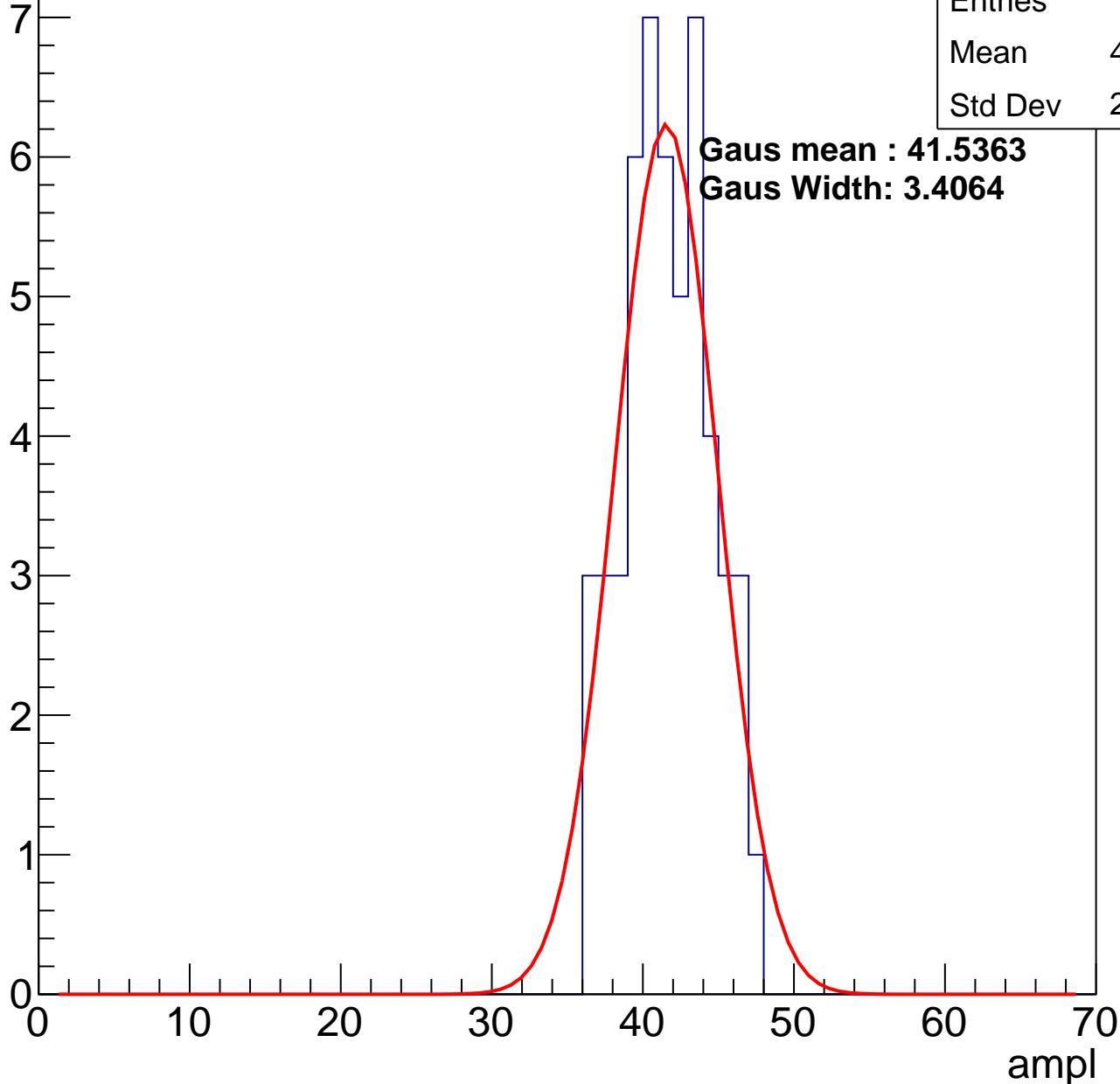
**Gaus Width: 3.9290**



# B0L102S, U17-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

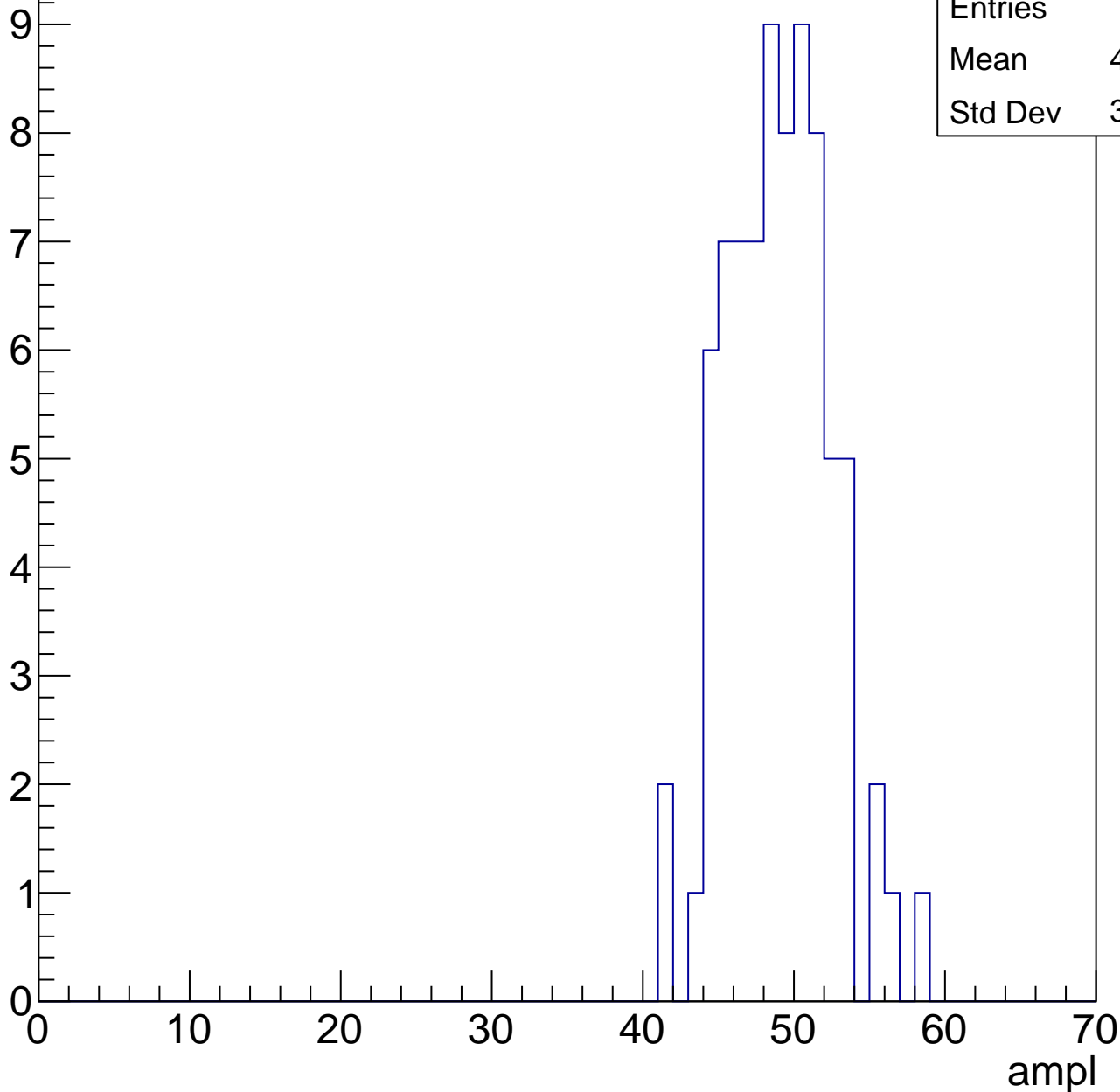


# B0L102S, U17-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	48.54
Std Dev	3.354

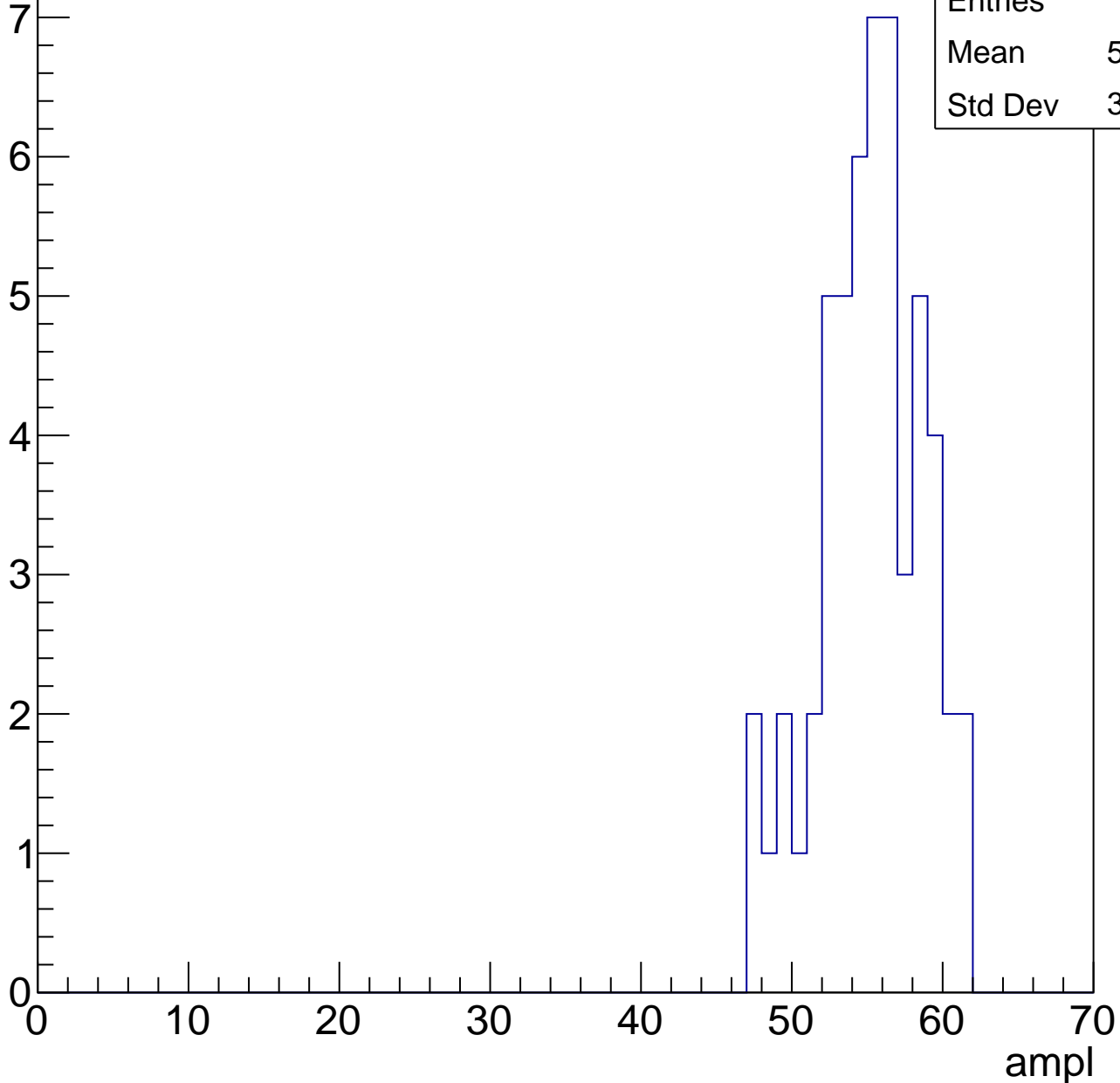


# B0L102S, U17-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

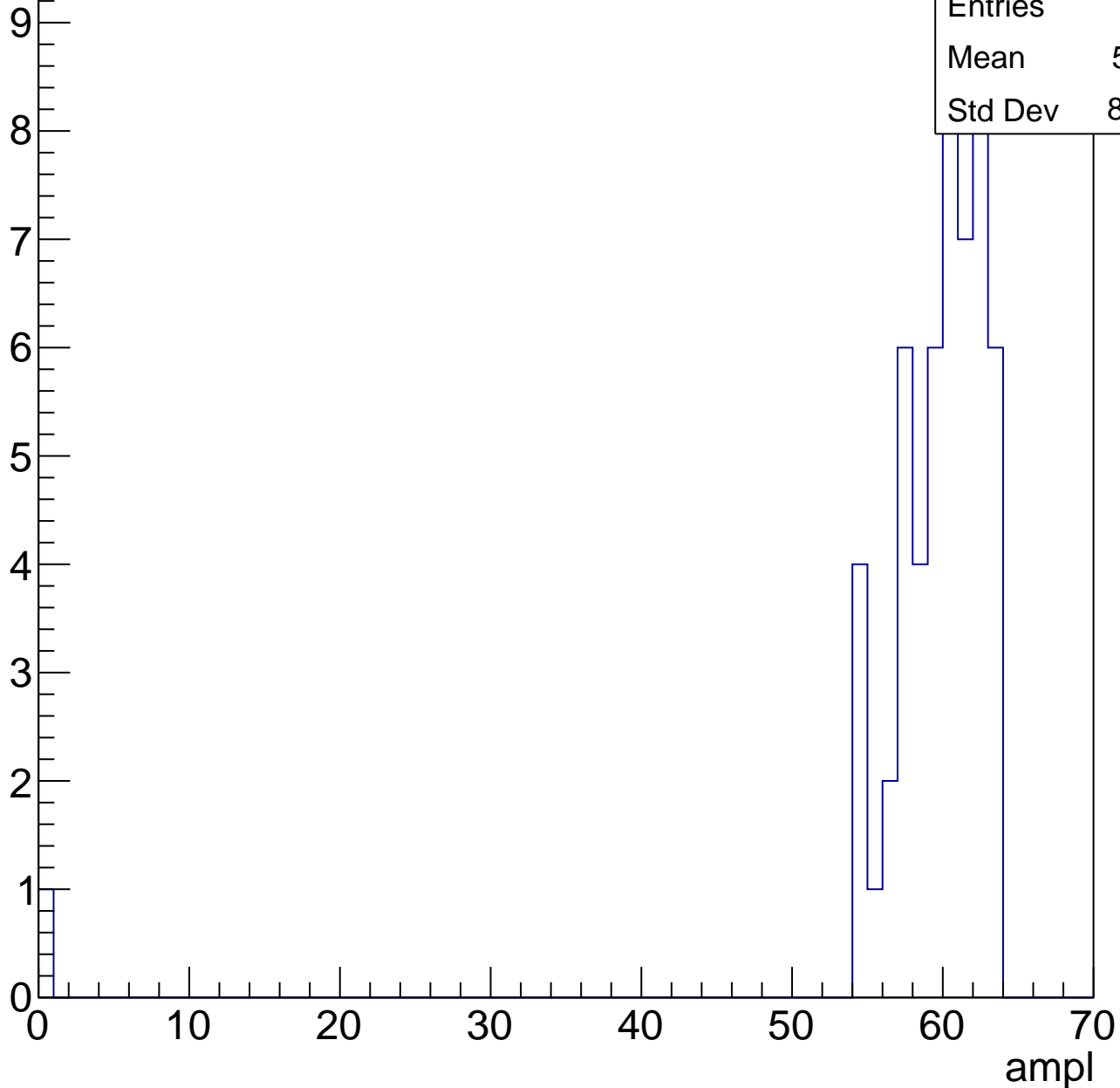
Entries	54
Mean	54.76
Std Dev	3.399



# B0L102S, U17-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

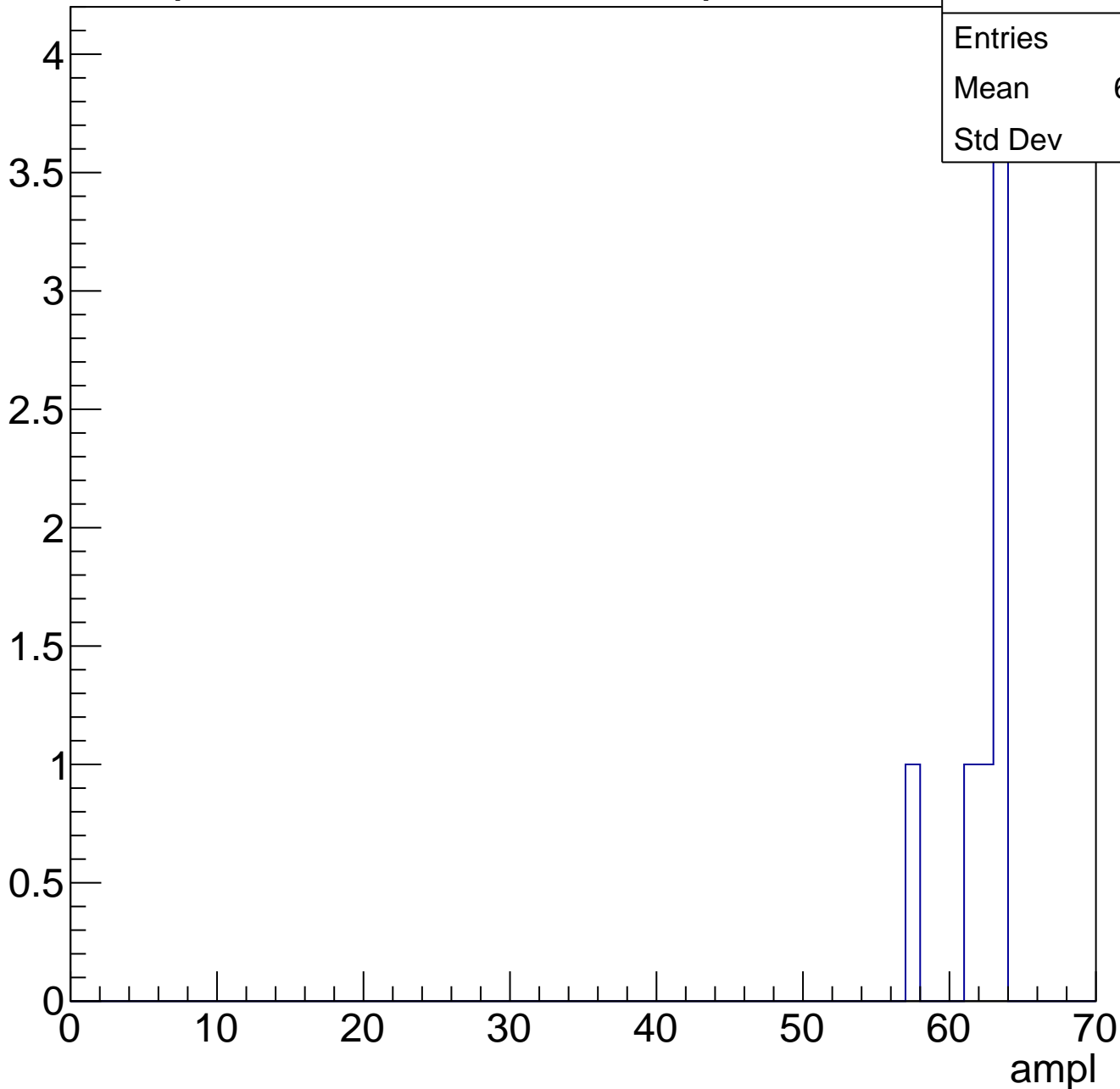
Entry



# B0L102S, U17-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	7
Mean	61.71
Std Dev	2.05



# B0L102S, U17-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	62
Mean	28.44
Std Dev	4.934

**Gaus mean : 28.4609**

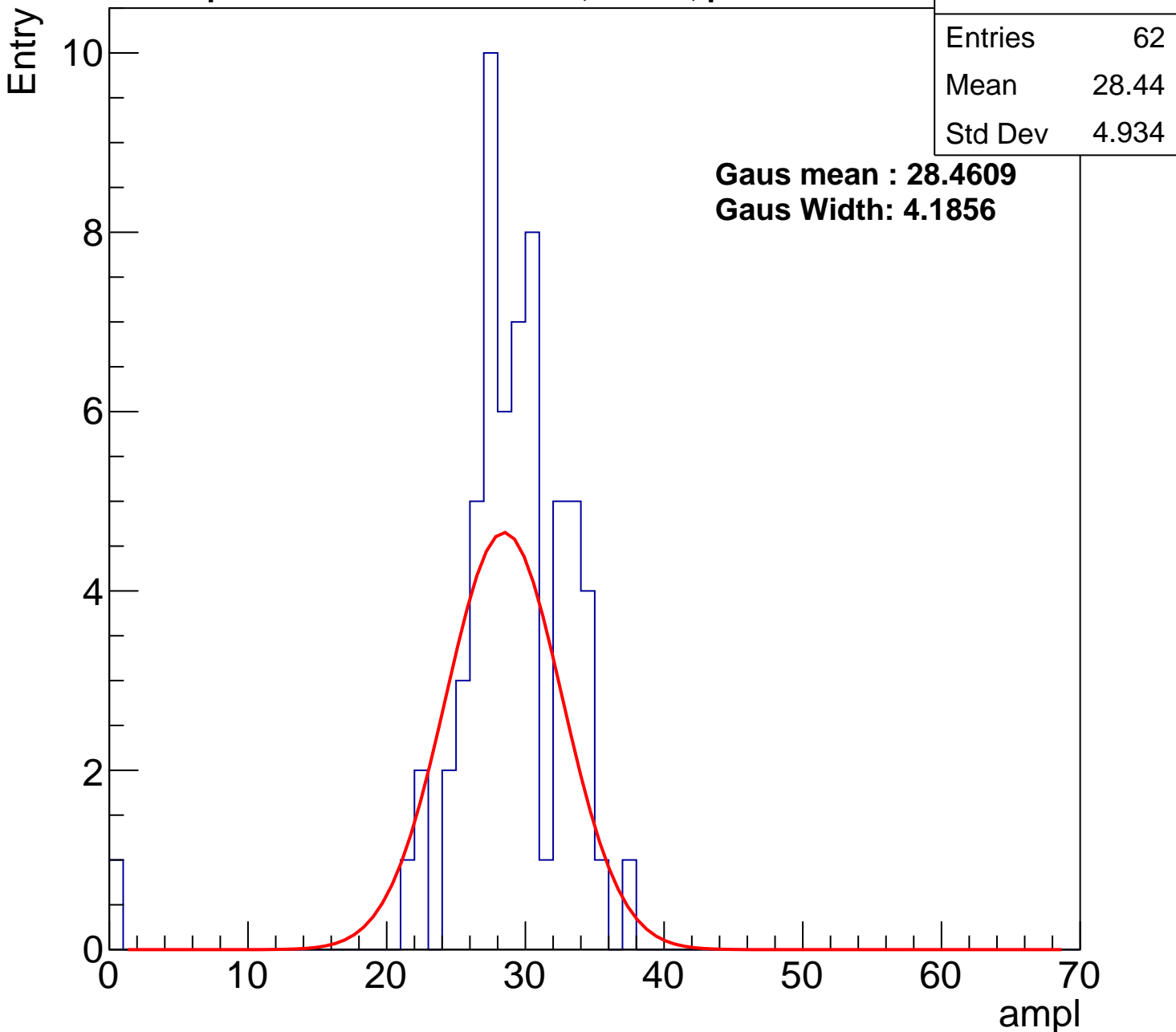
**Gaus Width: 4.1856**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch34, adc1

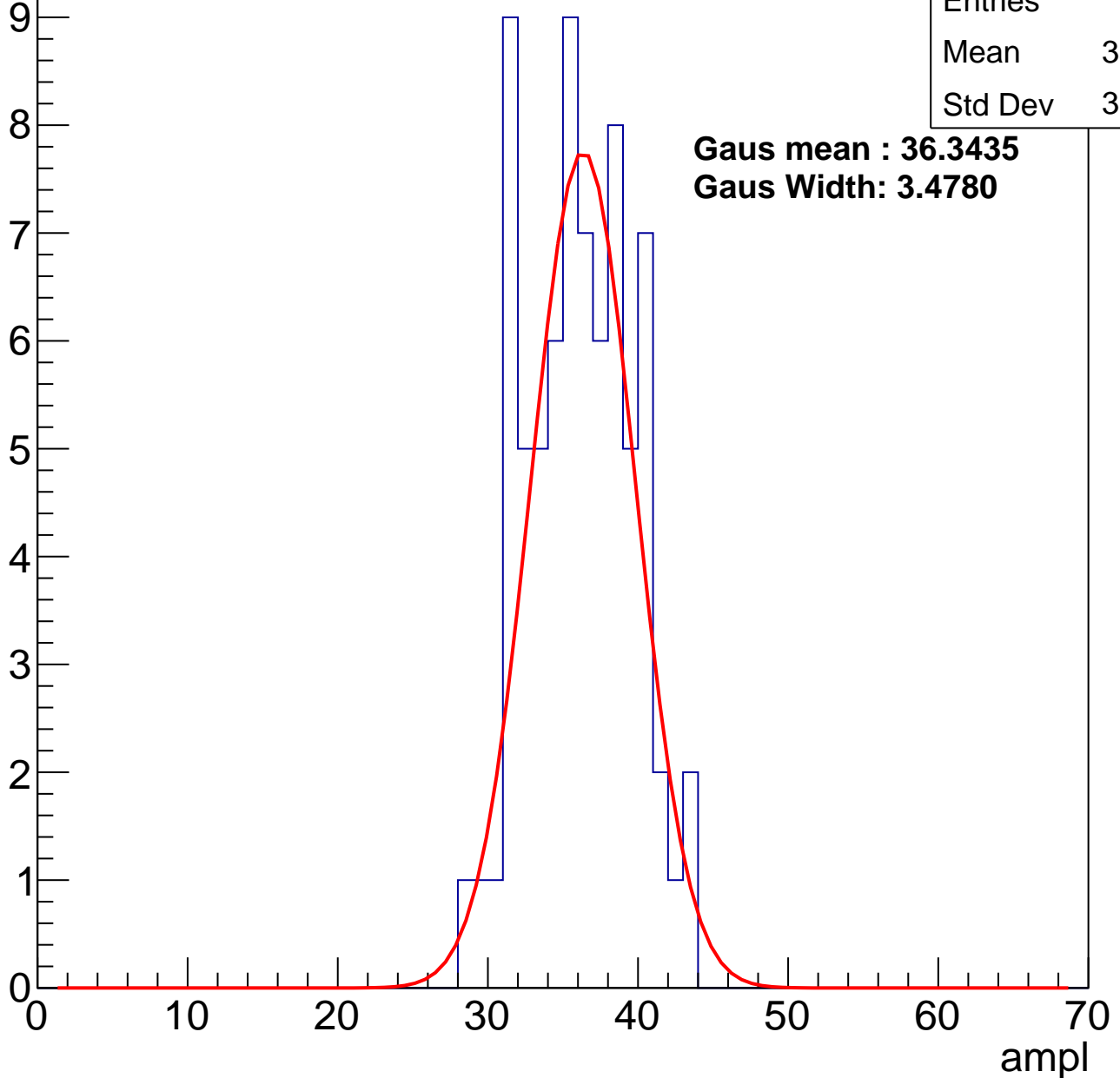
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.64
Std Dev	3.467

**Gaus mean : 36.3435**

**Gaus Width: 3.4780**



# B0L102S, U17-ch34, adc2

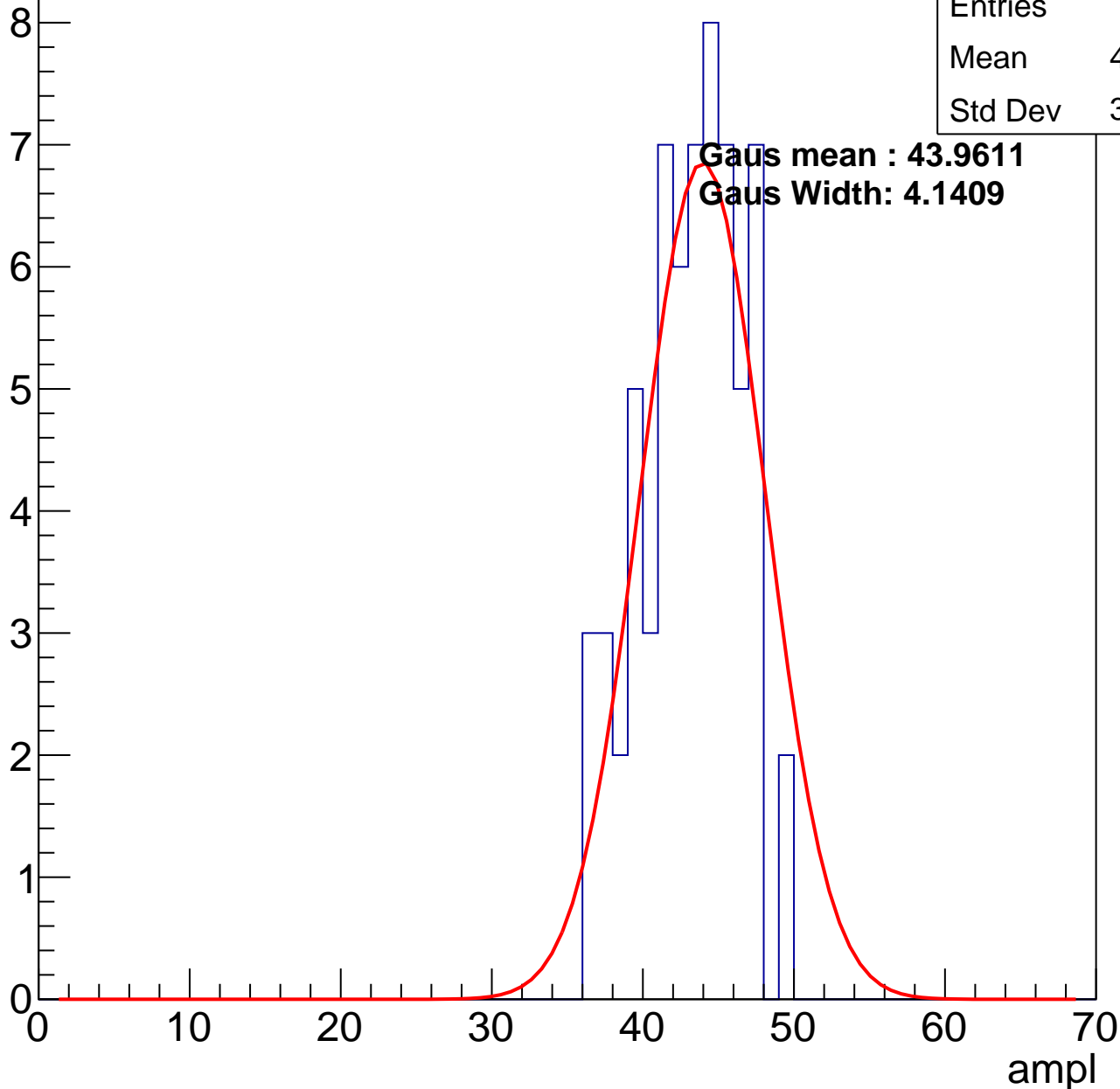
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.68
Std Dev	3.292

**Gaus mean : 43.9611**

**Gaus Width: 4.1409**

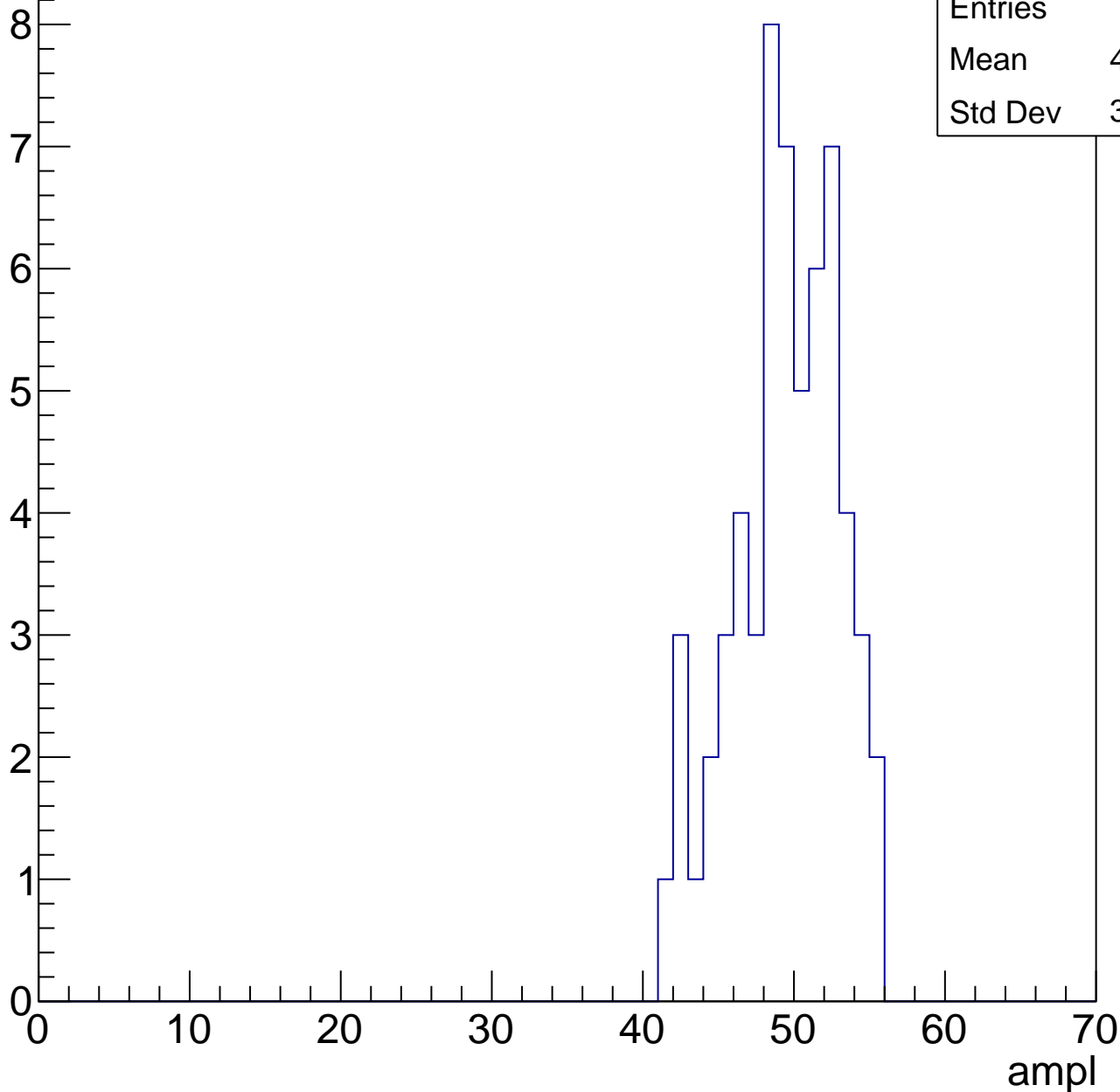


# B0L102S, U17-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

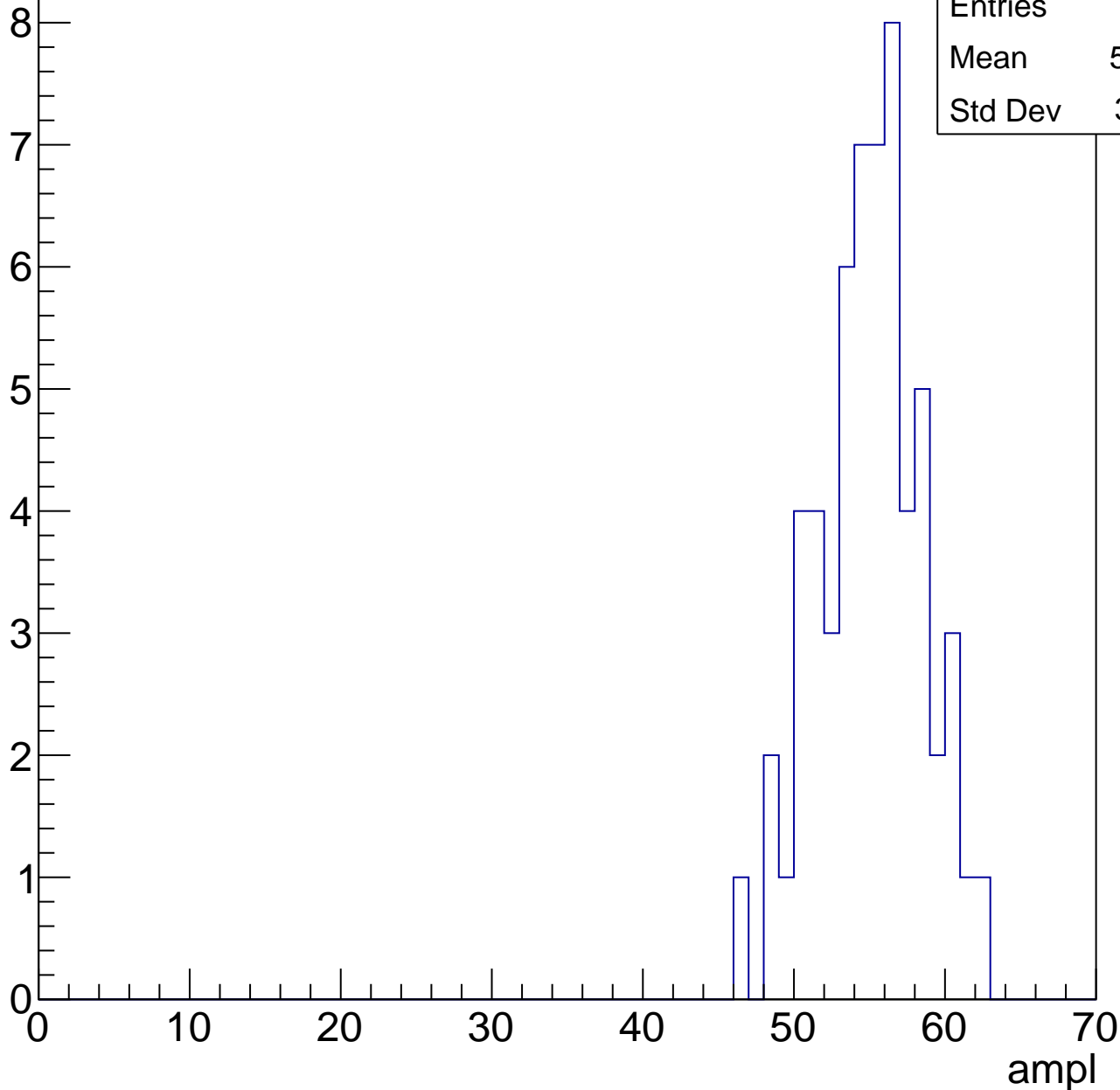
Entries	59
Mean	48.97
Std Dev	3.454



# B0L102S, U17-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

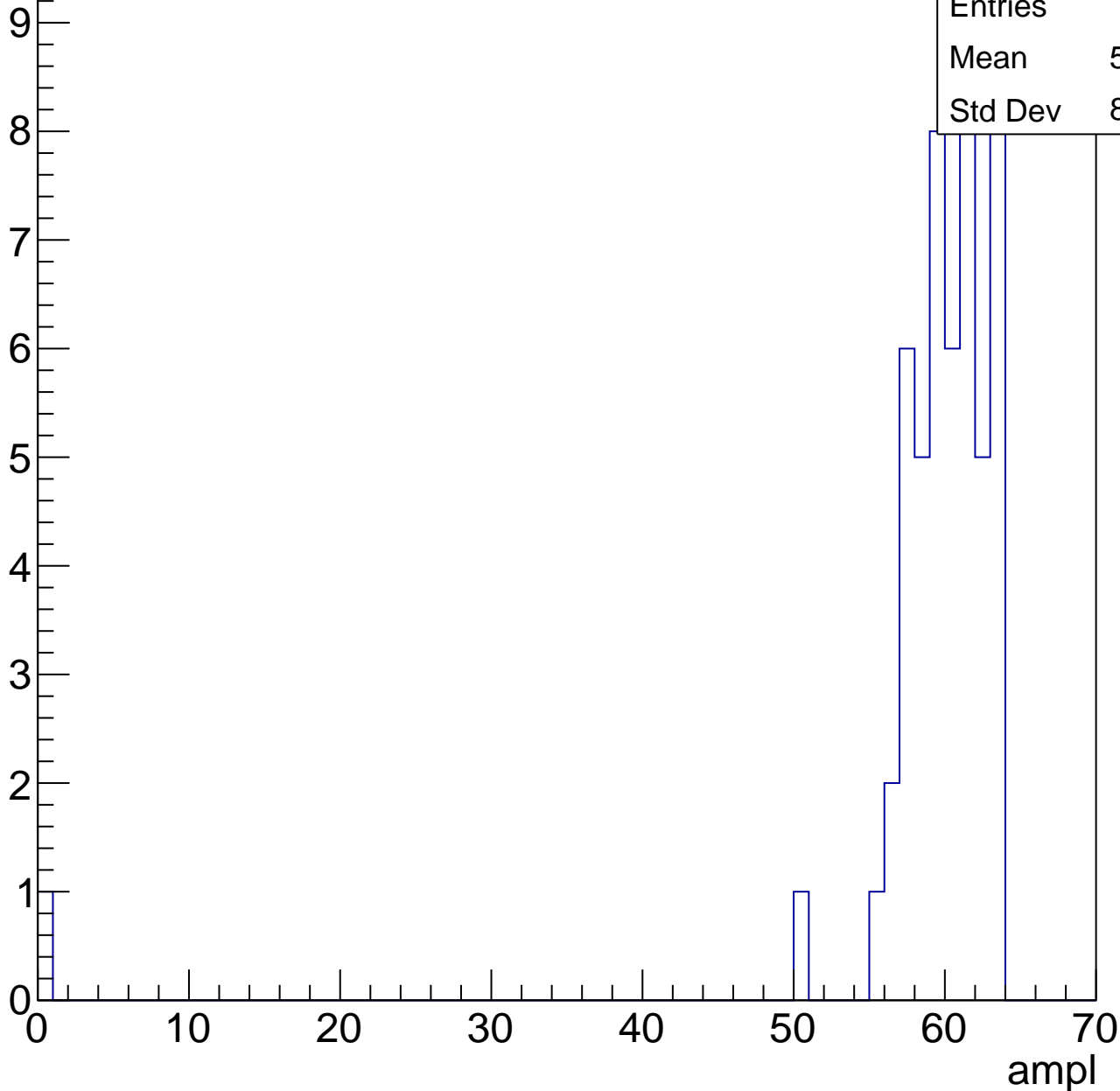


# B0L102S, U17-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

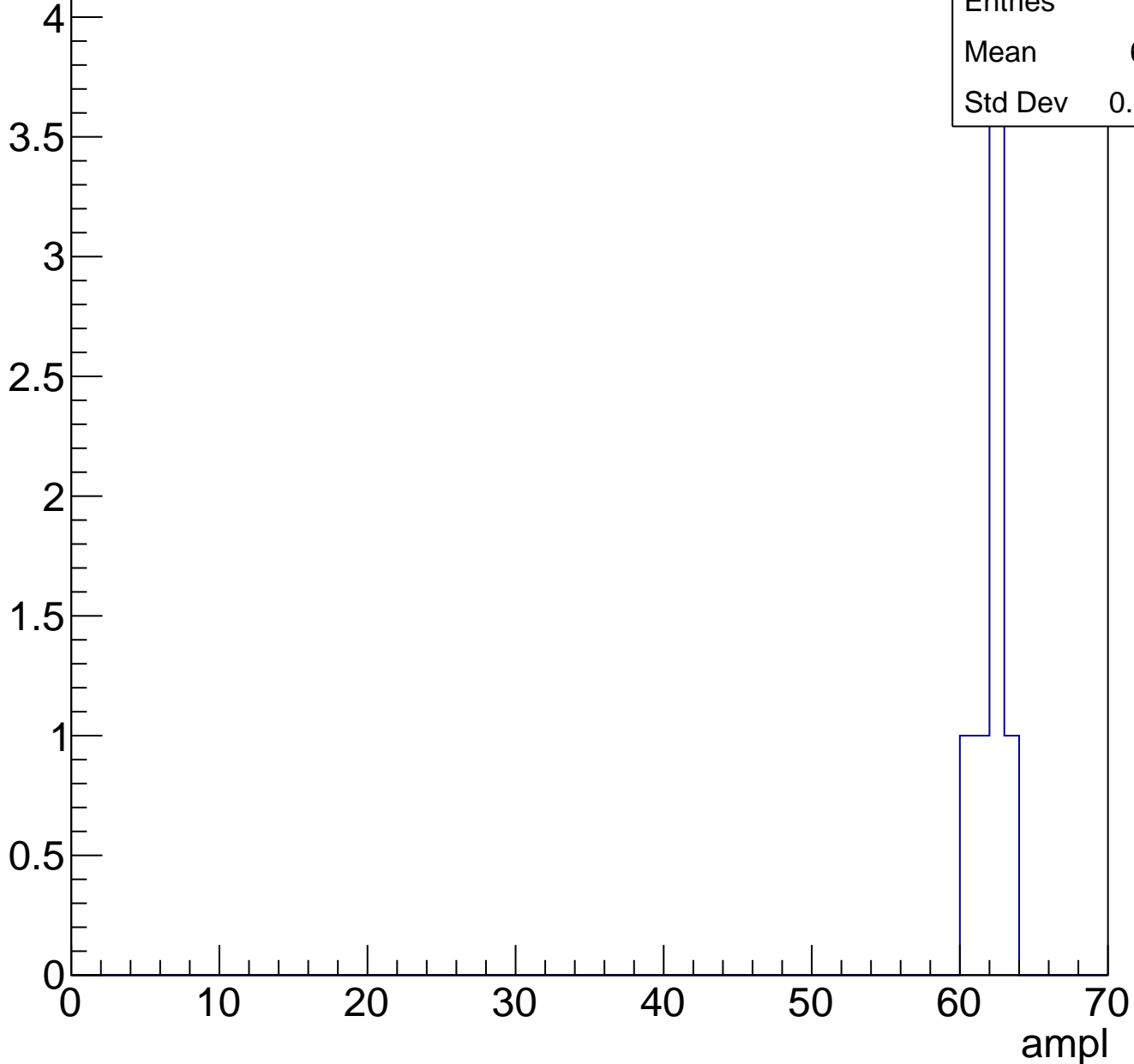
Entries	52
Mean	58.58
Std Dev	8.596



# B0L102S, U17-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch35, adc0

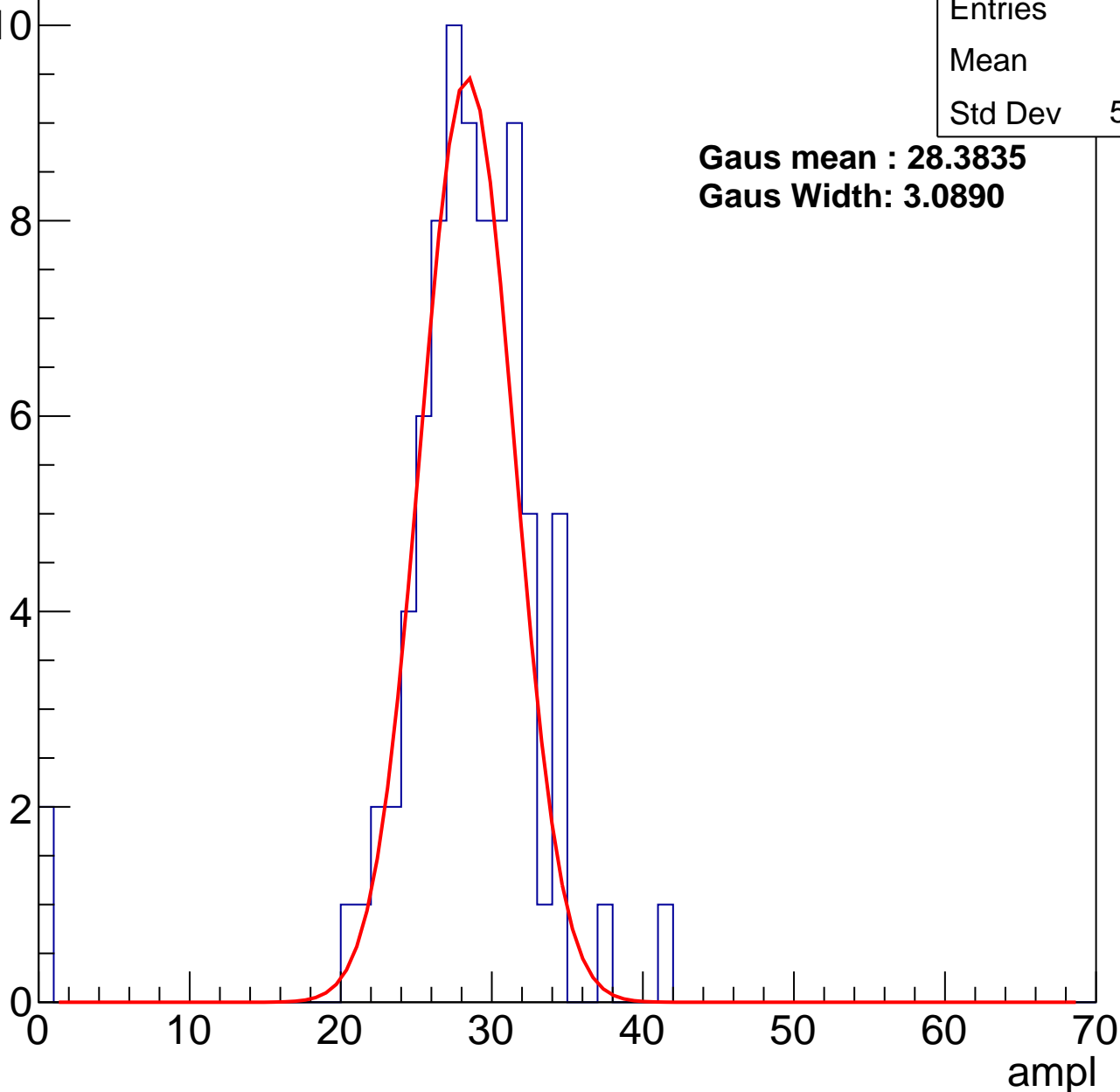
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	83
Mean	27.7
Std Dev	5.609

**Gaus mean : 28.3835**

**Gaus Width: 3.0890**



# B0L102S, U17-ch35, adc1

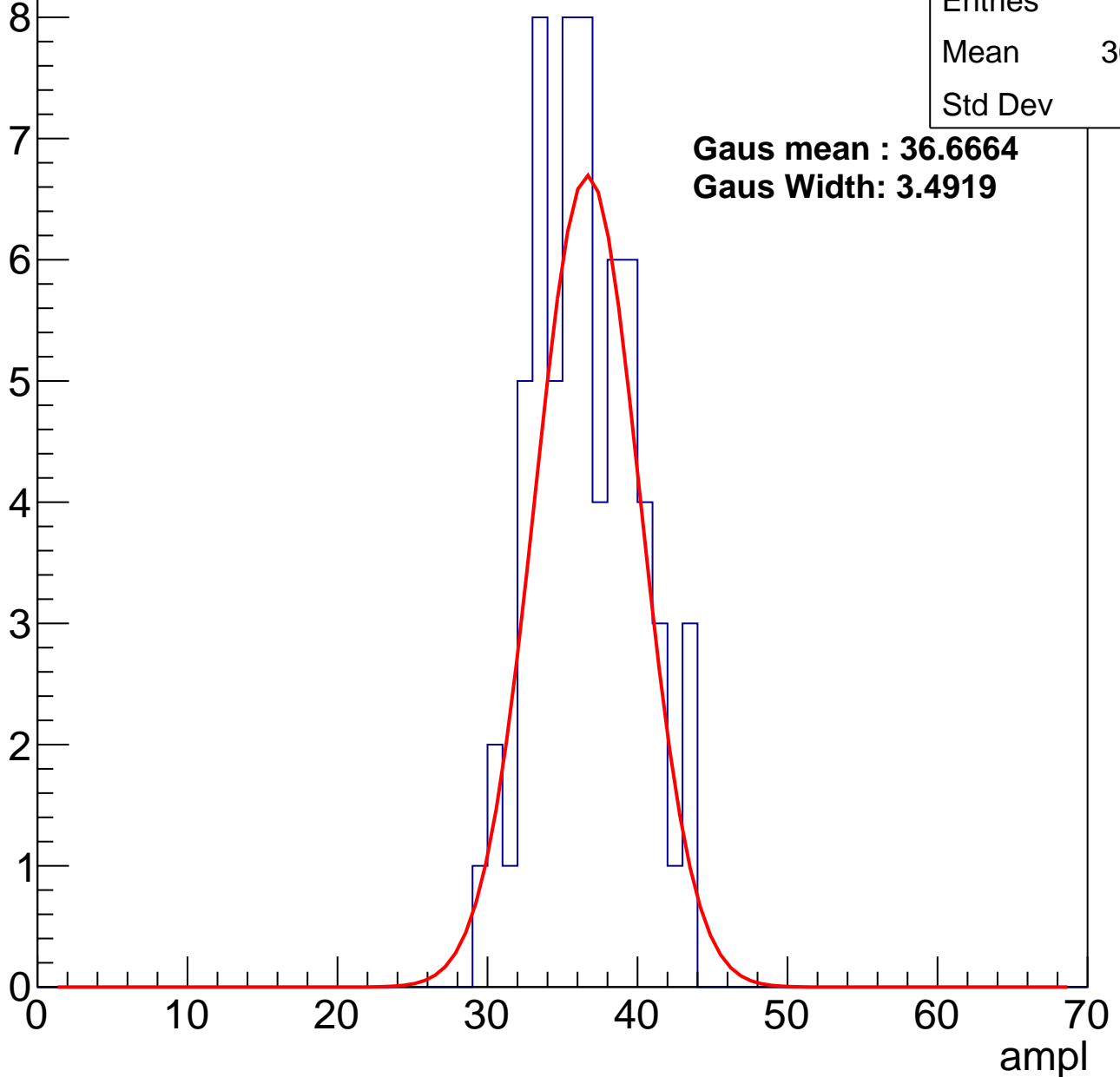
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.09
Std Dev	3.35

**Gaus mean : 36.6664**

**Gaus Width: 3.4919**



# B0L102S, U17-ch35, adc2

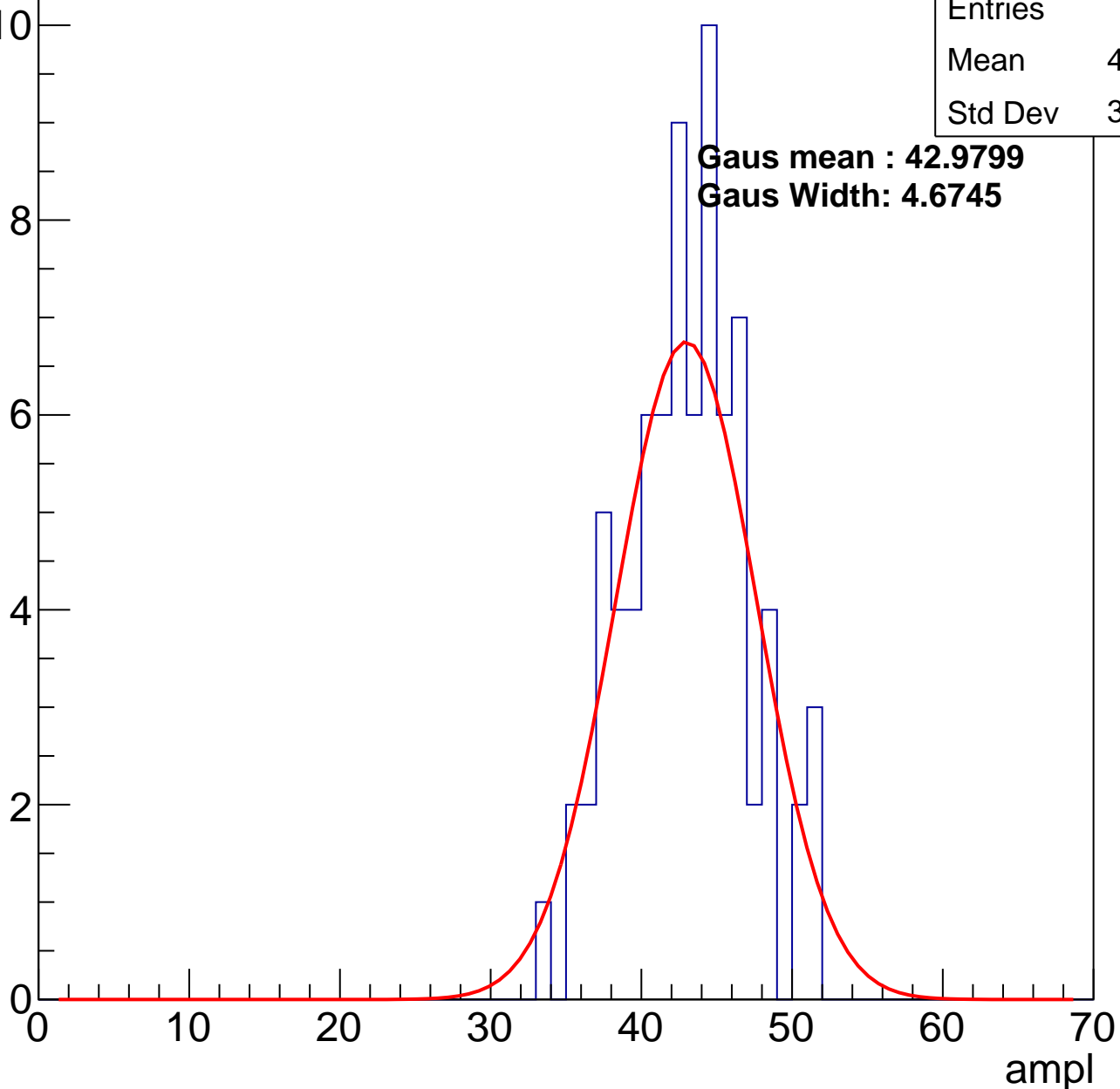
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	42.54
Std Dev	3.993

**Gaus mean : 42.9799**

**Gaus Width: 4.6745**



# B0L102S, U17-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	59
Mean	49.73
Std Dev	3.008

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

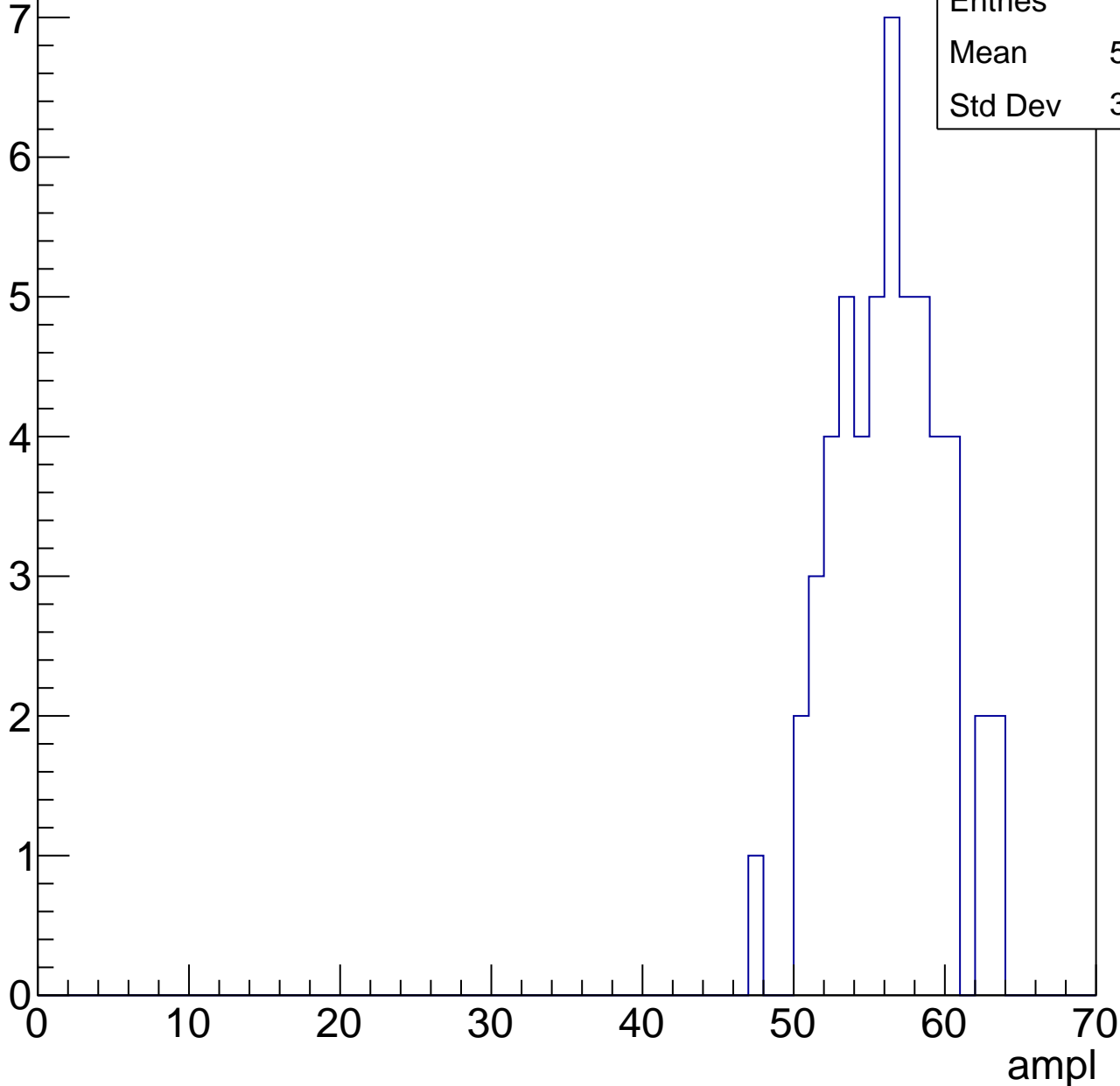
10

# B0L102S, U17-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	55.79
Std Dev	3.504

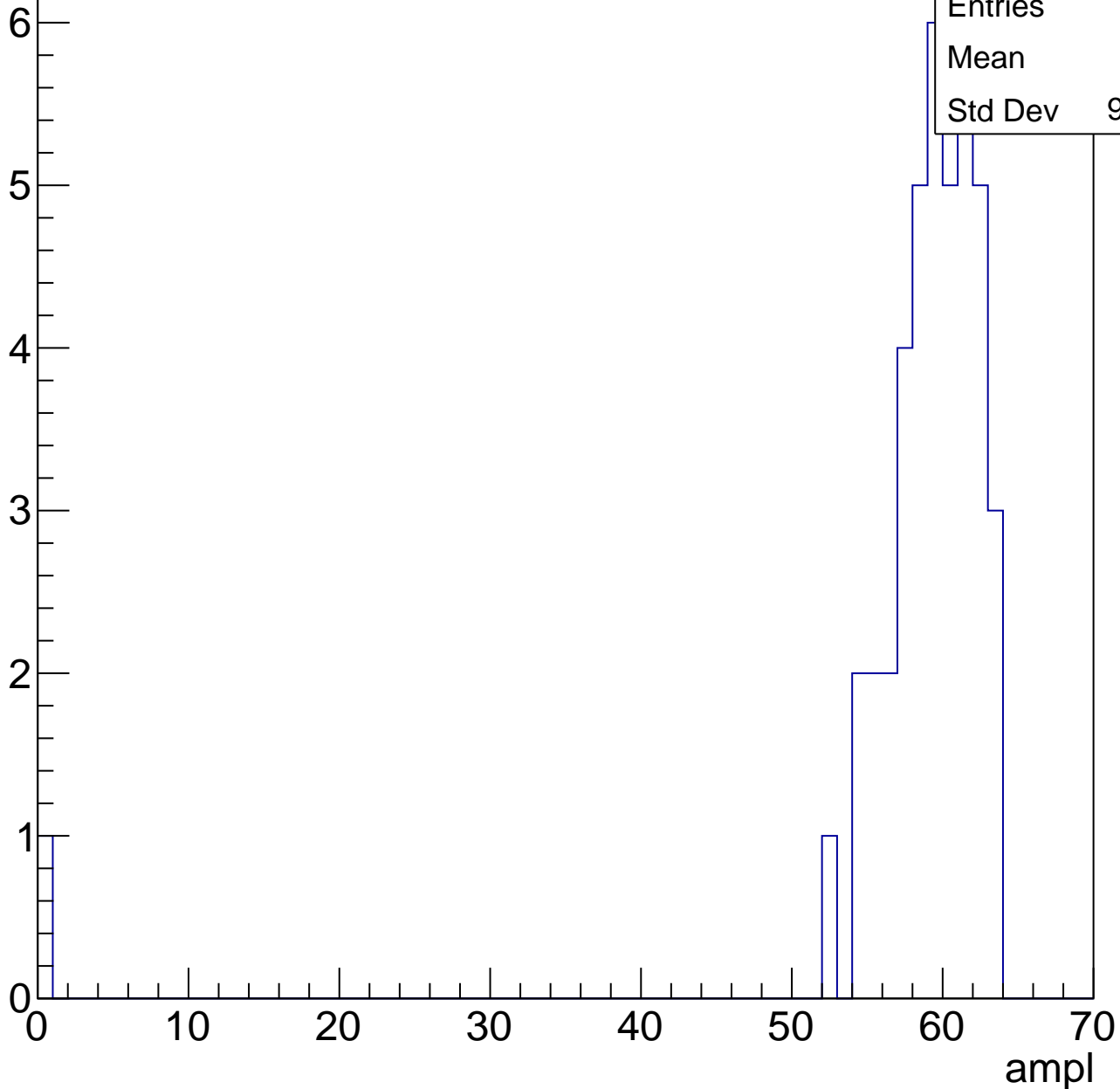


# B0L102S, U17-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	57.6
Std Dev	9.373

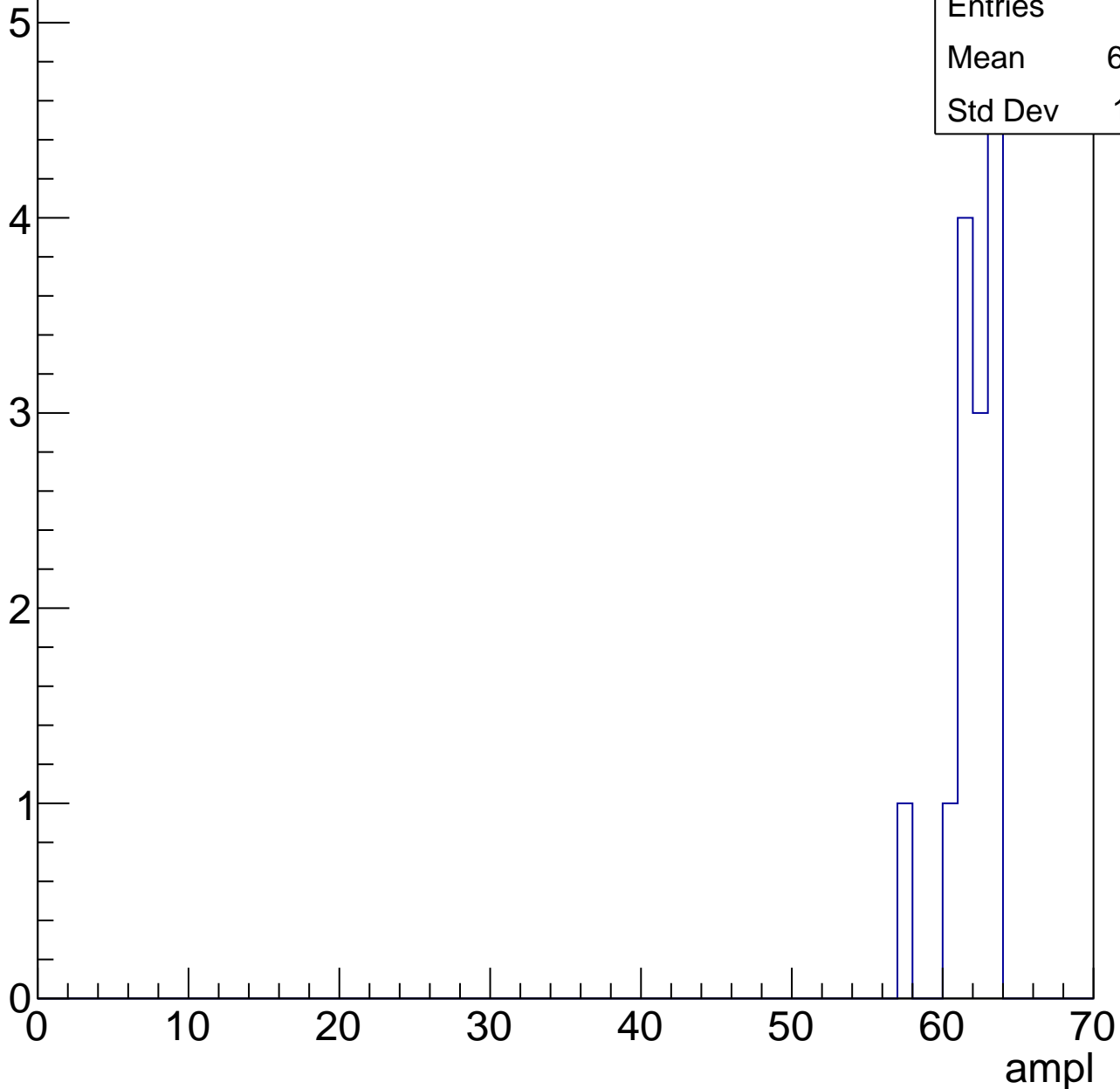


# B0L102S, U17-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.57
Std Dev	1.591





# B0L102S, U17-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch36, adc0

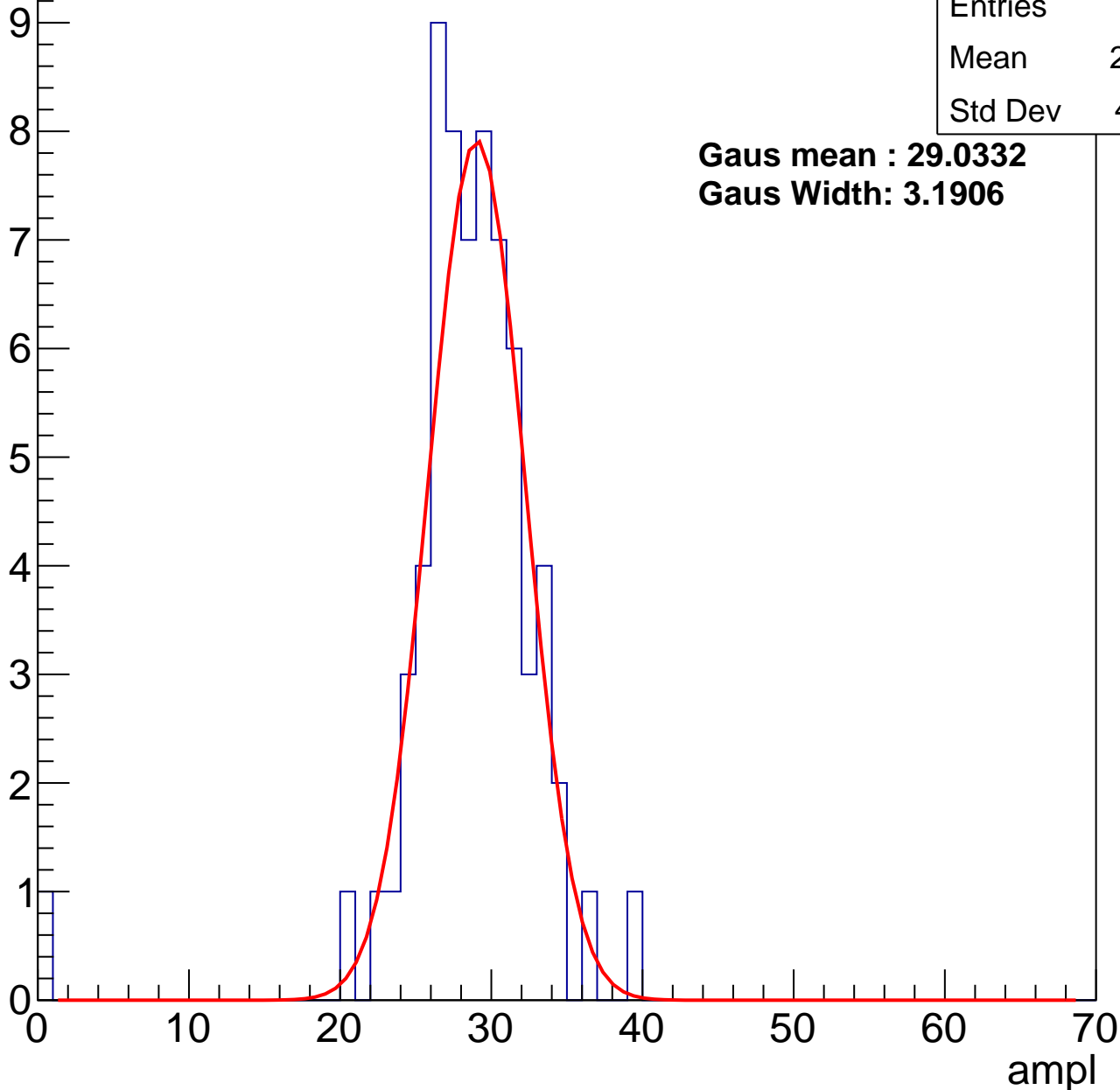
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	28.09
Std Dev	4.781

**Gaus mean : 29.0332**

**Gaus Width: 3.1906**



# B0L102S, U17-ch36, adc1

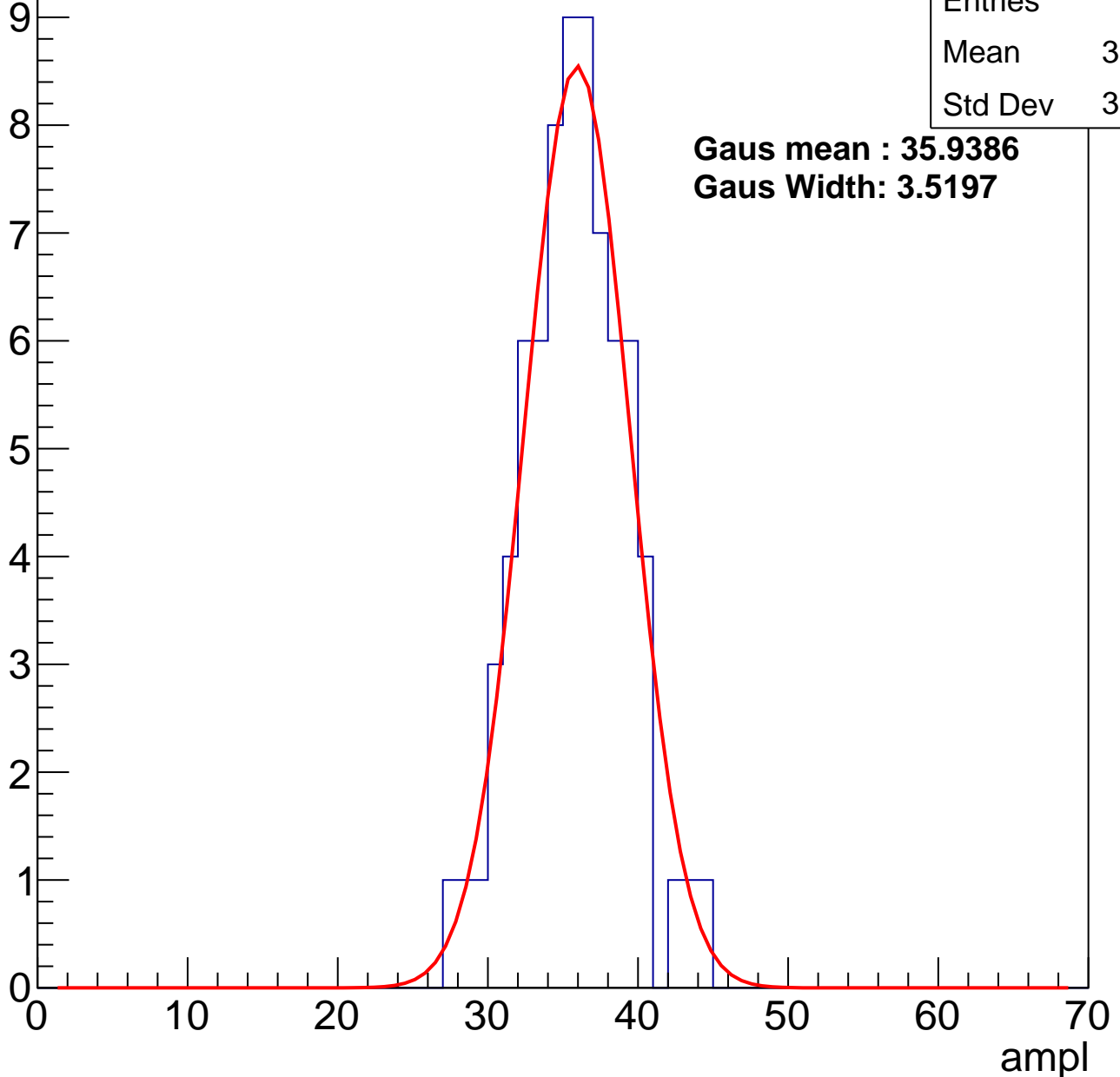
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	35.26
Std Dev	3.389

**Gaus mean : 35.9386**

**Gaus Width: 3.5197**



# B0L102S, U17-ch36, adc2

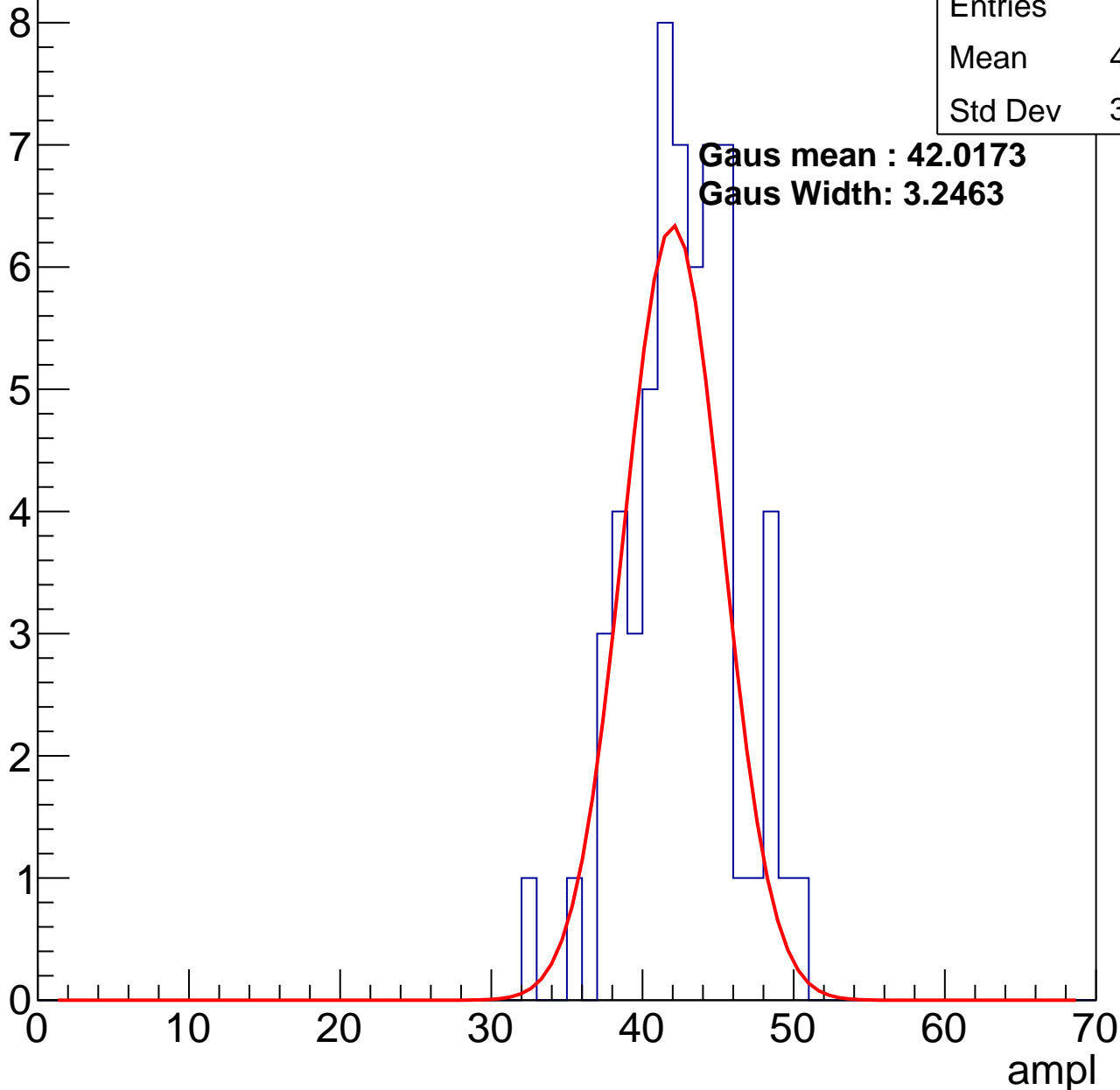
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.23
Std Dev	3.499

**Gaus mean : 42.0173**

**Gaus Width: 3.2463**

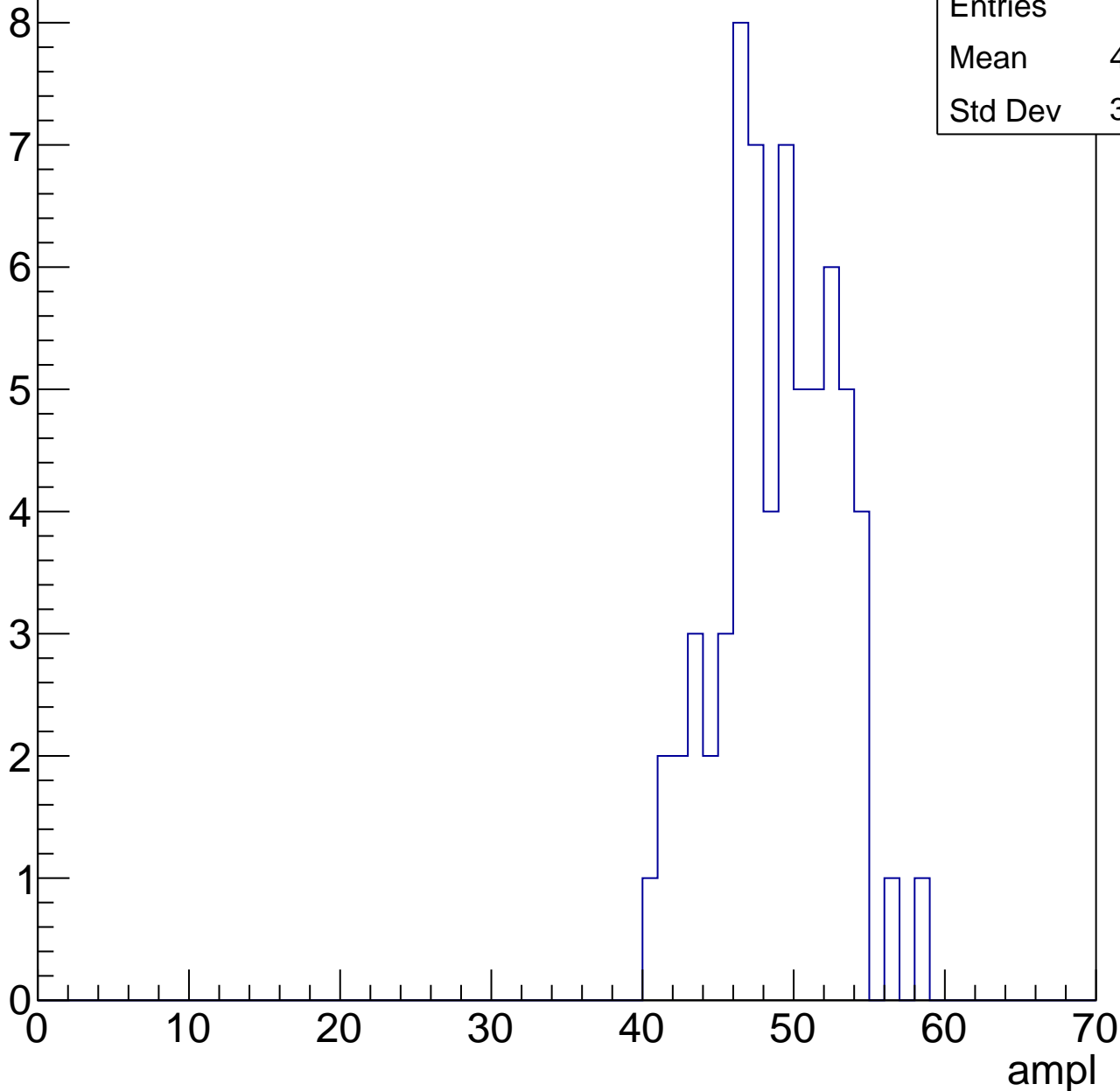


# B0L102S, U17-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.52
Std Dev	3.874

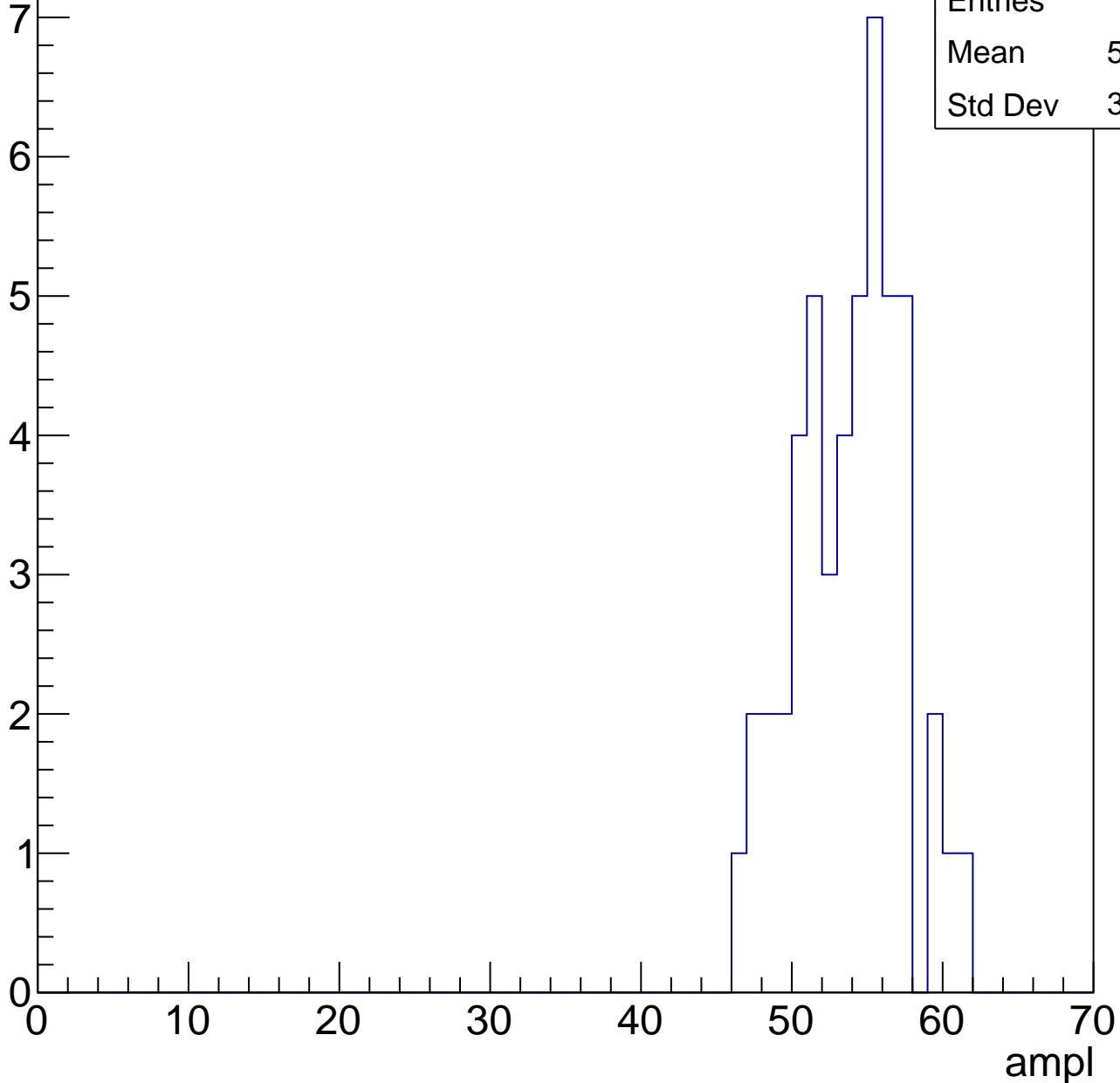


# B0L102S, U17-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

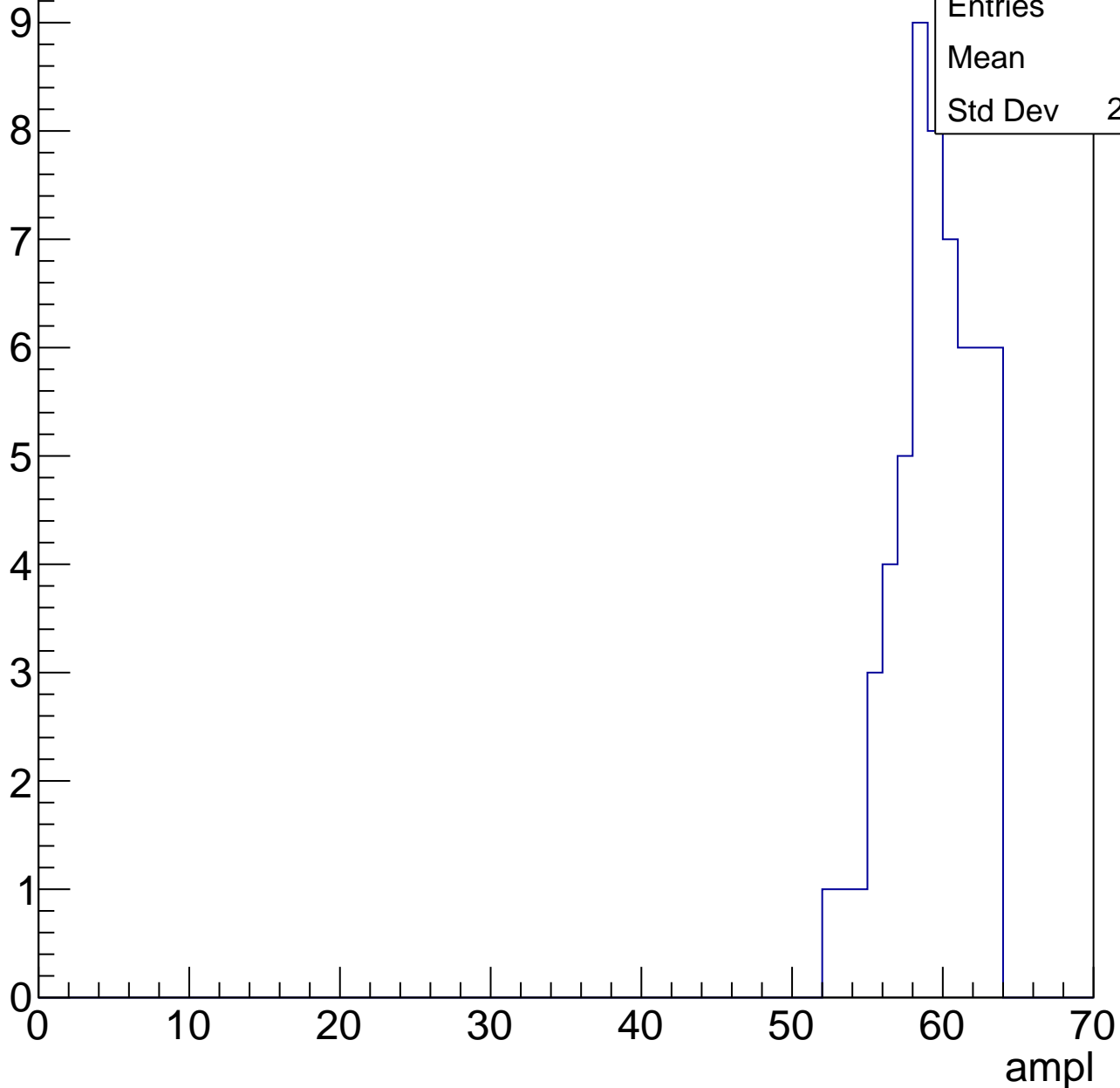
Entries	49
Mean	53.39
Std Dev	3.492



# B0L102S, U17-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

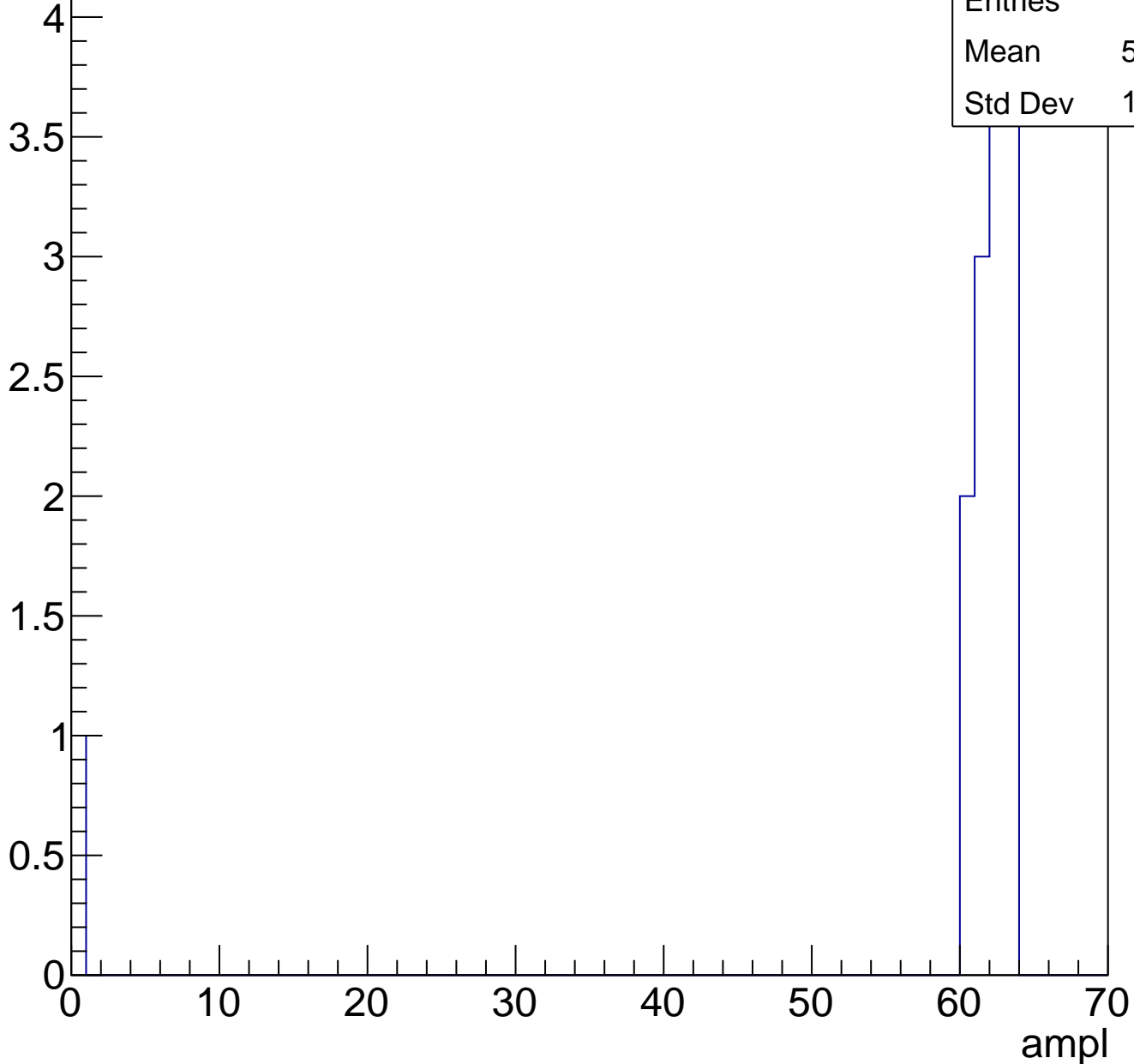


Entries	57
Mean	59
Std Dev	2.662

# B0L102S, U17-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

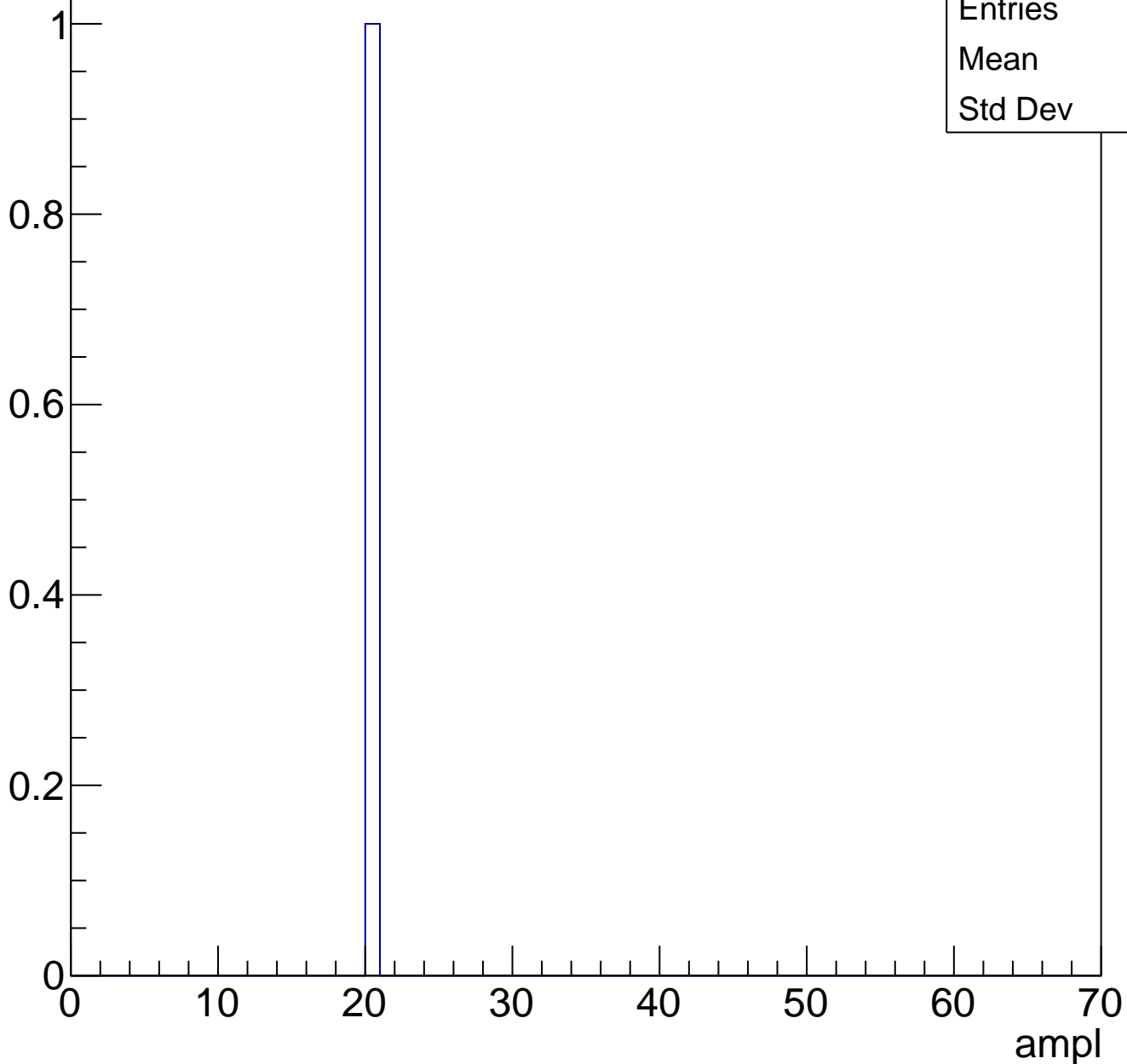




# B0L102S, U17-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L102S, U17-ch37, adc0

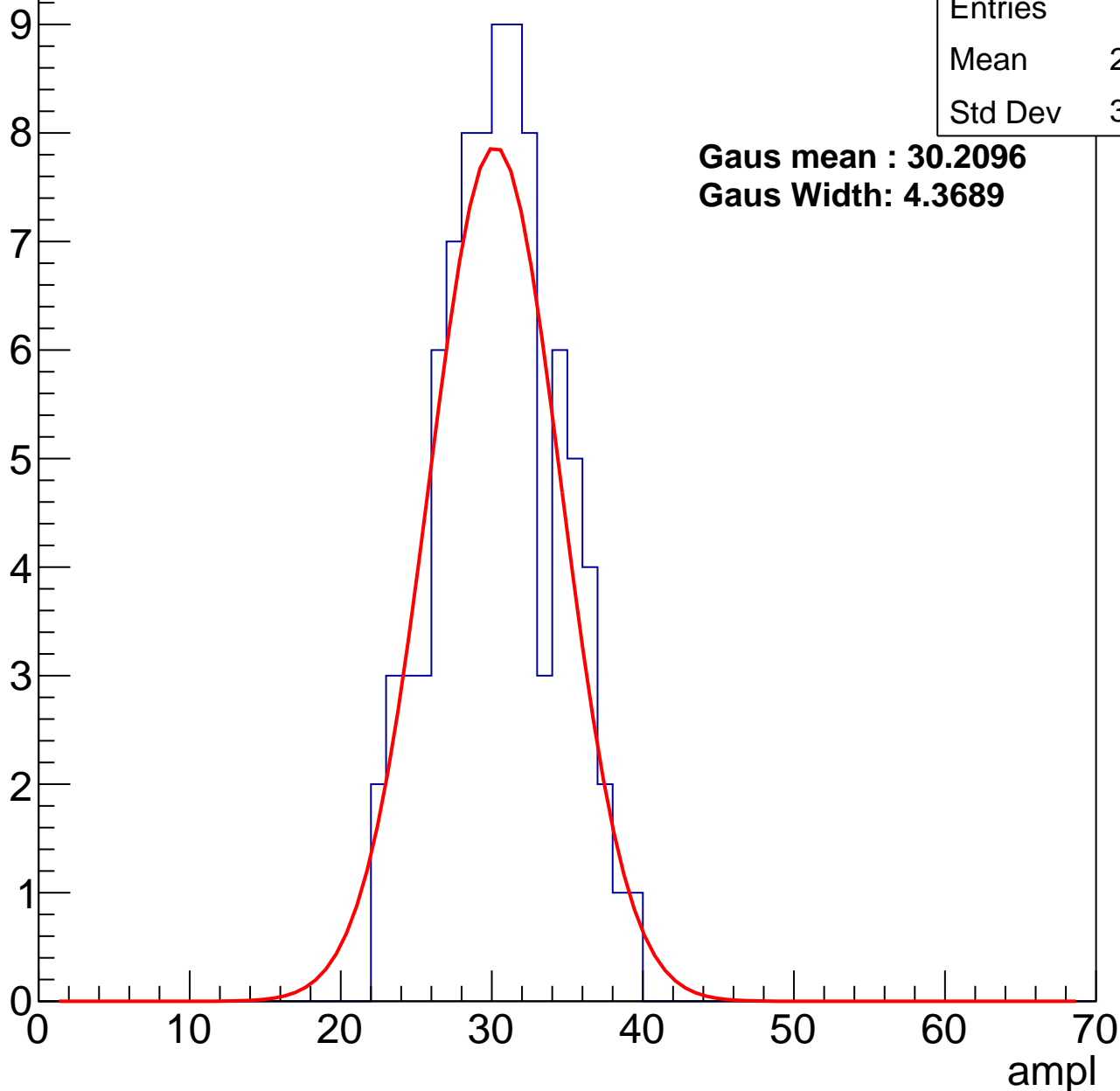
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	88
Mean	29.99
Std Dev	3.892

**Gaus mean : 30.2096**

**Gaus Width: 4.3689**



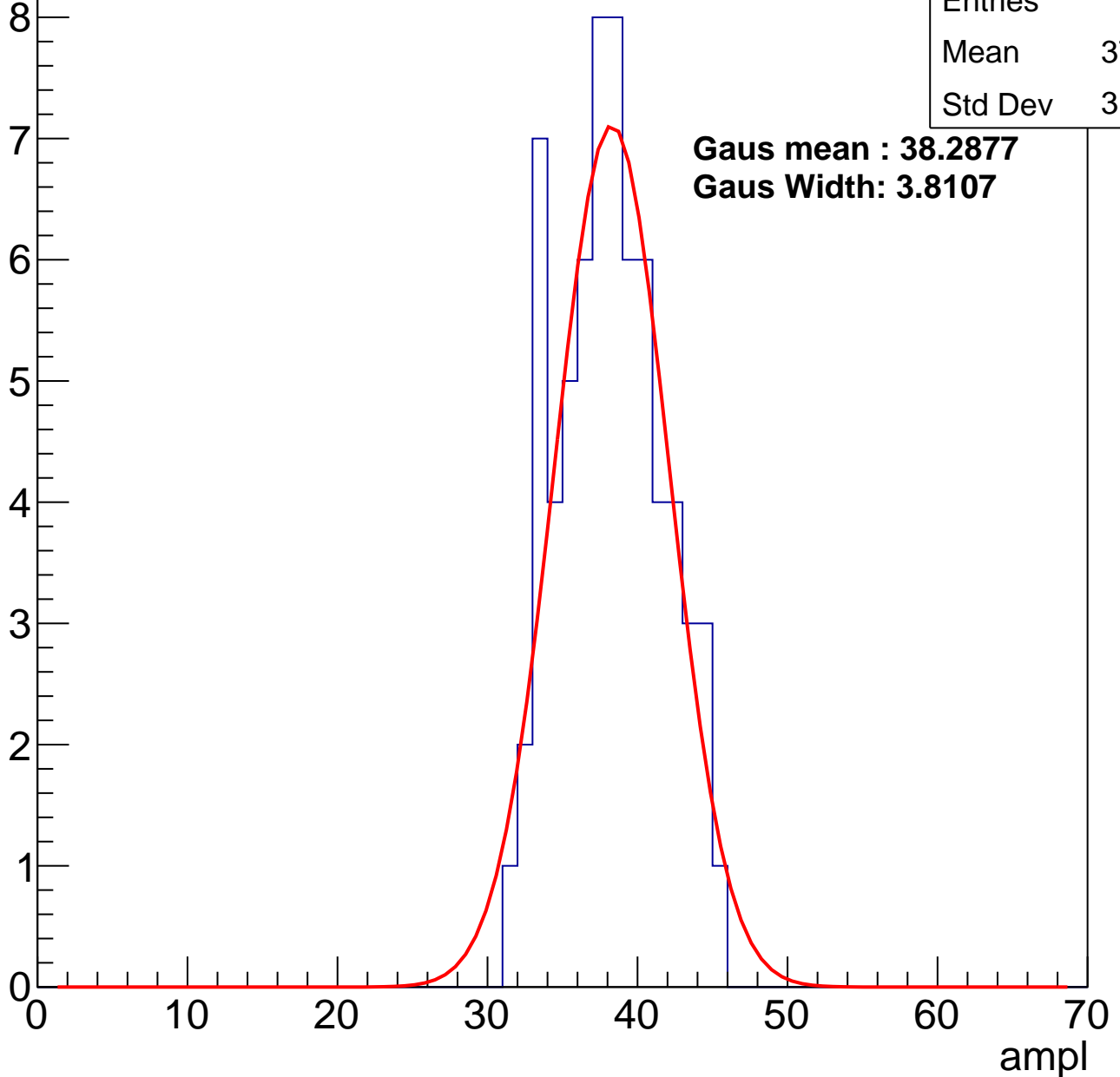
# B0L102S, U17-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	37.72
Std Dev	3.416

**Gaus mean : 38.2877**  
**Gaus Width: 3.8107**



# B0L102S, U17-ch37, adc2

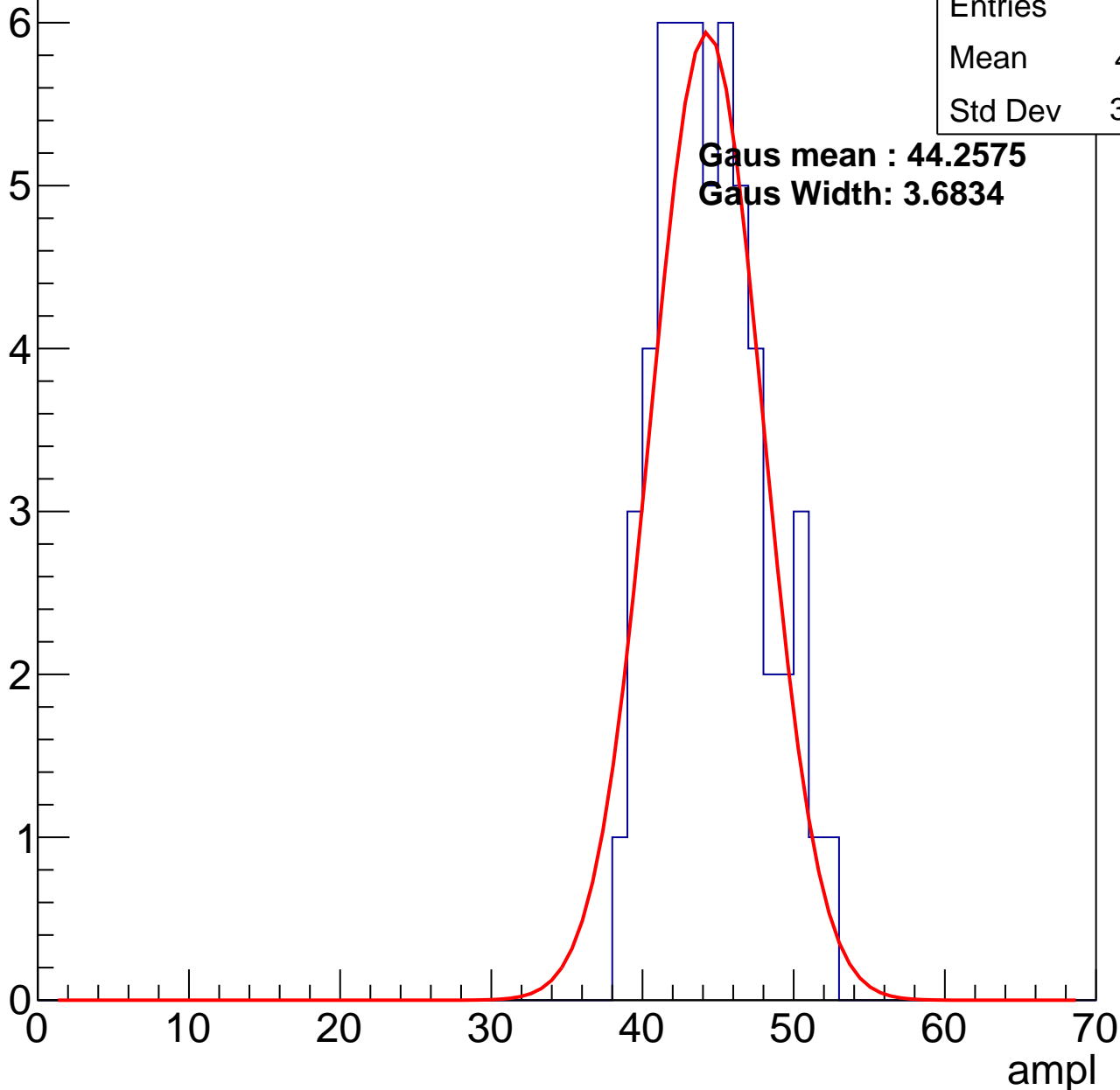
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	44.11
Std Dev	3.367

**Gaus mean : 44.2575**

**Gaus Width: 3.6834**

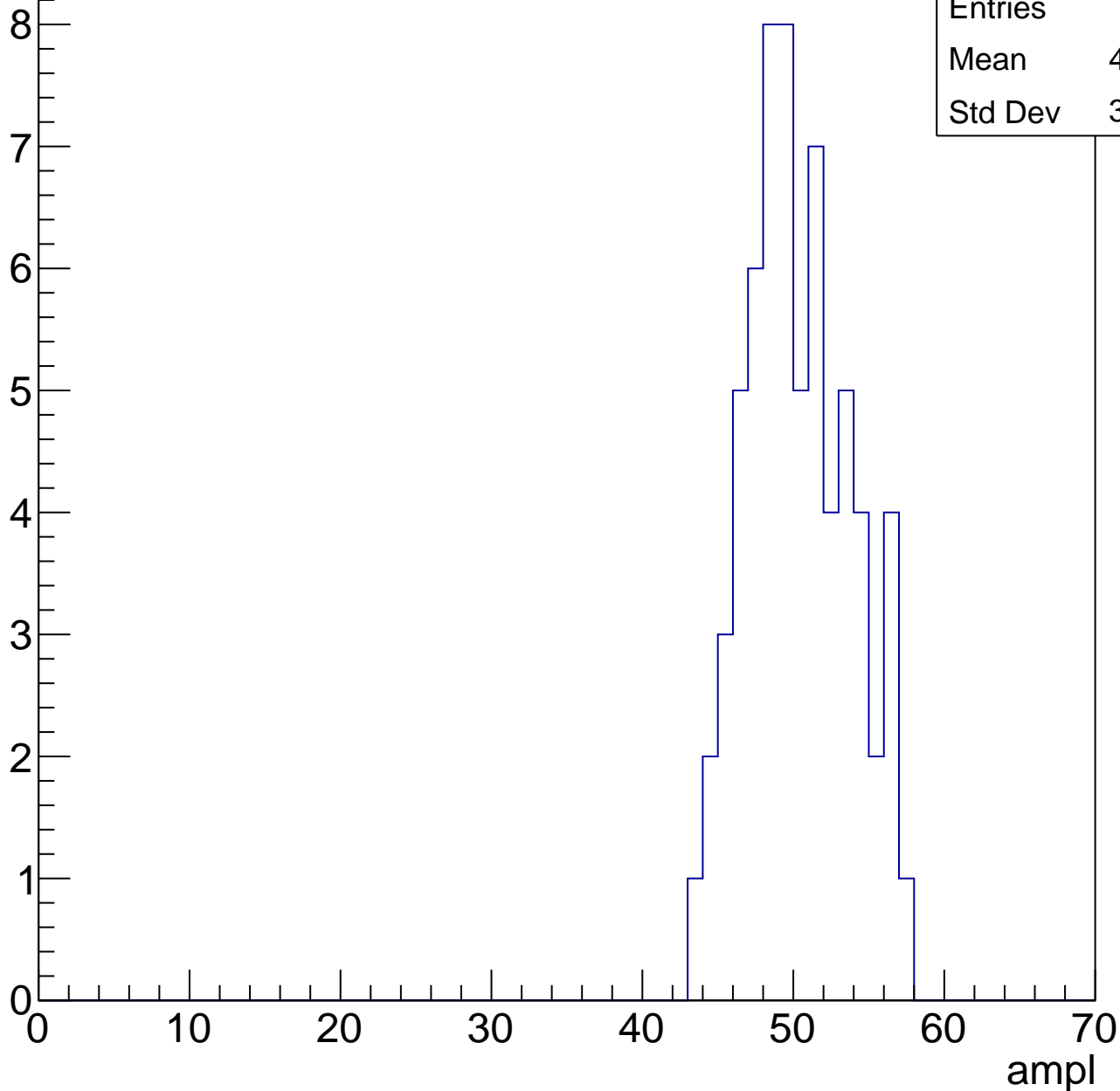


# B0L102S, U17-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

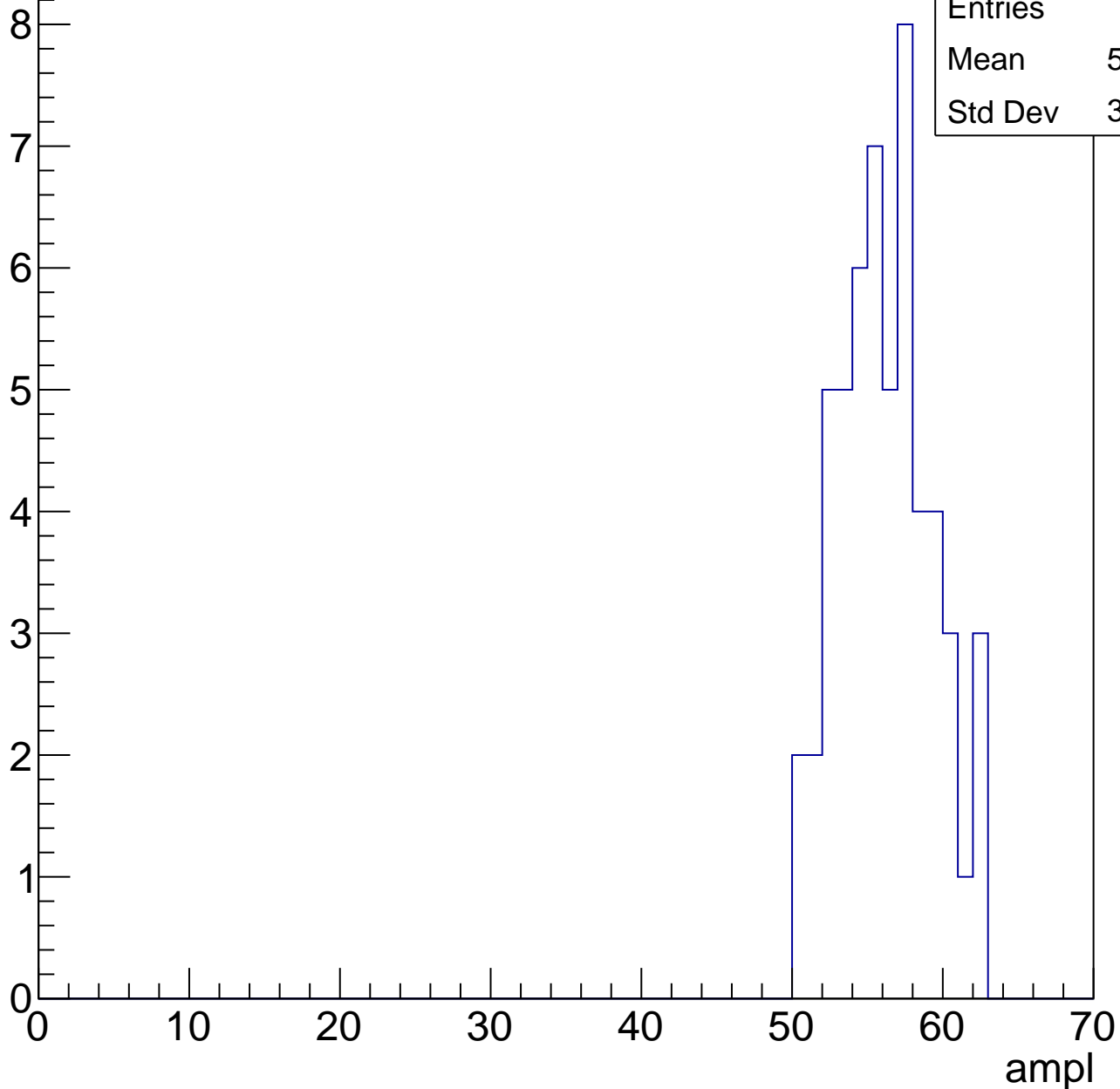
Entries	65
Mean	49.86
Std Dev	3.383



# B0L102S, U17-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	55
Mean	55.76
Std Dev	3.063

# B0L102S, U17-ch37, adc5

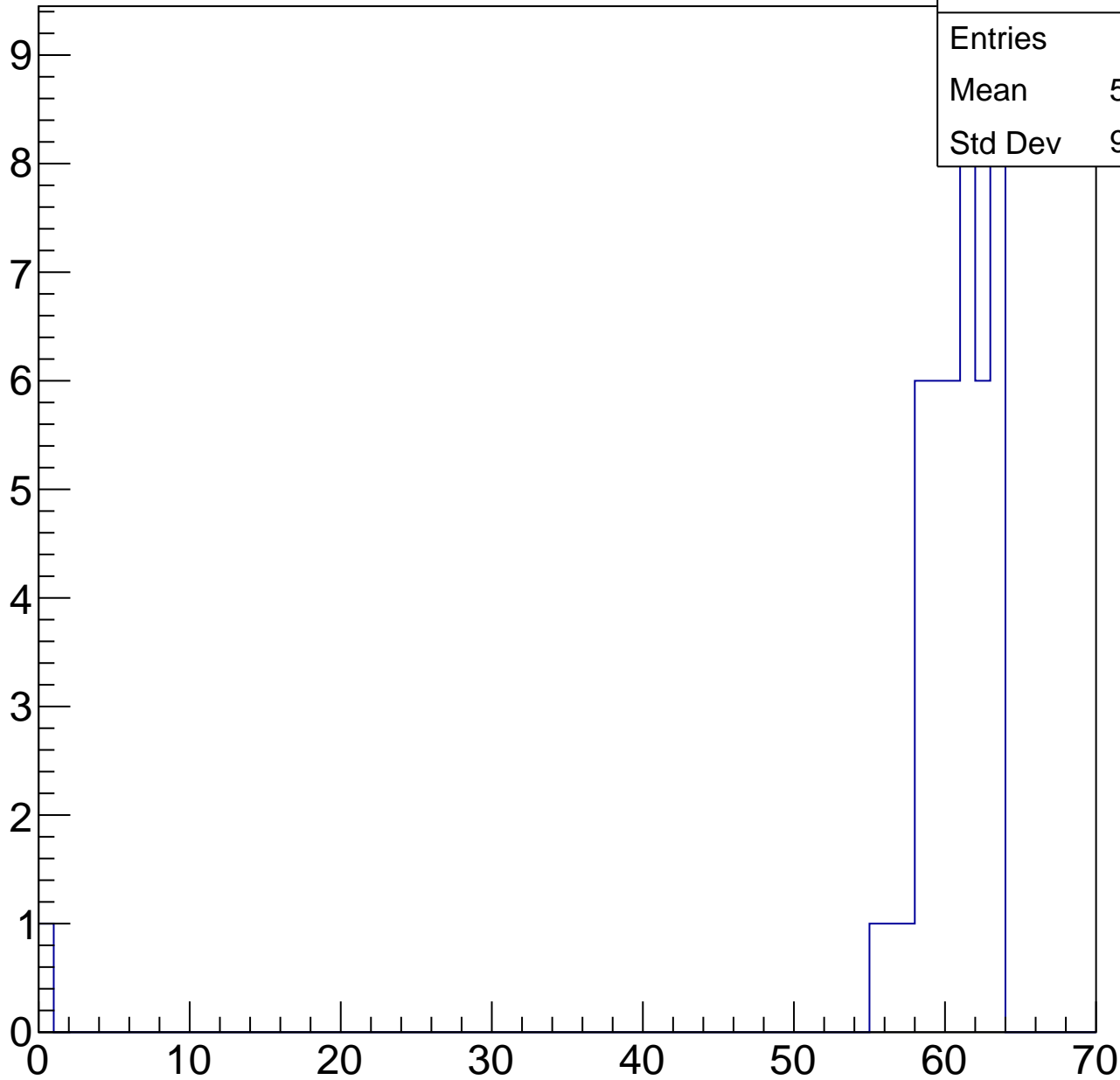
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.09
Std Dev	9.036

ampl



# B0L102S, U17-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch38, adc0

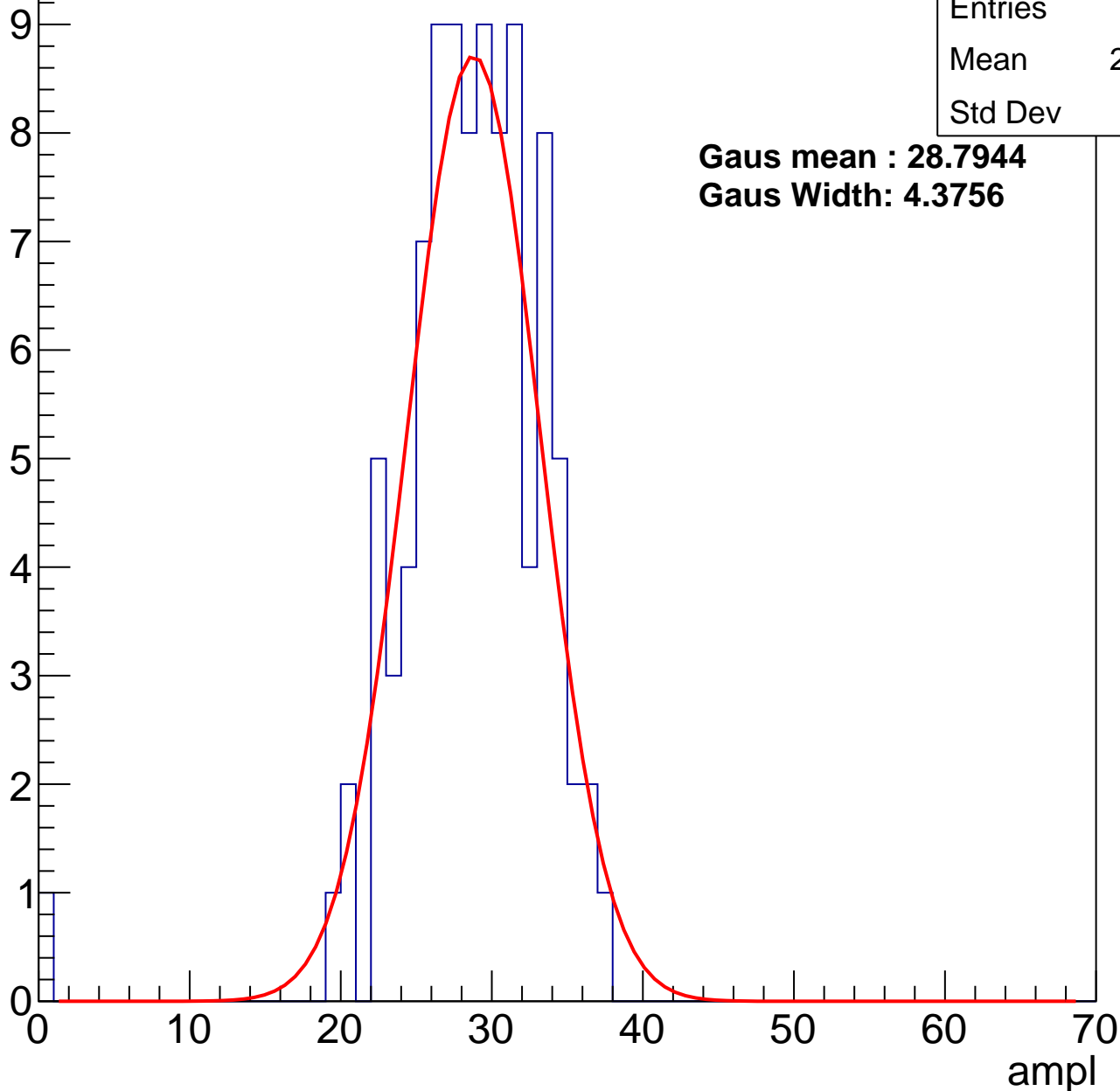
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	97
Mean	28.15
Std Dev	4.86

**Gaus mean : 28.7944**

**Gaus Width: 4.3756**



# B0L102S, U17-ch38, adc1

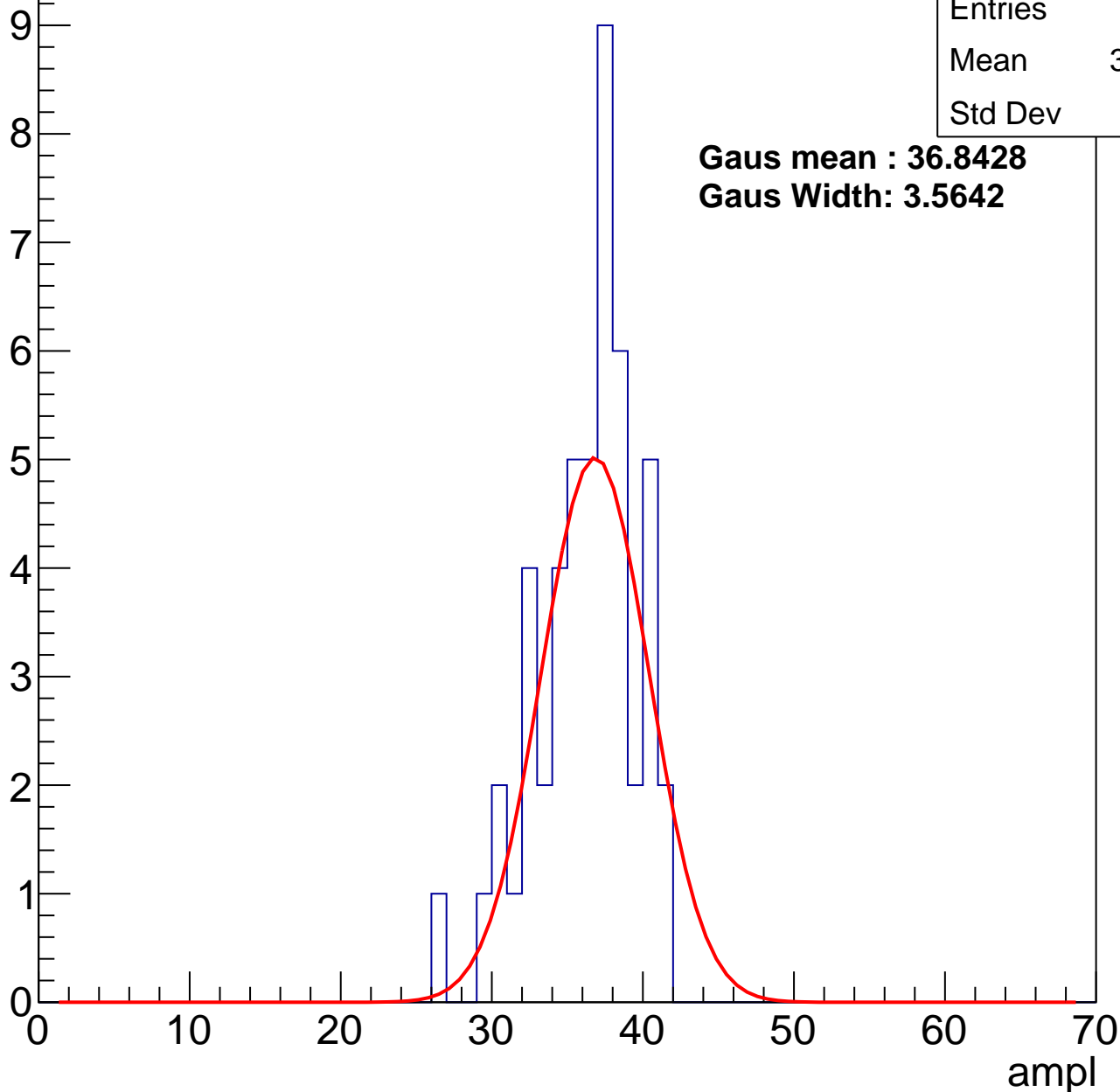
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	35.76
Std Dev	3.28

**Gaus mean : 36.8428**

**Gaus Width: 3.5642**



# B0L102S, U17-ch38, adc2

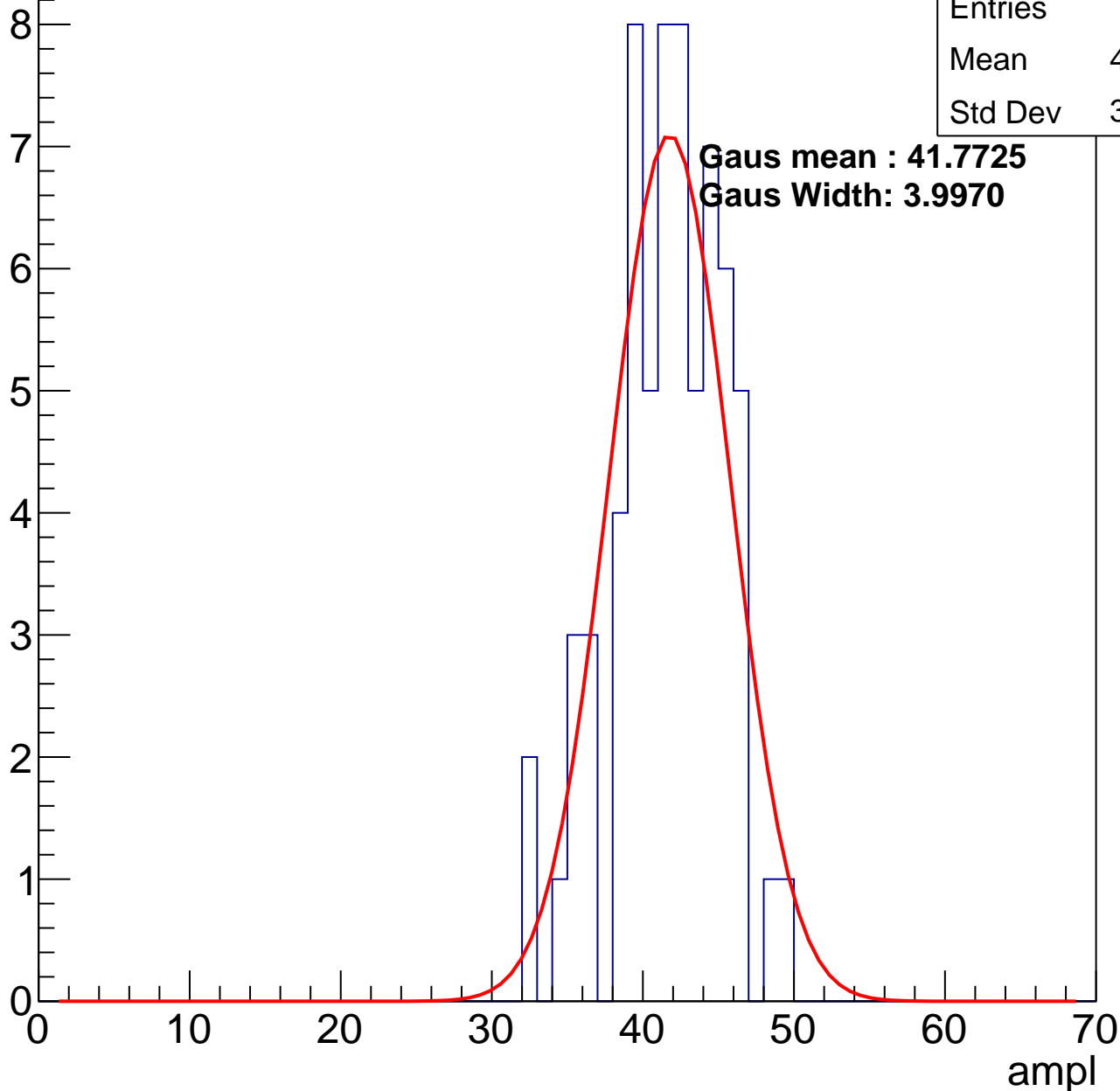
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	41.18
Std Dev	3.644

**Gaus mean : 41.7725**

**Gaus Width: 3.9970**

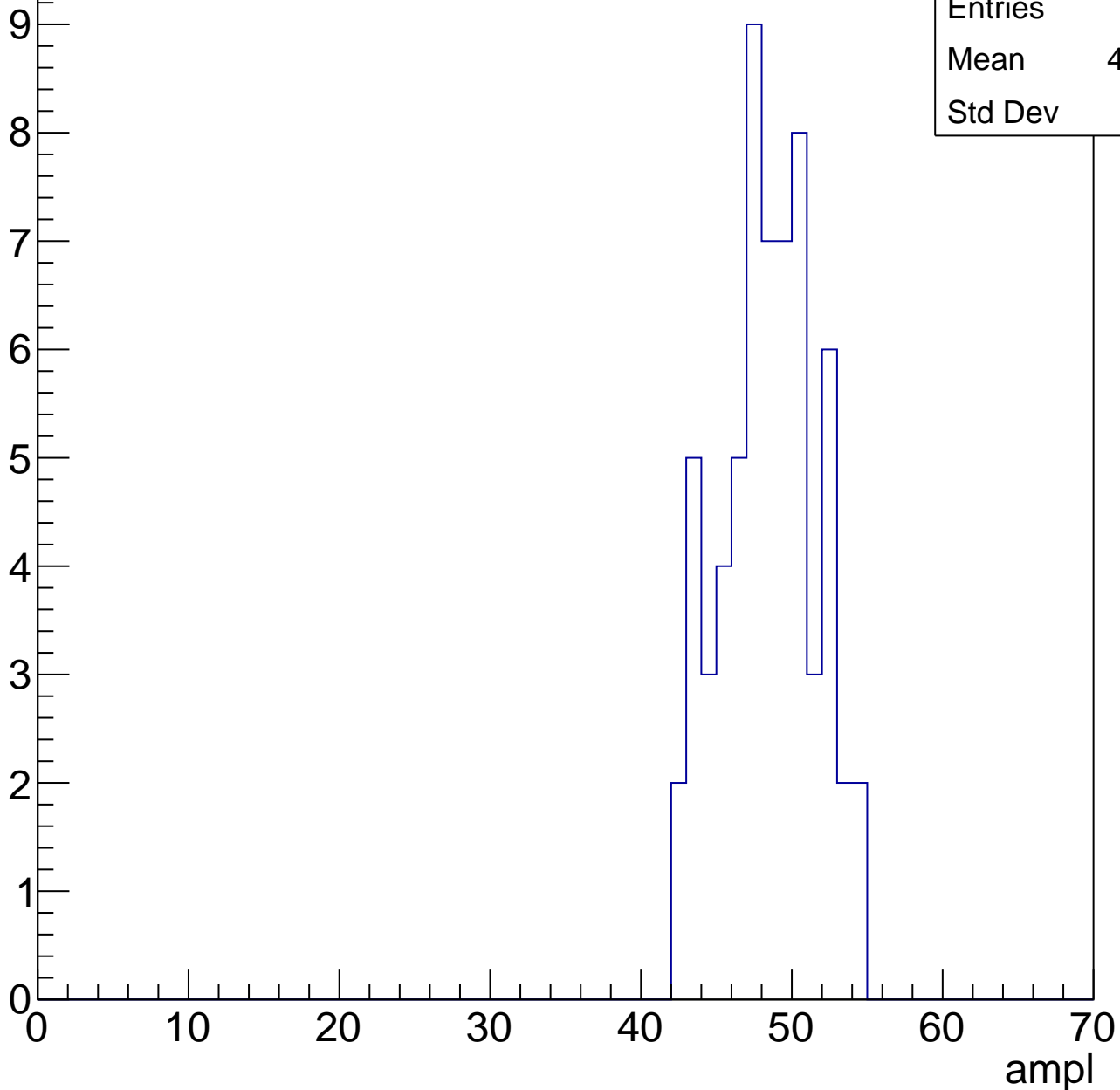


# B0L102S, U17-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	47.97
Std Dev	3.07

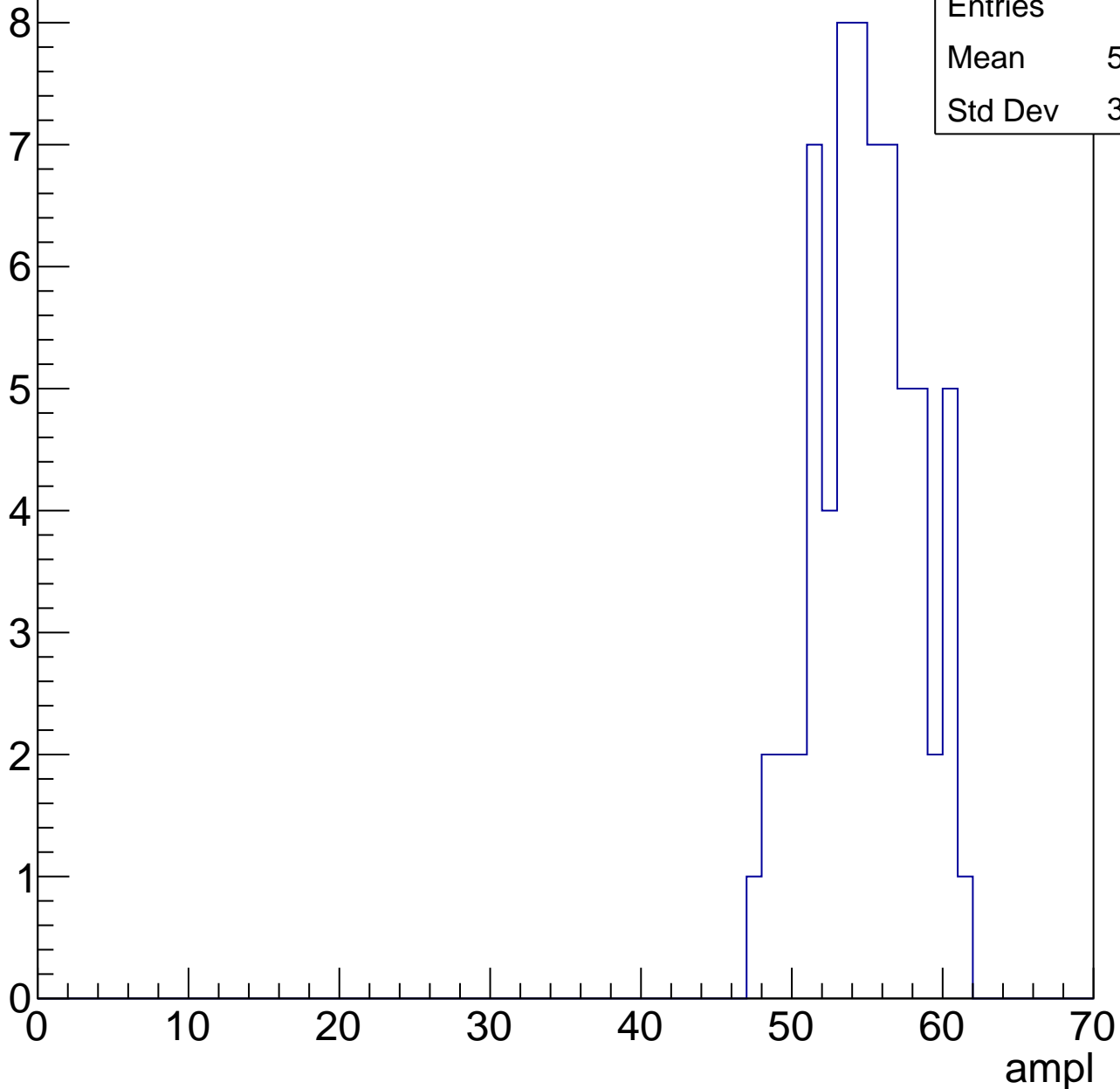


# B0L102S, U17-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

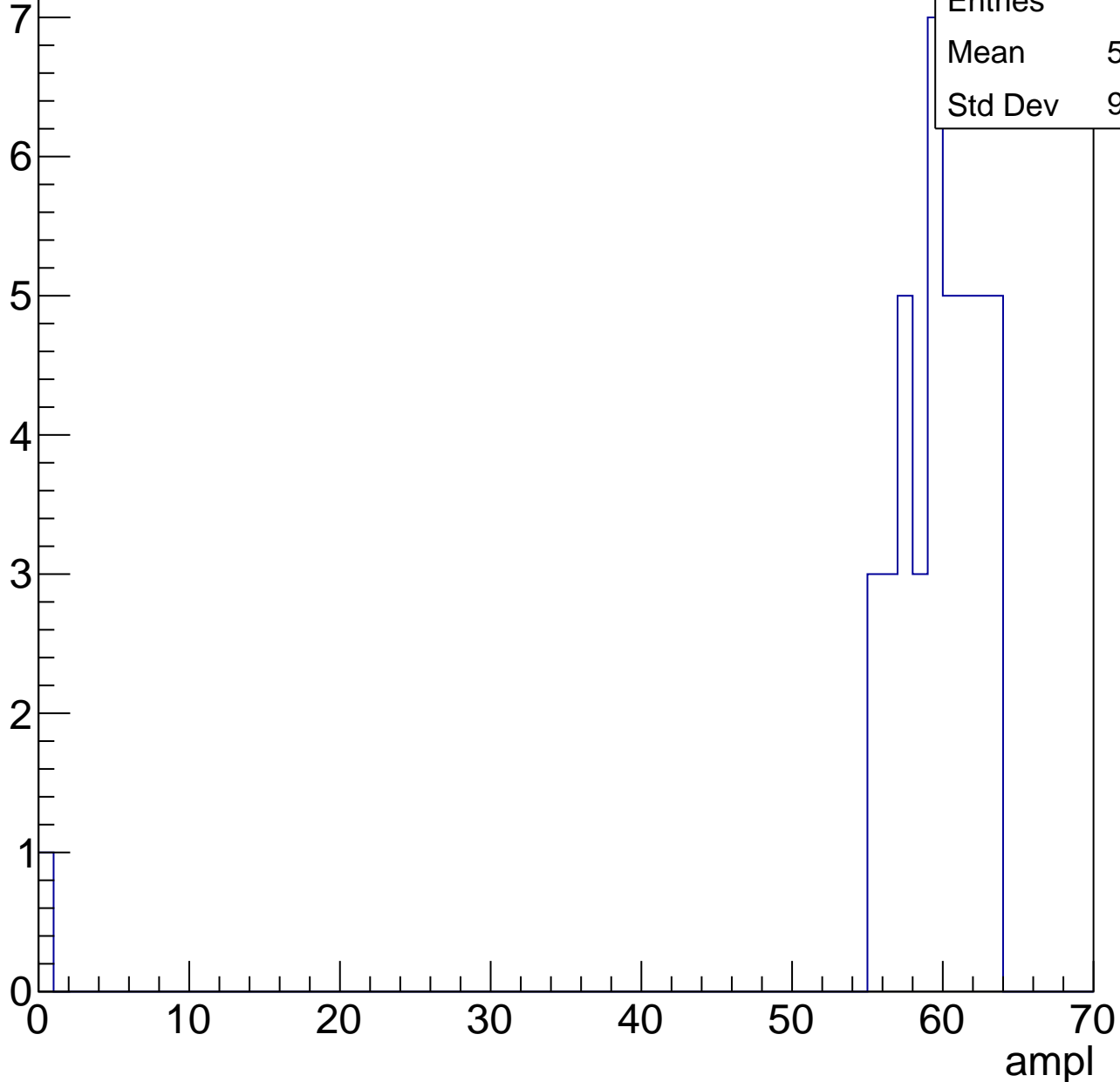
Entries	66
Mean	54.44
Std Dev	3.294



# B0L102S, U17-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

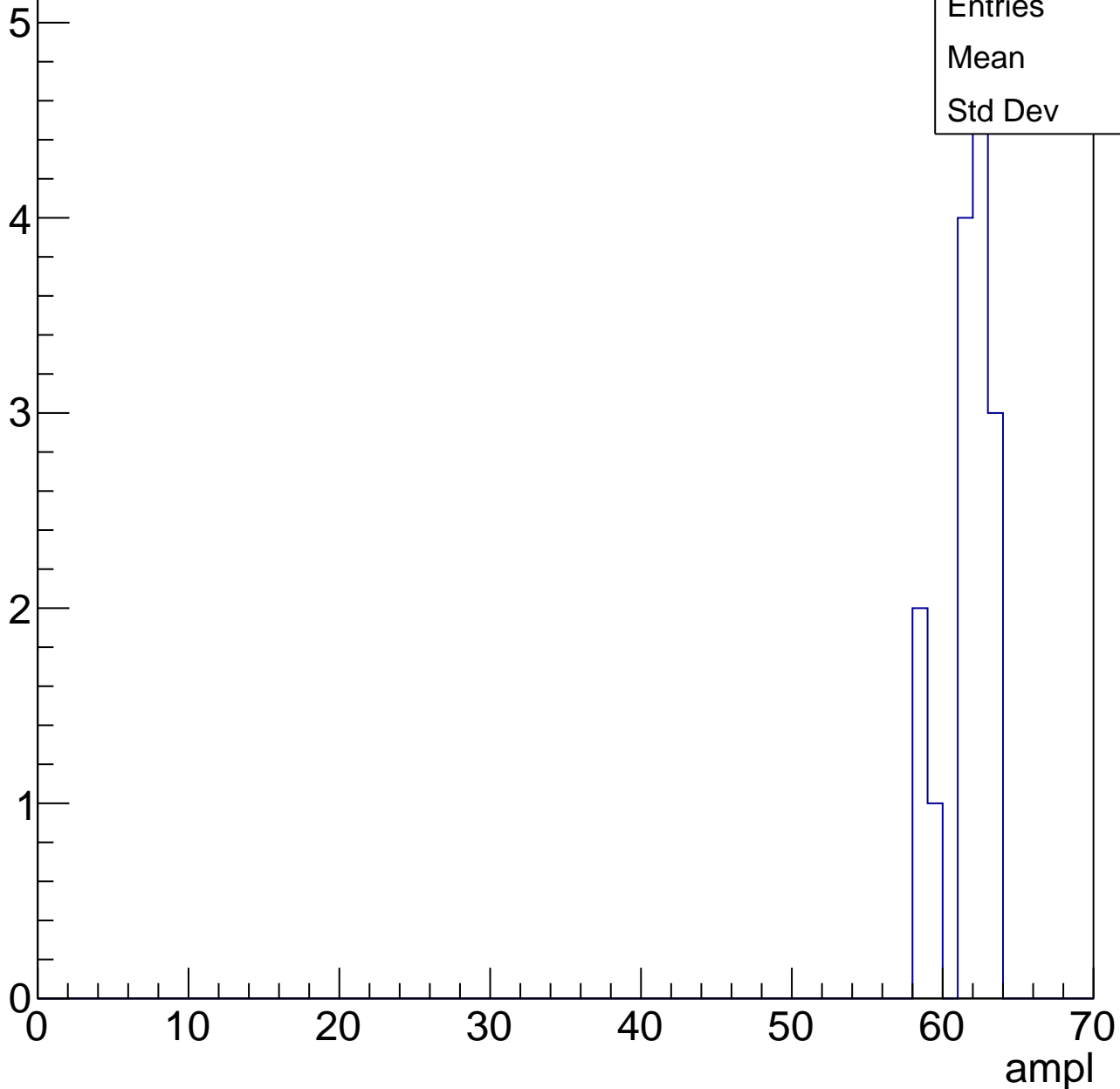


# B0L102S, U17-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.2
Std Dev	1.6





# B0L102S, U17-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch39, adc0

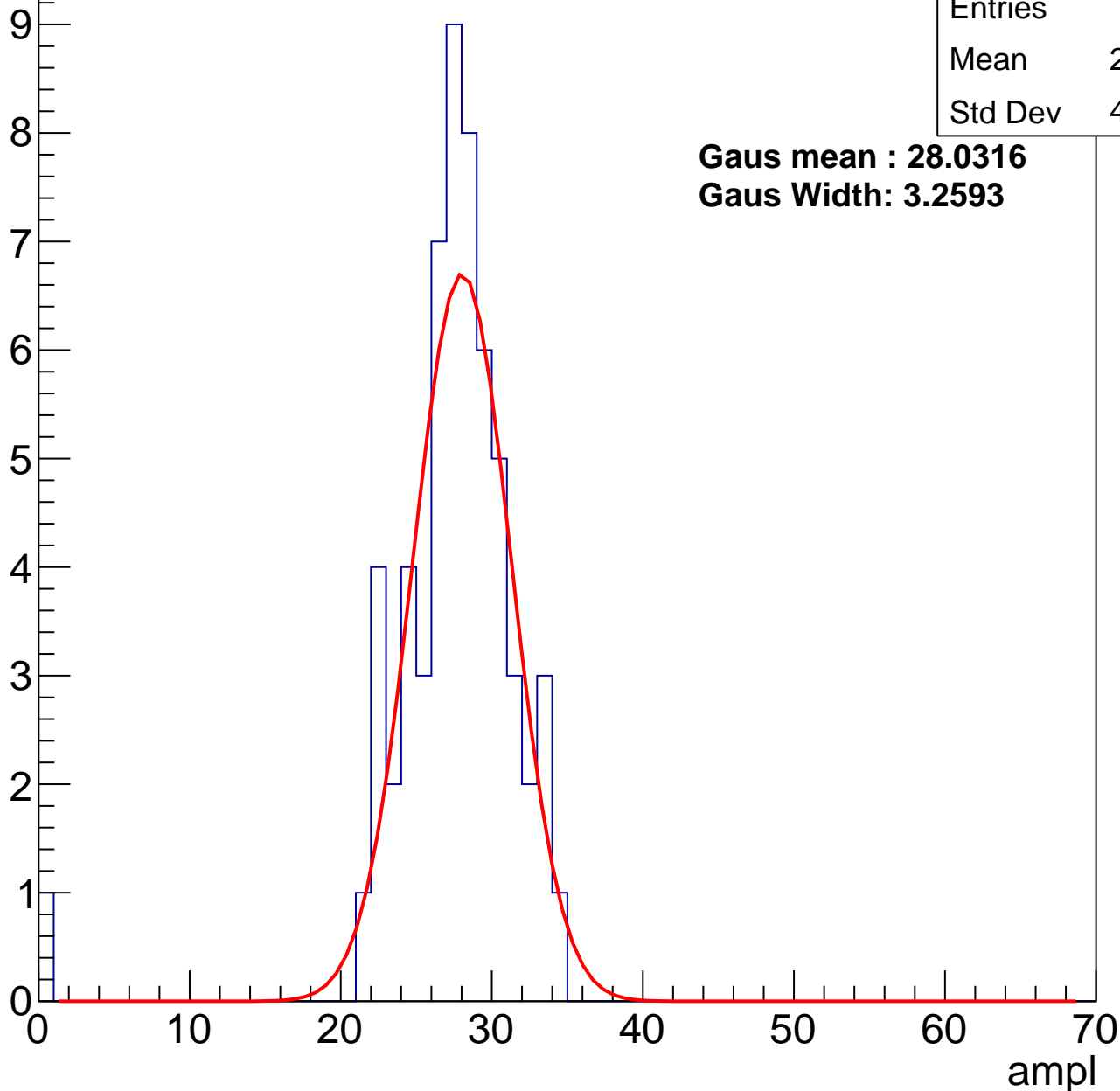
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	26.93
Std Dev	4.665

**Gaus mean : 28.0316**

**Gaus Width: 3.2593**



# B0L102S, U17-ch39, adc1

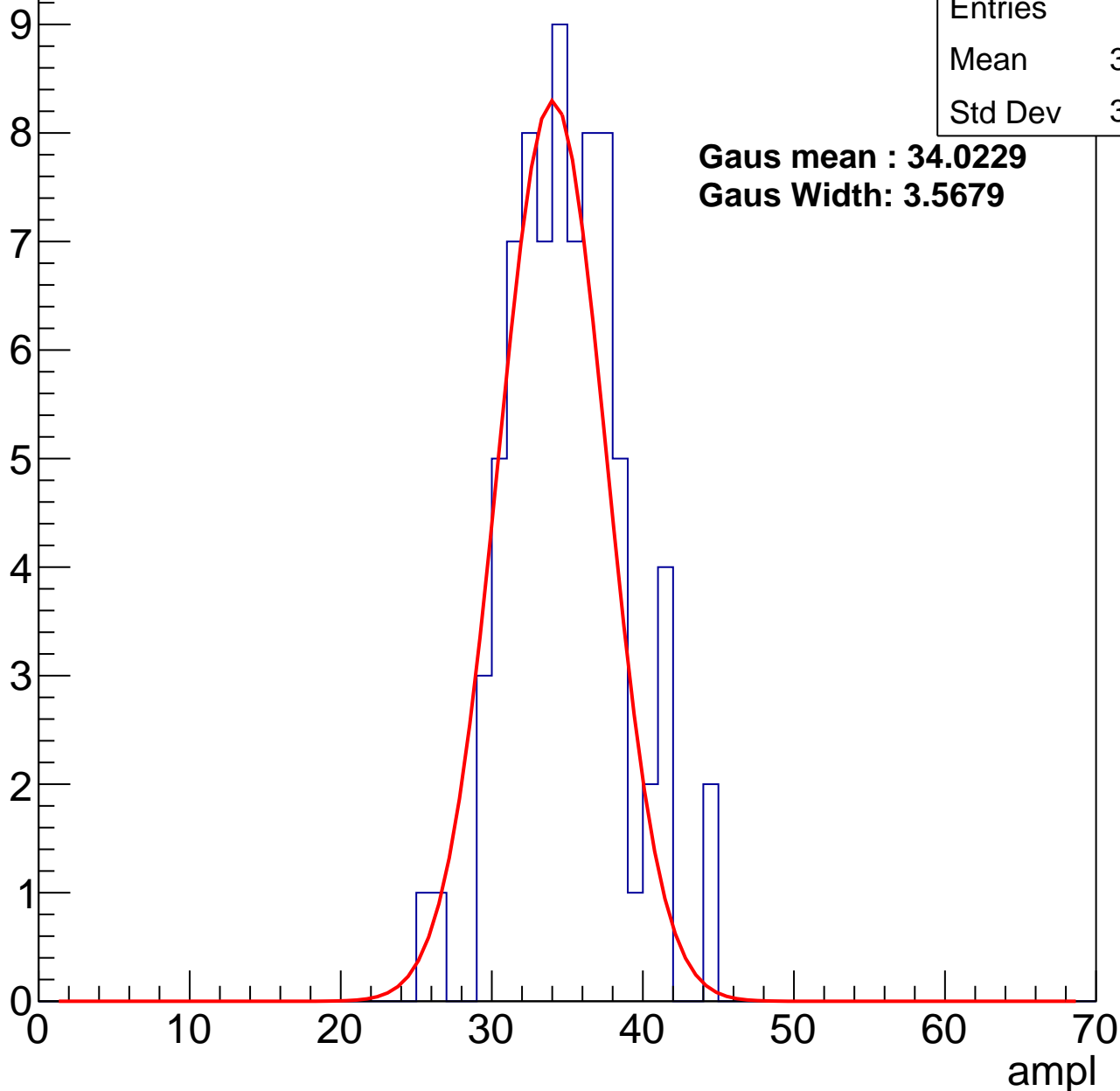
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	34.46
Std Dev	3.703

**Gaus mean : 34.0229**

**Gaus Width: 3.5679**



# B0L102S, U17-ch39, adc2

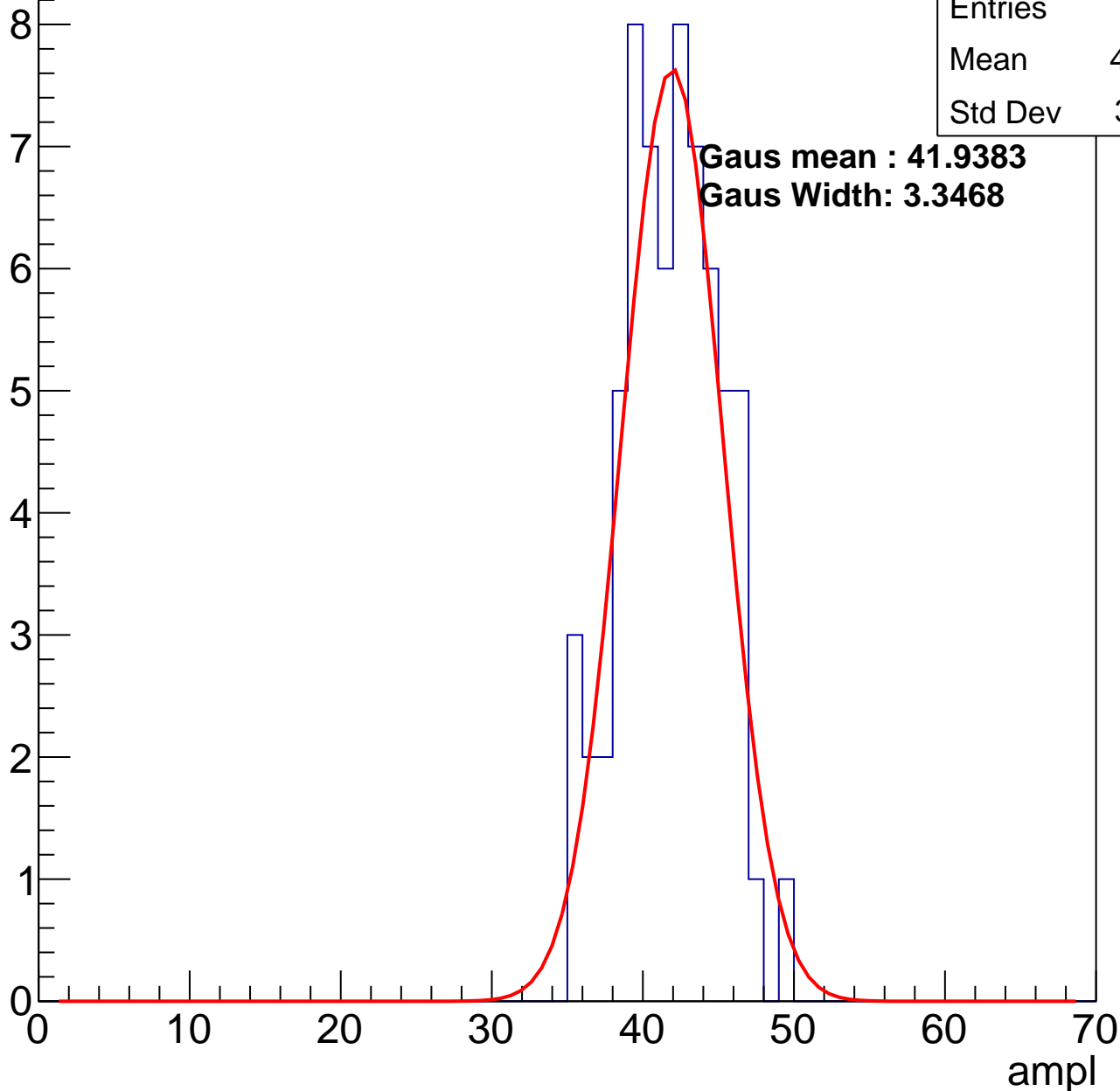
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.38
Std Dev	3.161

**Gaus mean : 41.9383**

**Gaus Width: 3.3468**



# B0L102S, U17-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	63
Mean	47.83
Std Dev	3.36

Entry

10

8

6

4

2

0

0

10

20

30

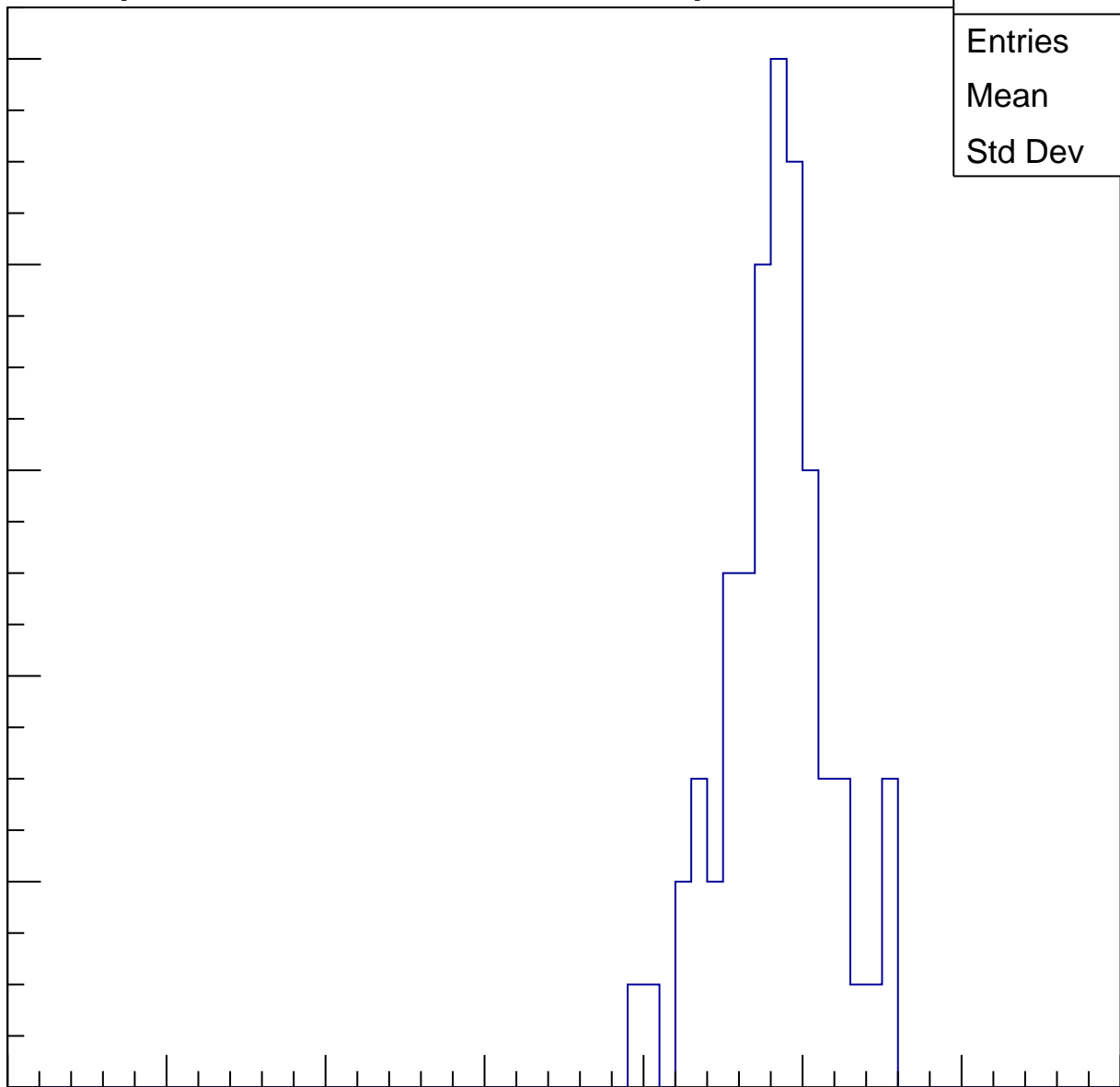
40

50

60

70

ampl

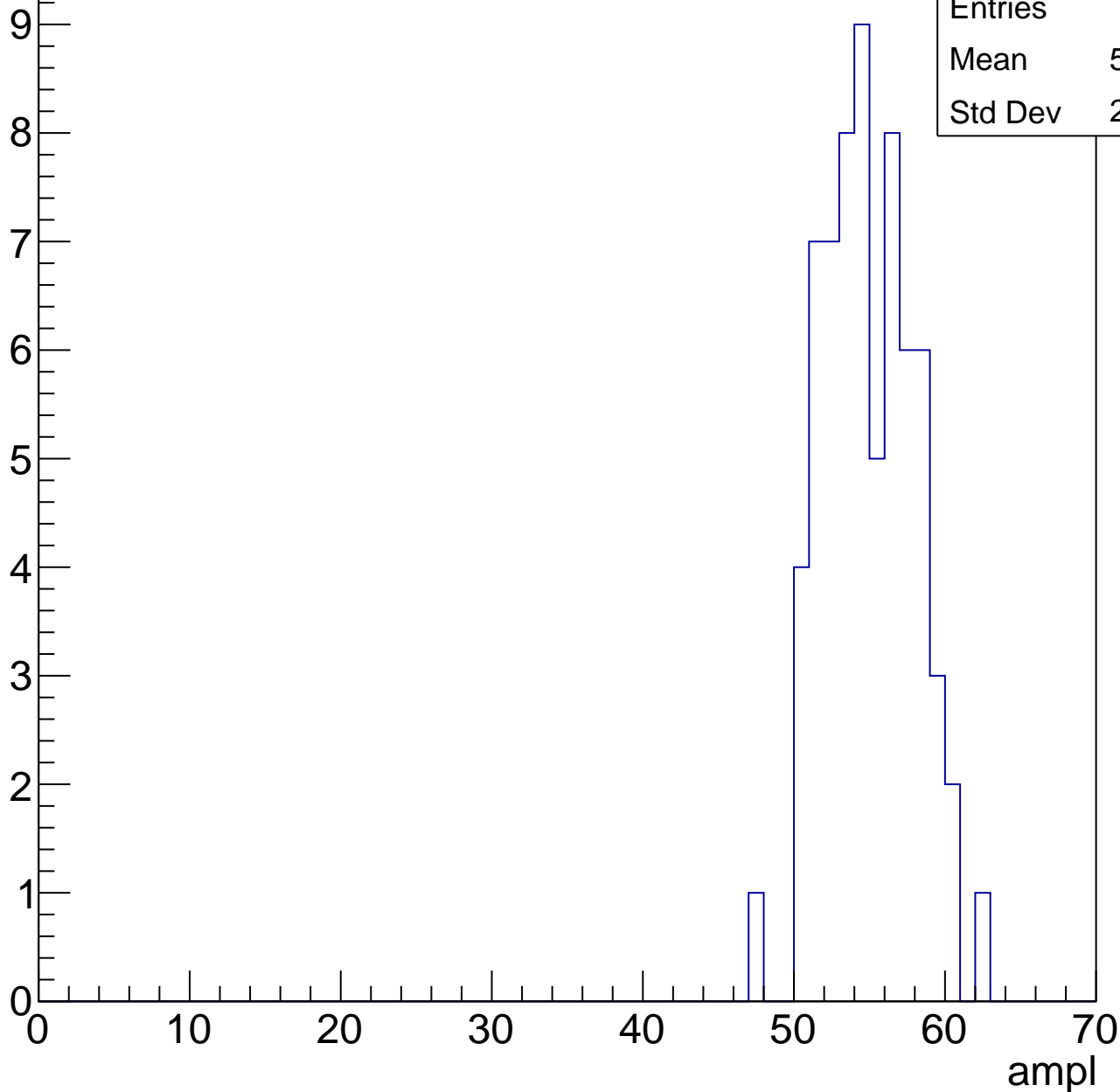


# B0L102S, U17-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	54.48
Std Dev	2.979

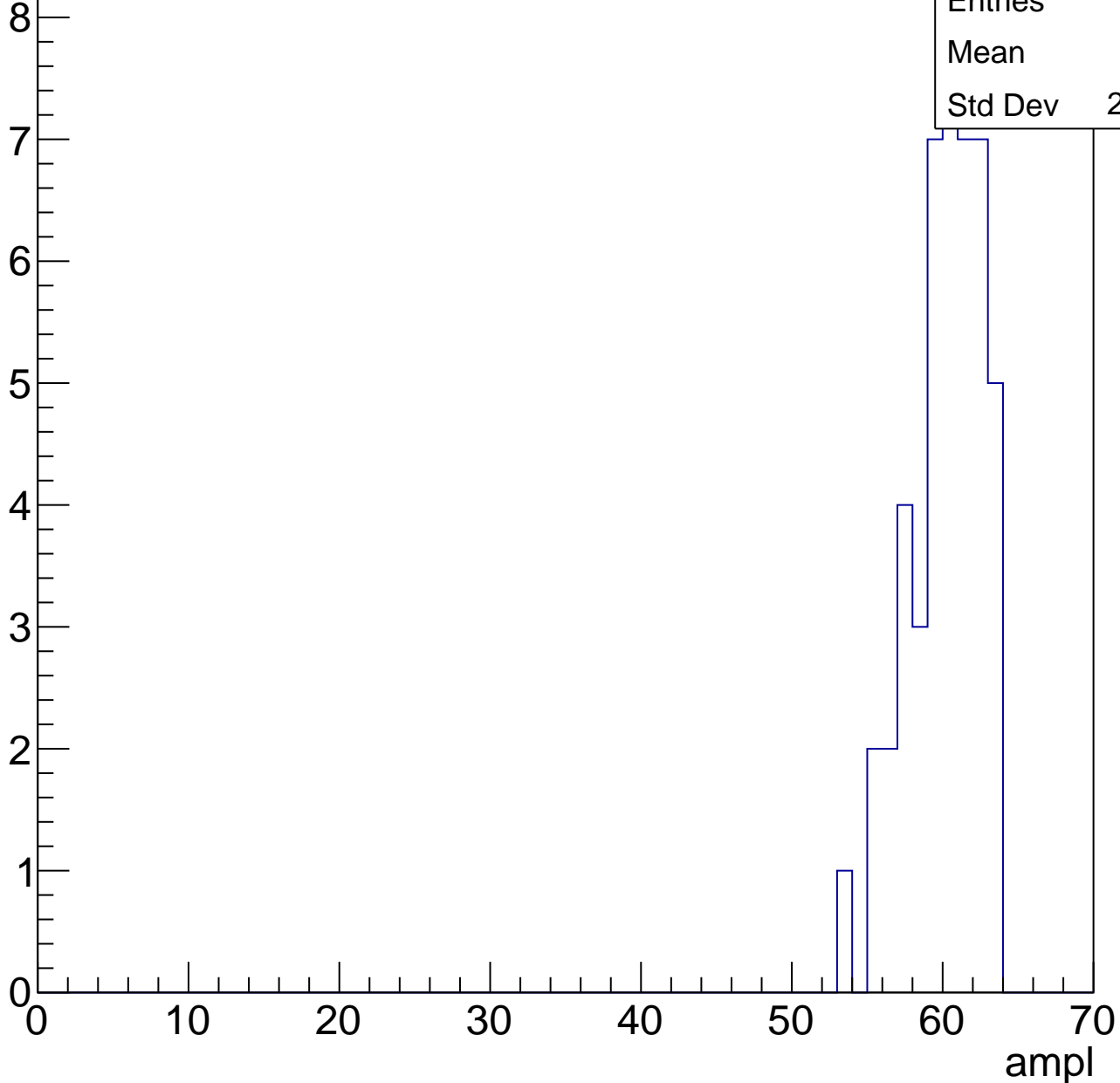


# B0L102S, U17-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	59.7
Std Dev	2.385

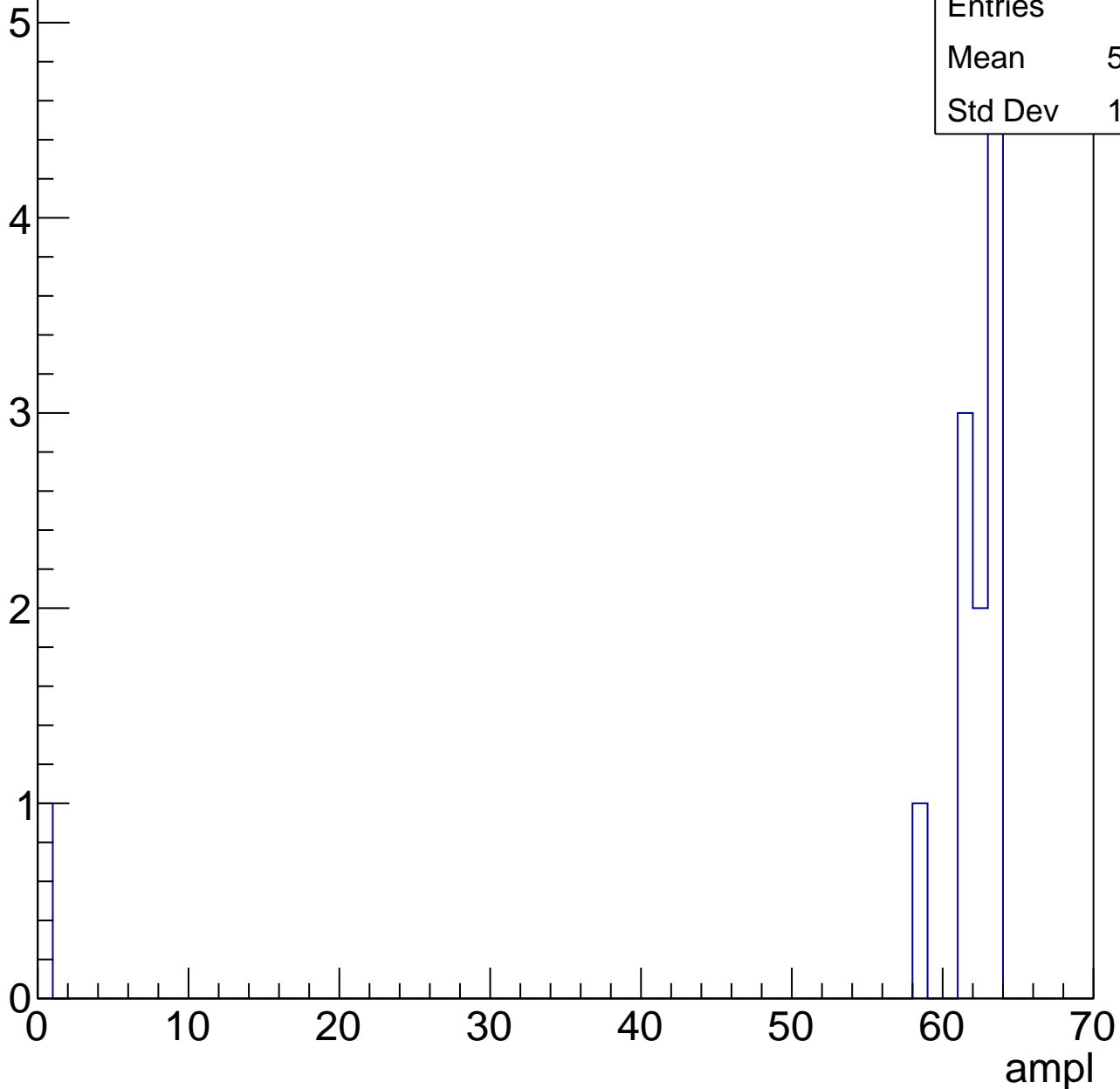


# B0L102S, U17-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	56.67
Std Dev	17.14





# B0L102S, U17-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch40, adc0

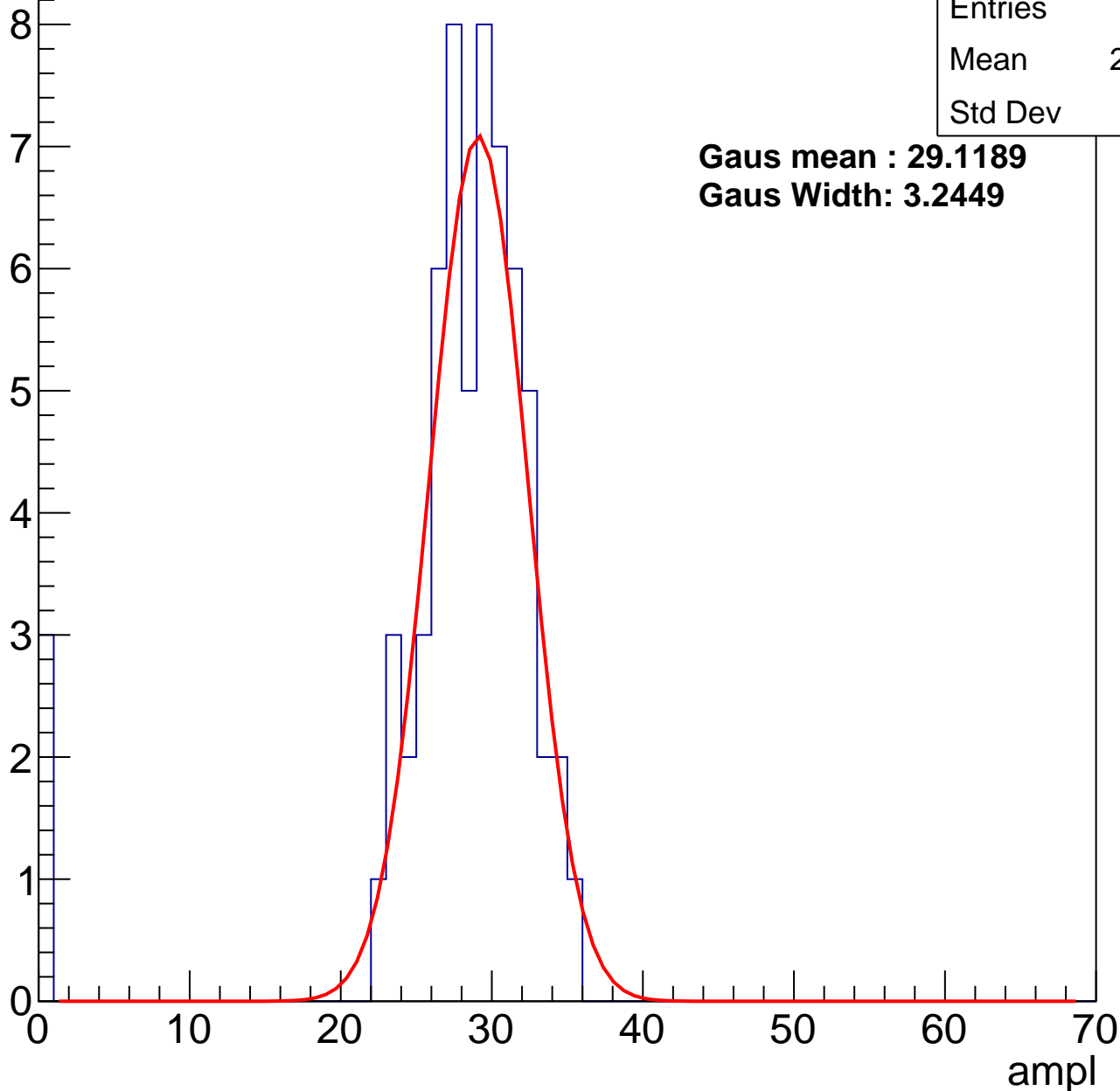
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	27.15
Std Dev	6.78

**Gaus mean : 29.1189**

**Gaus Width: 3.2449**



# B0L102S, U17-ch40, adc1

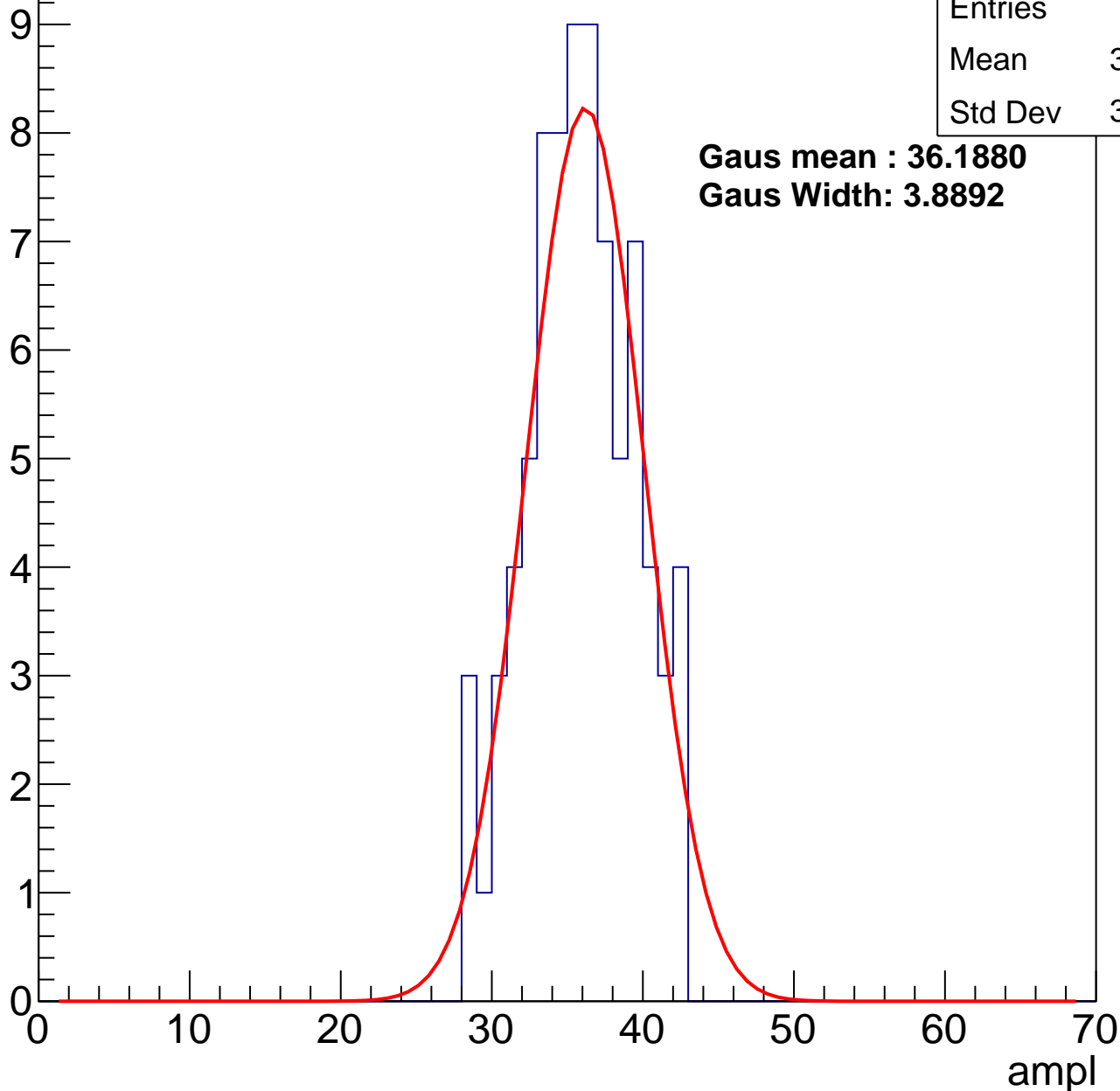
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	35.44
Std Dev	3.517

**Gaus mean : 36.1880**

**Gaus Width: 3.8892**



# B0L102S, U17-ch40, adc2

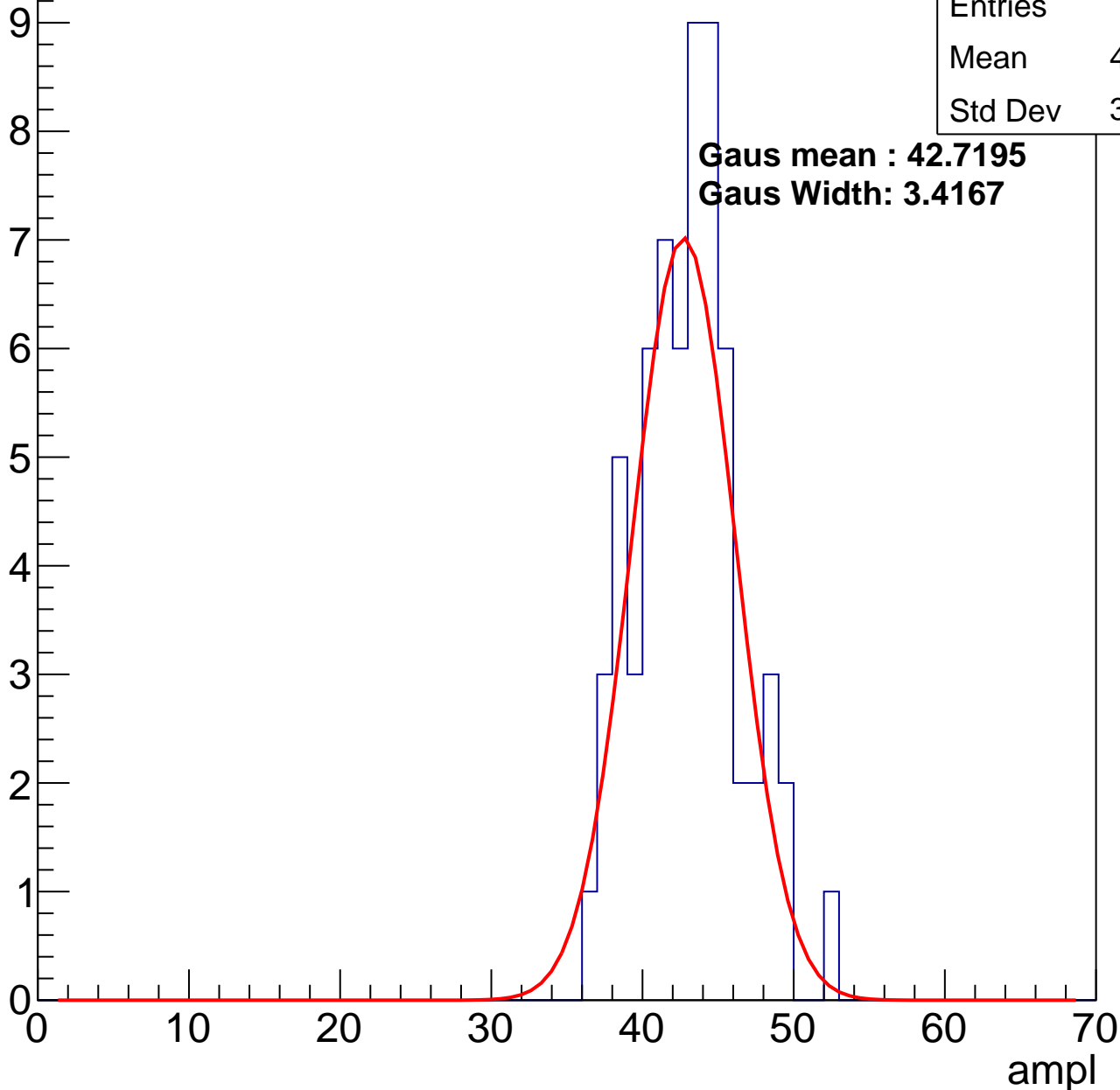
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.55
Std Dev	3.319

**Gaus mean : 42.7195**

**Gaus Width: 3.4167**

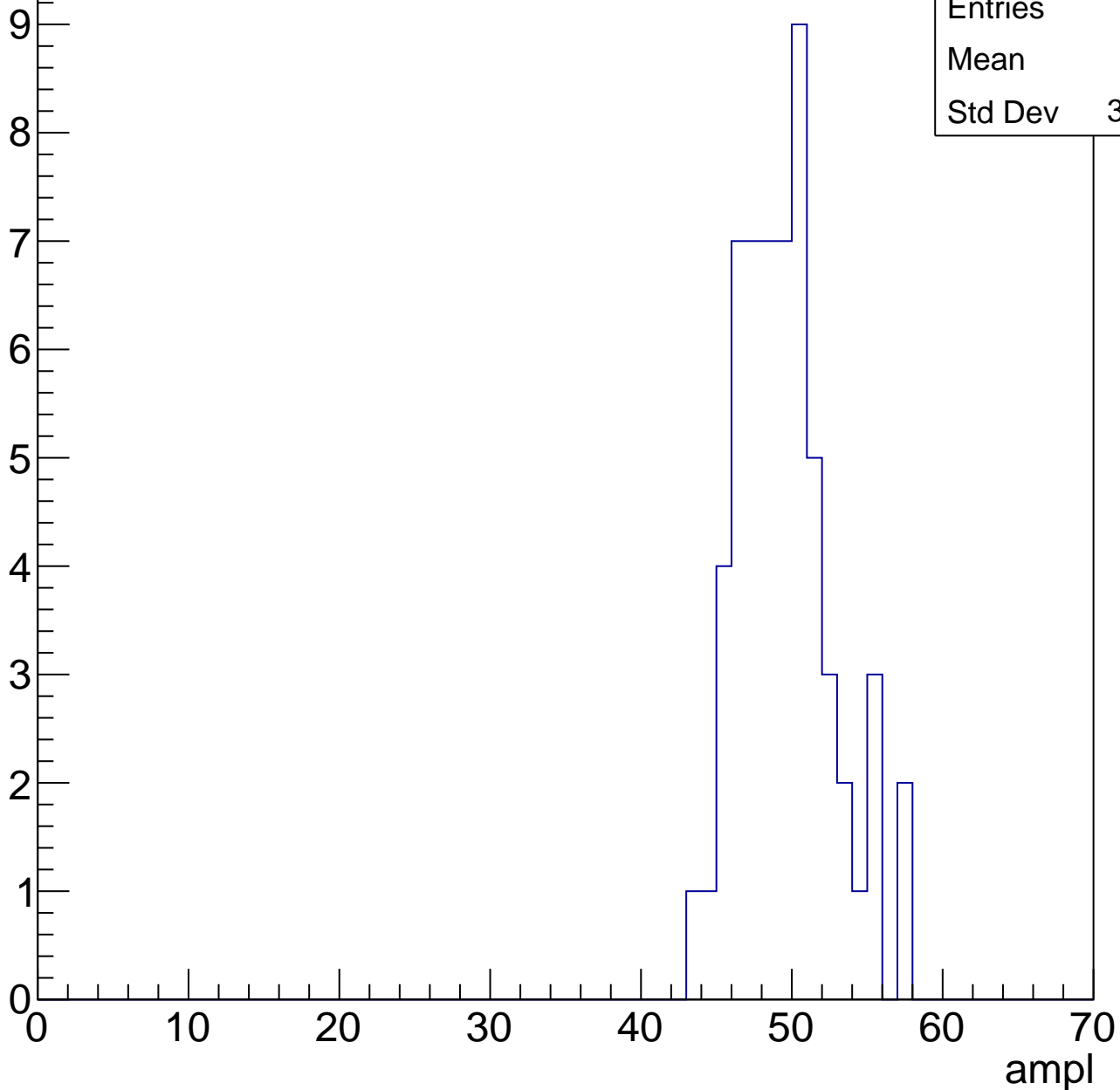


# B0L102S, U17-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

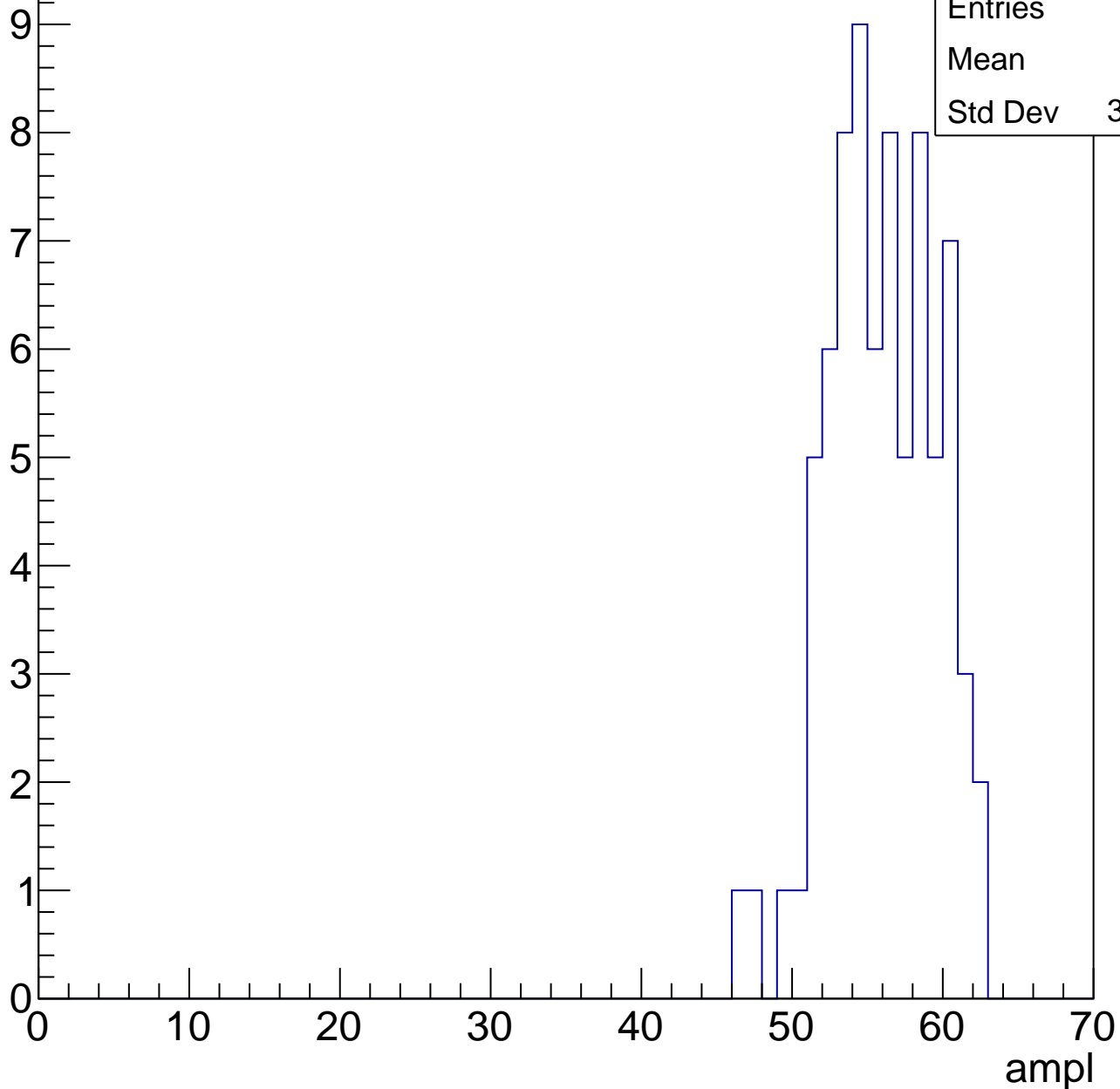
Entries	59
Mean	49.1
Std Dev	3.112



# B0L102S, U17-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



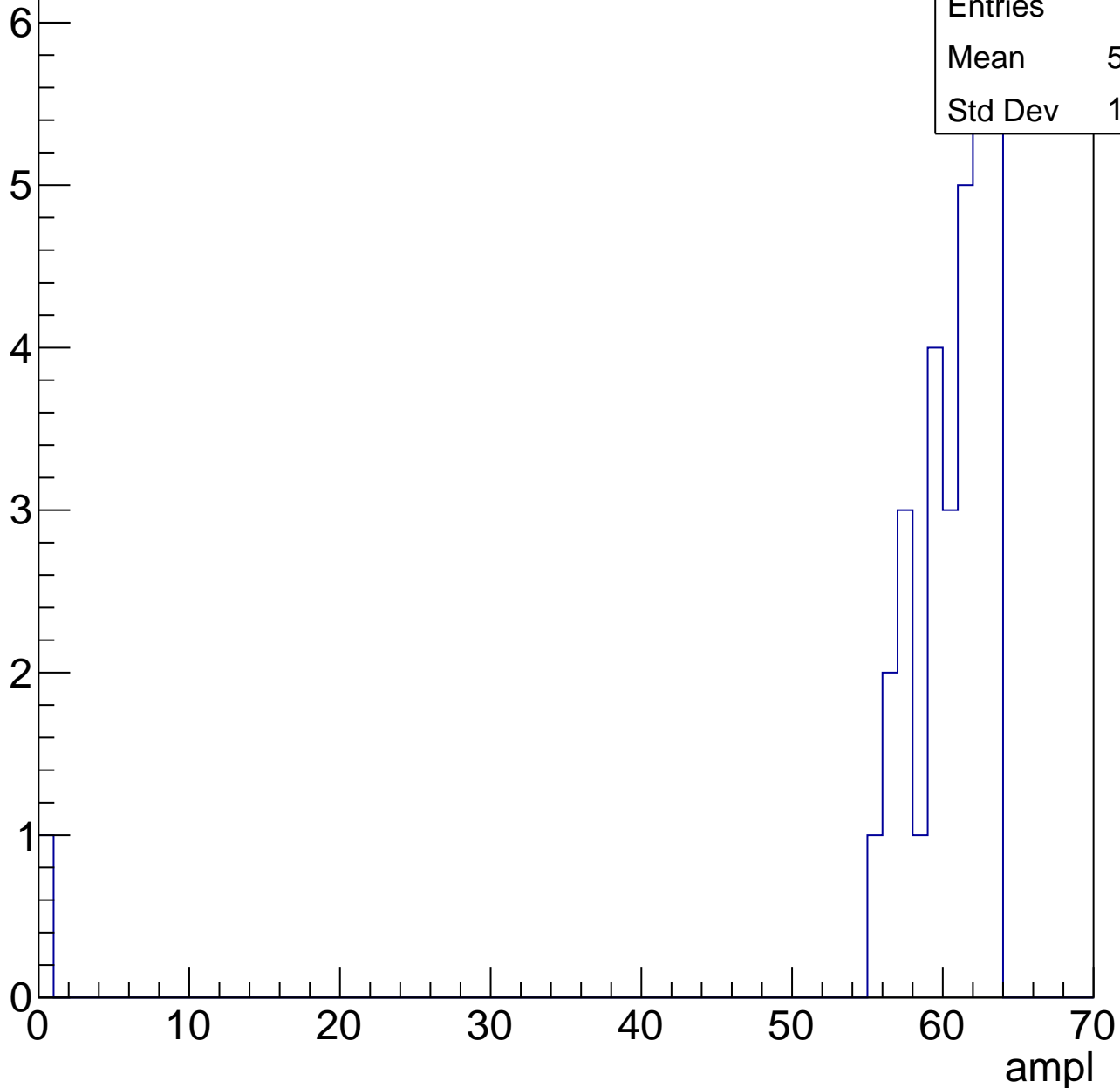
Entries	76
Mean	55.5
Std Dev	3.485

# B0L102S, U17-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

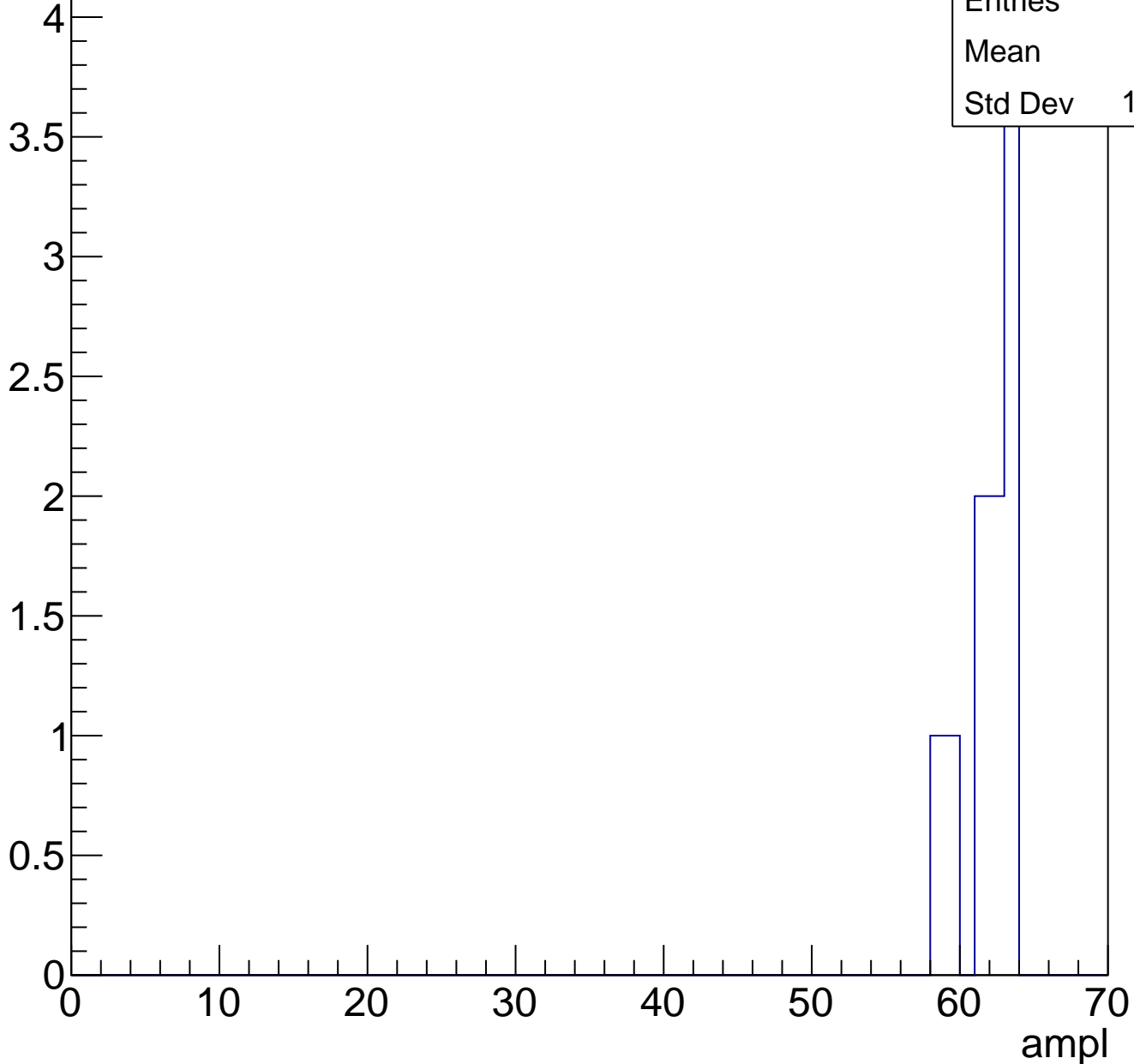
Entries	32
Mean	58.34
Std Dev	10.73



# B0L102S, U17-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch41, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	30.04
Std Dev	5.988

**Gaus mean : 31.8055**

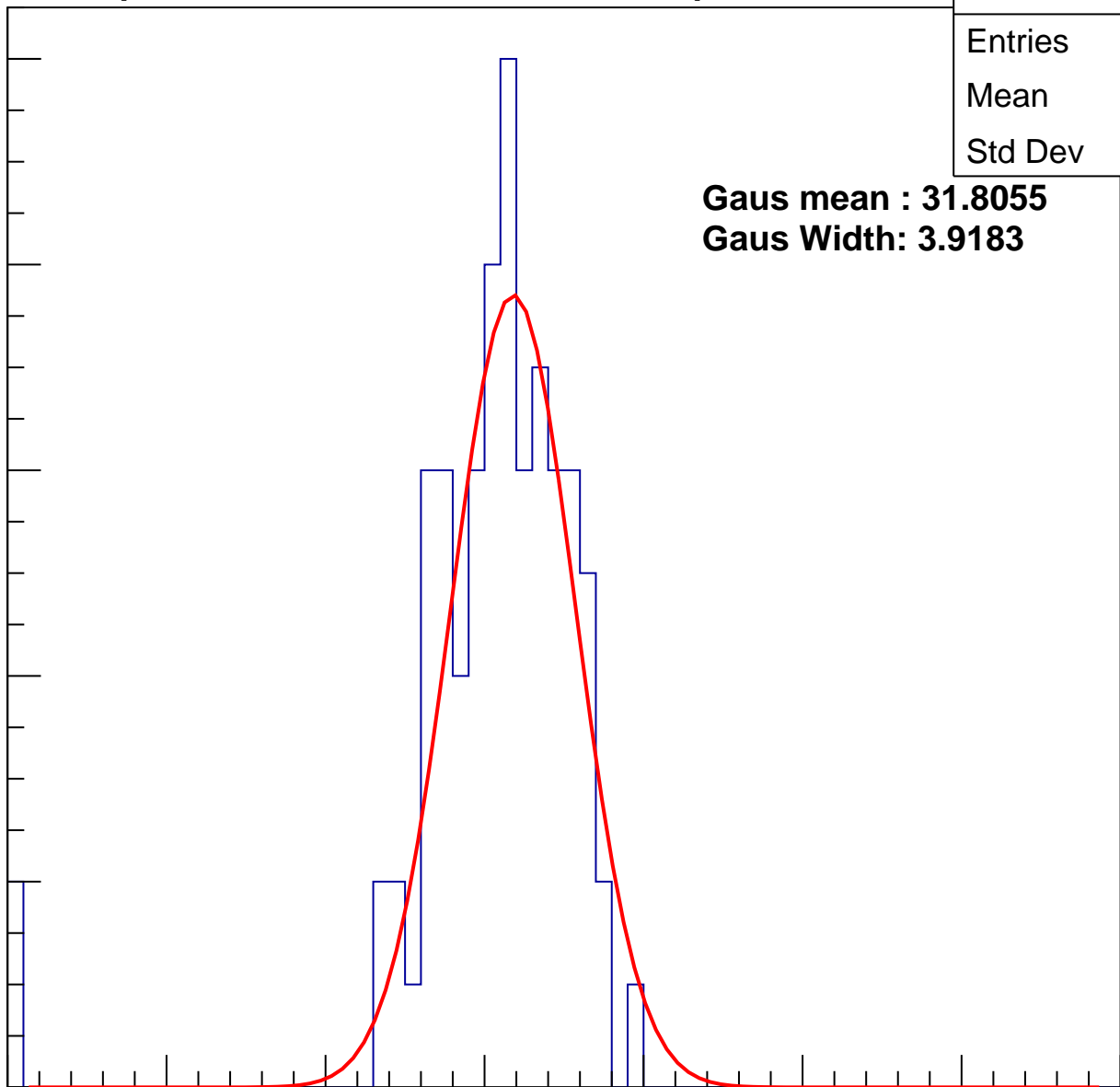
**Gaus Width: 3.9183**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



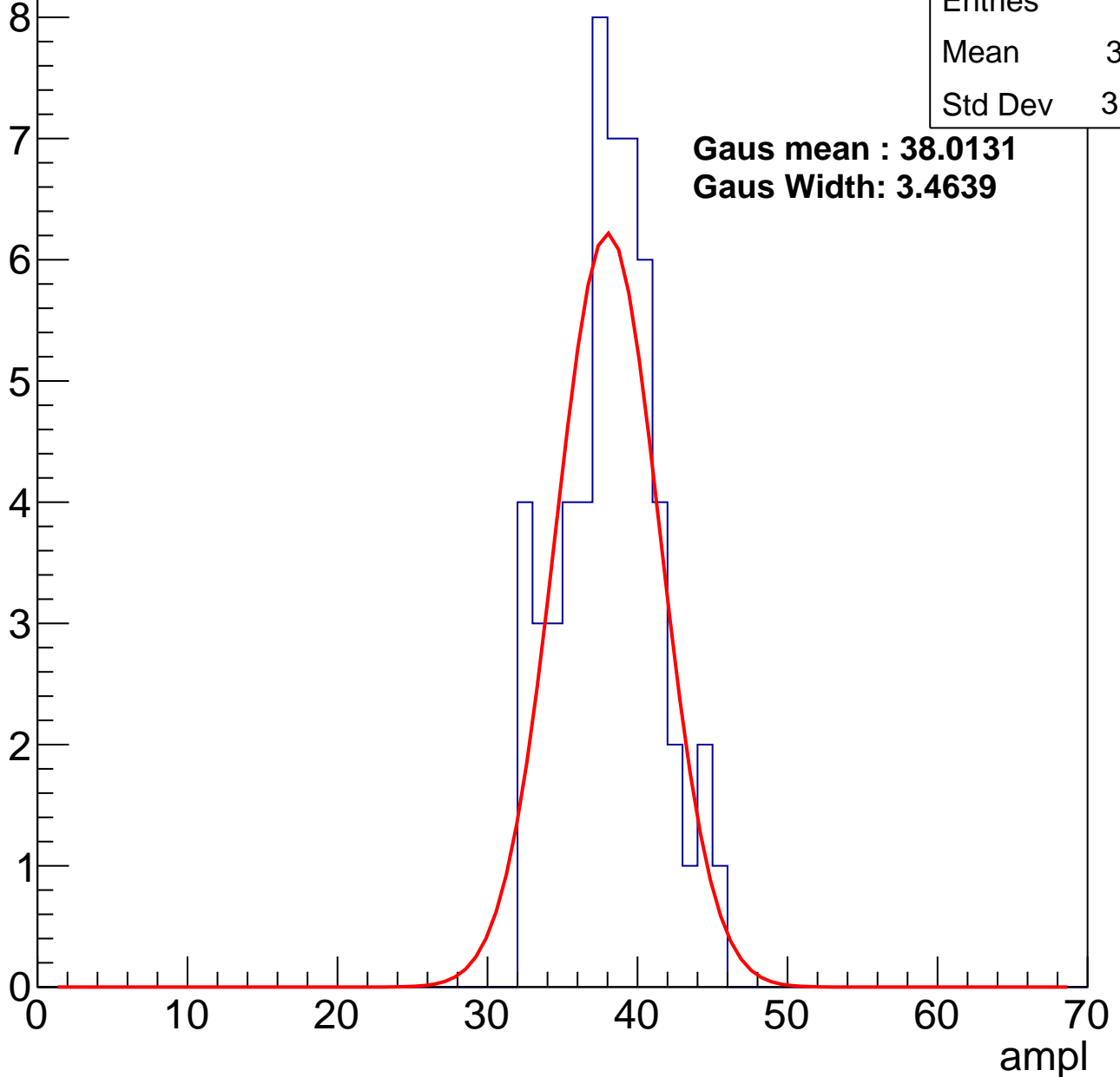
# B0L102S, U17-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	37.71
Std Dev	3.183

**Gaus mean : 38.0131**  
**Gaus Width: 3.4639**



# B0L102S, U17-ch41, adc2

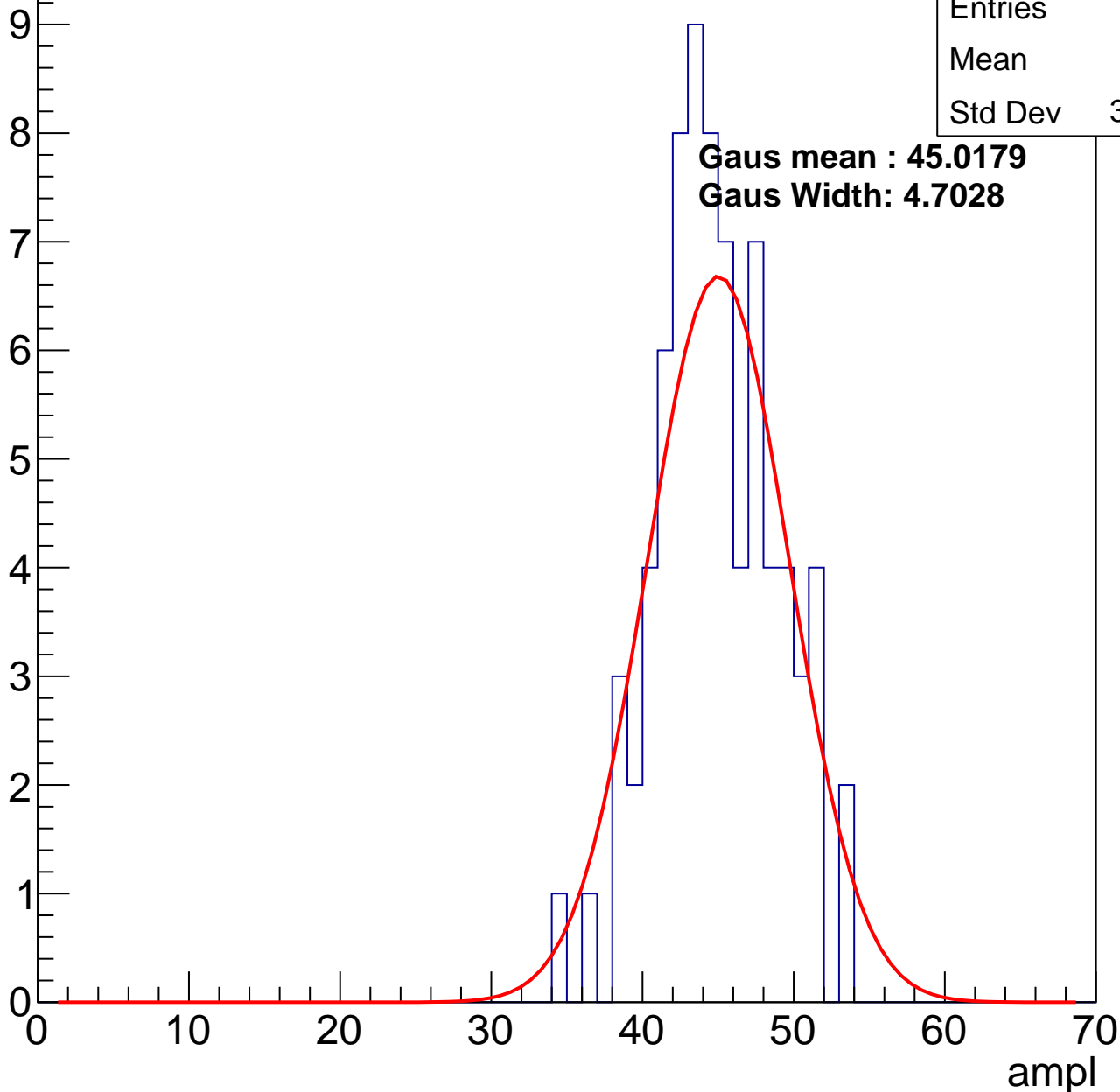
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	44.4
Std Dev	3.922

**Gaus mean : 45.0179**

**Gaus Width: 4.7028**

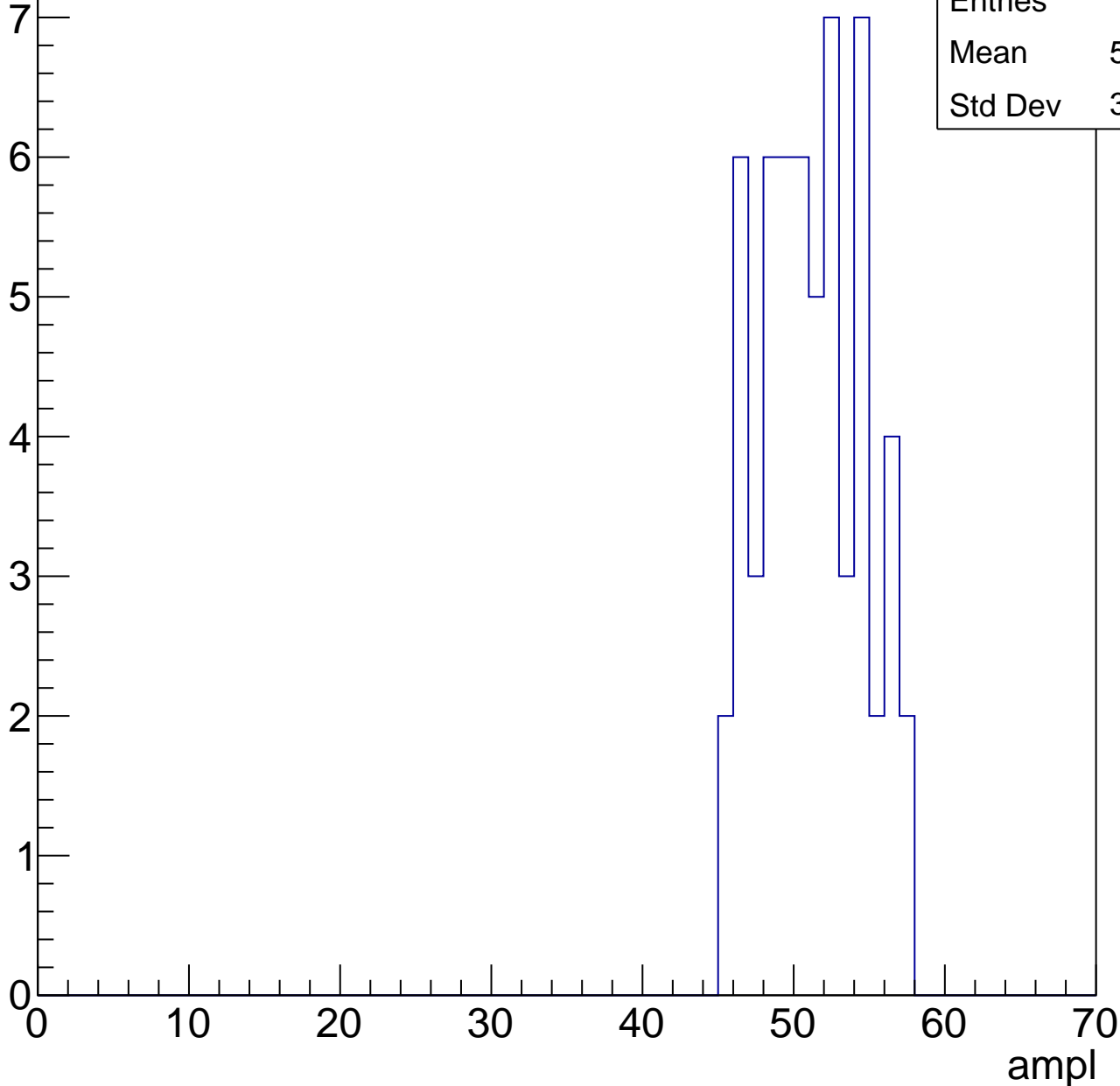


# B0L102S, U17-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	50.73
Std Dev	3.282

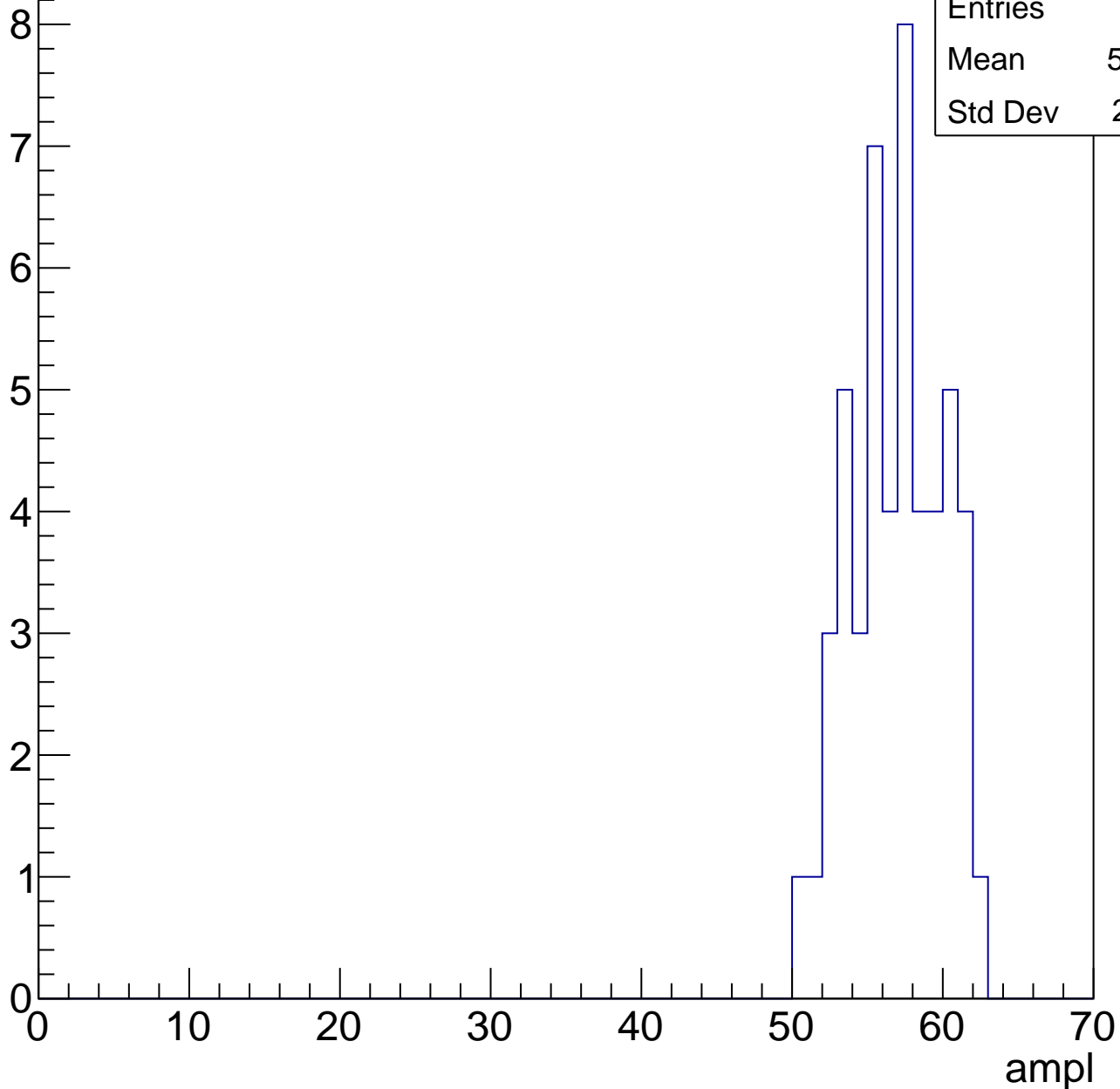


# B0L102S, U17-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	56.46
Std Dev	2.961

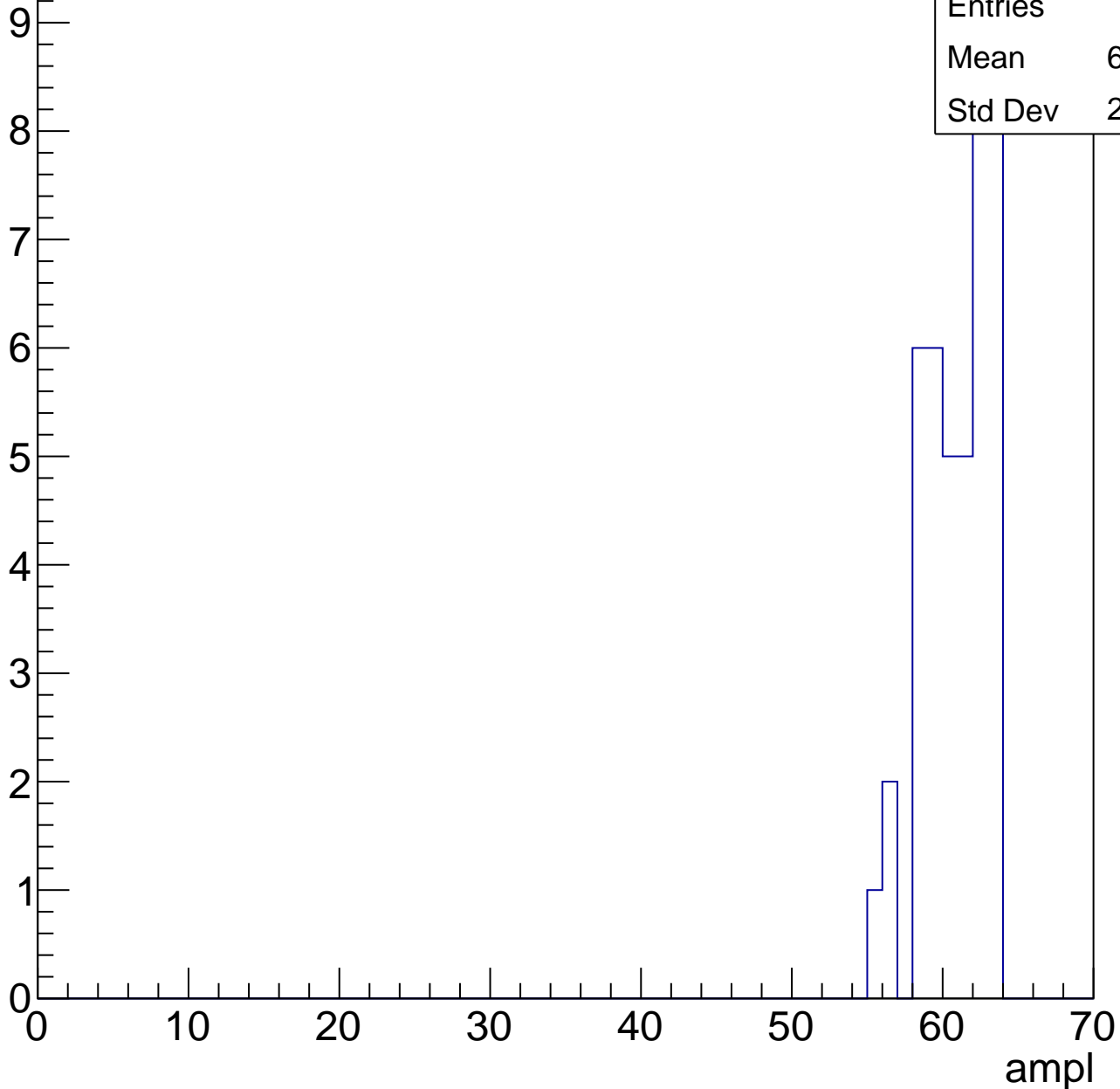


# B0L102S, U17-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

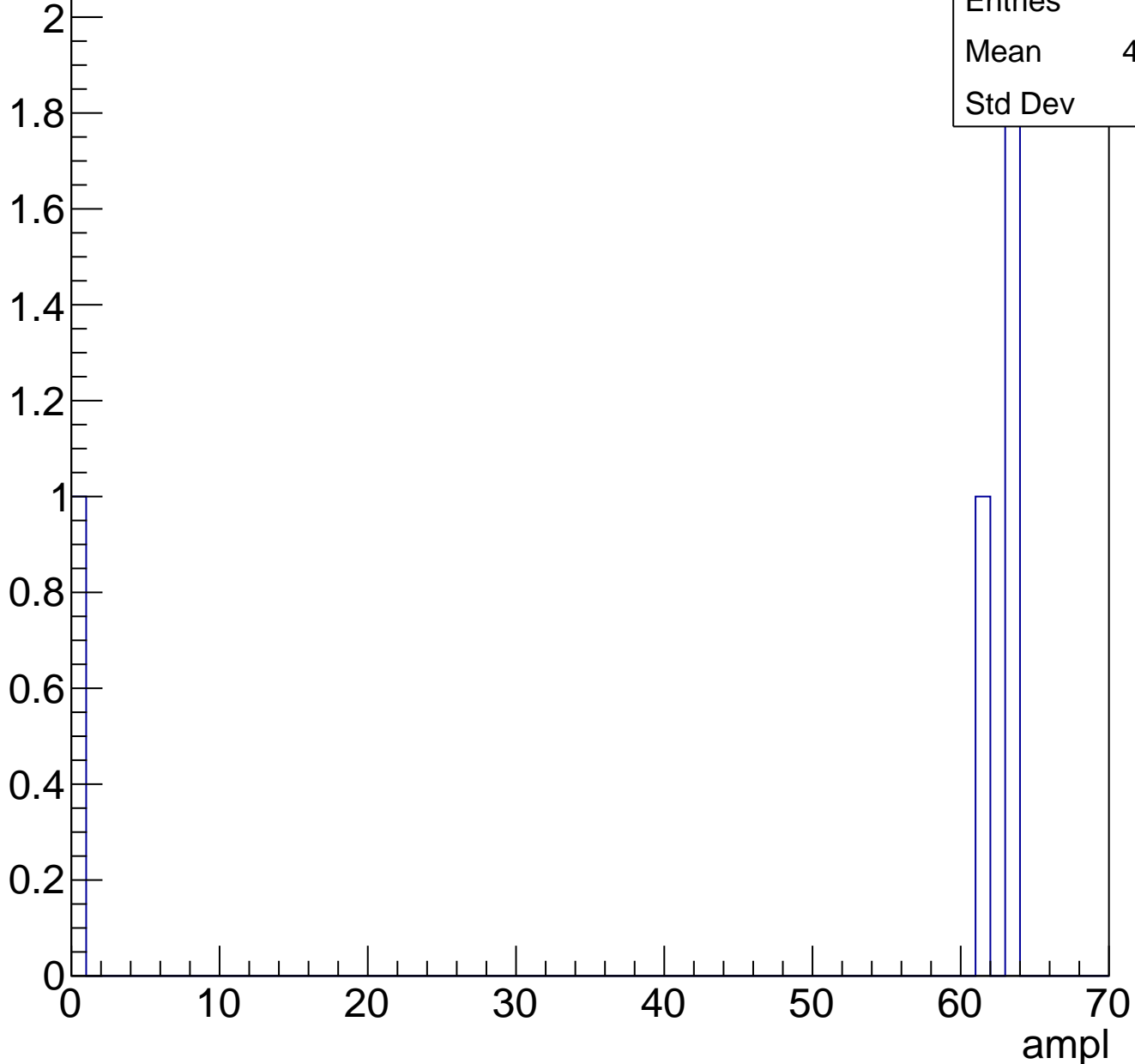
Entries	42
Mean	60.38
Std Dev	2.149



# B0L102S, U17-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



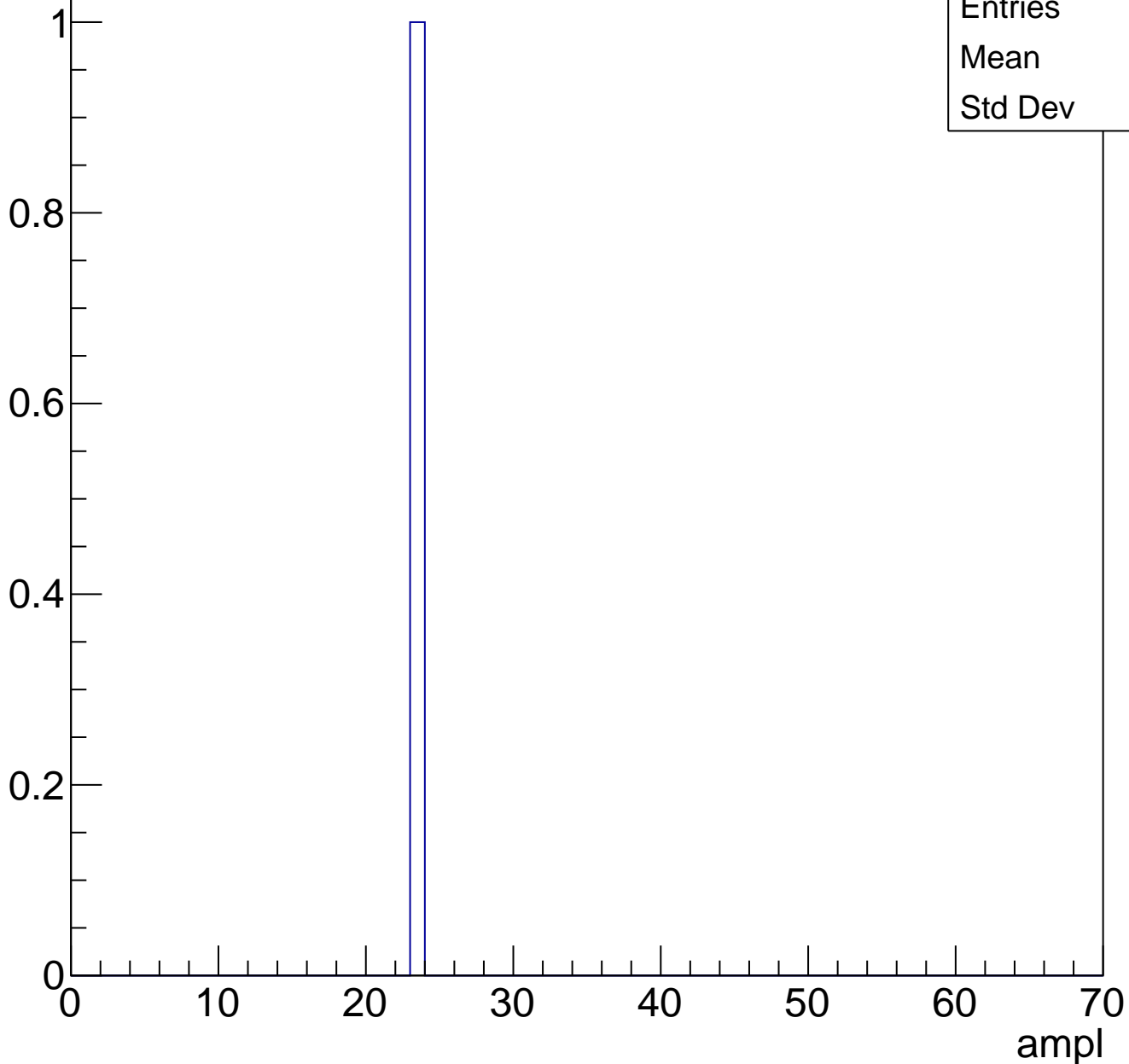
Entries	4
Mean	46.75
Std Dev	27



# B0L102S, U17-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch42, adc0

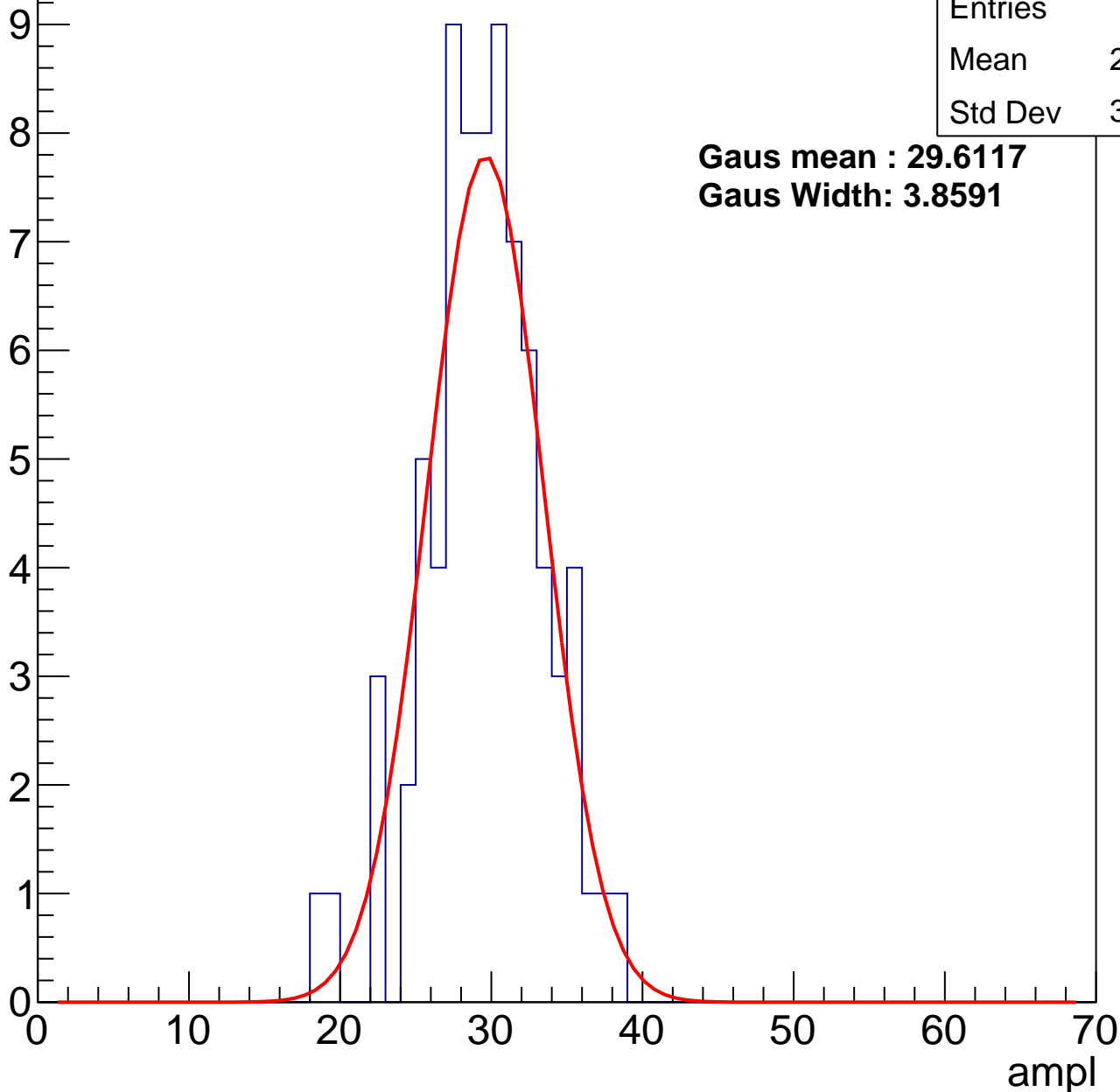
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	29.13
Std Dev	3.856

**Gaus mean : 29.6117**

**Gaus Width: 3.8591**



# B0L102S, U17-ch42, adc1

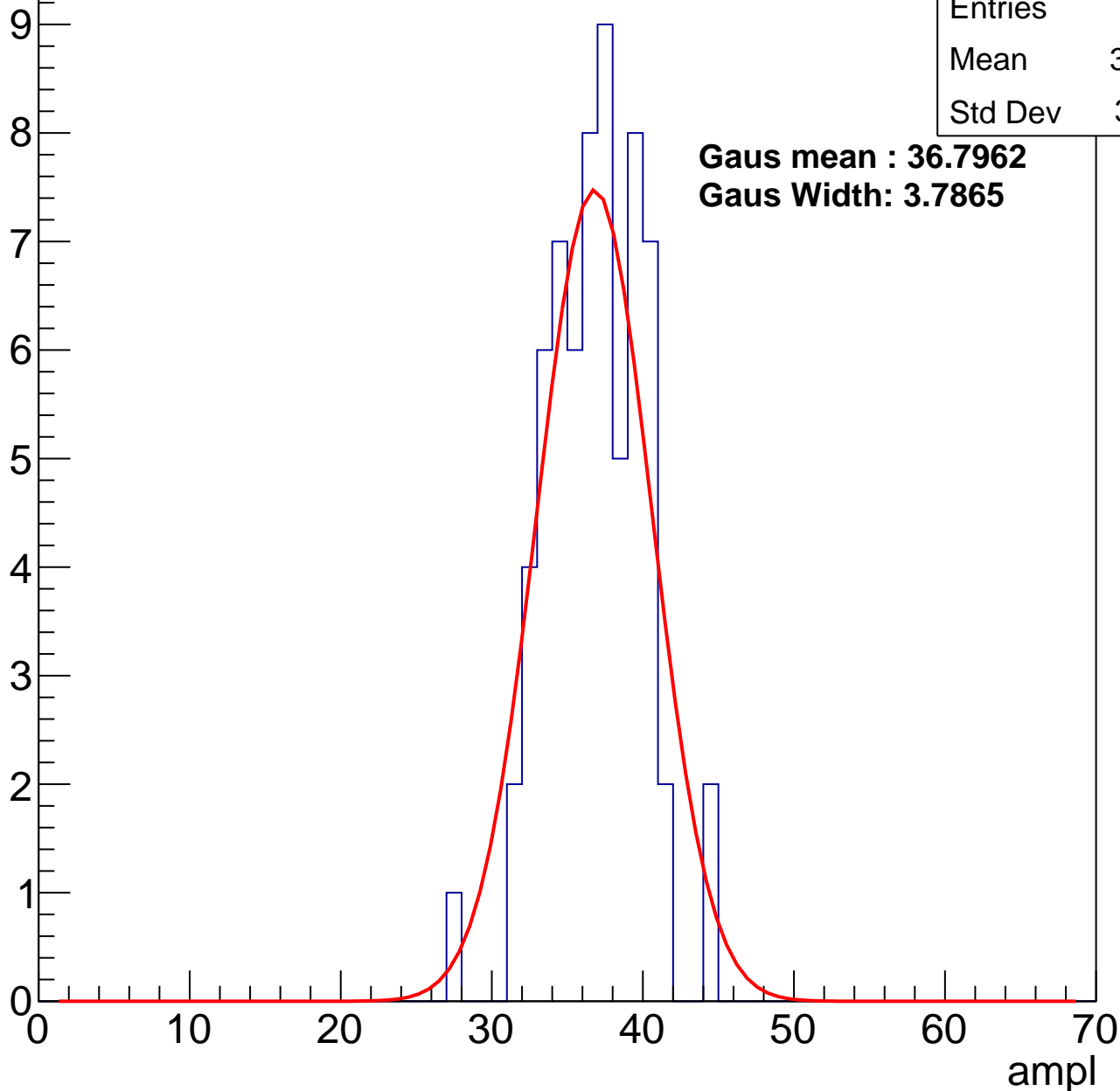
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.36
Std Dev	3.151

**Gaus mean : 36.7962**

**Gaus Width: 3.7865**



# B0L102S, U17-ch42, adc2

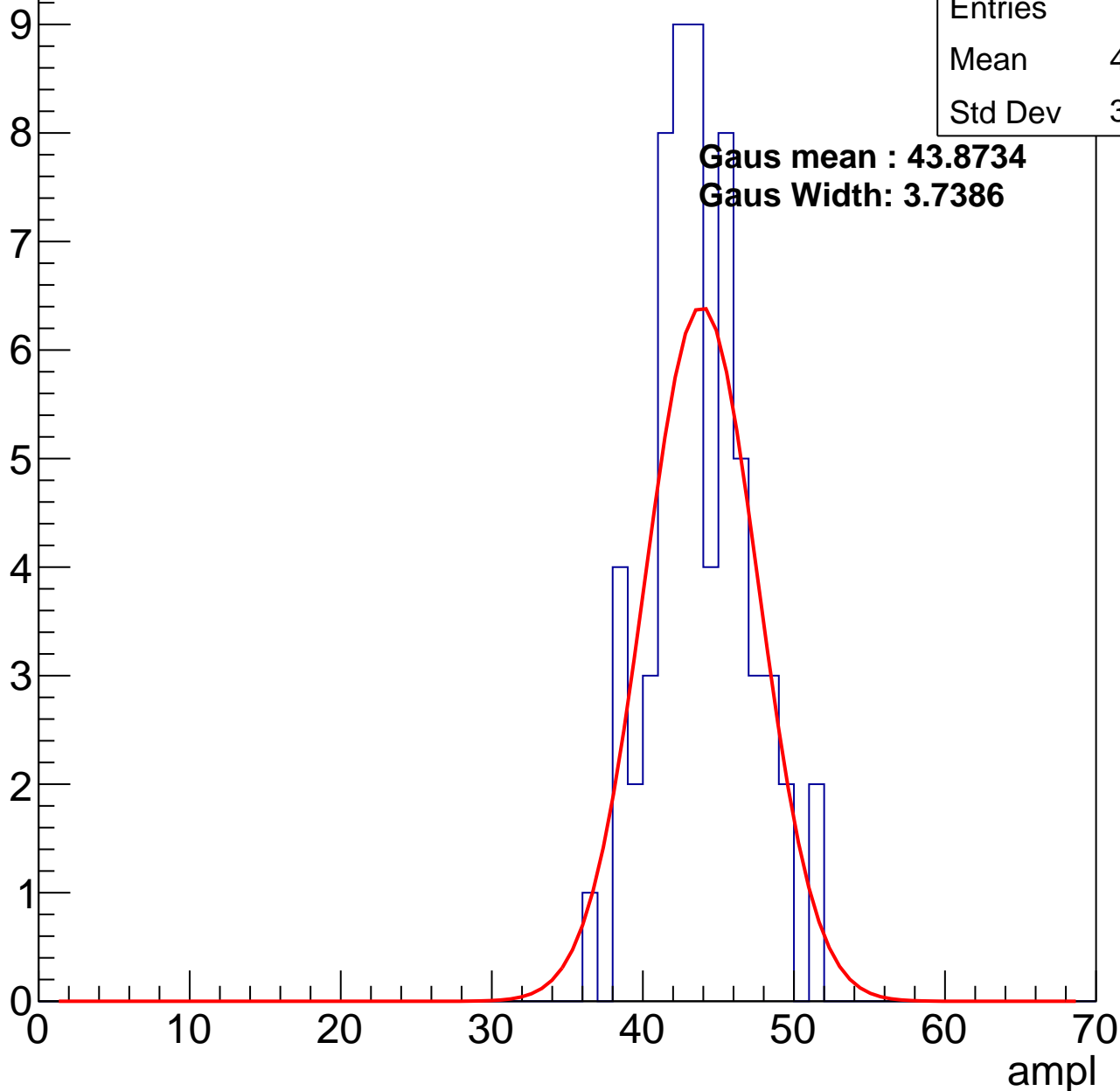
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.33
Std Dev	3.202

**Gaus mean : 43.8734**

**Gaus Width: 3.7386**

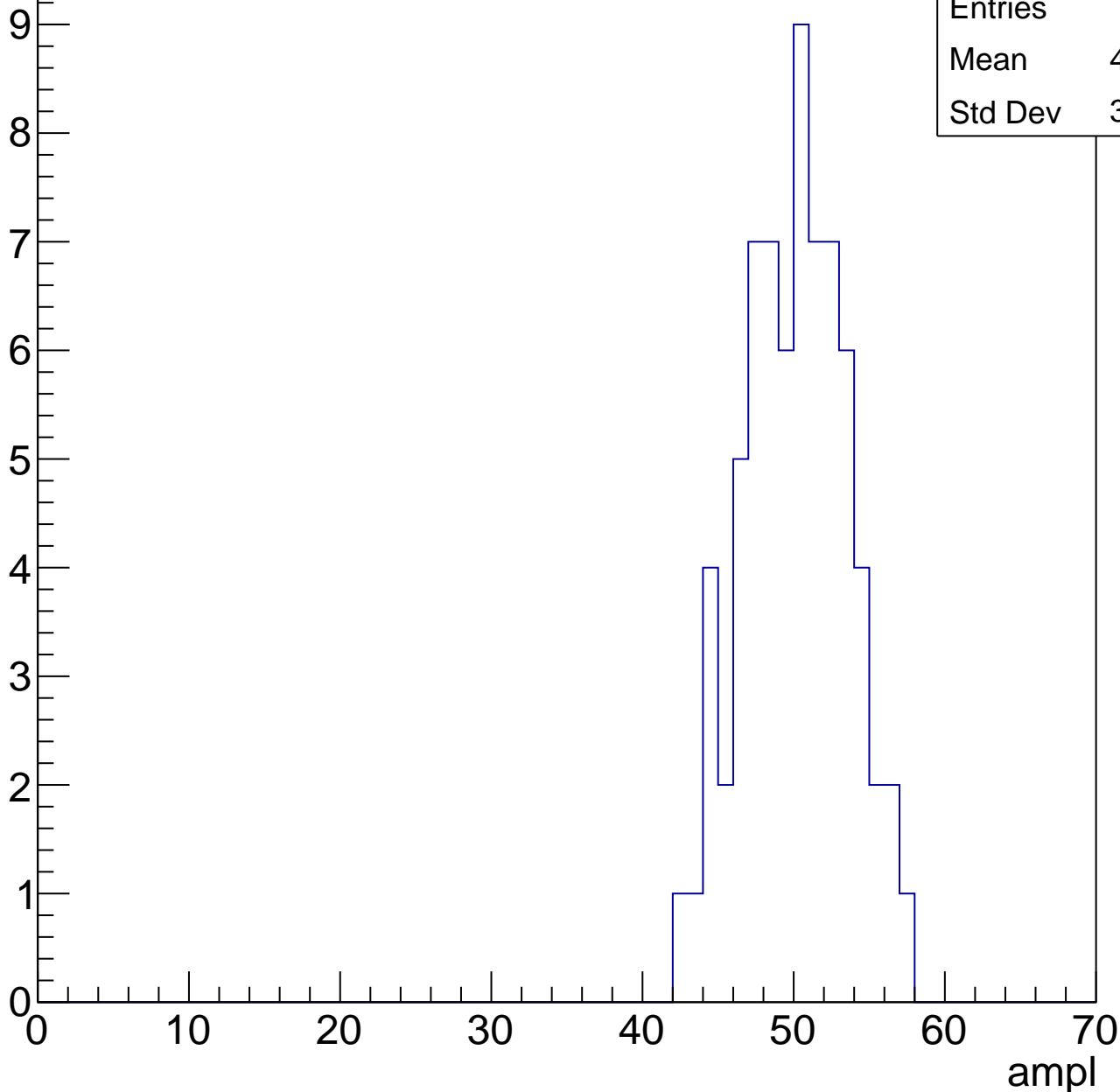


# B0L102S, U17-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	49.63
Std Dev	3.354

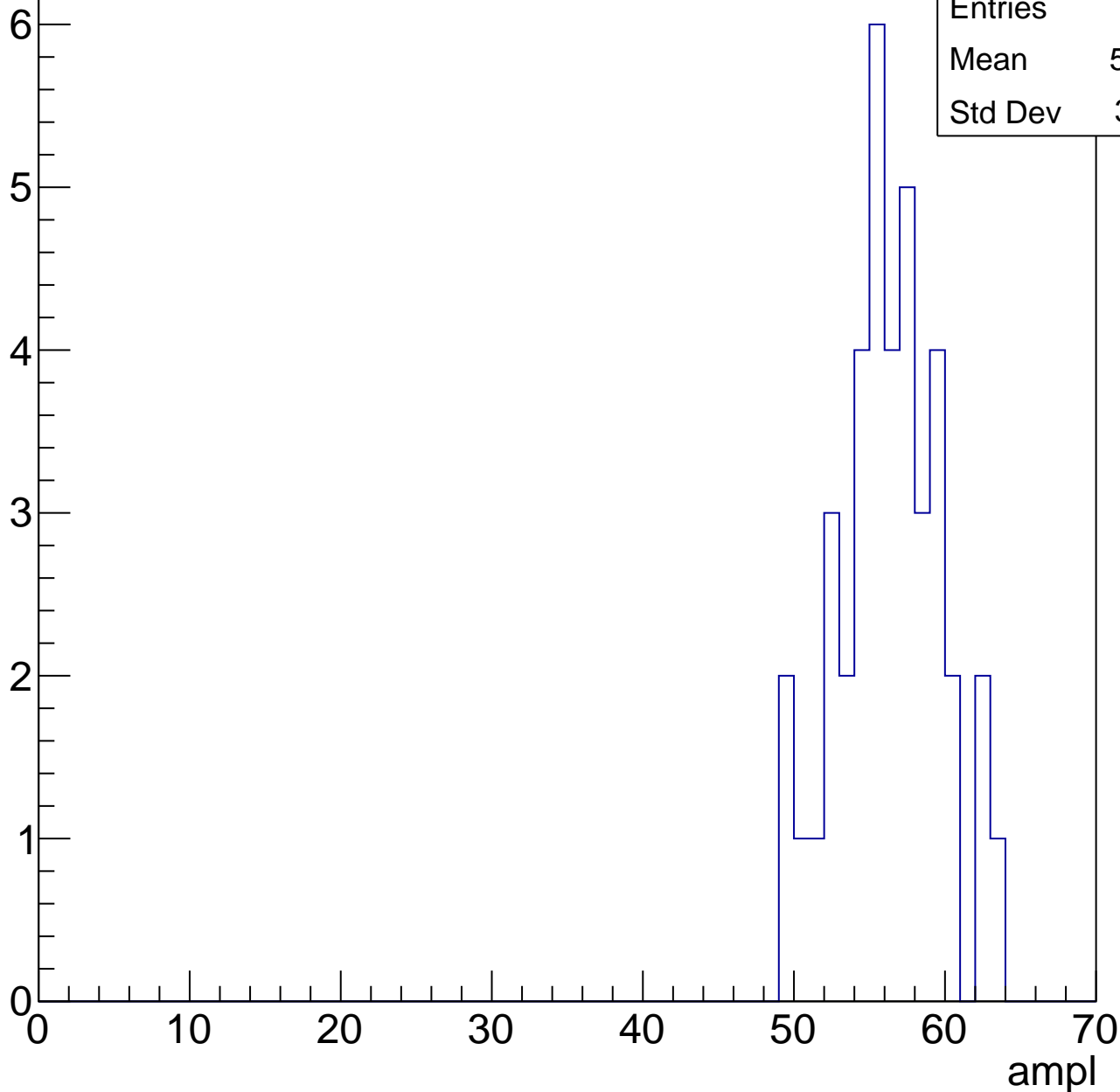


# B0L102S, U17-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	55.83
Std Dev	3.361

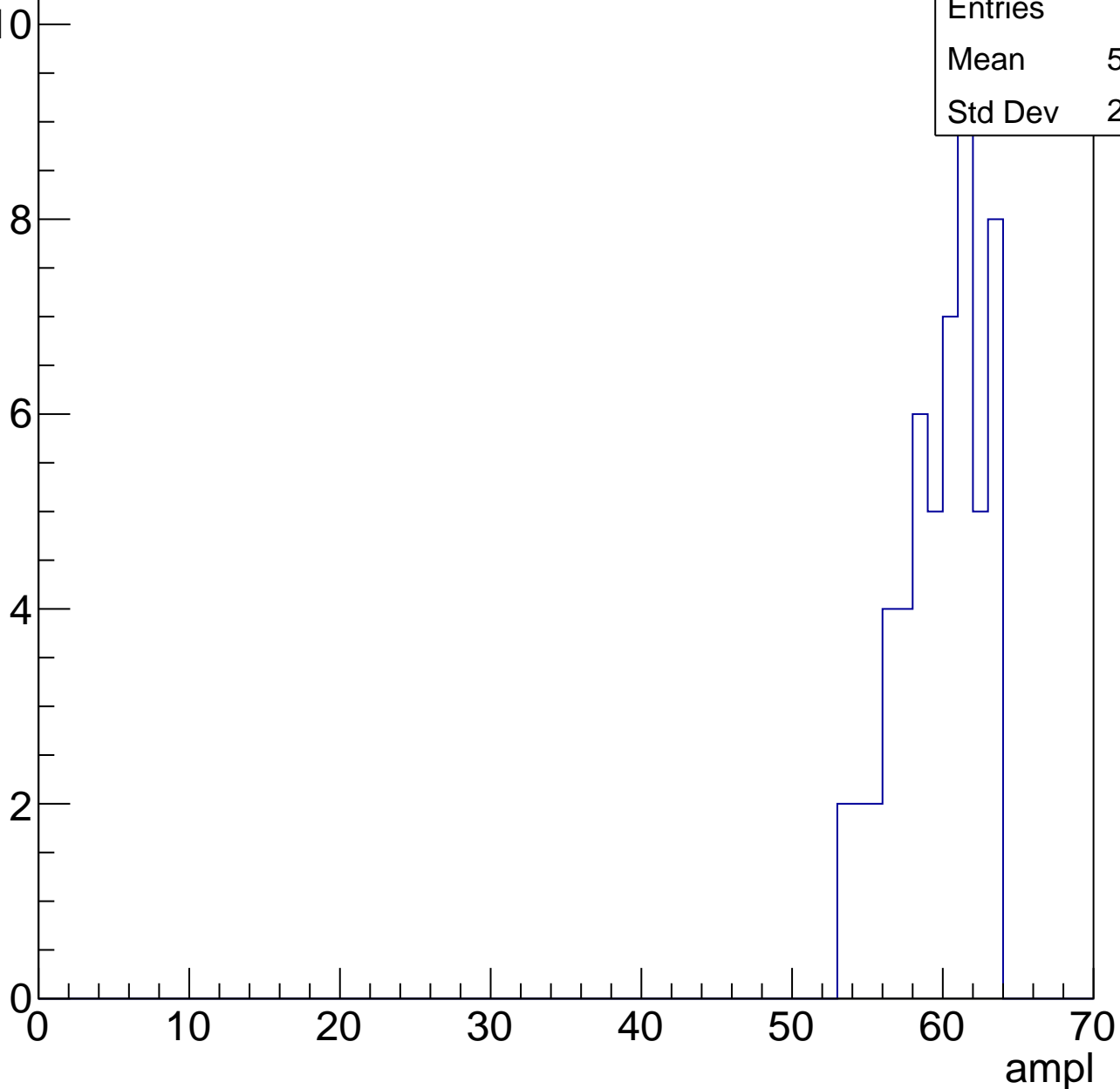


# B0L102S, U17-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

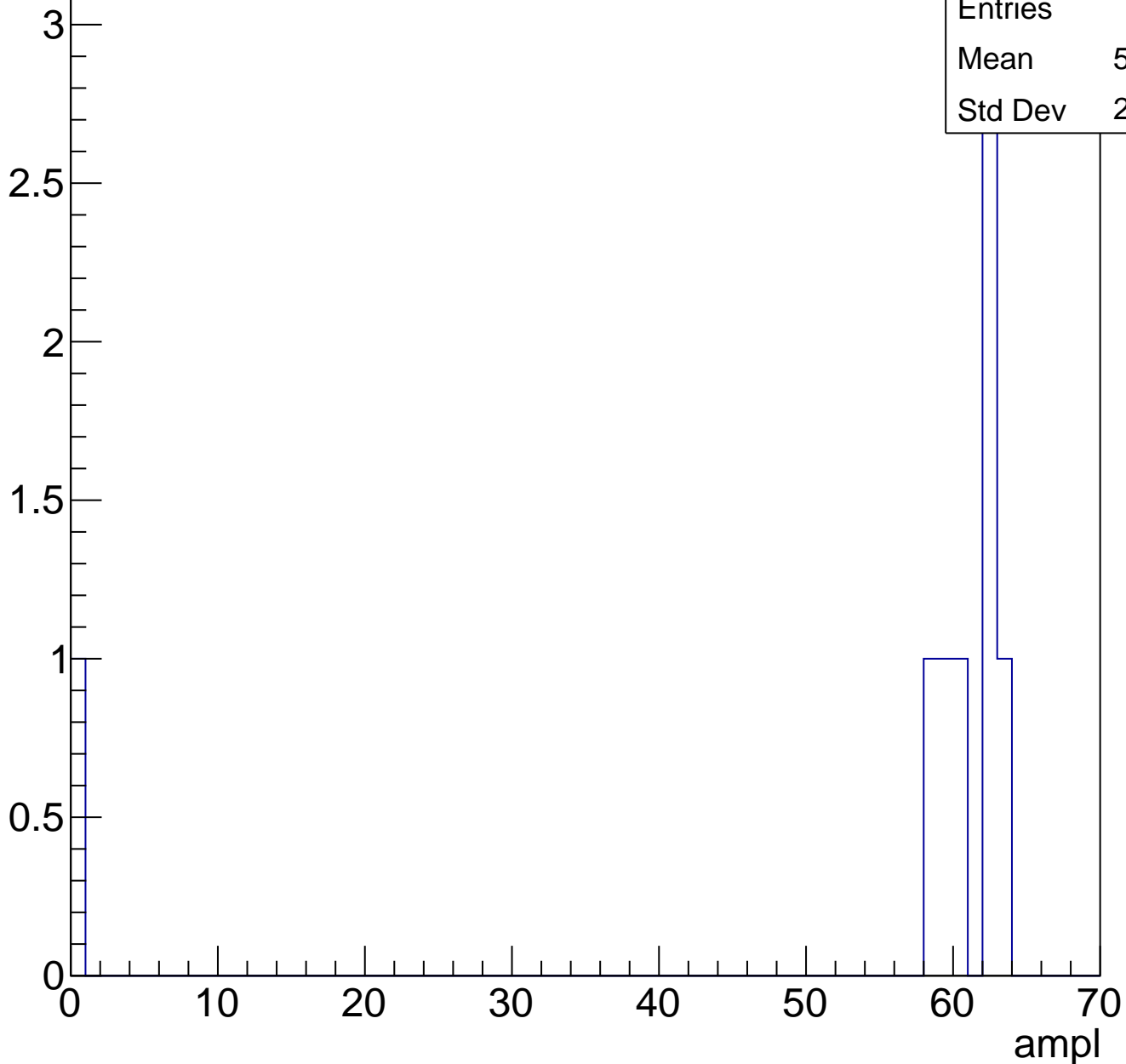
Entries	55
Mean	59.33
Std Dev	2.783



# B0L102S, U17-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch43, adc0

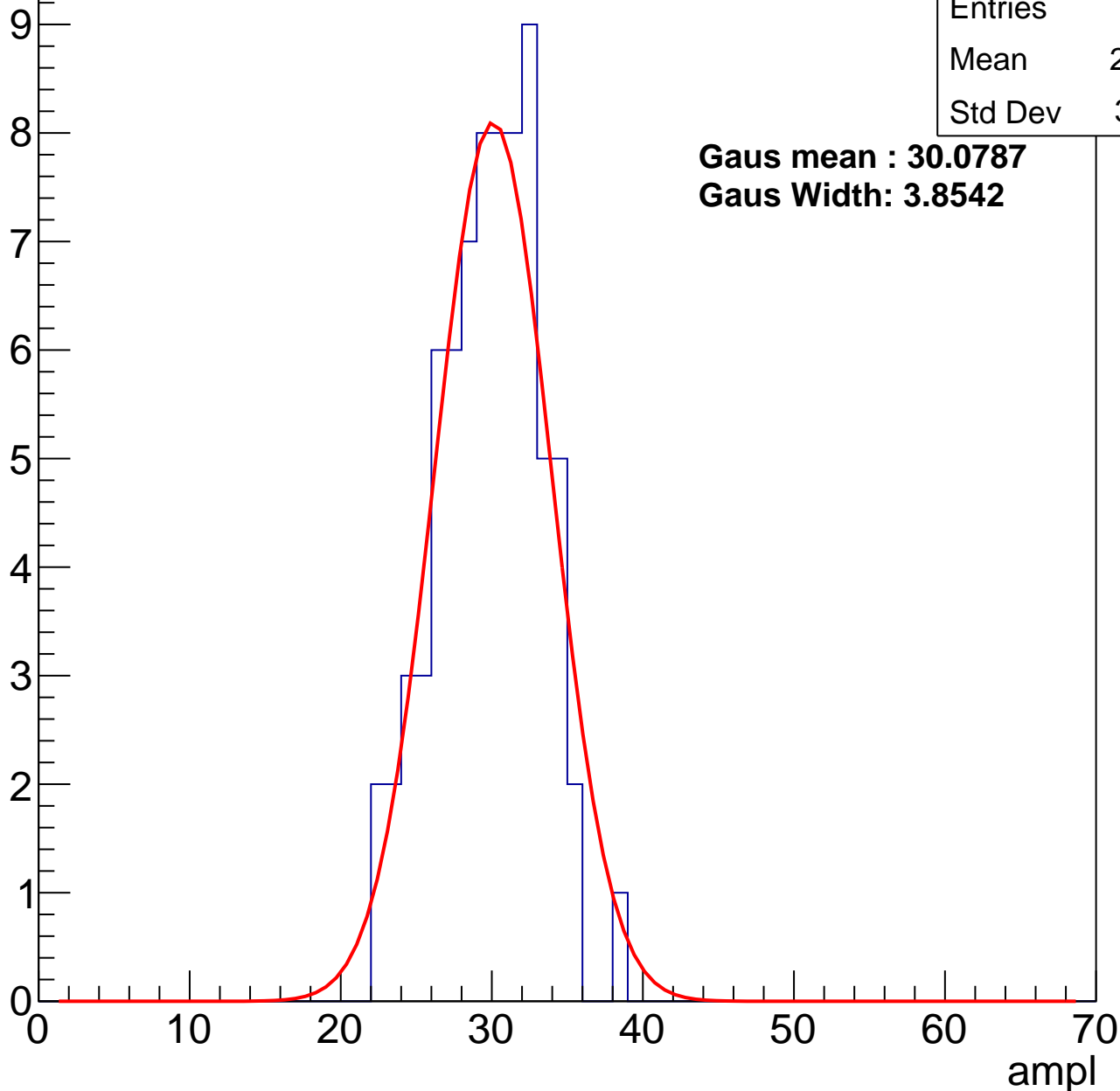
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	29.36
Std Dev	3.361

**Gaus mean : 30.0787**

**Gaus Width: 3.8542**

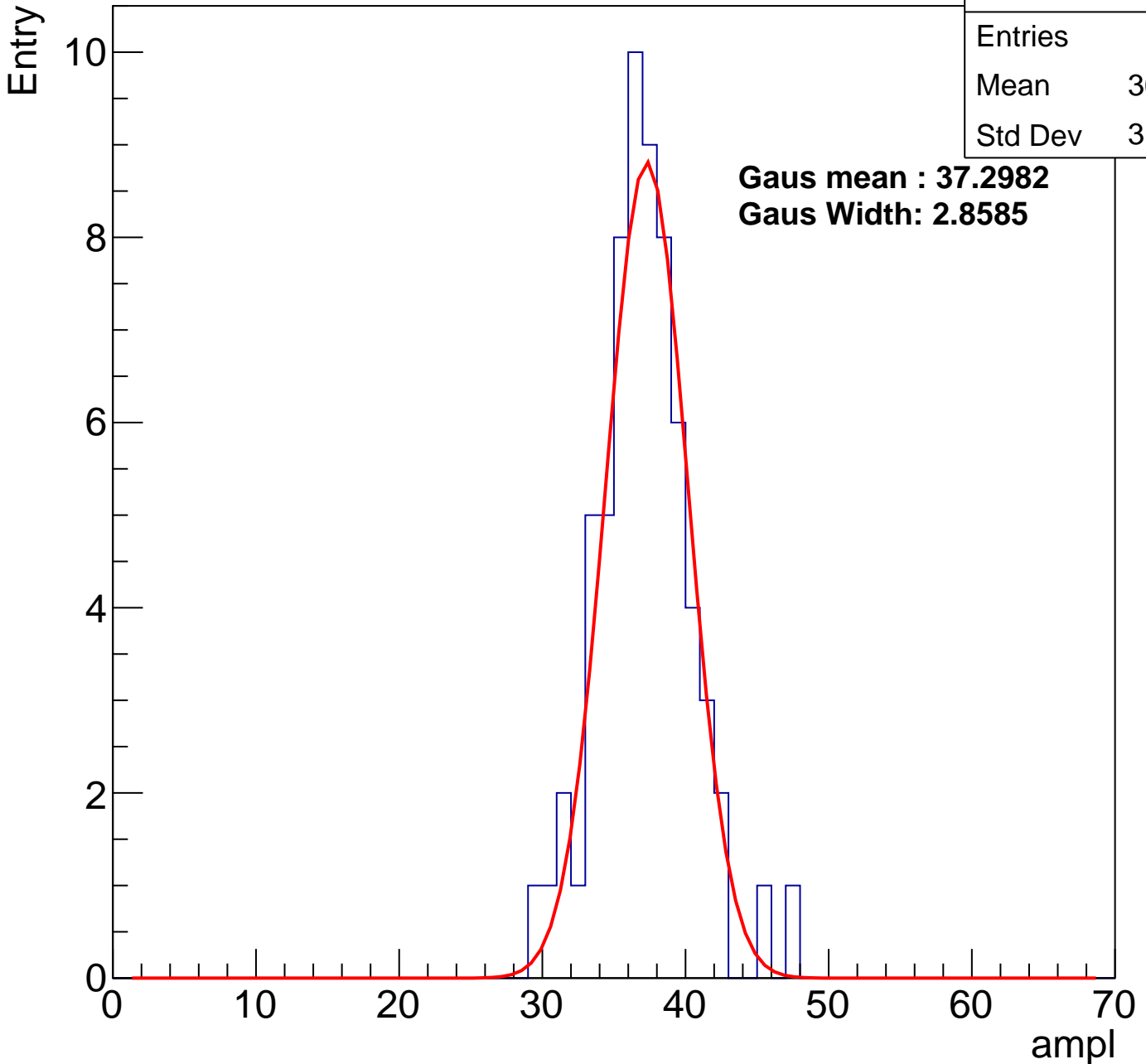


# B0L102S, U17-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	67
Mean	36.69
Std Dev	3.233

**Gaus mean : 37.2982**  
**Gaus Width: 2.8585**



# B0L102S, U17-ch43, adc2

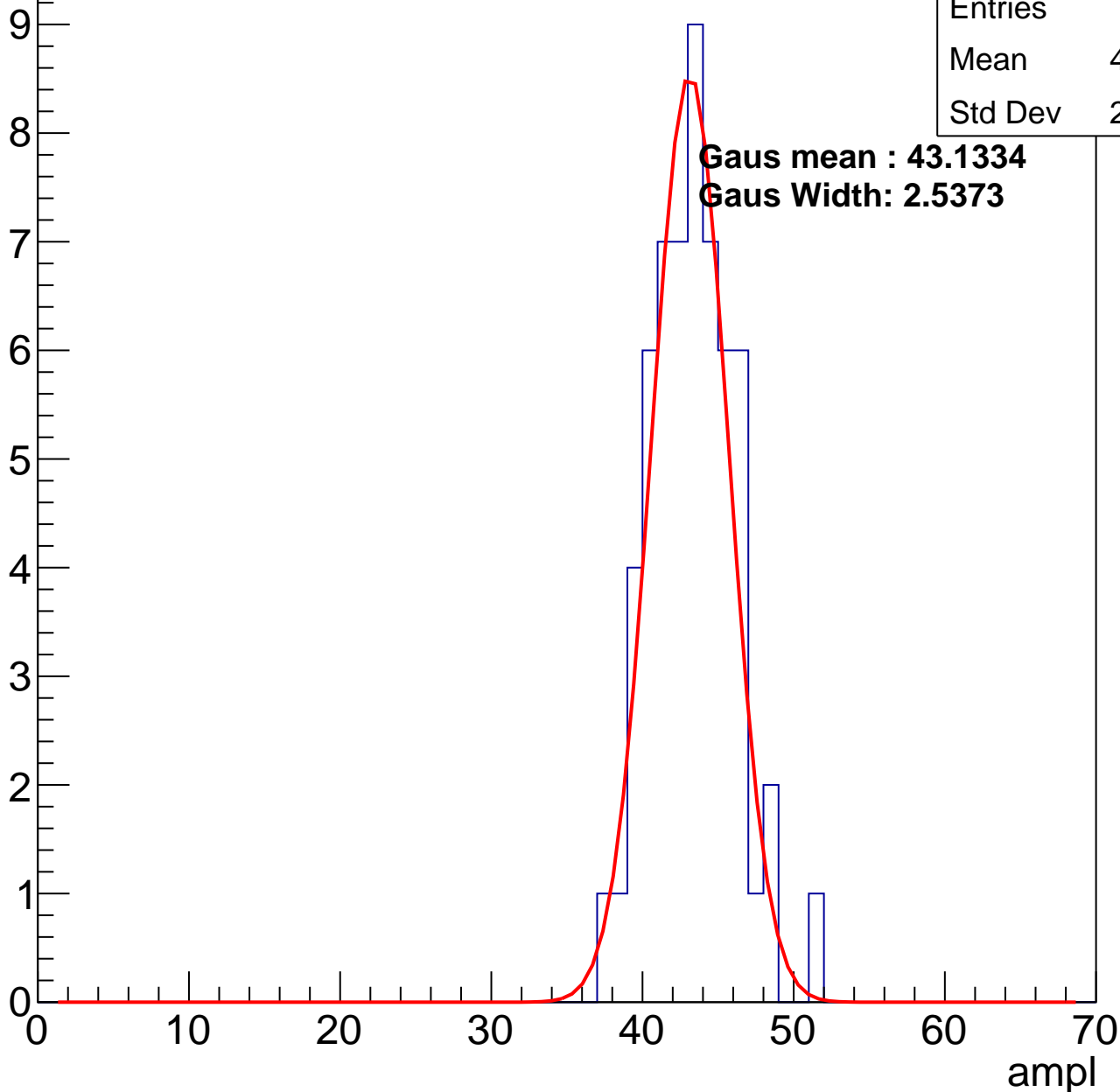
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	42.88
Std Dev	2.717

**Gaus mean : 43.1334**

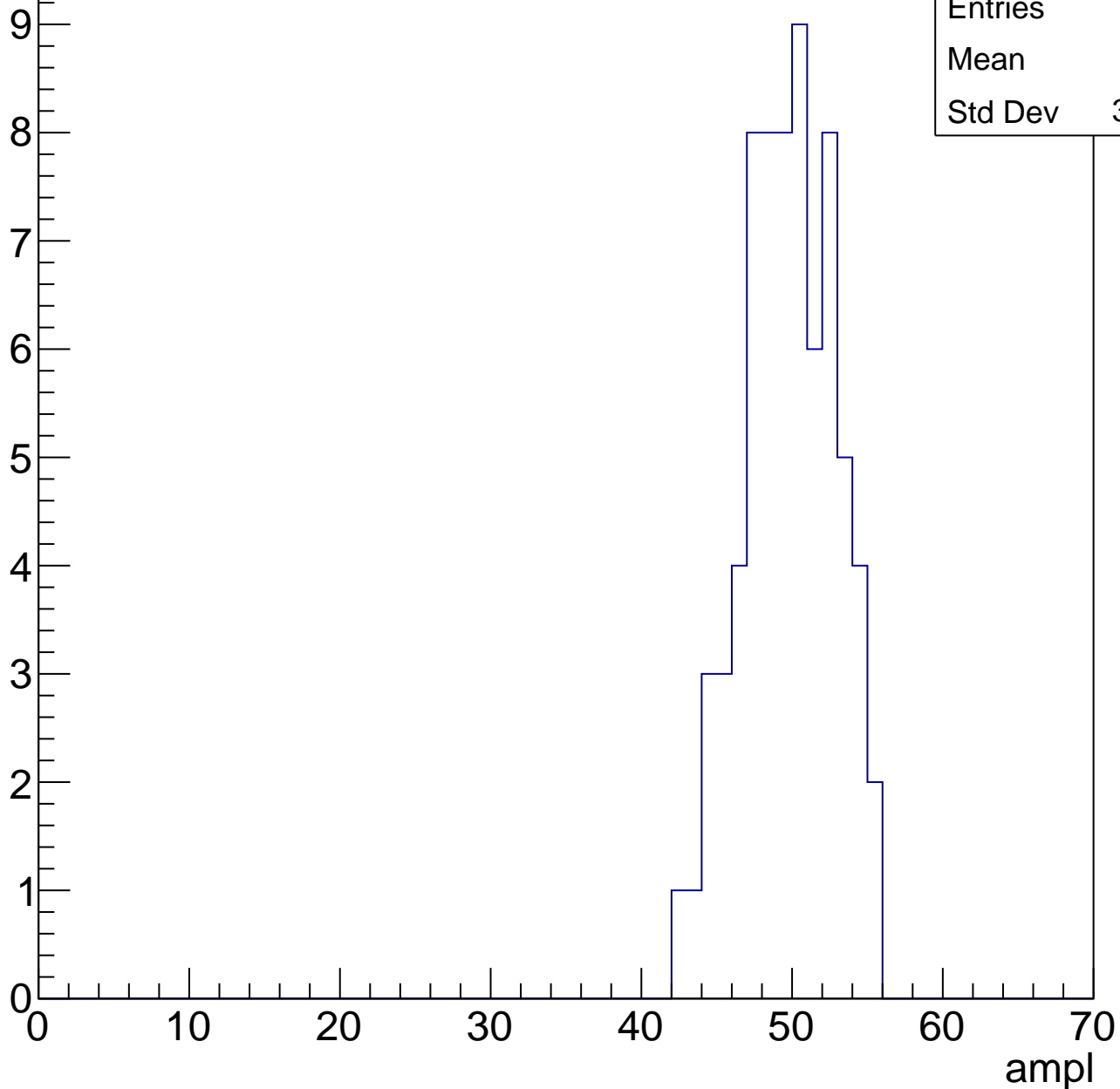
**Gaus Width: 2.5373**



# B0L102S, U17-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

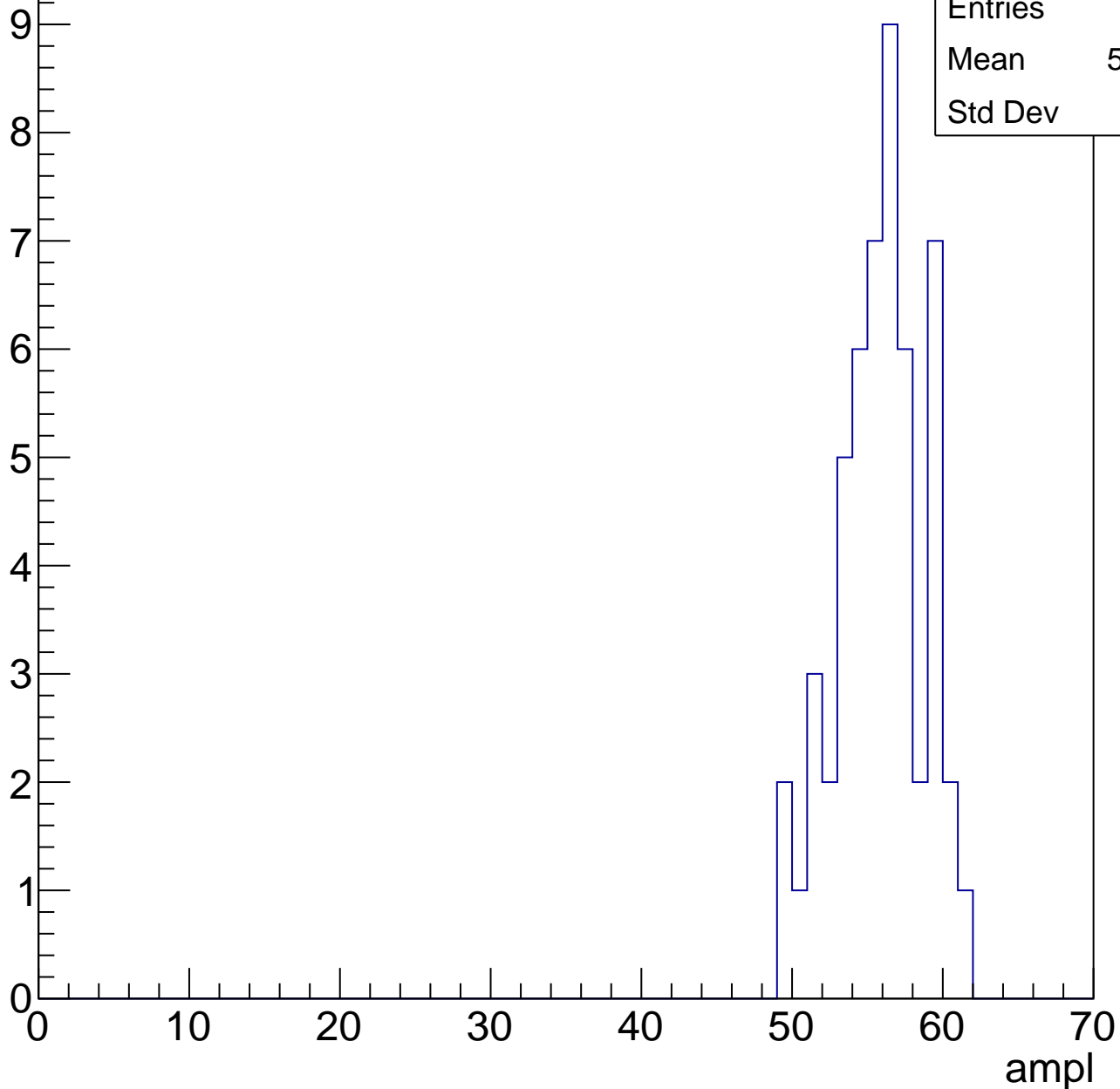
Entry



# B0L102S, U17-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

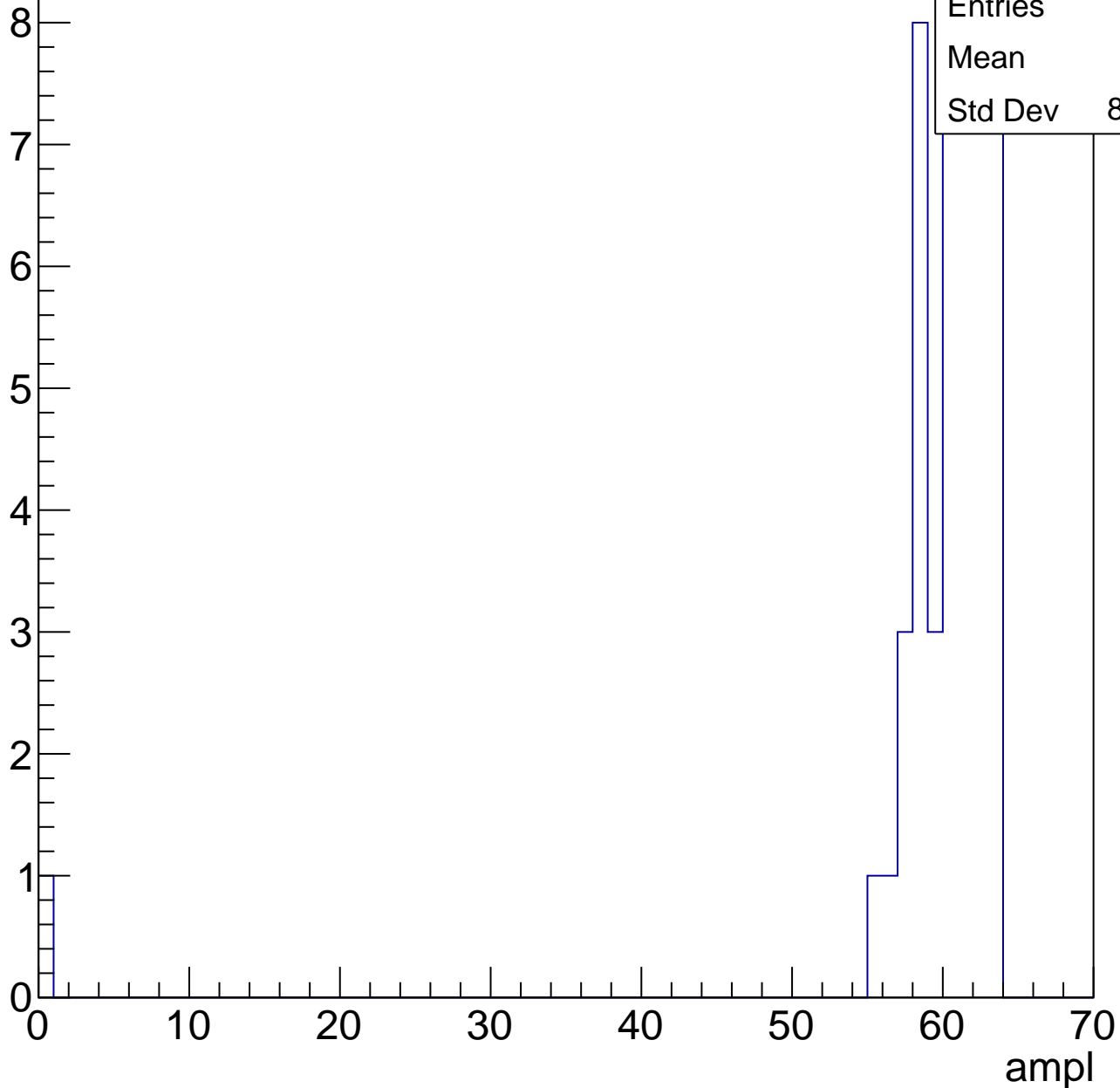


Entries	53
Mean	55.38
Std Dev	2.85

# B0L102S, U17-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

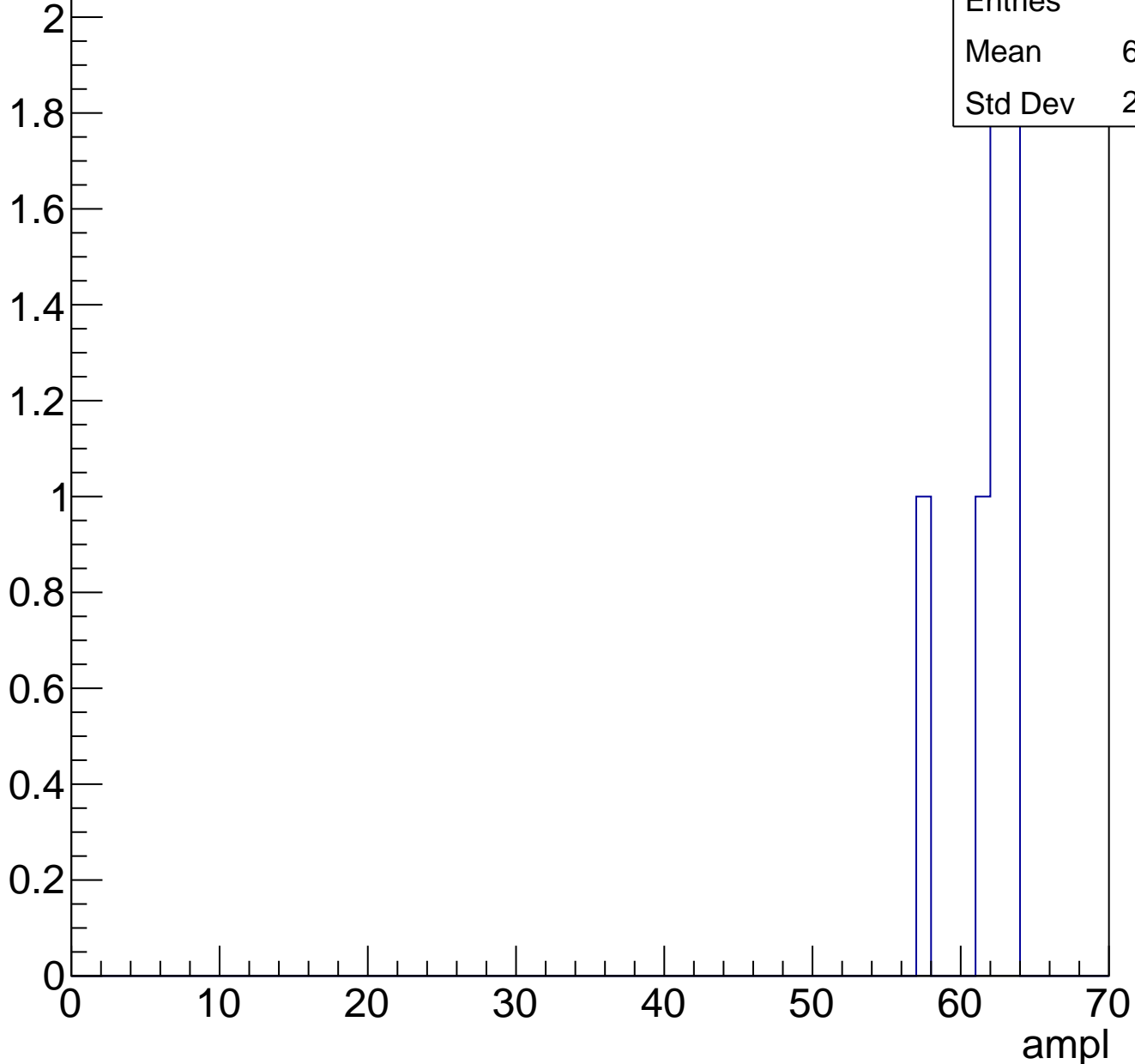
Entry



# B0L102S, U17-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch44, adc0

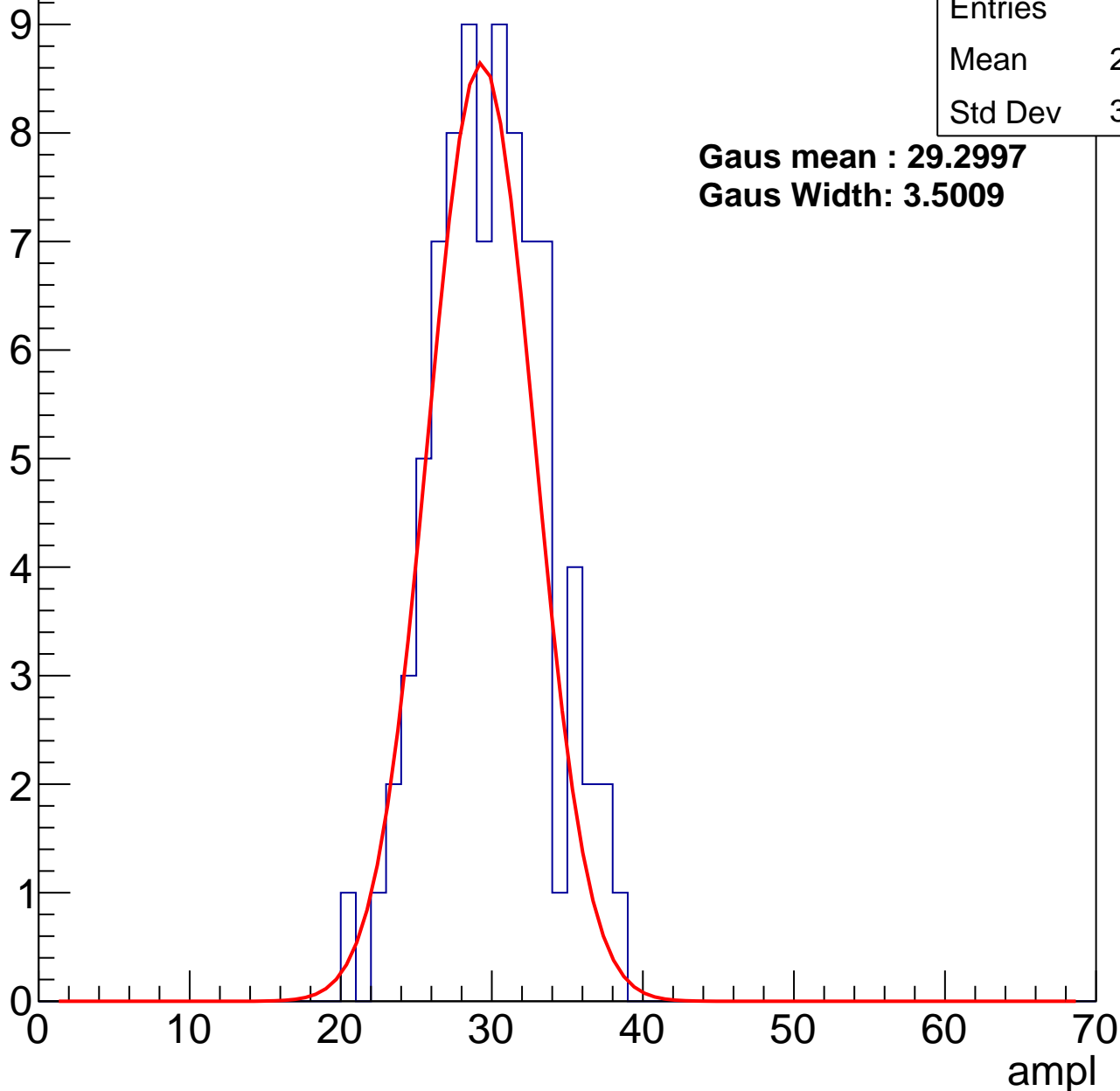
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	29.39
Std Dev	3.684

**Gaus mean : 29.2997**

**Gaus Width: 3.5009**



# B0L102S, U17-ch44, adc1

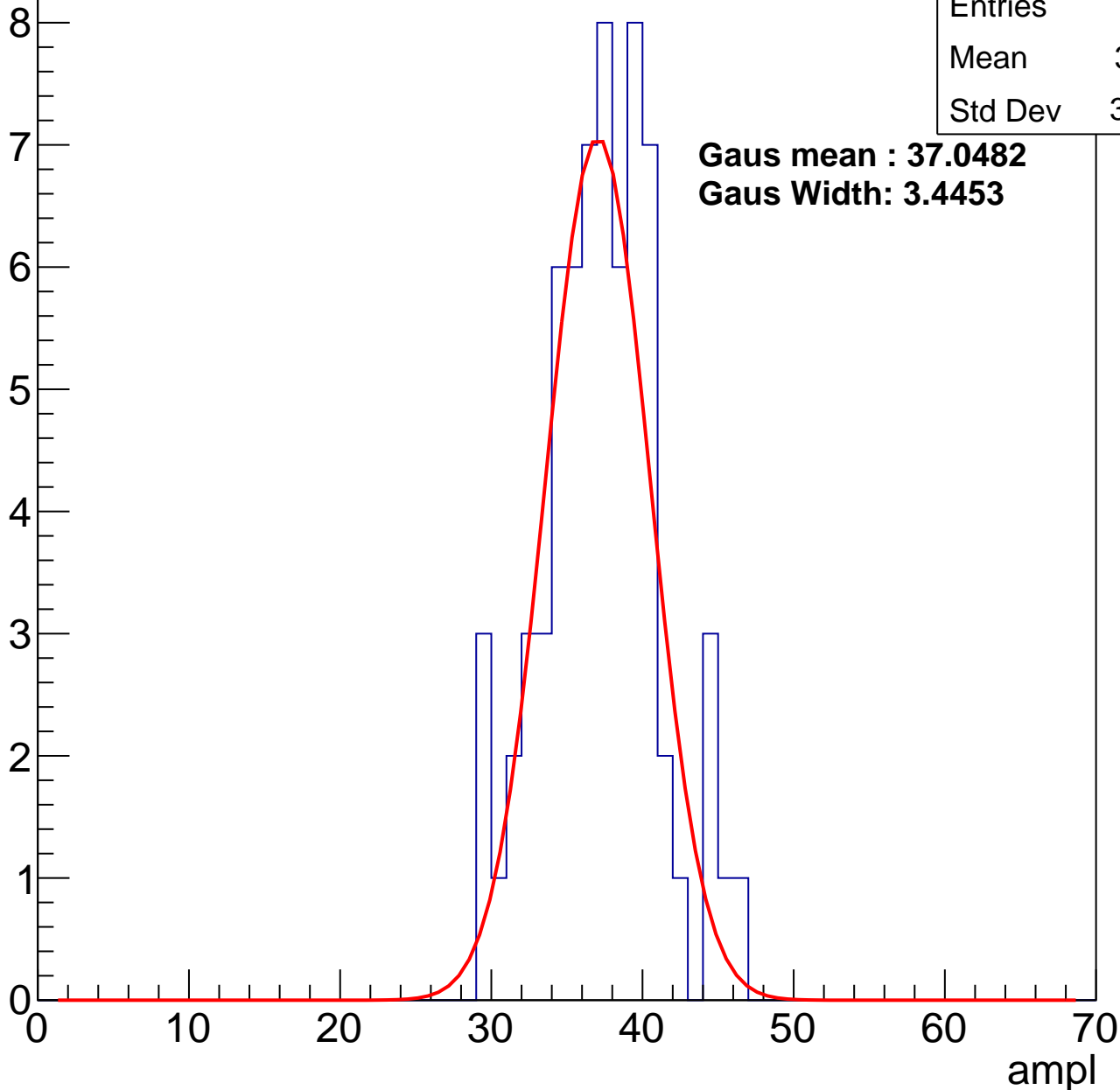
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.81
Std Dev	3.774

**Gaus mean : 37.0482**

**Gaus Width: 3.4453**



# B0L102S, U17-ch44, adc2

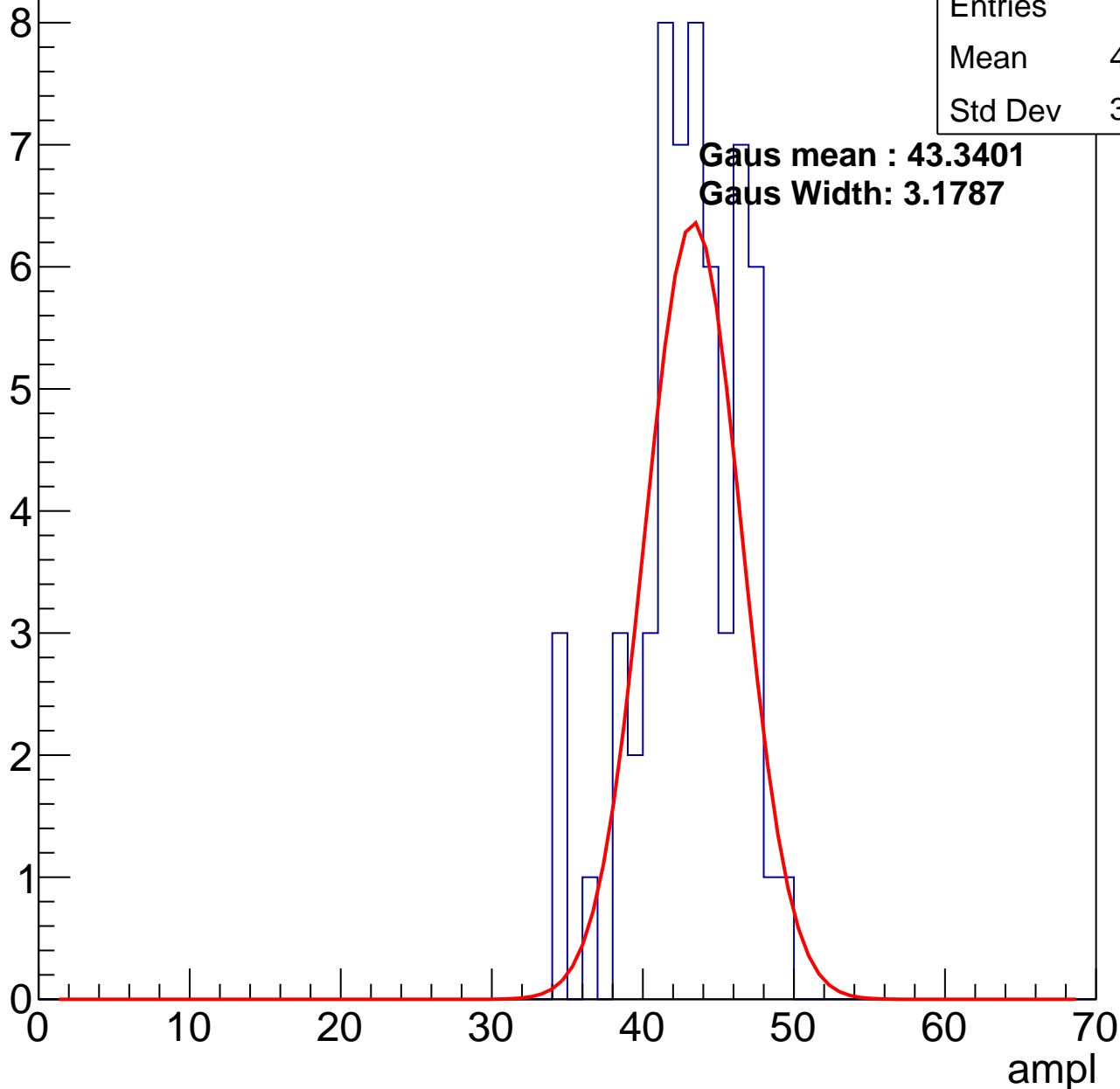
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.64
Std Dev	3.433

**Gaus mean : 43.3401**

**Gaus Width: 3.1787**

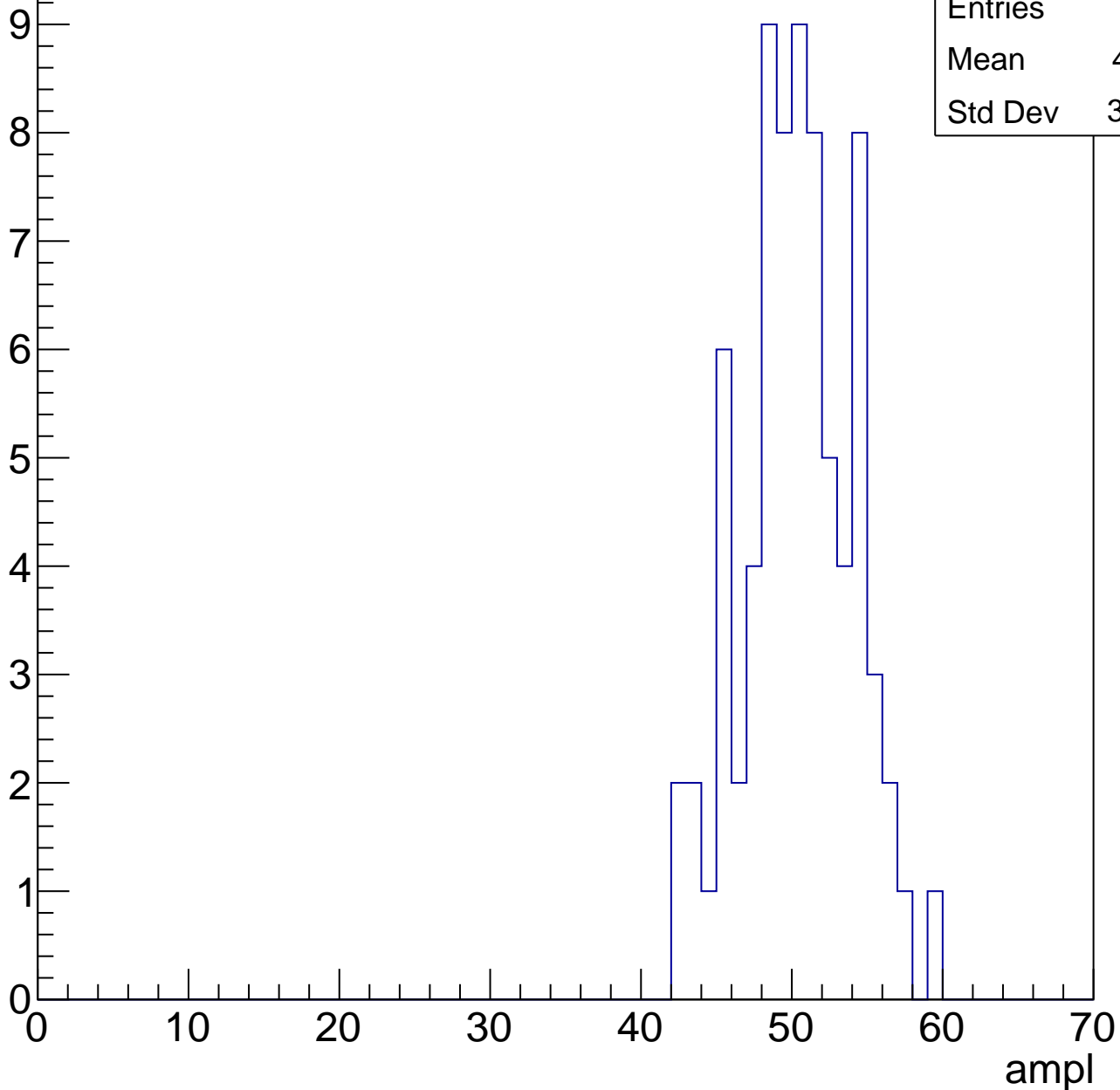


# B0L102S, U17-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	49.91
Std Dev	3.637

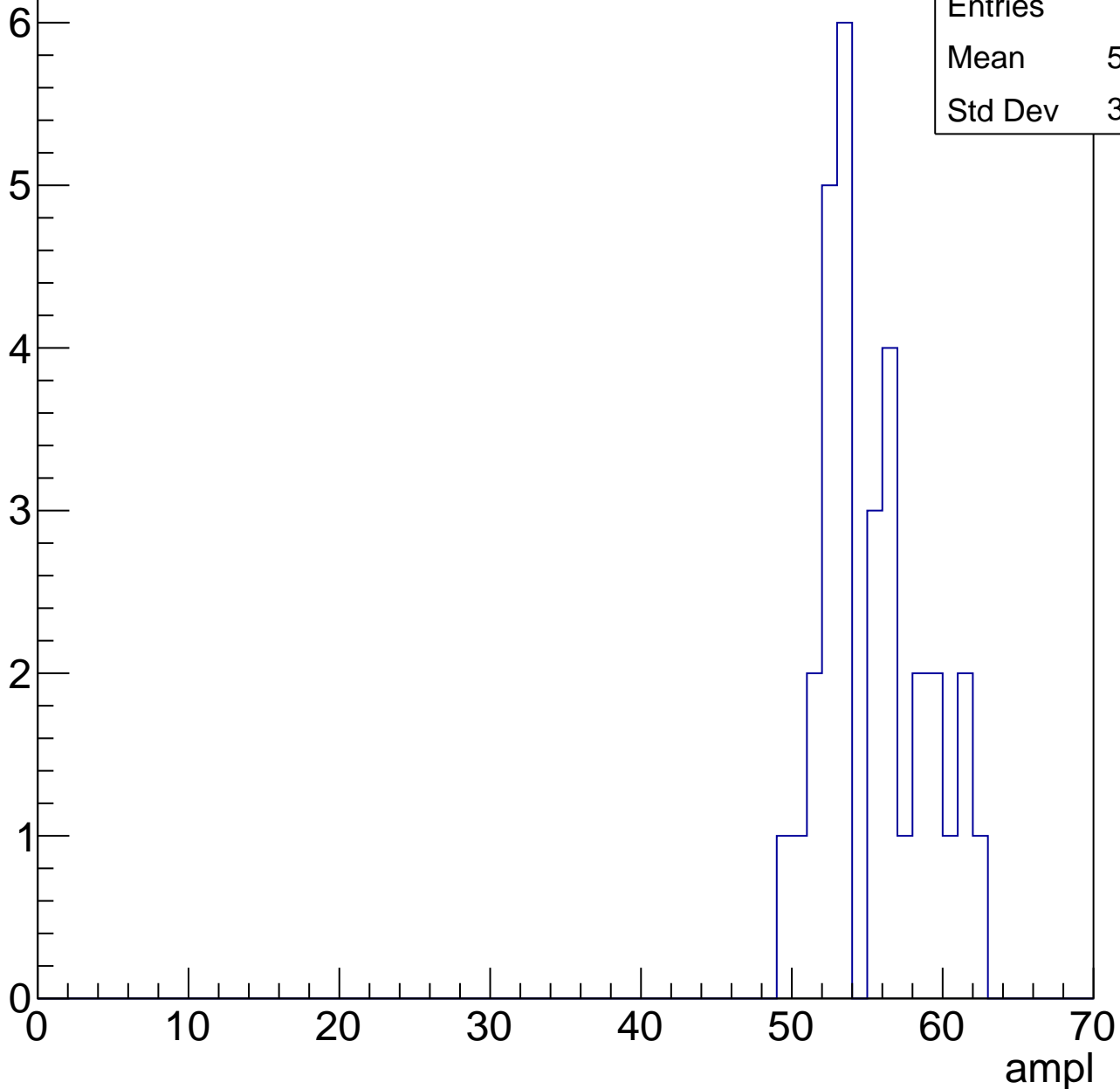


# B0L102S, U17-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	31
Mean	54.94
Std Dev	3.435



# B0L102S, U17-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	58.1
Std Dev	7.832

ampl

0

10

20

30

40

50

60

70

# B0L102S, U17-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch45, adc0

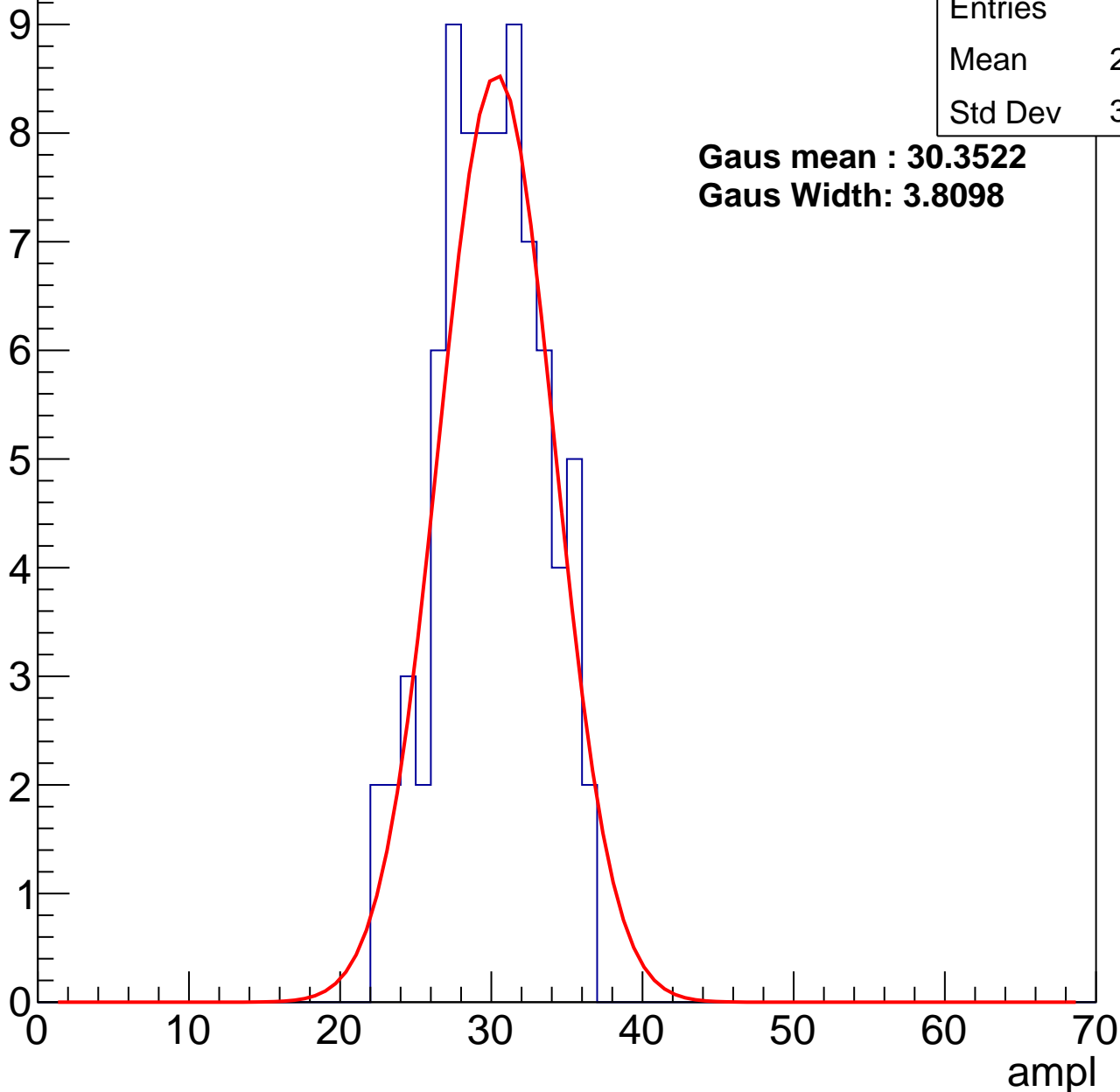
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.52
Std Dev	3.396

**Gaus mean : 30.3522**

**Gaus Width: 3.8098**



# B0L102S, U17-ch45, adc1

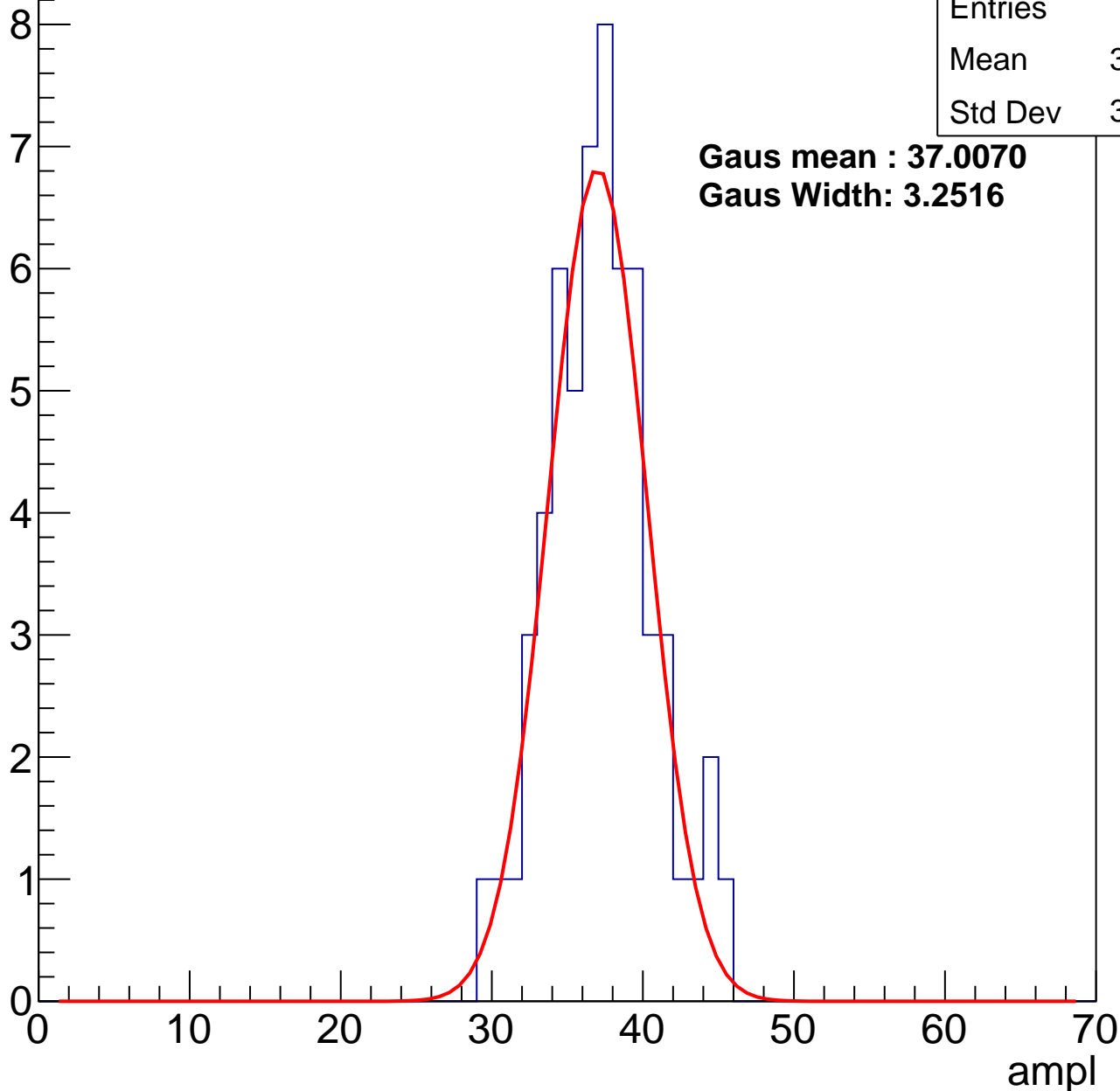
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	36.75
Std Dev	3.428

**Gaus mean : 37.0070**

**Gaus Width: 3.2516**



# B0L102S, U17-ch45, adc2

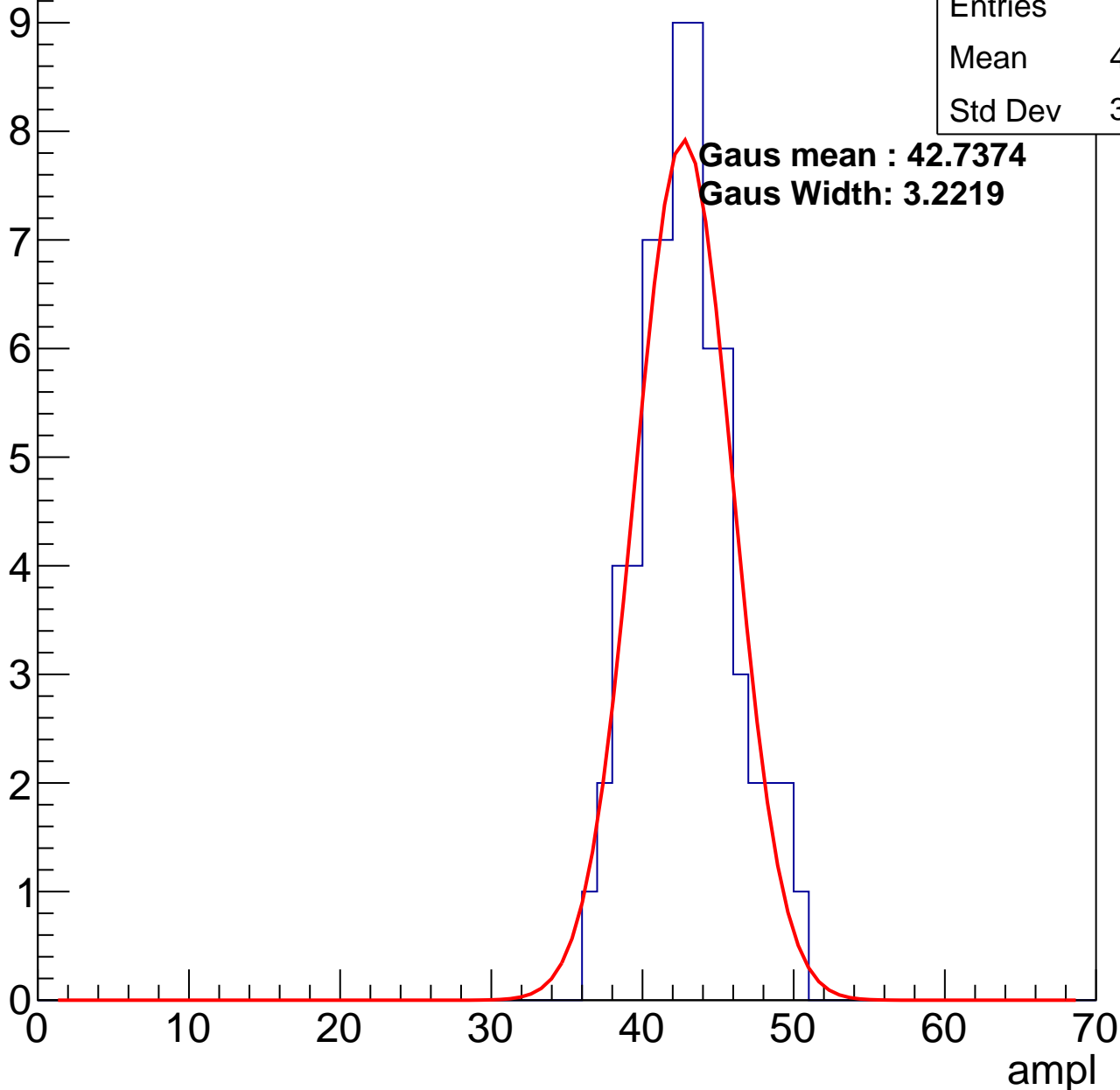
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.46
Std Dev	3.104

**Gaus mean : 42.7374**

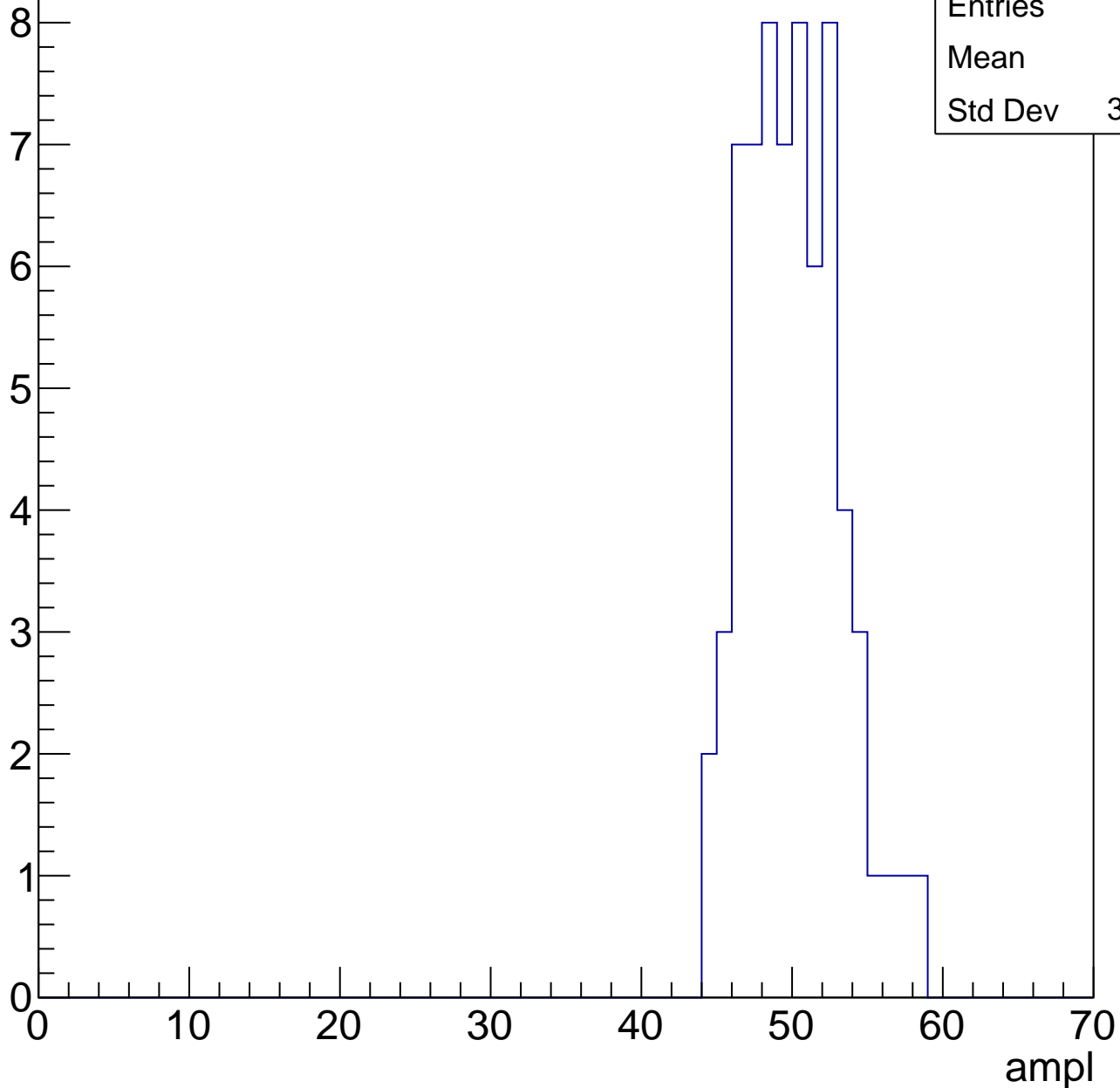
**Gaus Width: 3.2219**



# B0L102S, U17-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

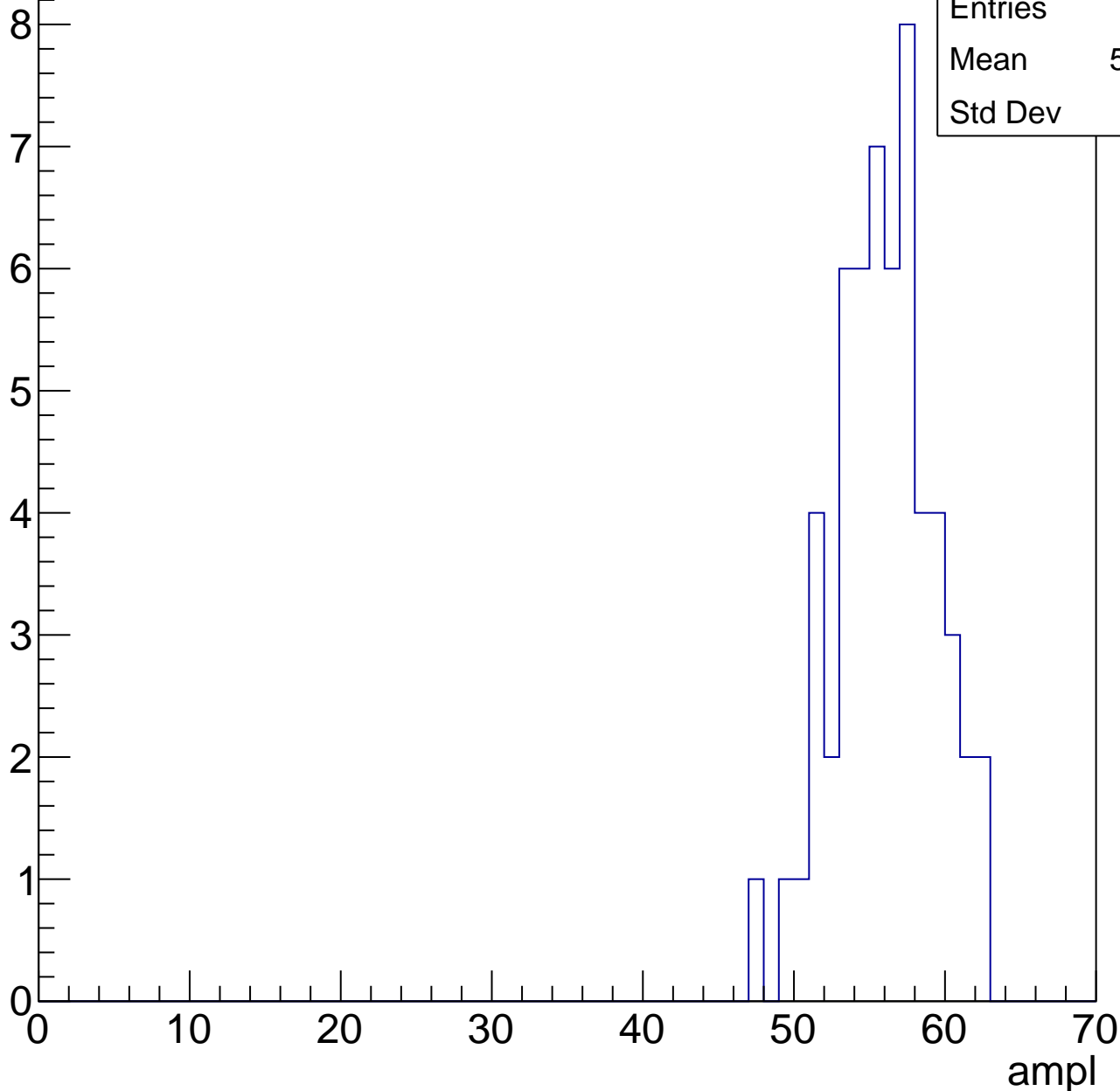


# B0L102S, U17-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	55.56
Std Dev	3.25

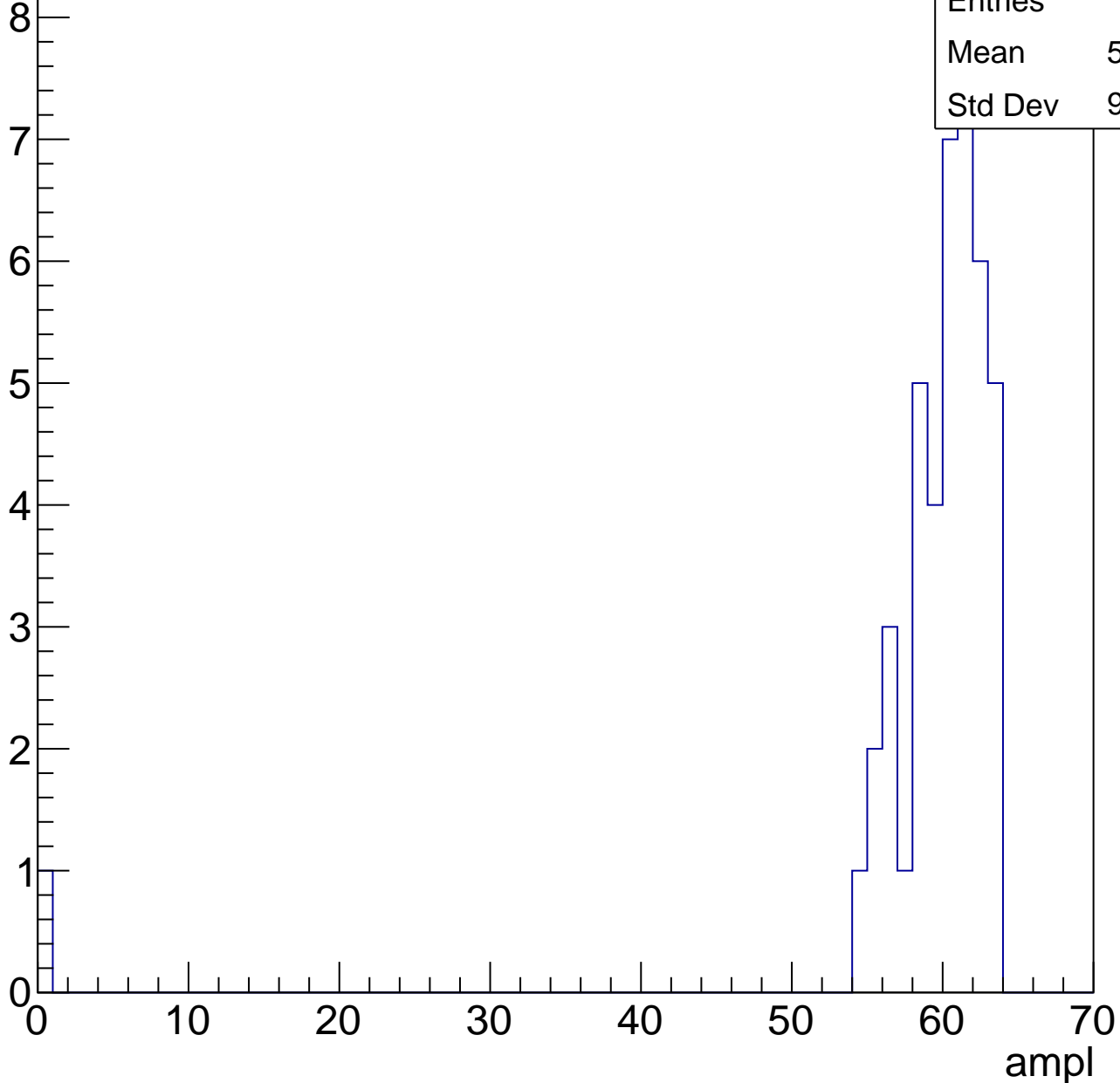


# B0L102S, U17-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

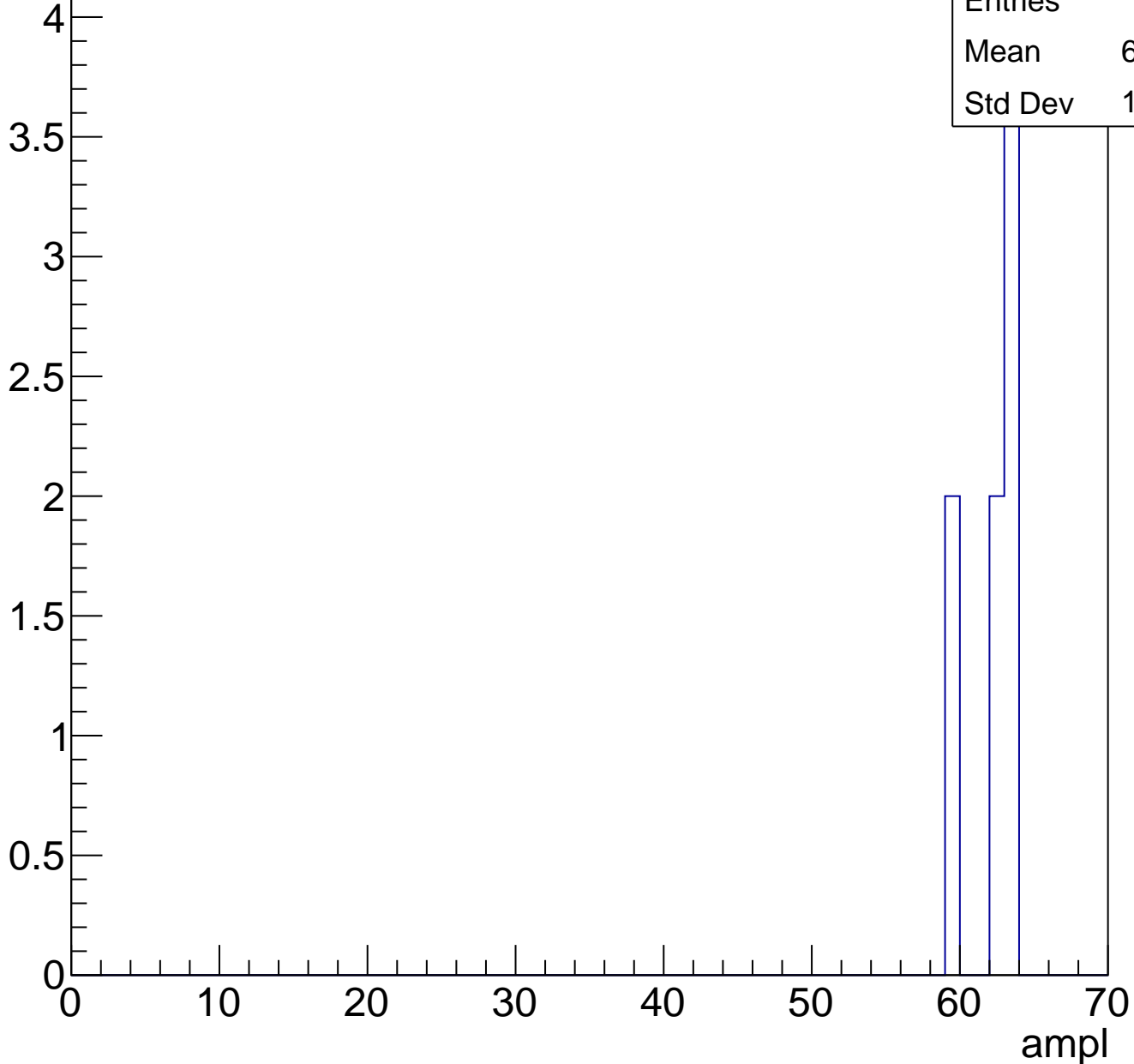
Entries	43
Mean	58.37
Std Dev	9.314



# B0L102S, U17-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch46, adc0

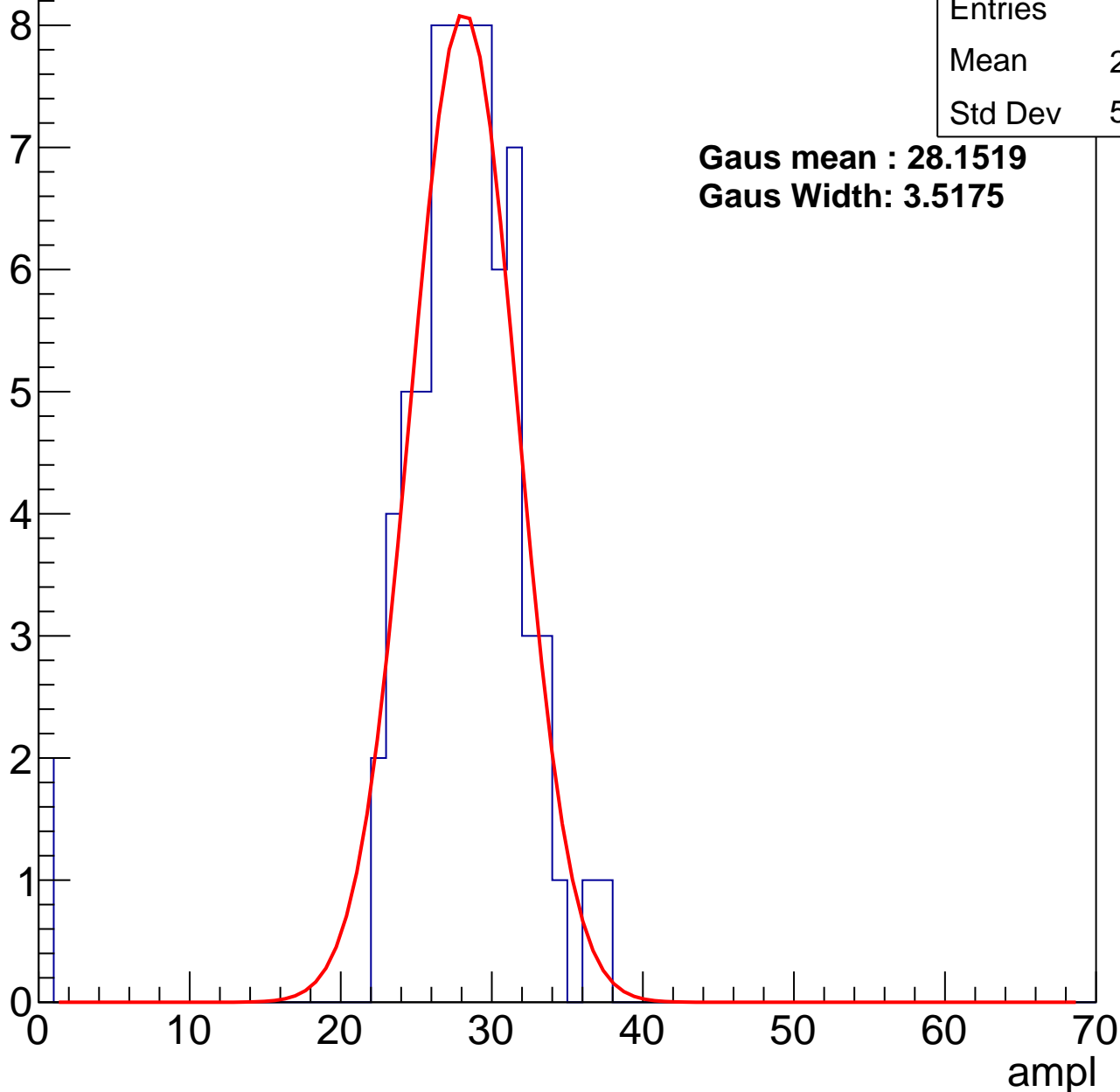
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	27.22
Std Dev	5.603

**Gaus mean : 28.1519**

**Gaus Width: 3.5175**



# B0L102S, U17-ch46, adc1

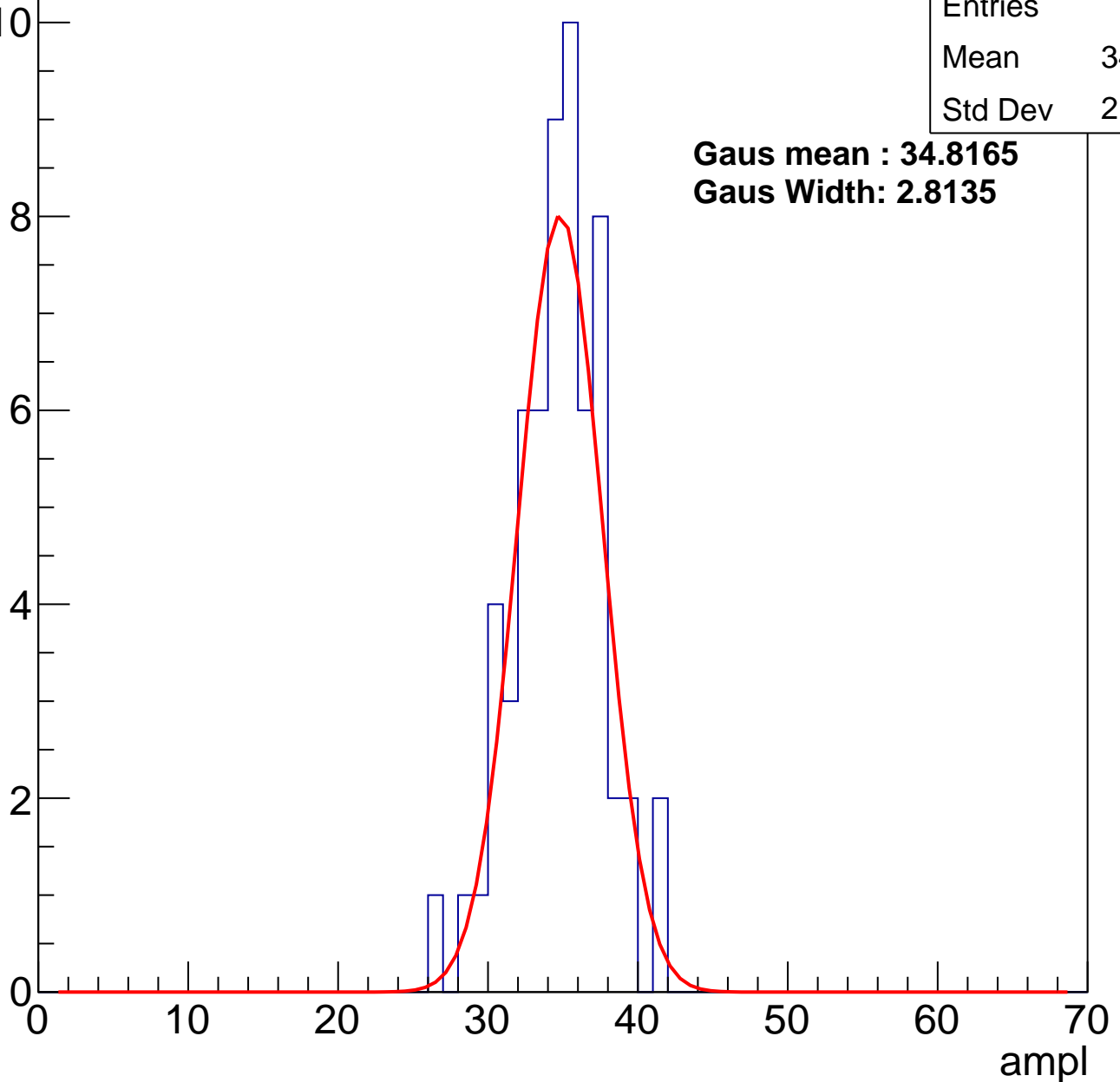
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	34.26
Std Dev	2.942

**Gaus mean : 34.8165**

**Gaus Width: 2.8135**



# B0L102S, U17-ch46, adc2

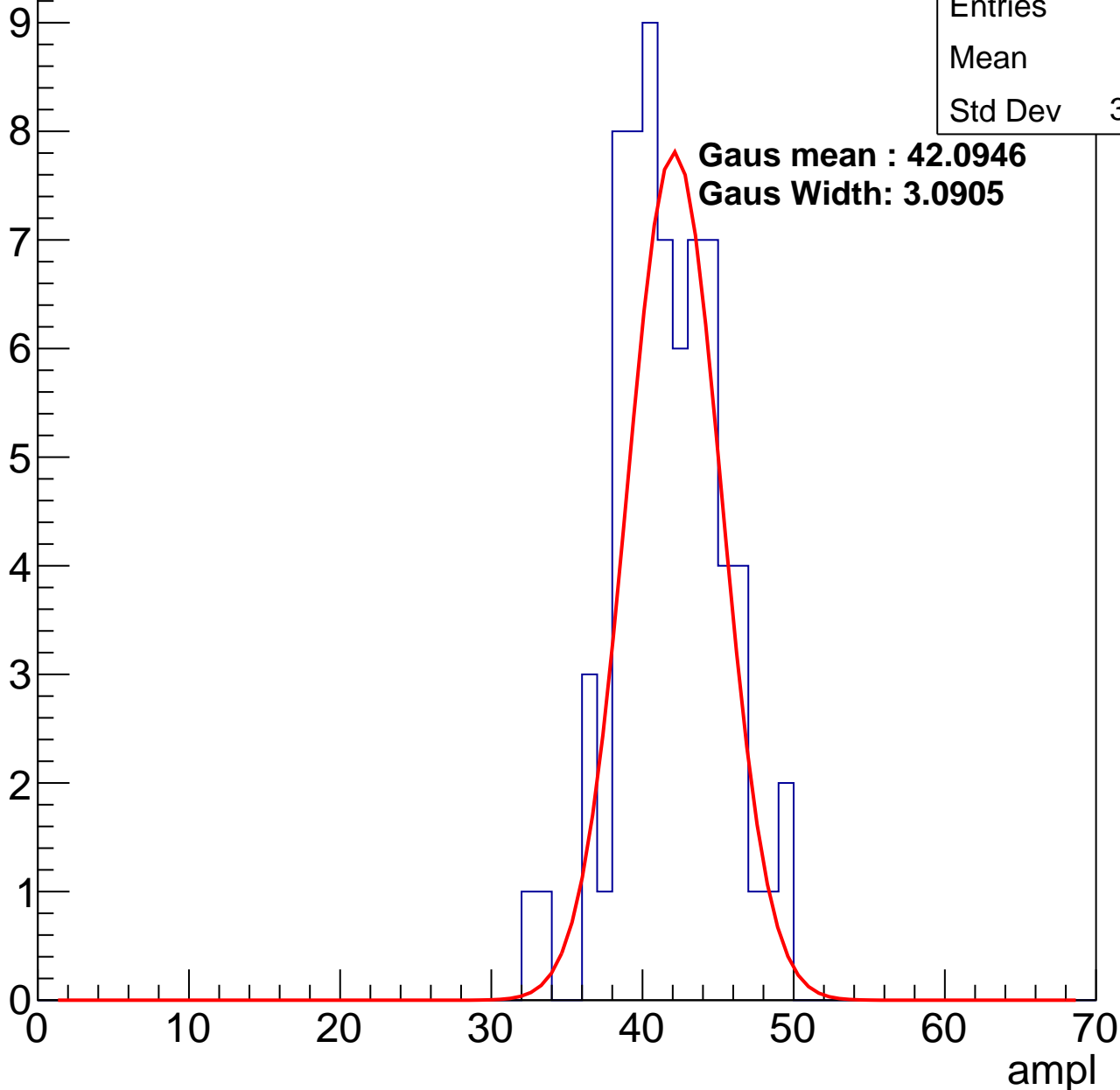
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	41.3
Std Dev	3.416

**Gaus mean : 42.0946**

**Gaus Width: 3.0905**

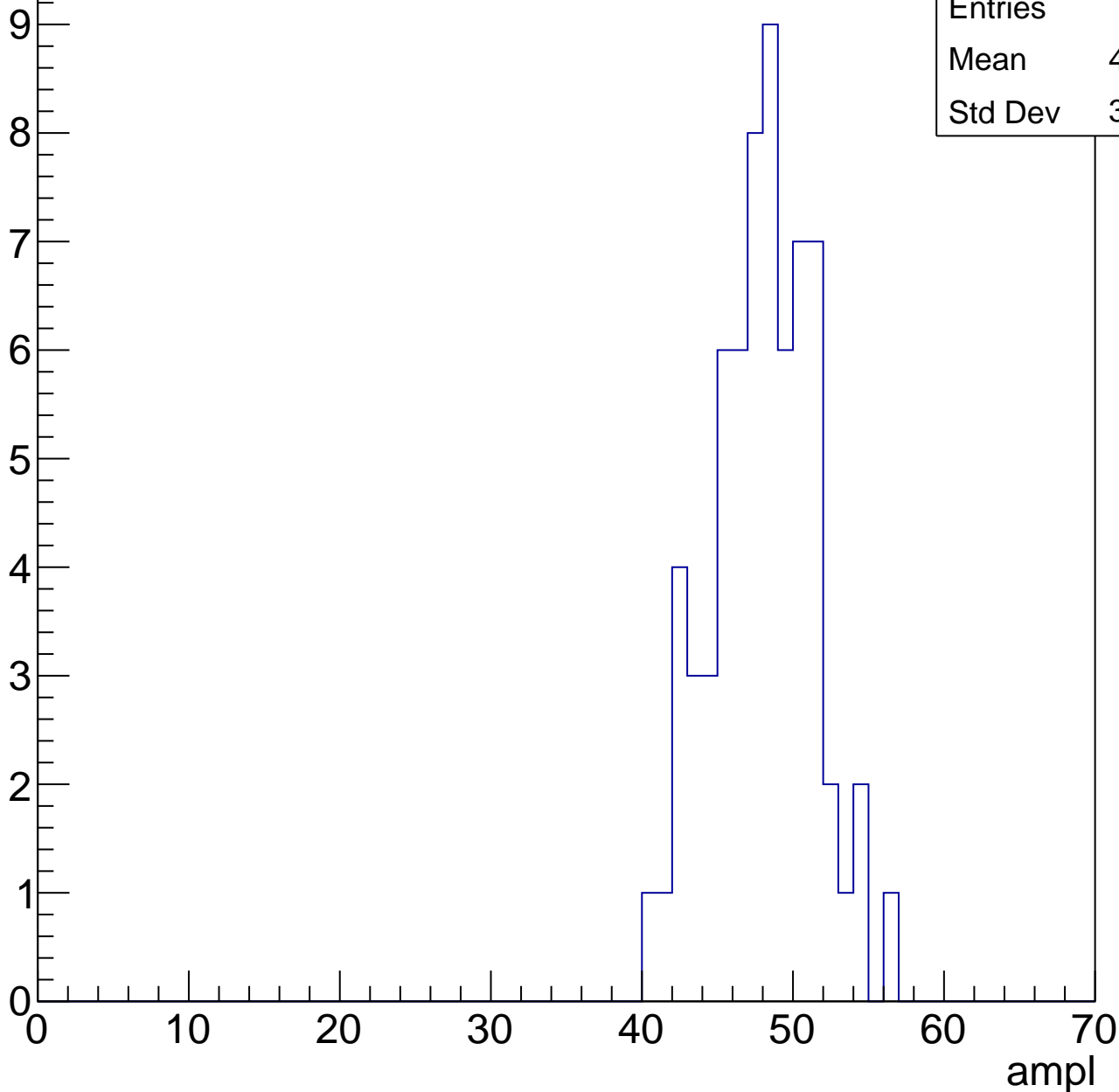


# B0L102S, U17-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	47.55
Std Dev	3.329

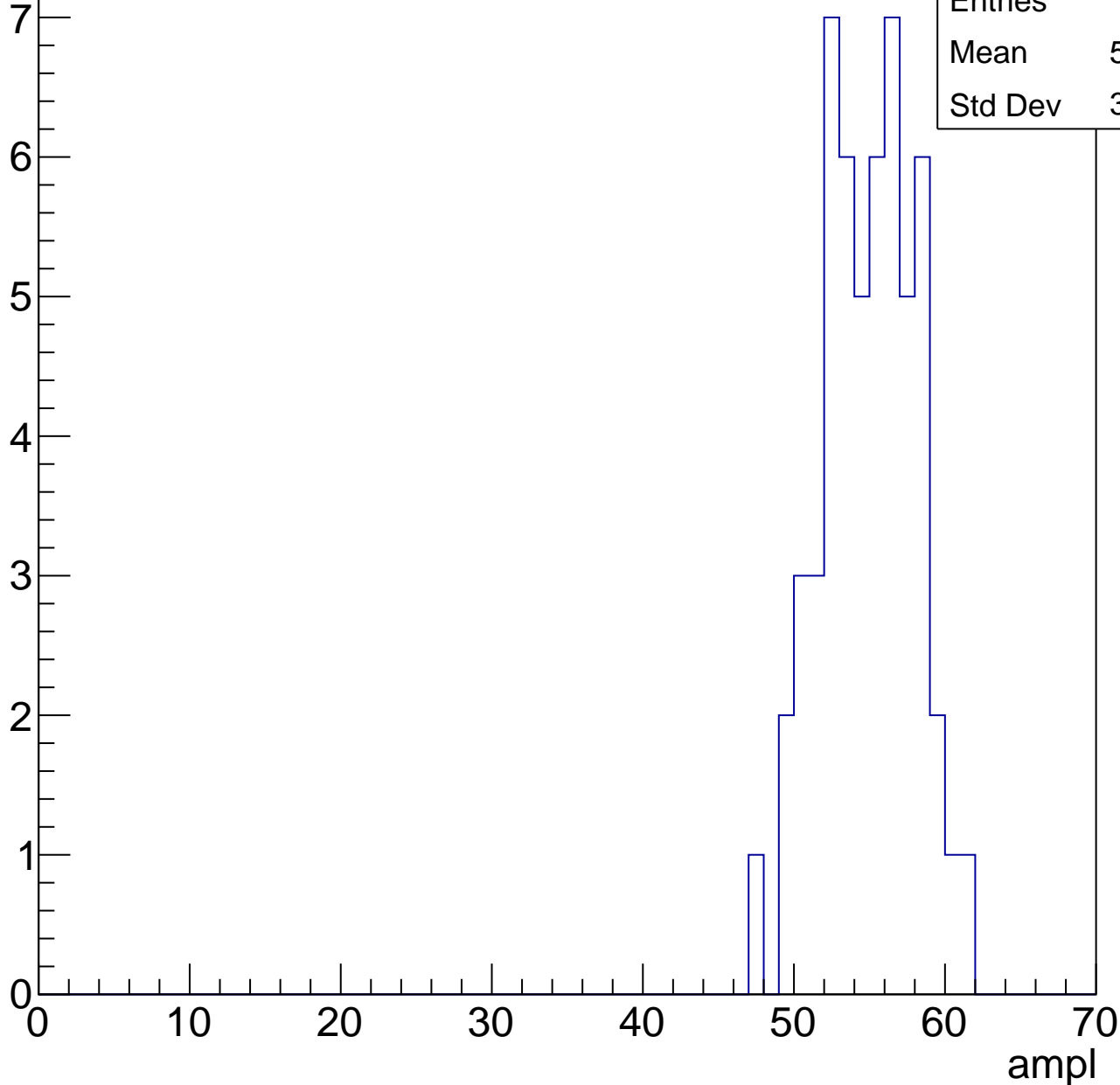


# B0L102S, U17-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	54.44
Std Dev	3.038

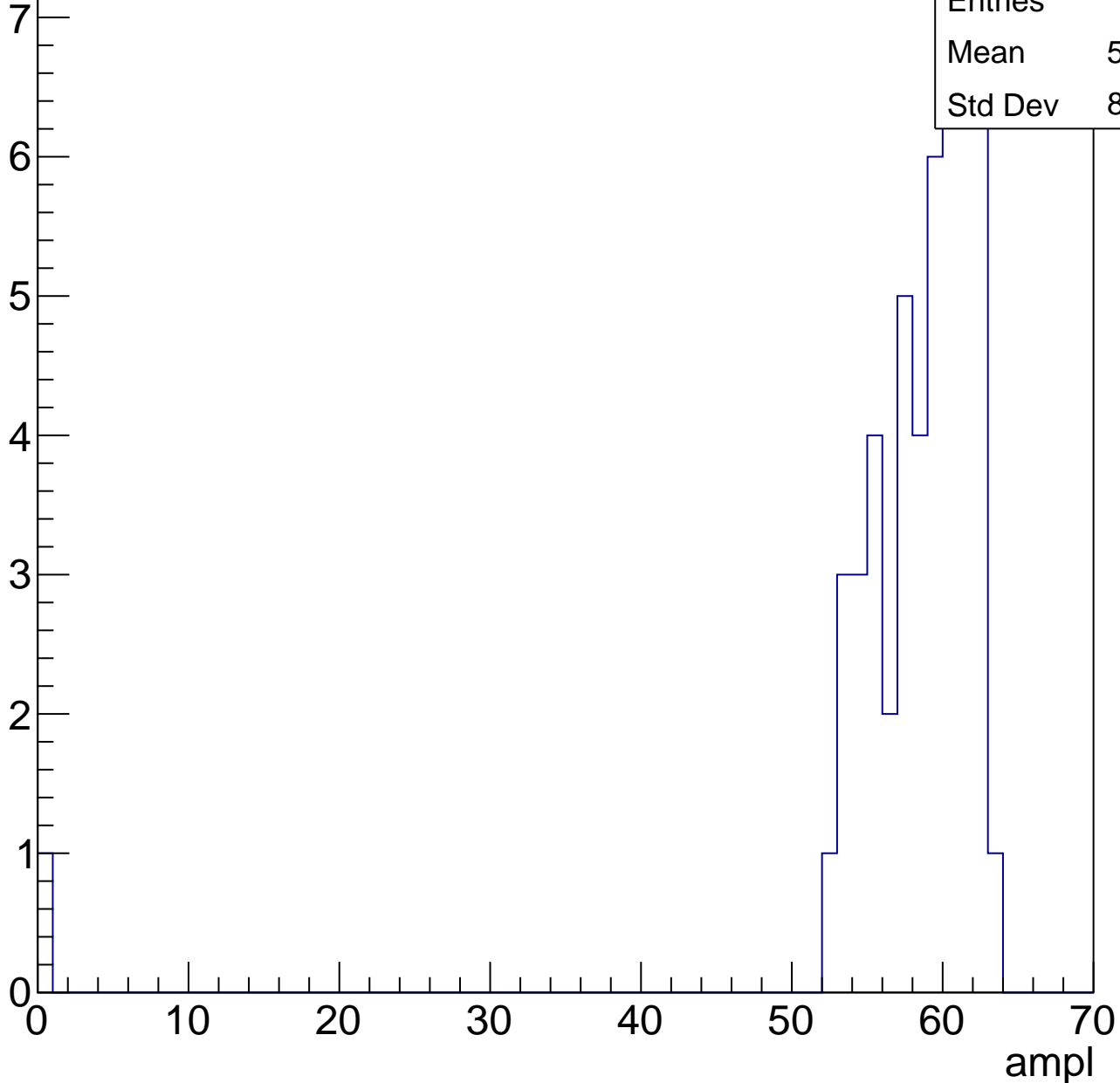


# B0L102S, U17-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	57.25
Std Dev	8.607



# B0L102S, U17-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	16
Mean	61.75
Std Dev	1.479

ampl

0 10 20 30 40 50 60 70

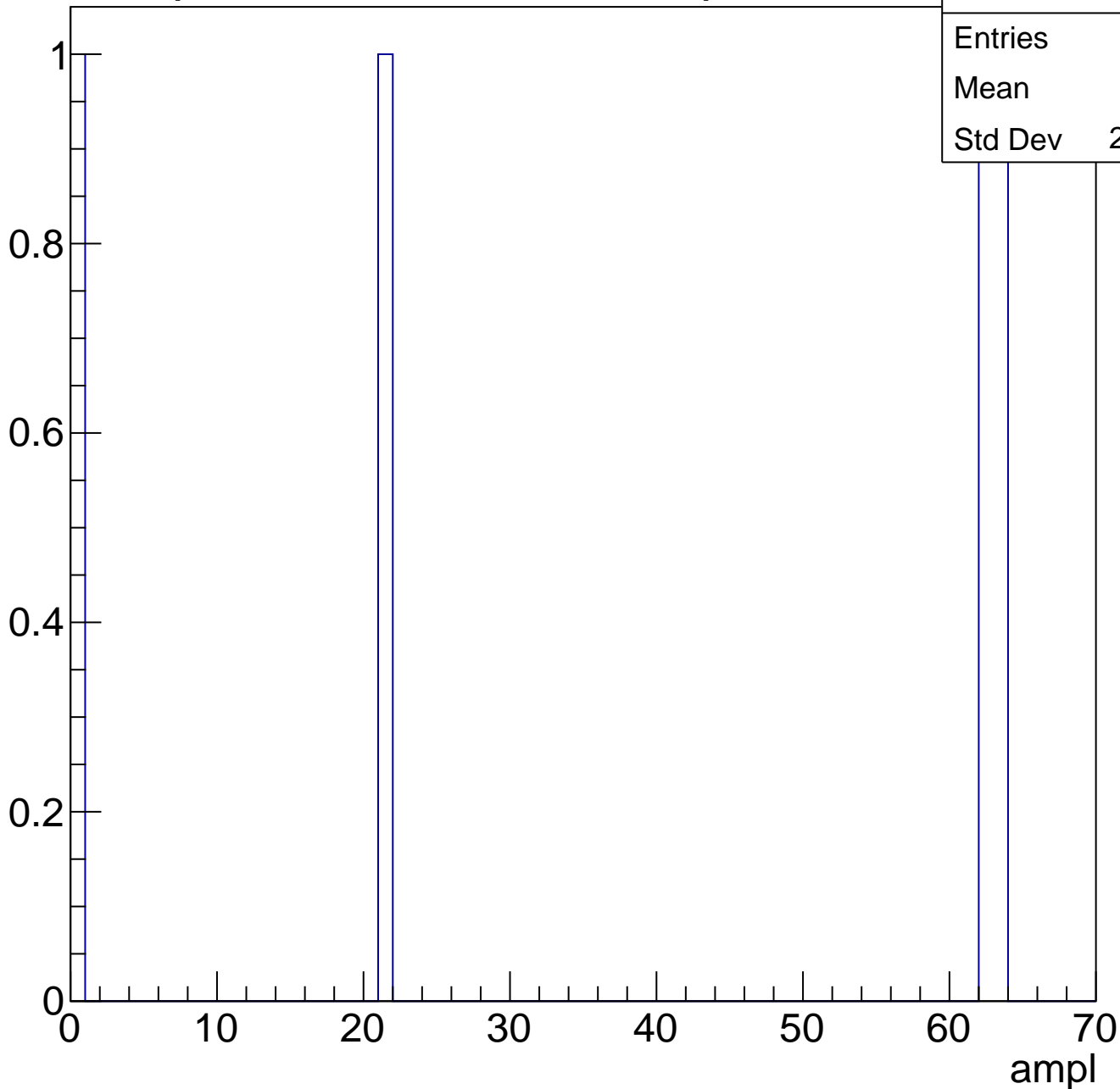
0  
1  
2  
3  
4  
5  
6  
7  
8



# B0L102S, U17-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch47, adc0

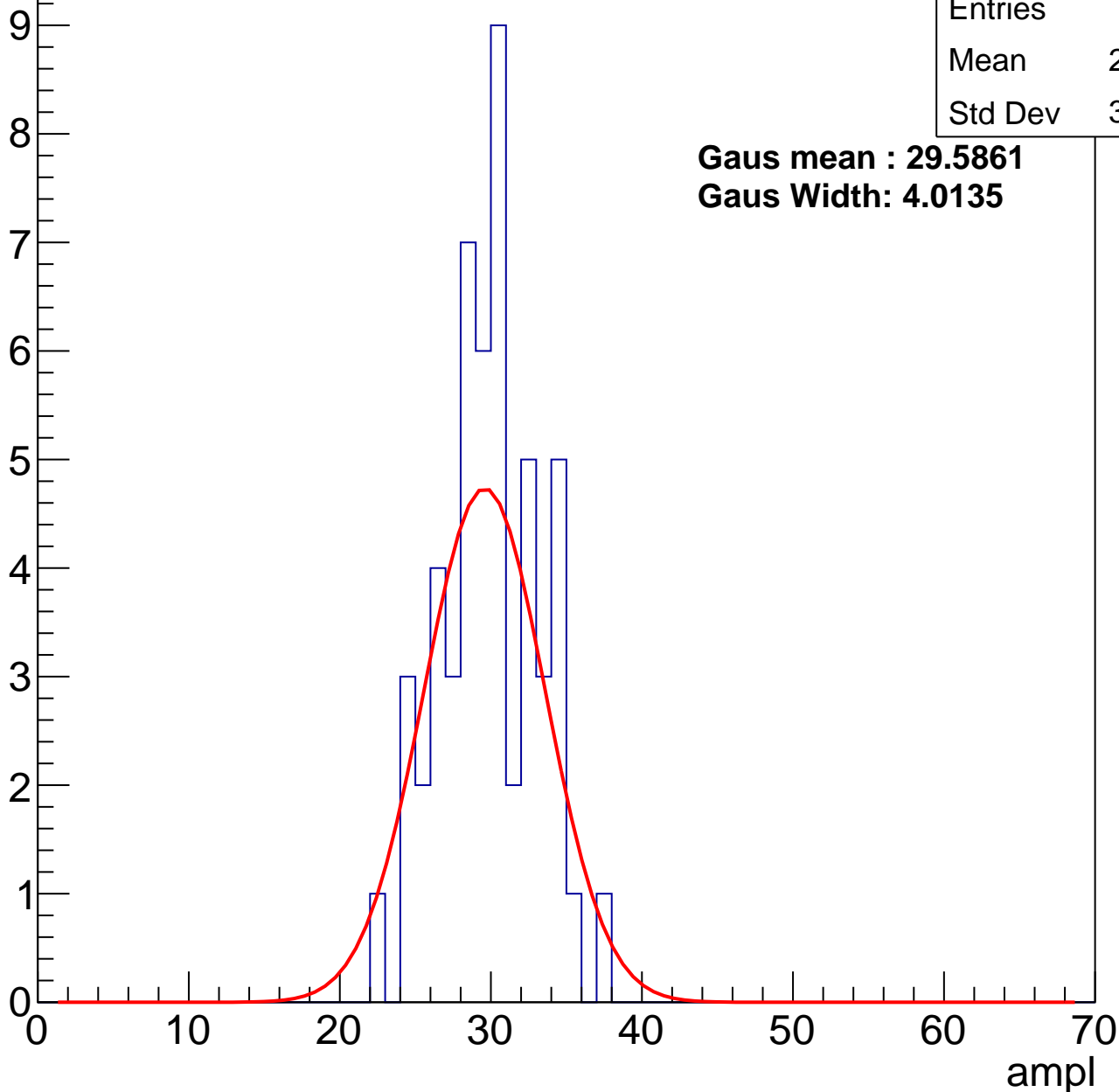
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	29.46
Std Dev	3.207

**Gaus mean : 29.5861**

**Gaus Width: 4.0135**



# B0L102S, U17-ch47, adc1

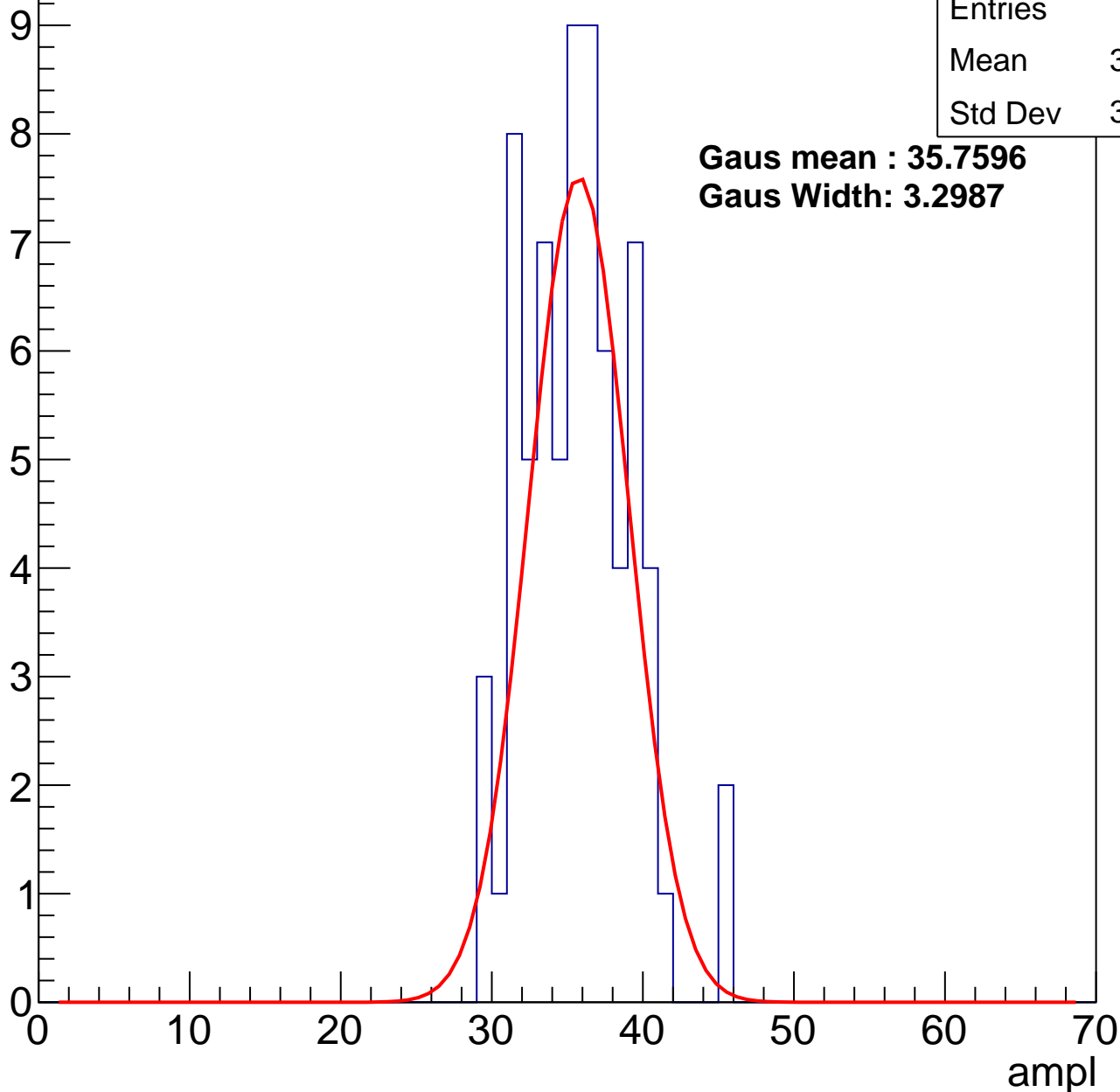
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	35.25
Std Dev	3.463

**Gaus mean : 35.7596**

**Gaus Width: 3.2987**



# B0L102S, U17-ch47, adc2

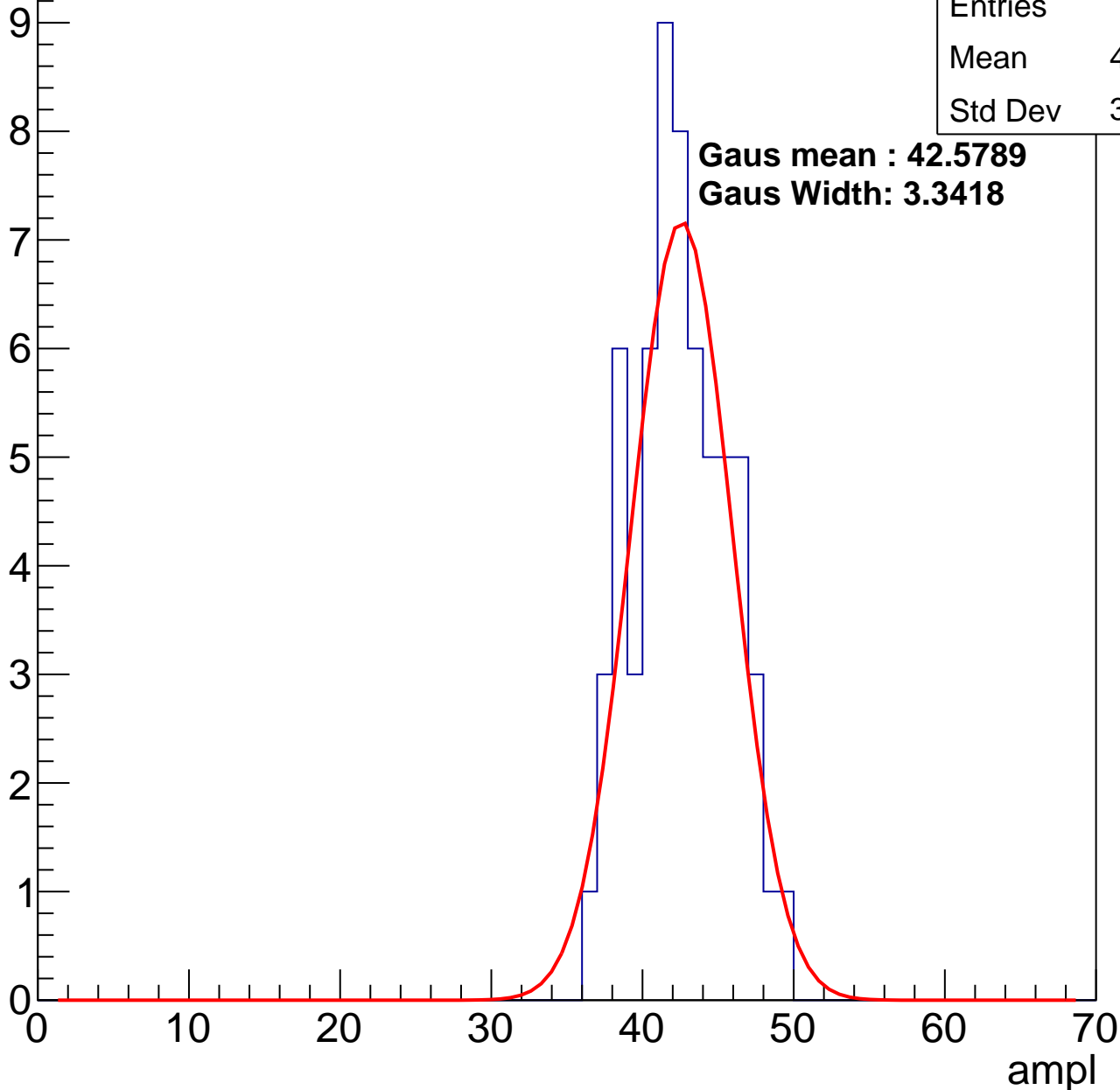
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.06
Std Dev	3.053

**Gaus mean : 42.5789**

**Gaus Width: 3.3418**

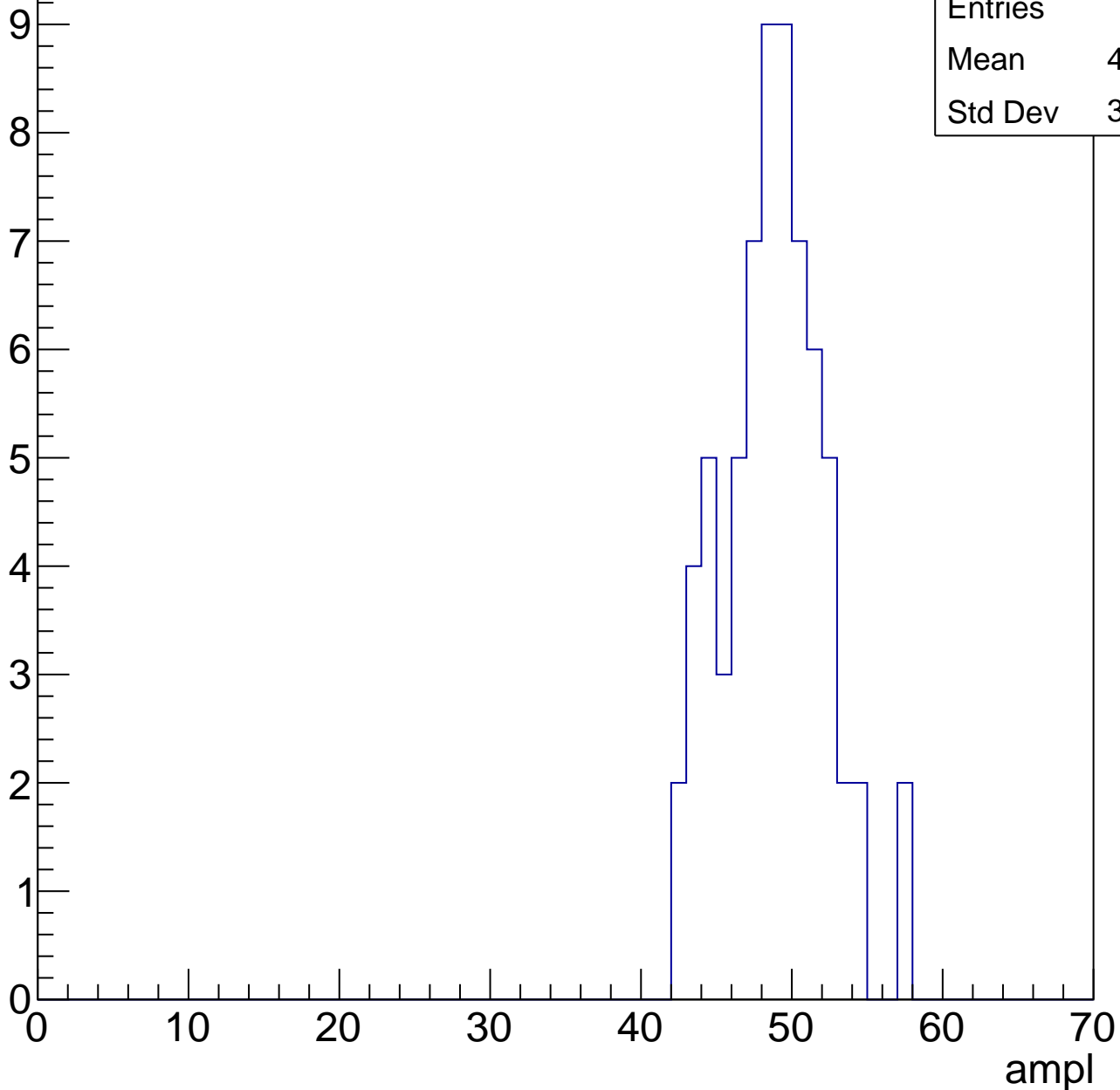


# B0L102S, U17-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

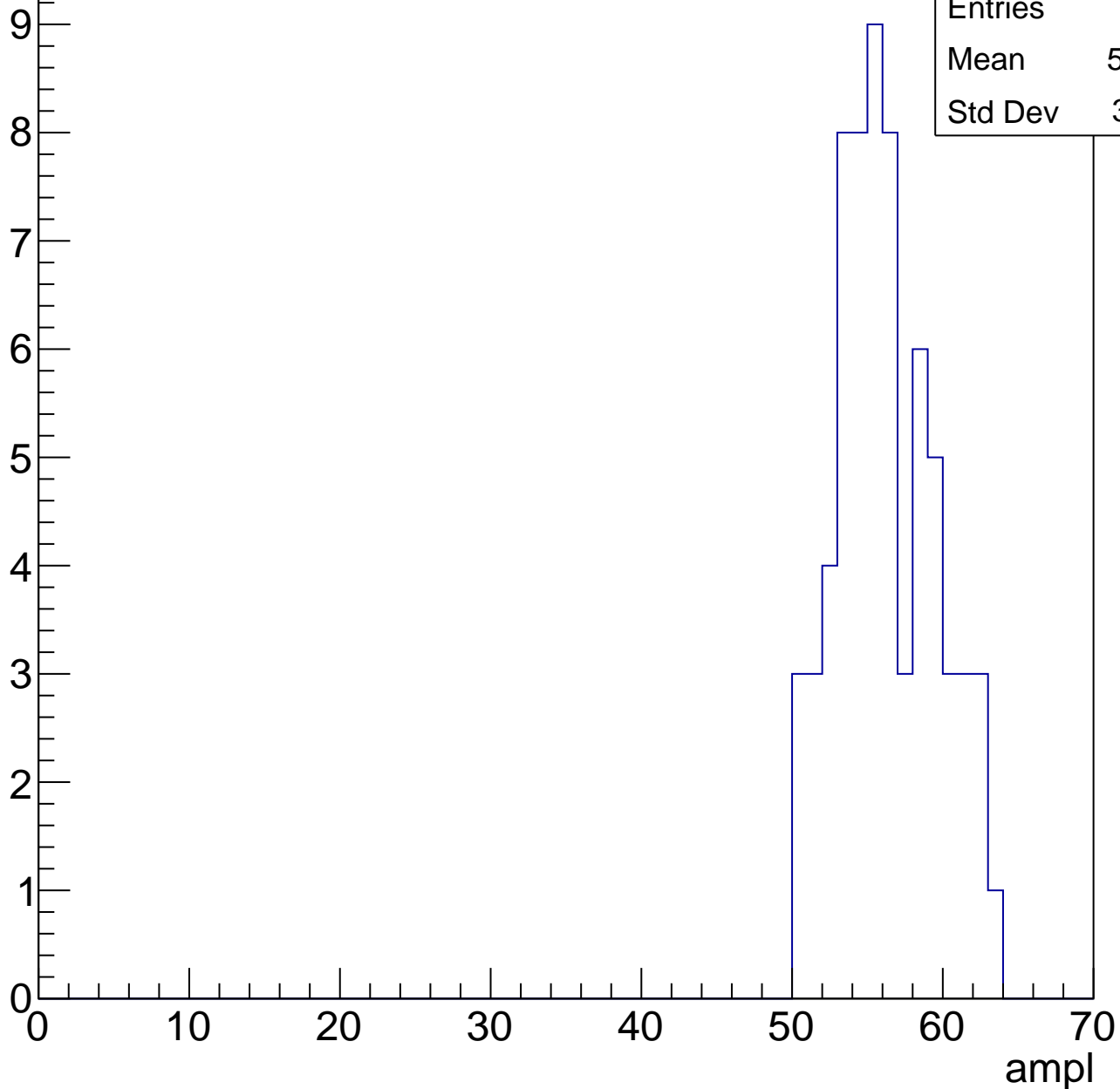
Entries	68
Mean	48.34
Std Dev	3.328



# B0L102S, U17-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



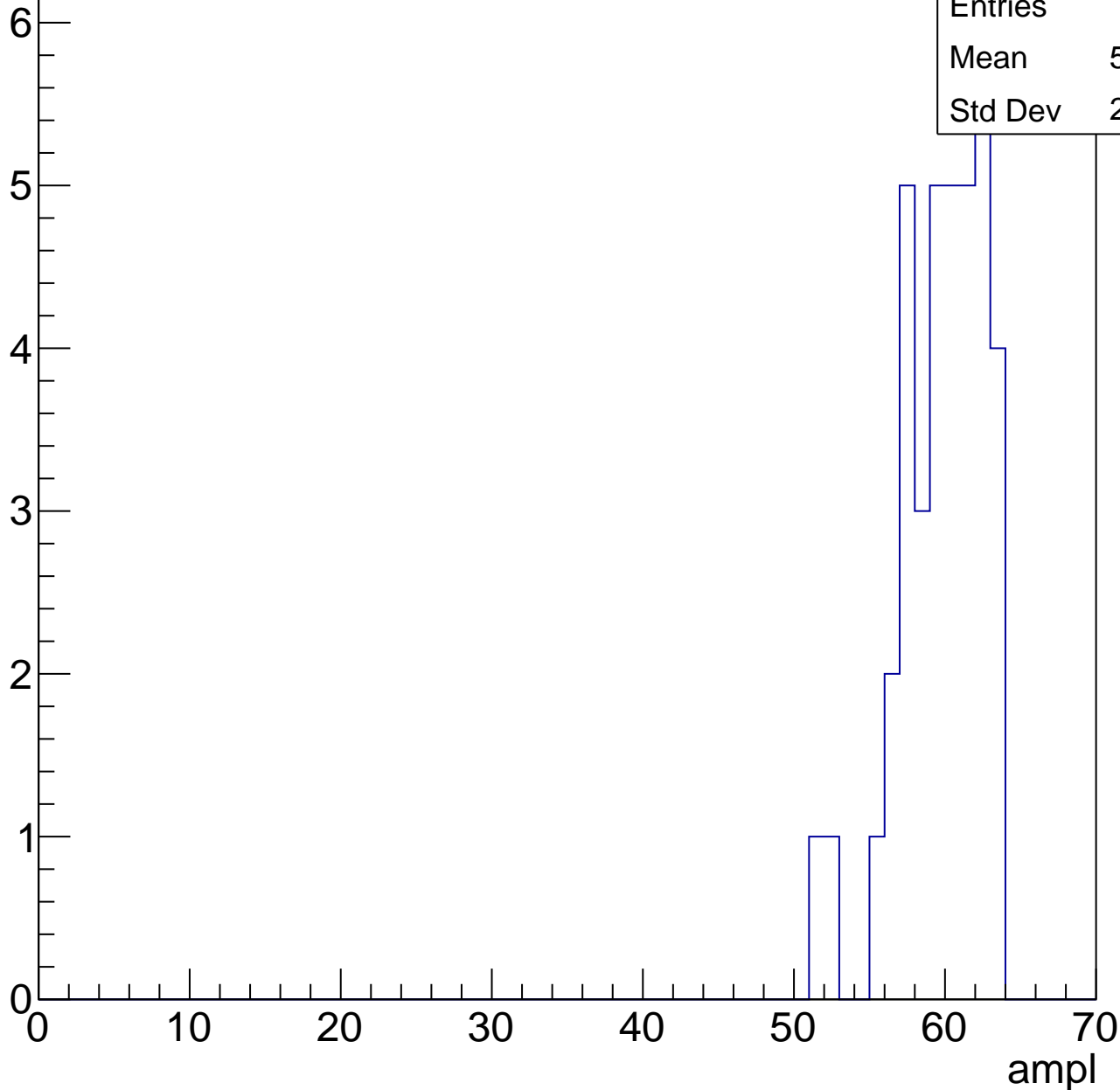
Entries	67
Mean	55.76
Std Dev	3.251

# B0L102S, U17-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	59.29
Std Dev	2.855

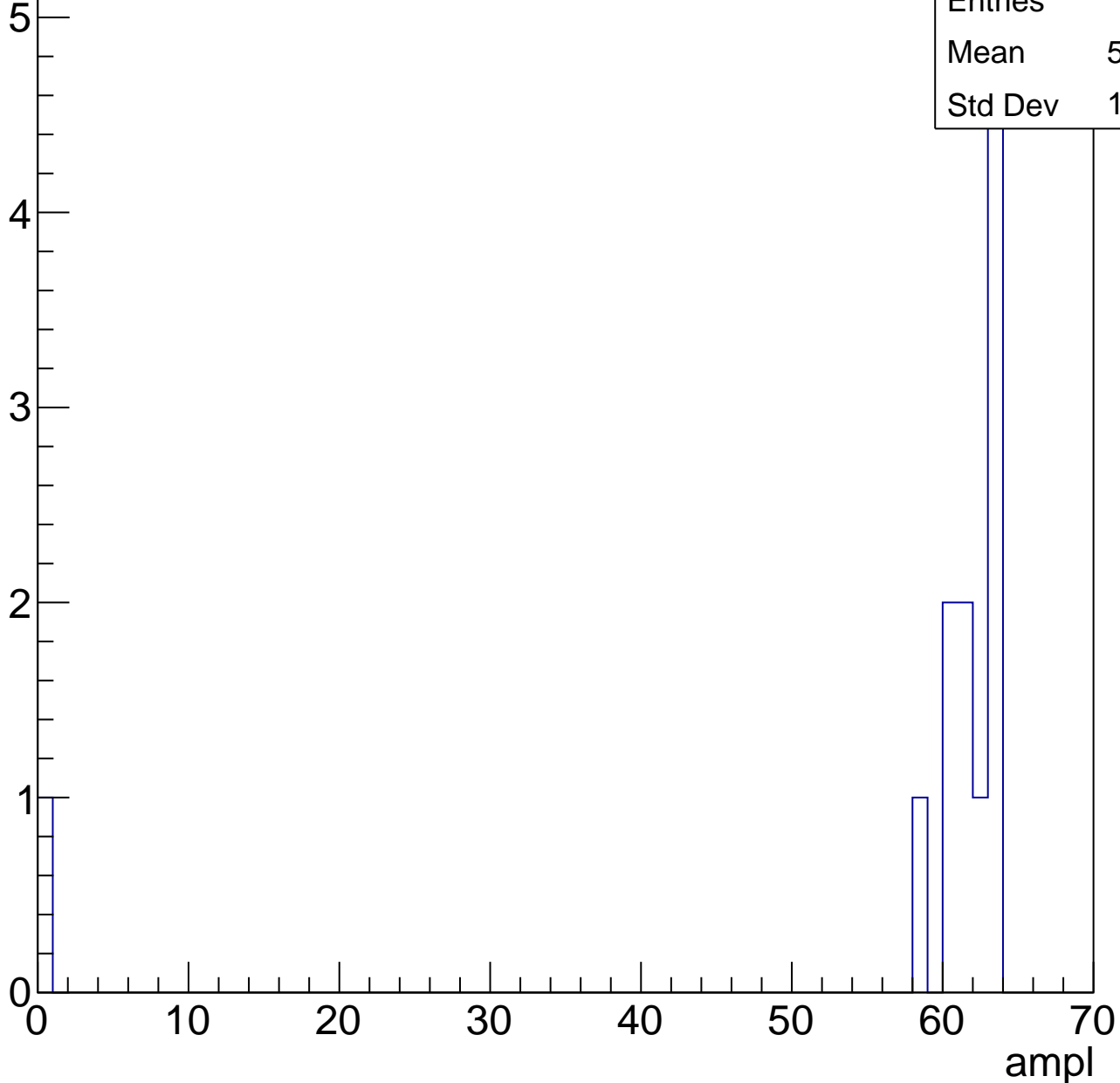


# B0L102S, U17-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	56.42
Std Dev	17.08





# B0L102S, U17-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch48, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	105
Mean	27.78
Std Dev	7.078

**Gaus mean : 29.5997**

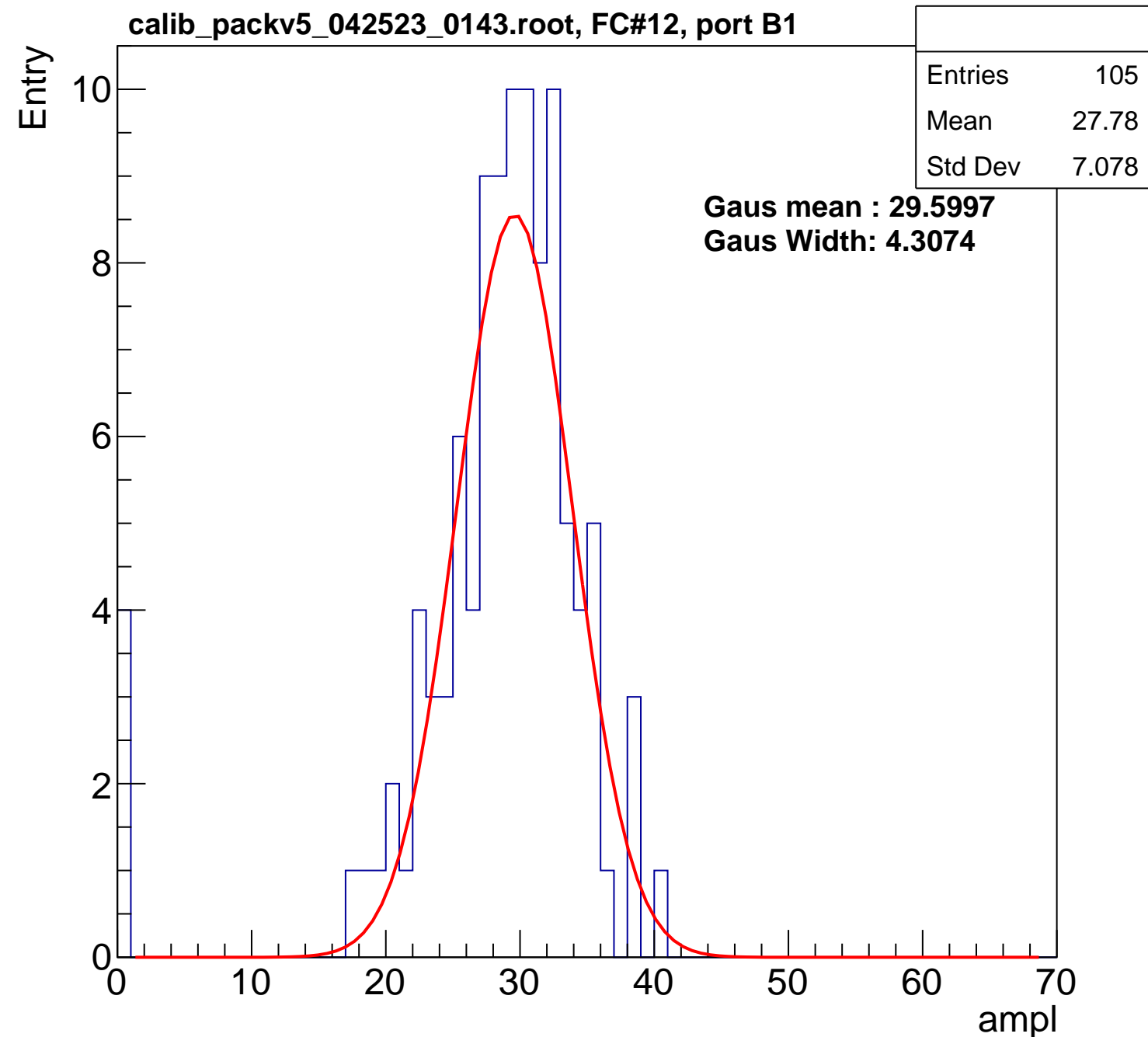
**Gaus Width: 4.3074**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch48, adc1

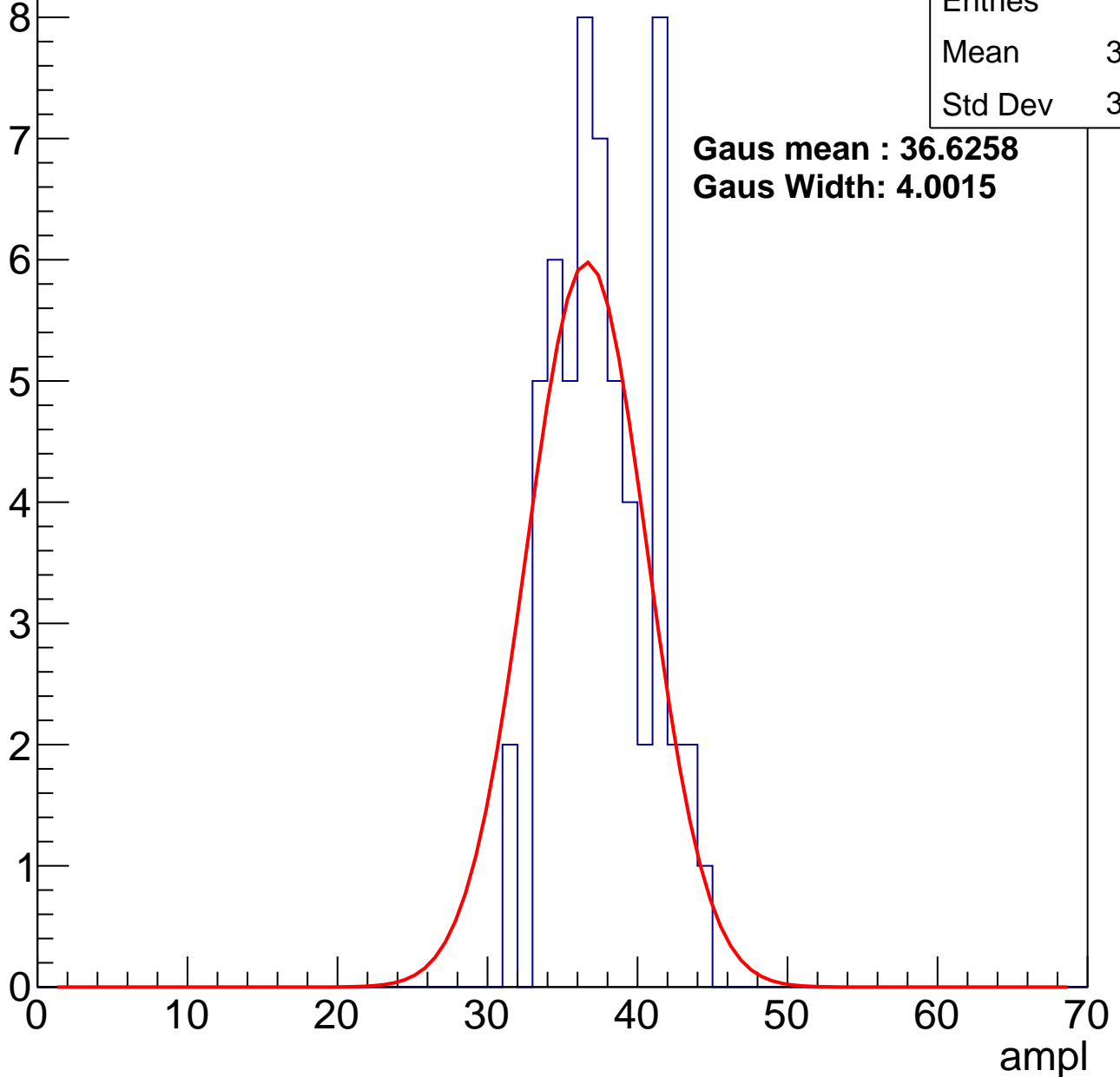
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	37.21
Std Dev	3.161

**Gaus mean : 36.6258**

**Gaus Width: 4.0015**



# B0L102S, U17-ch48, adc2

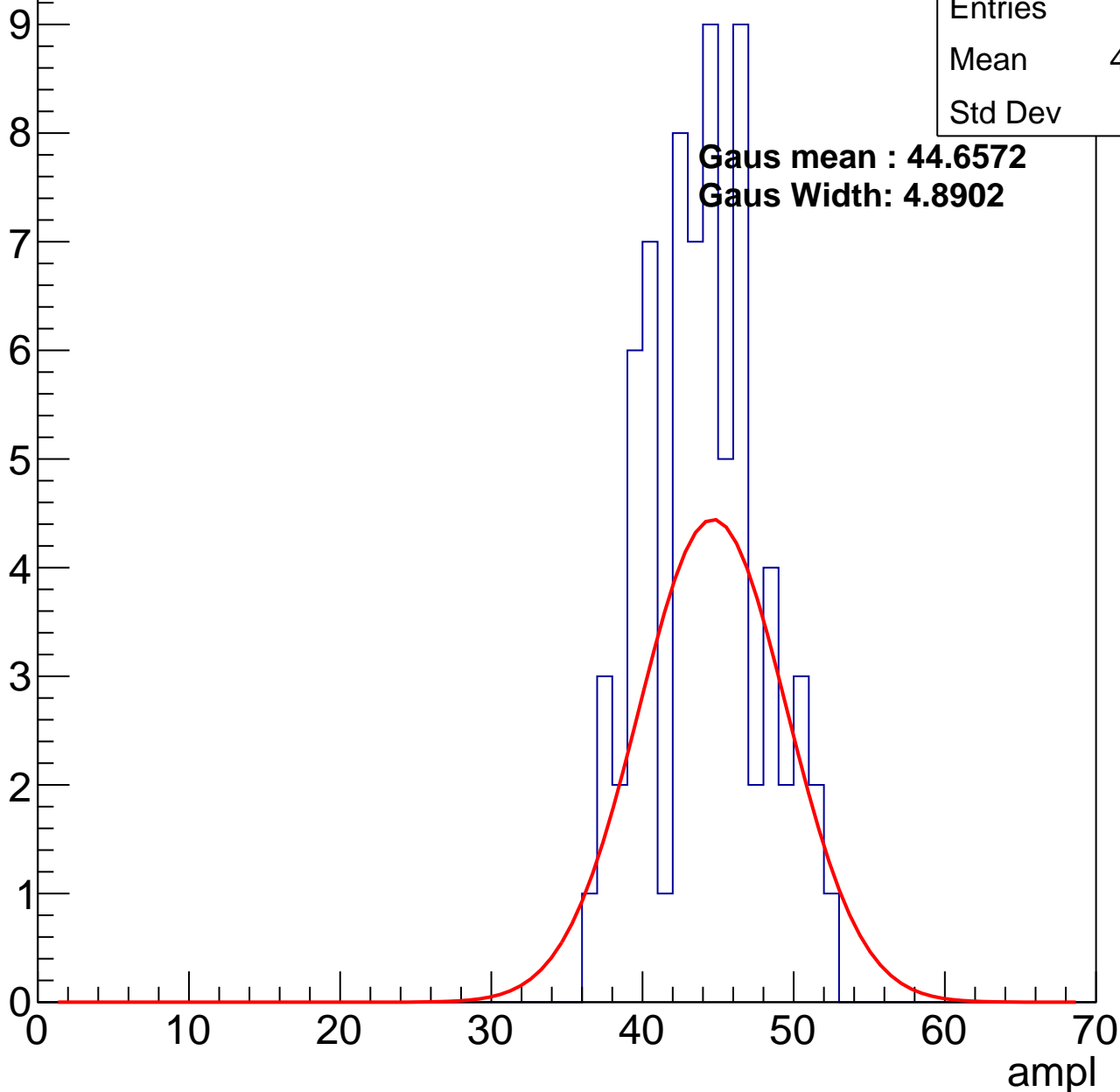
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	43.58
Std Dev	3.77

**Gaus mean : 44.6572**

**Gaus Width: 4.8902**

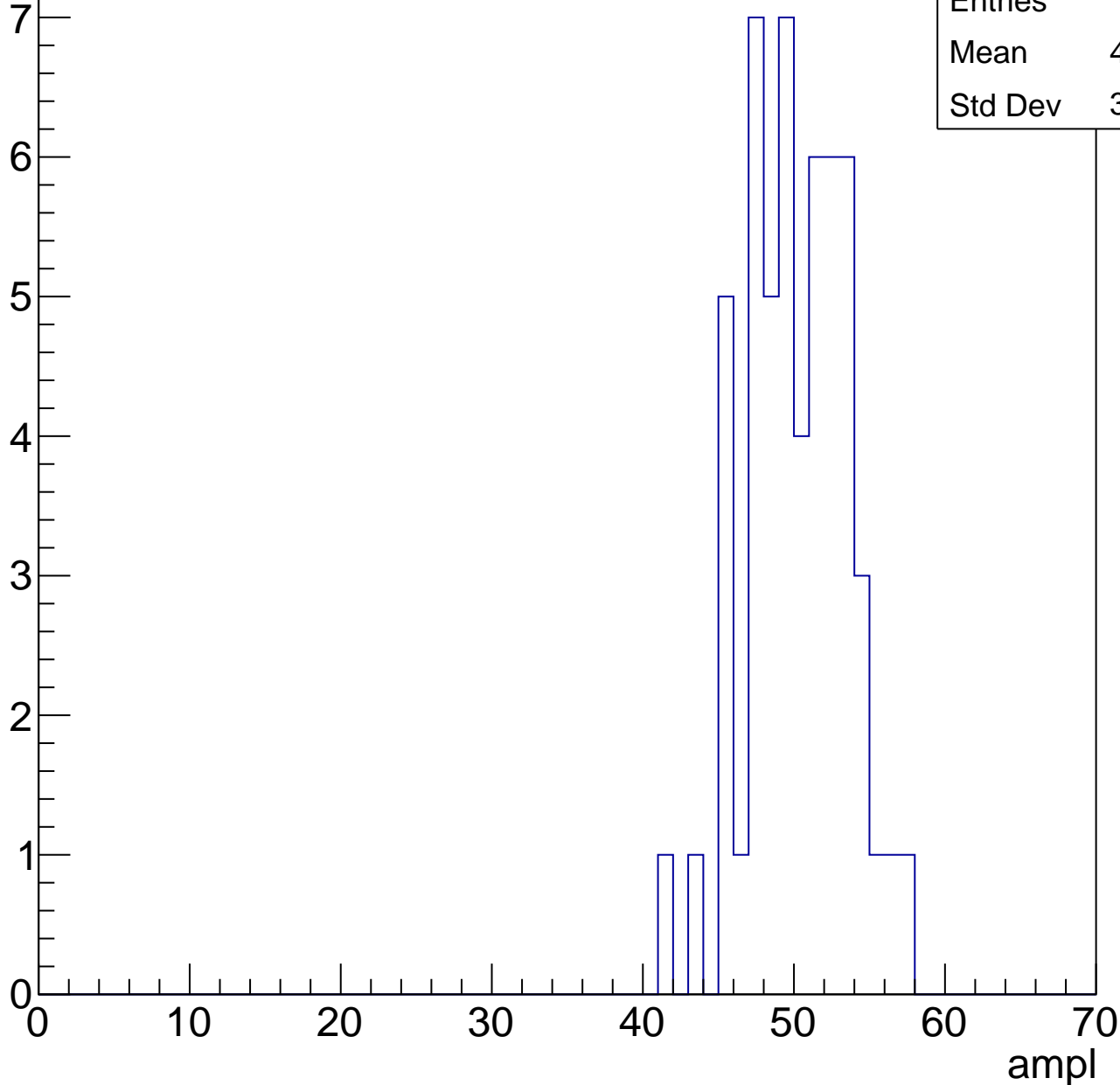


# B0L102S, U17-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

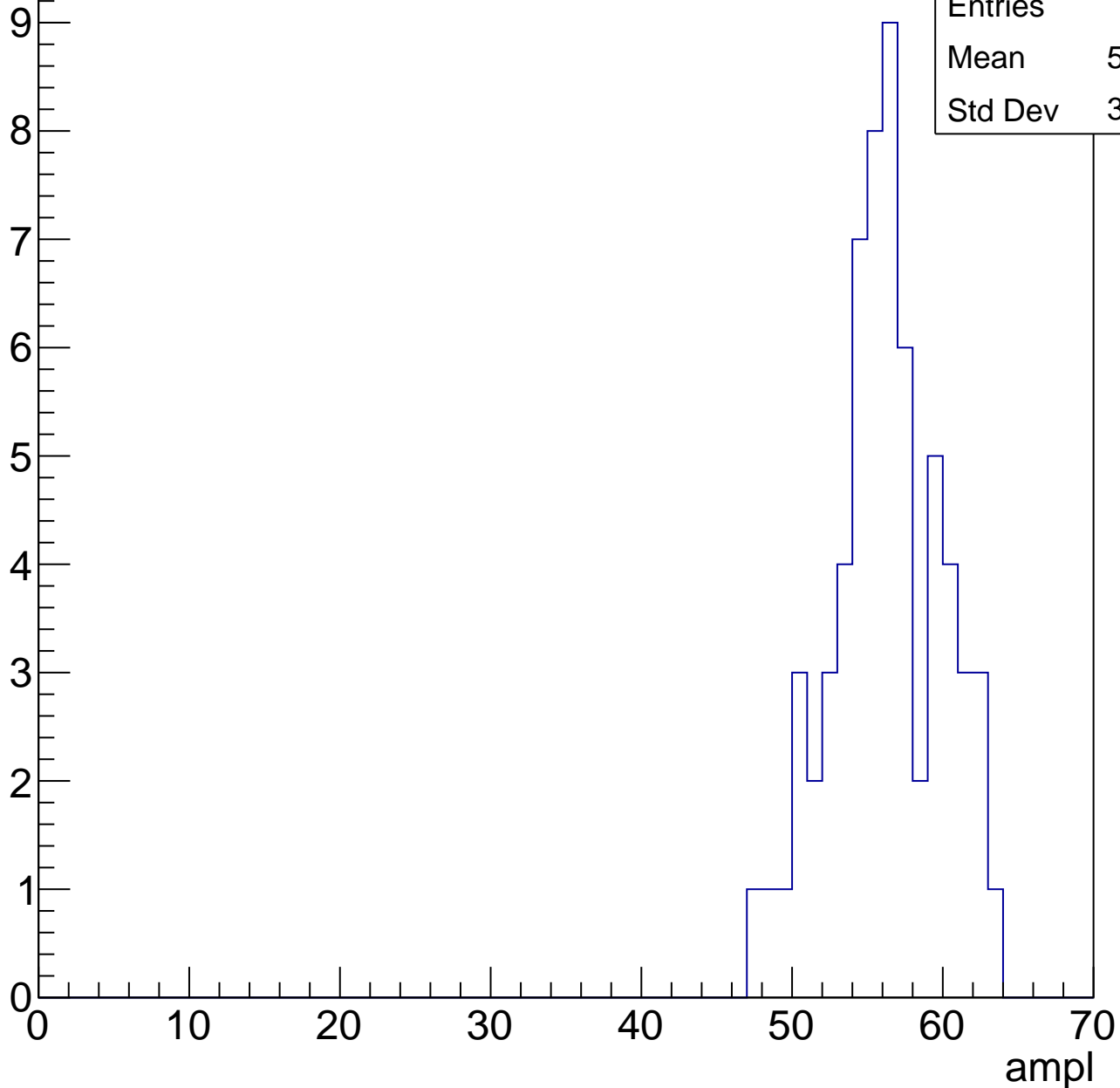
Entries	55
Mean	49.69
Std Dev	3.297



# B0L102S, U17-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



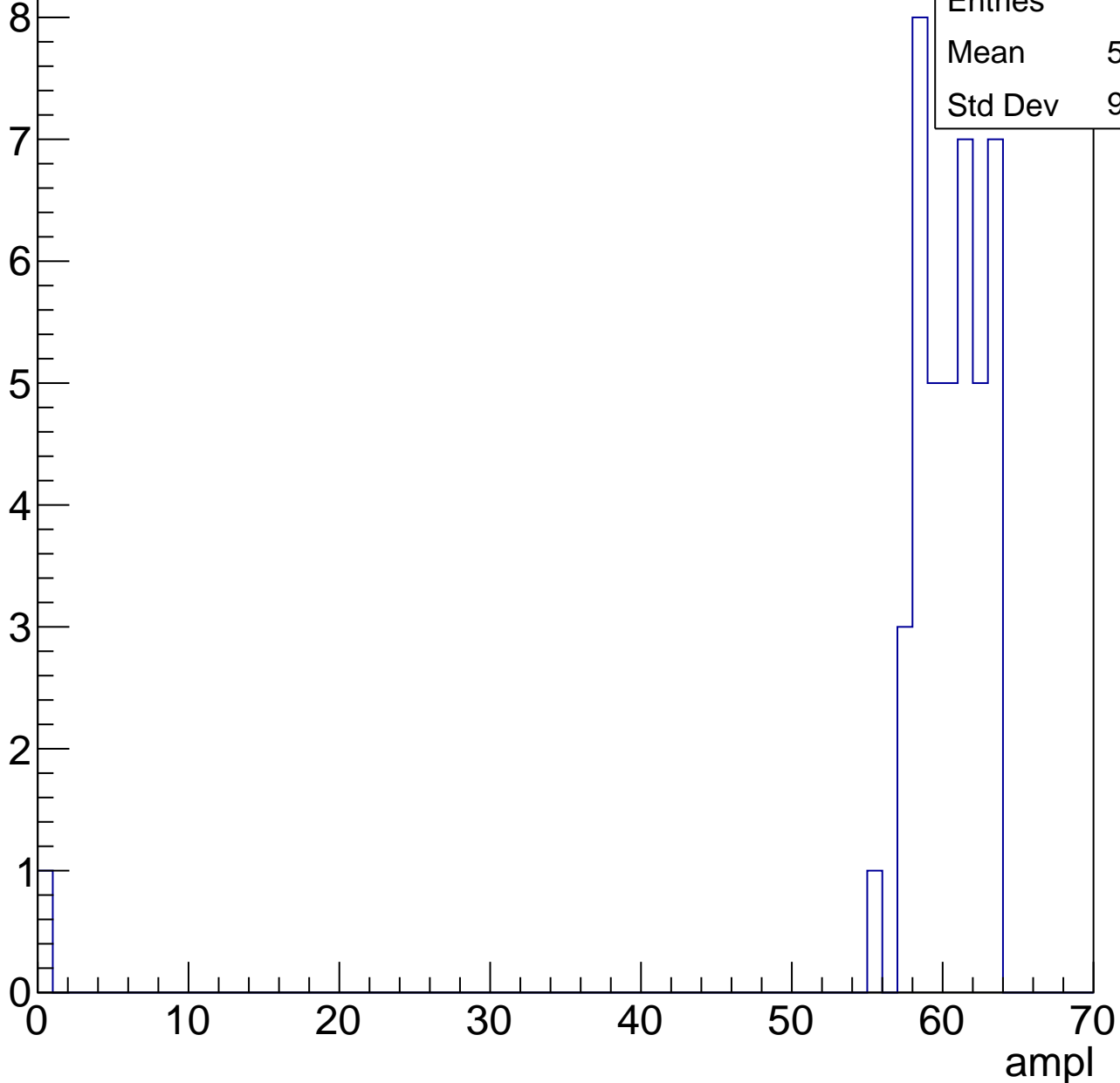
Entries	63
Mean	55.73
Std Dev	3.613

# B0L102S, U17-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	58.64
Std Dev	9.388



# B0L102S, U17-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch49, adc0

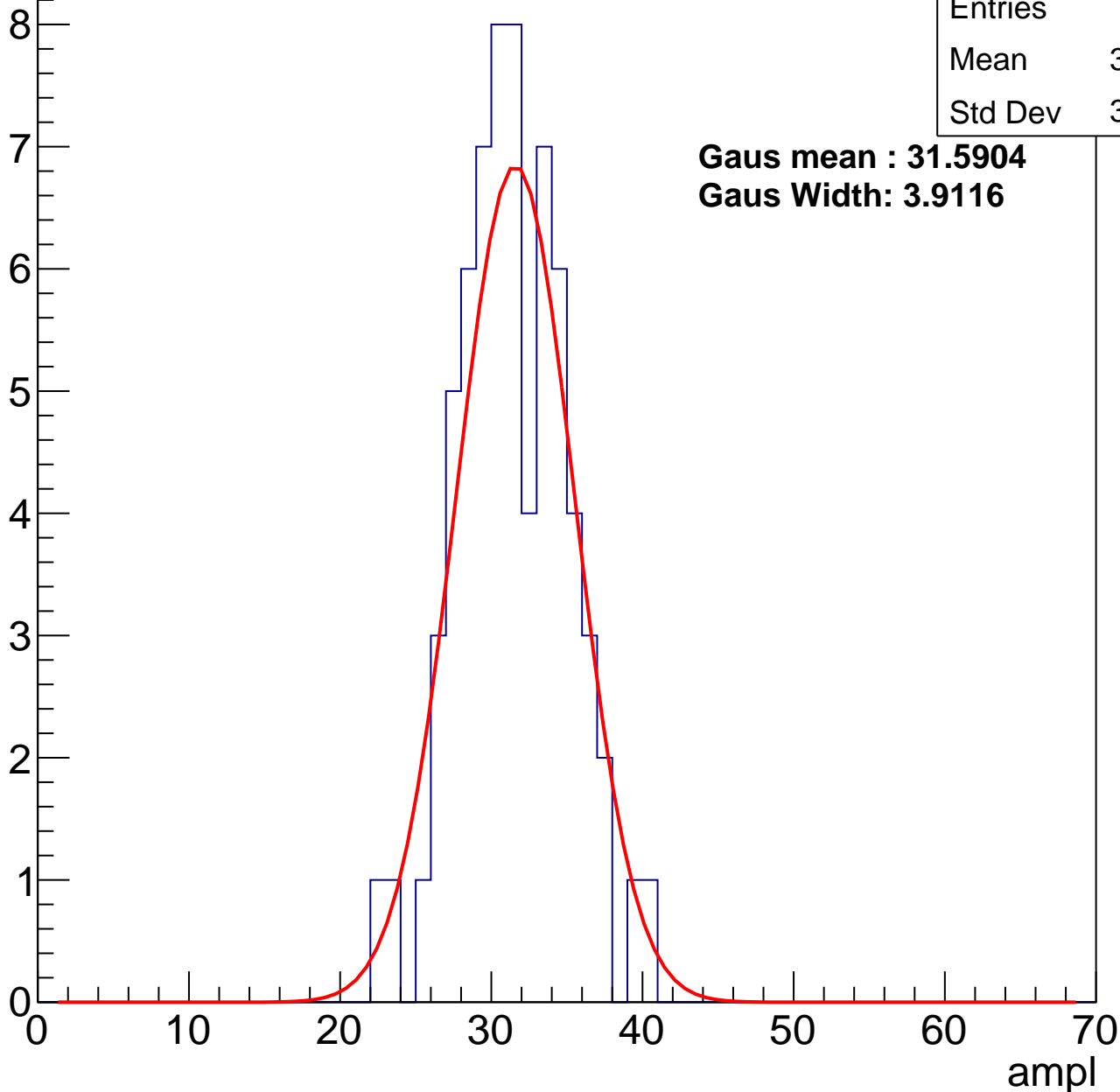
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	30.97
Std Dev	3.573

**Gaus mean : 31.5904**

**Gaus Width: 3.9116**



# B0L102S, U17-ch49, adc1

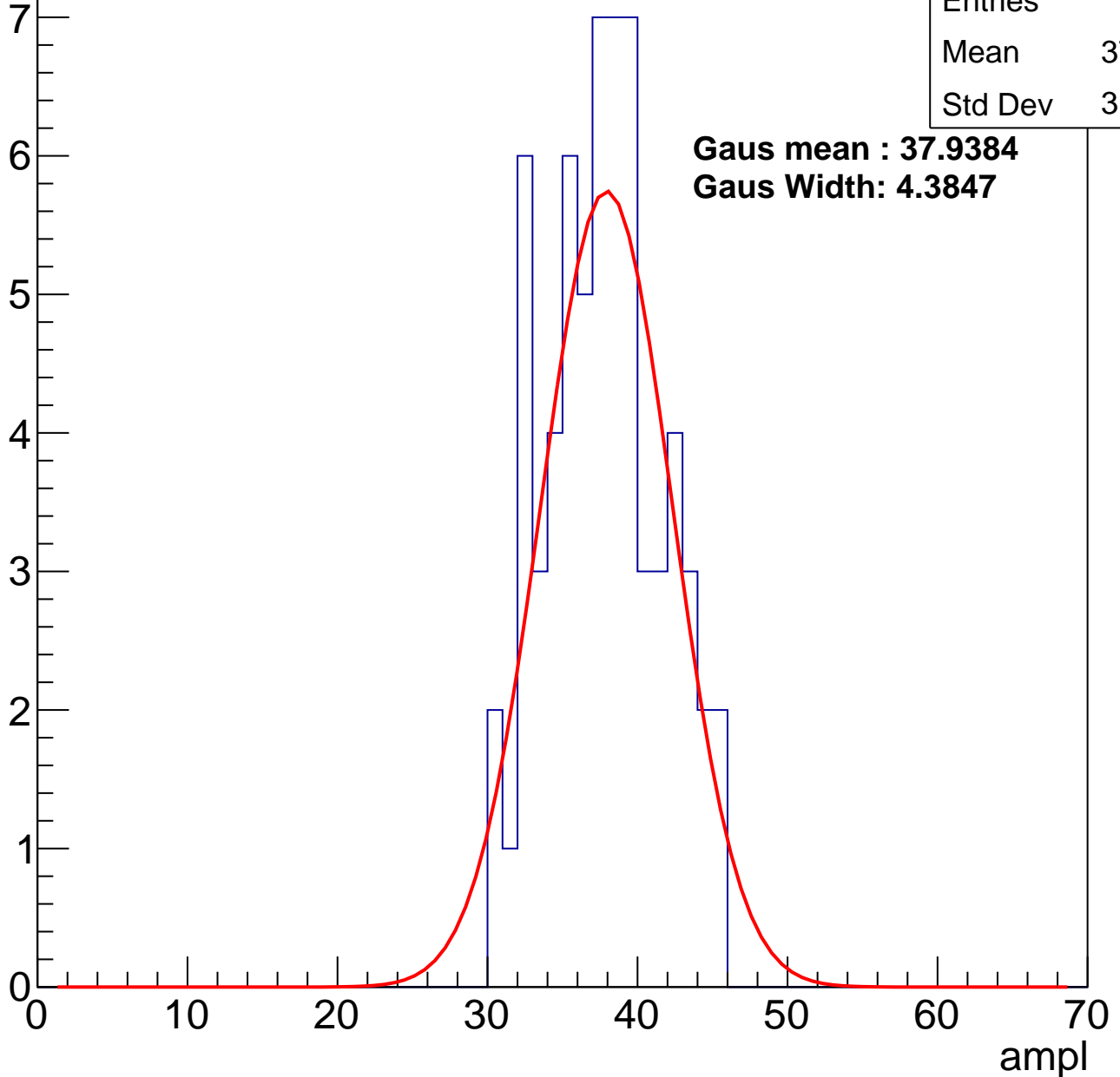
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.29
Std Dev	3.794

**Gaus mean : 37.9384**

**Gaus Width: 4.3847**



# B0L102S, U17-ch49, adc2

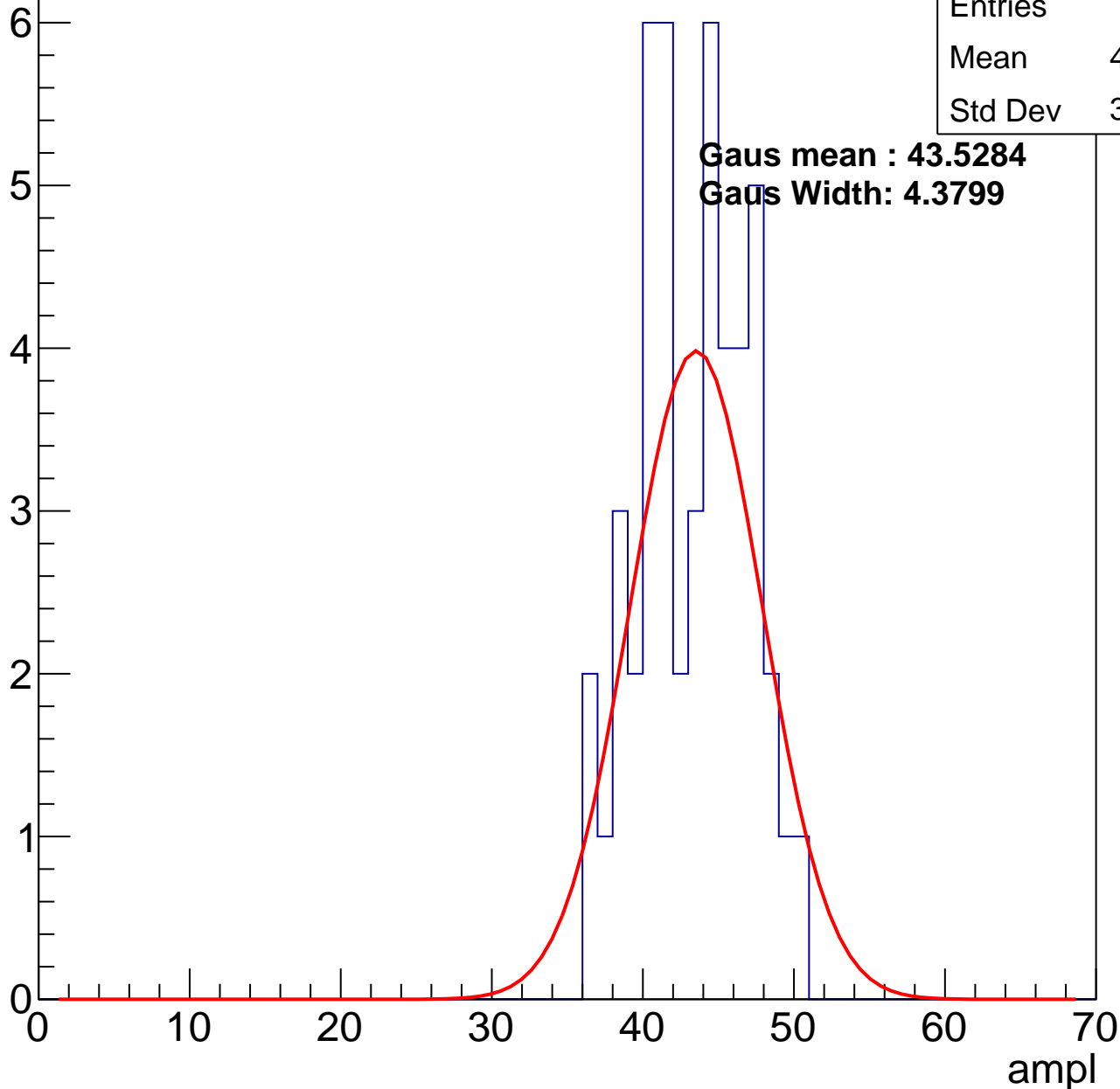
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	42.88
Std Dev	3.516

**Gaus mean : 43.5284**

**Gaus Width: 4.3799**

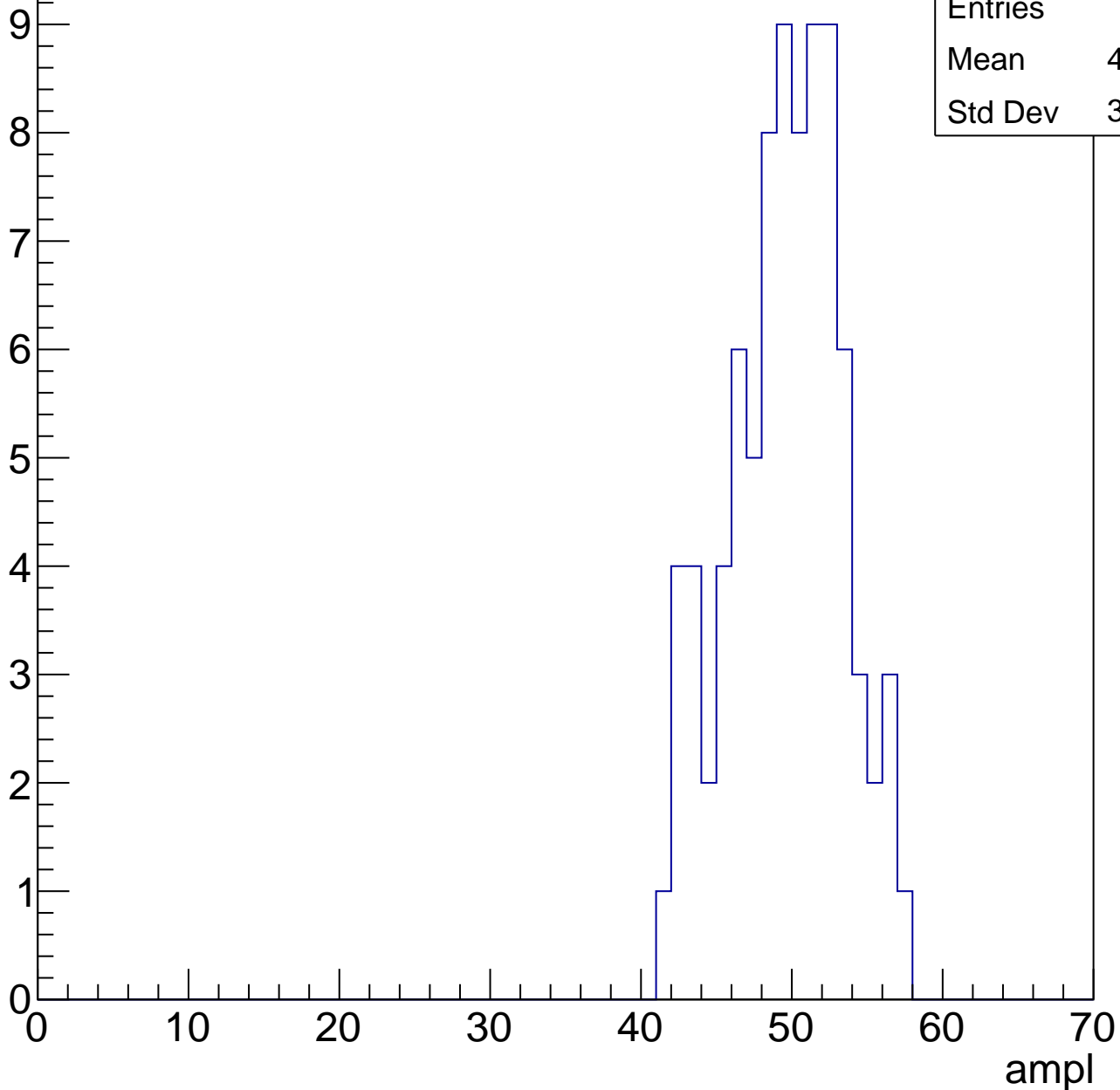


# B0L102S, U17-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

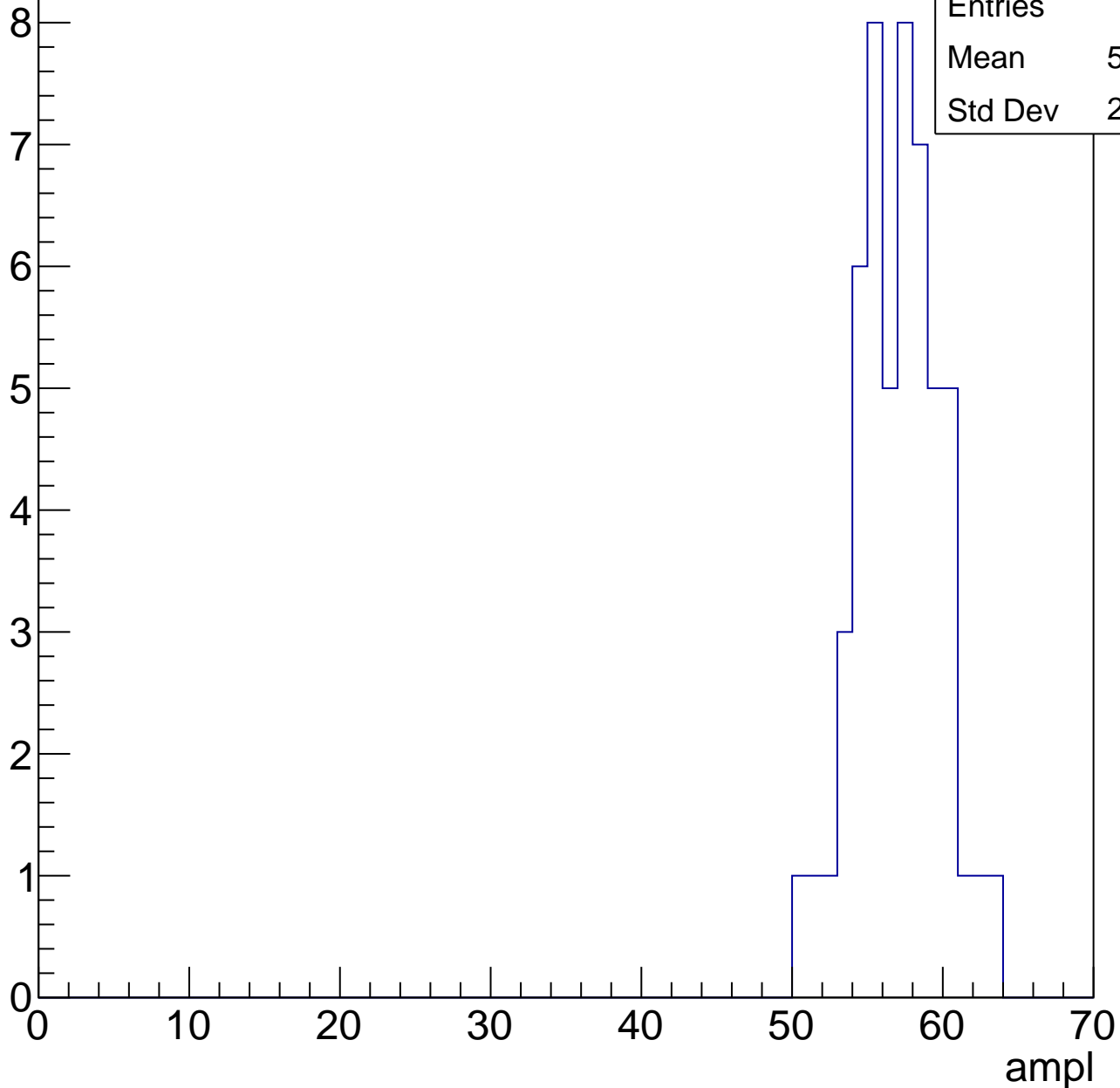
Entries	84
Mean	49.13
Std Dev	3.744



# B0L102S, U17-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

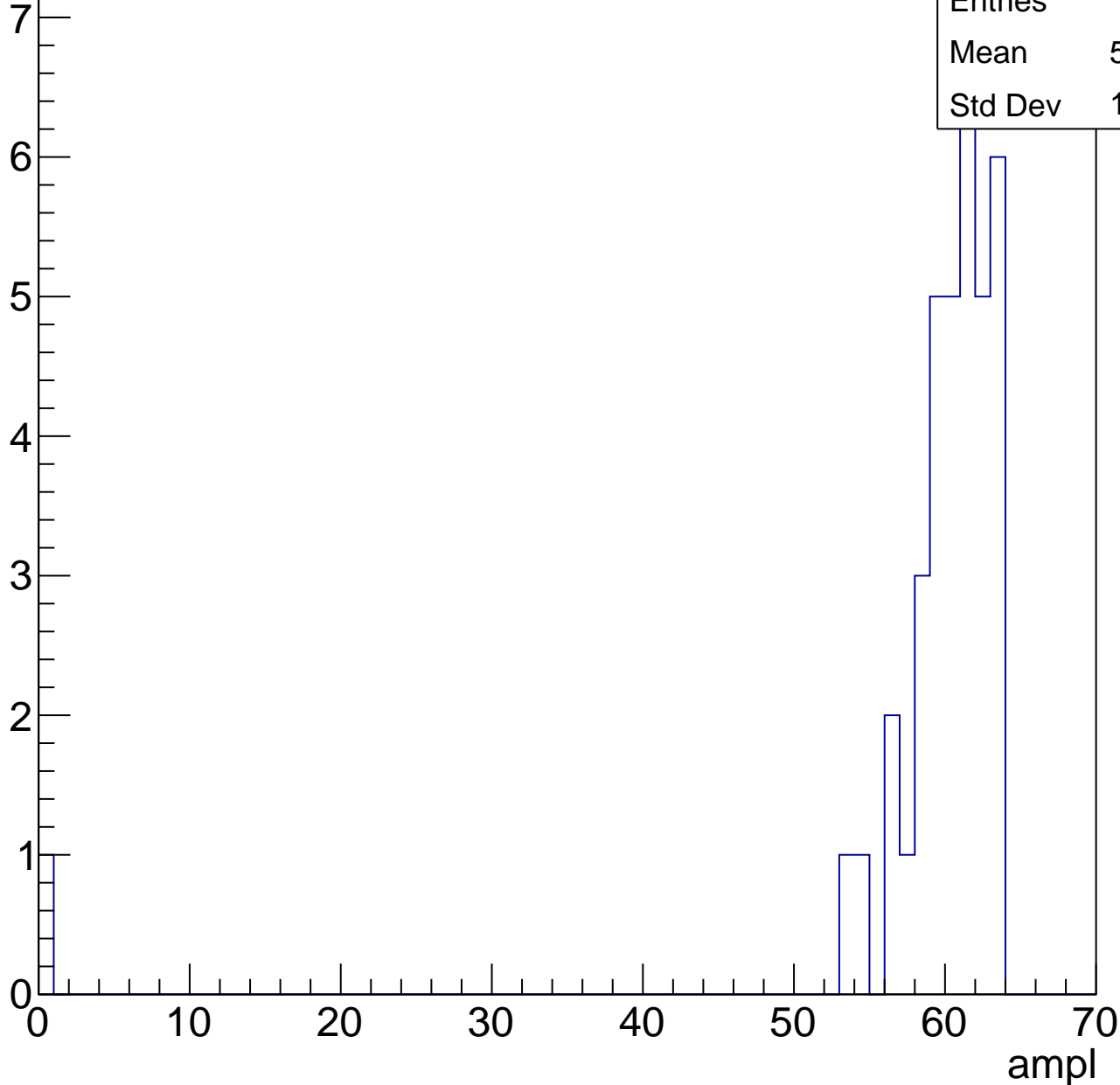


# B0L102S, U17-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

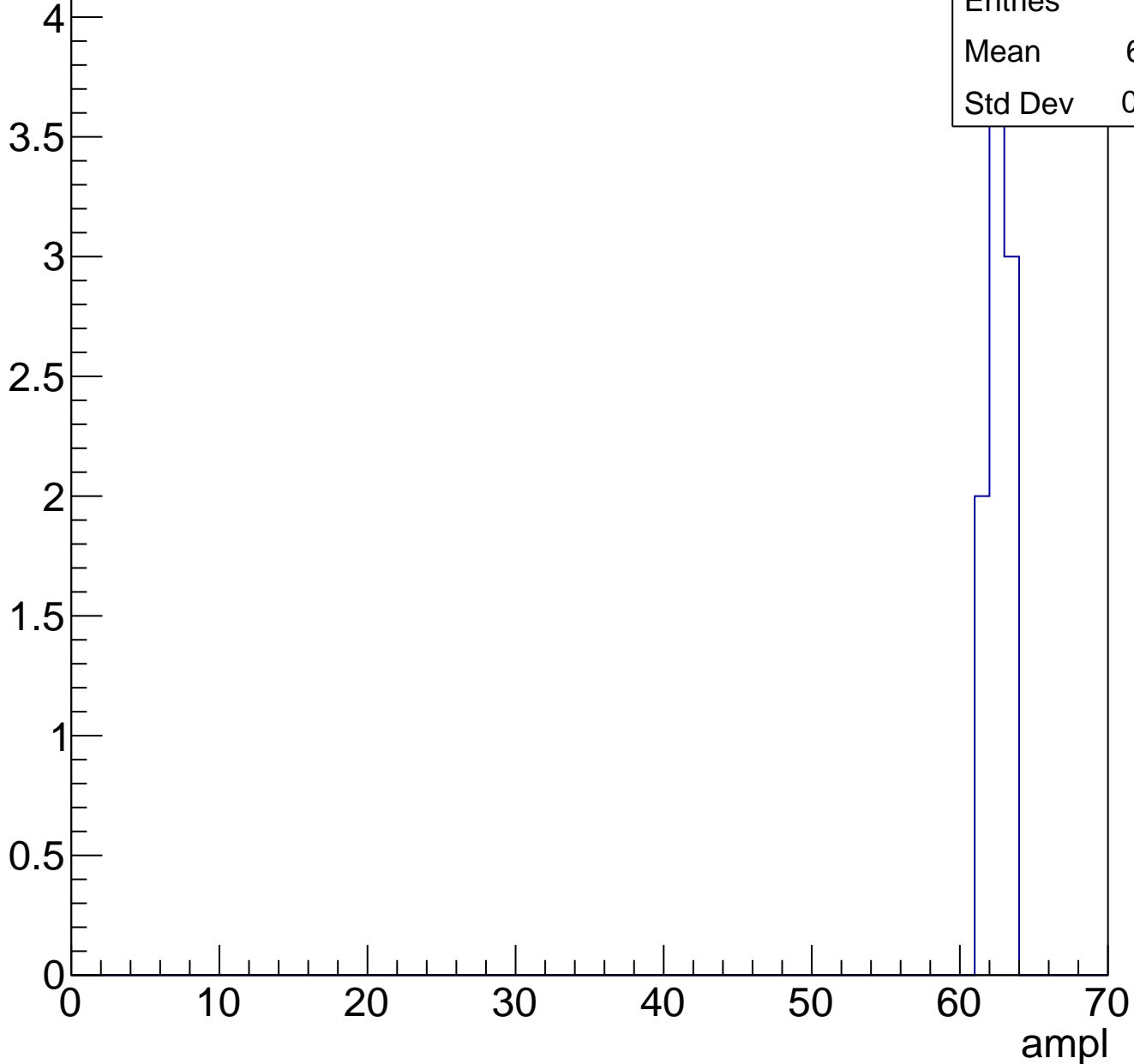
Entries	37
Mean	58.38
Std Dev	10.04



# B0L102S, U17-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch50, adc0

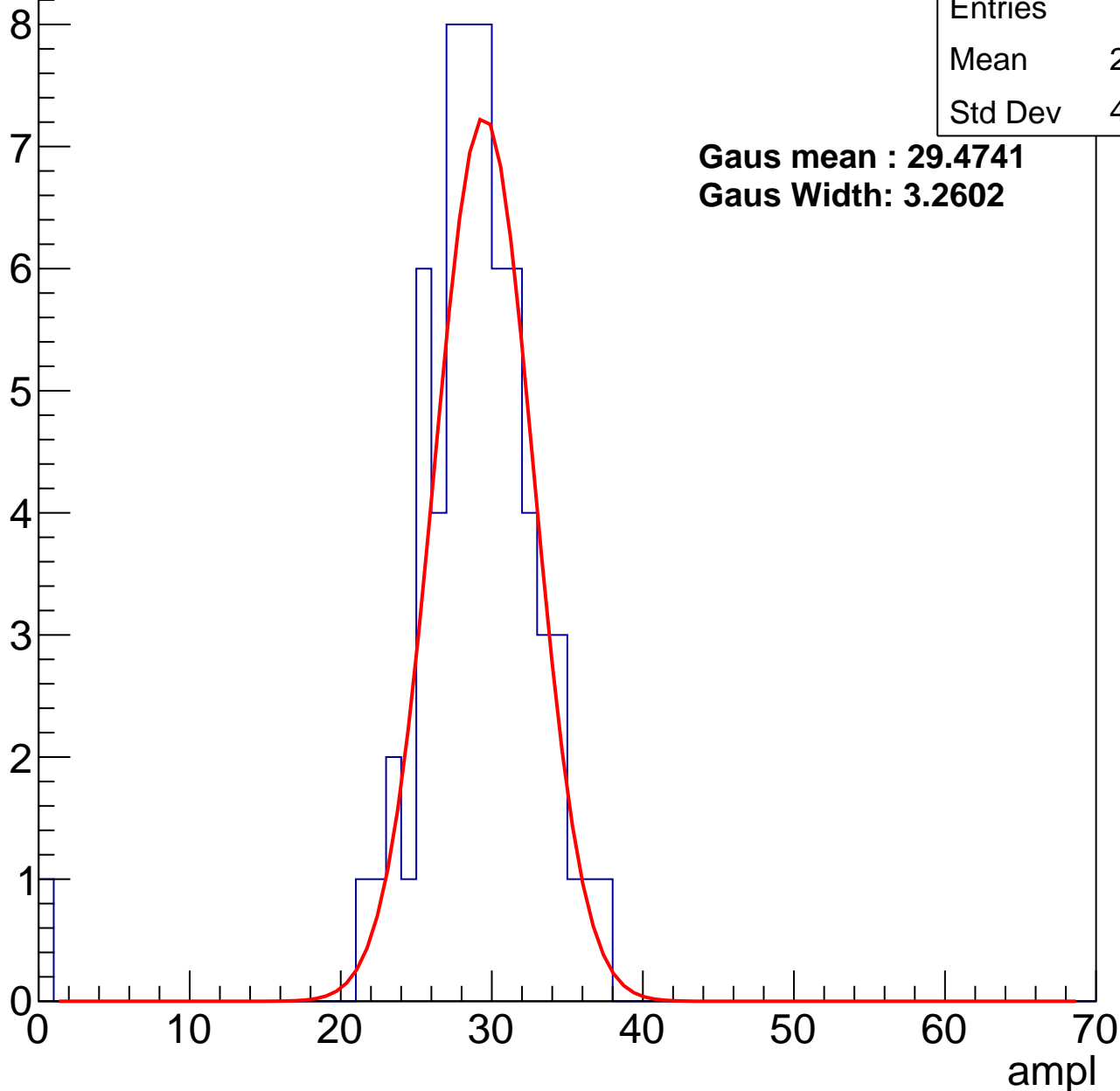
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	28.34
Std Dev	4.856

**Gaus mean : 29.4741**

**Gaus Width: 3.2602**



# B0L102S, U17-ch50, adc1

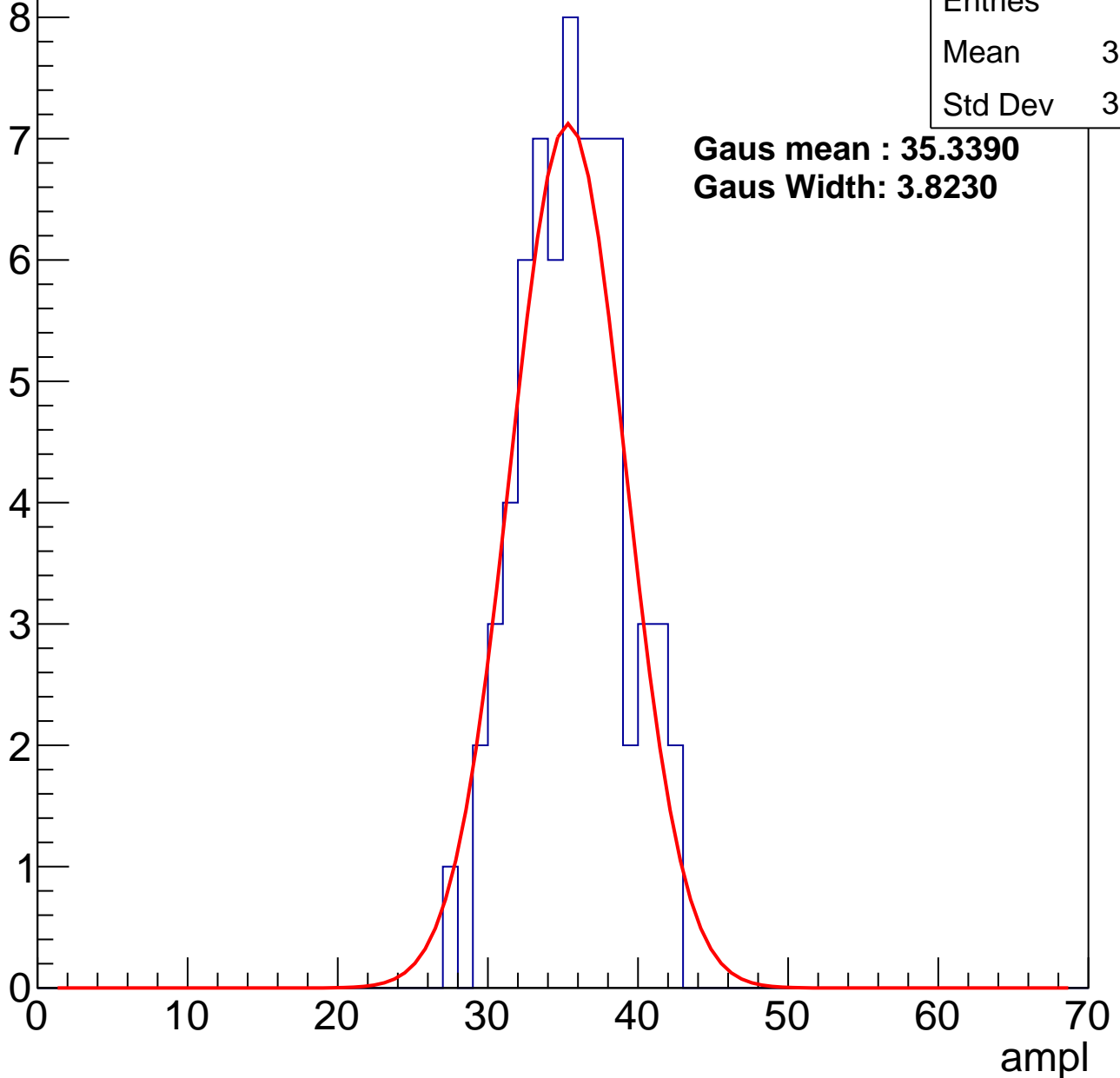
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	35.12
Std Dev	3.372

**Gaus mean : 35.3390**

**Gaus Width: 3.8230**



# B0L102S, U17-ch50, adc2

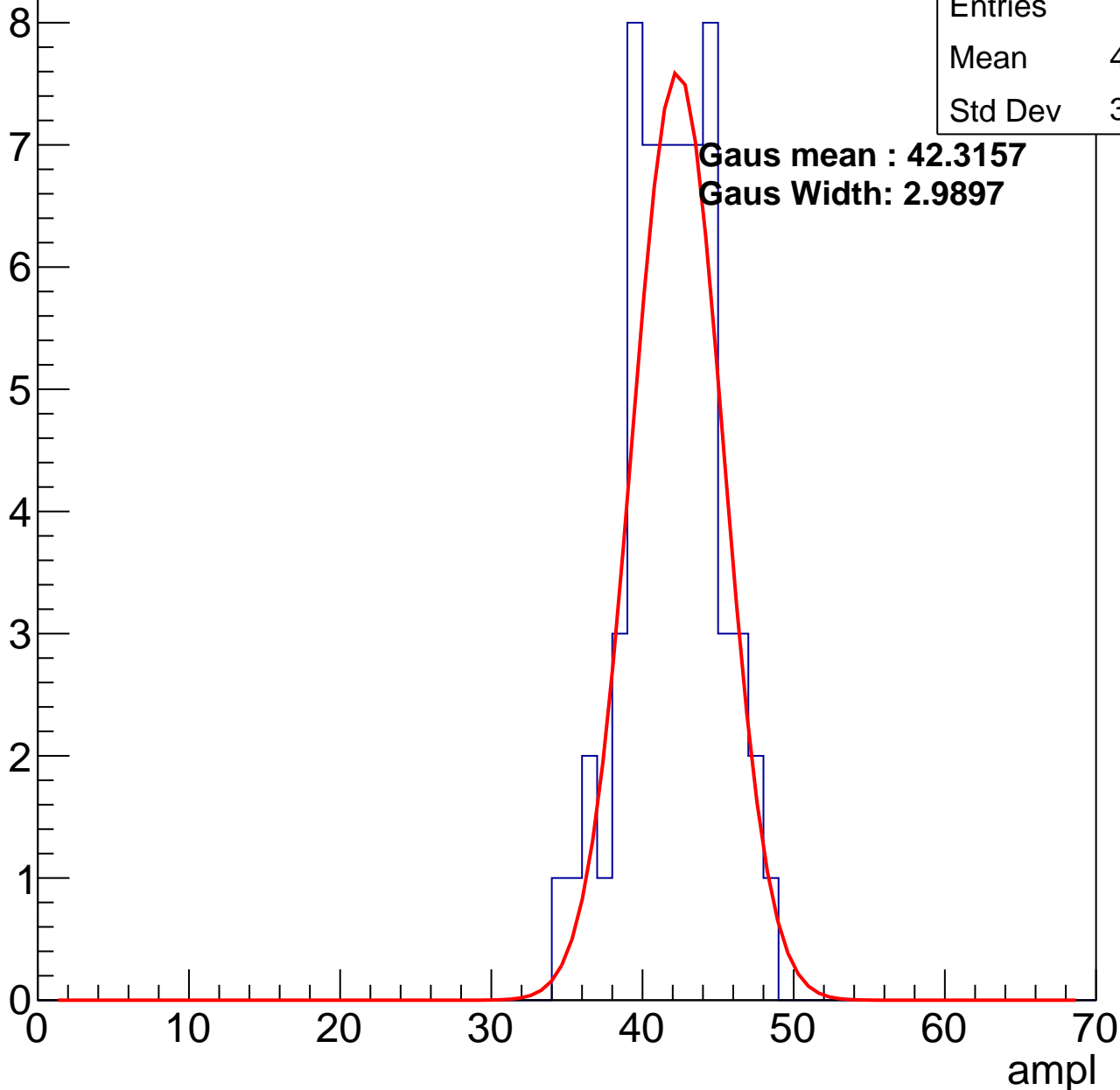
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	41.52
Std Dev	3.006

**Gaus mean : 42.3157**

**Gaus Width: 2.9897**

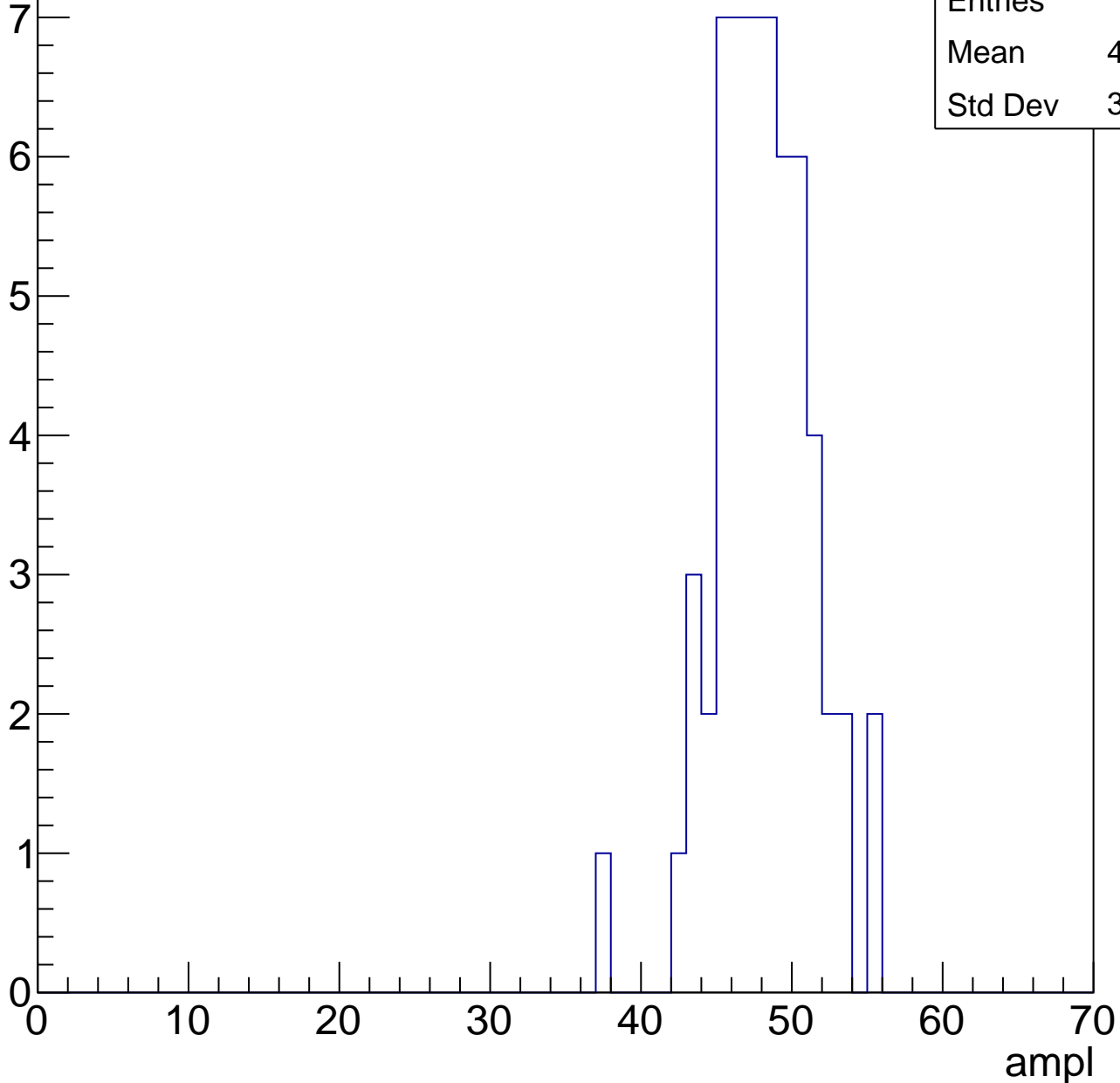


# B0L102S, U17-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	47.65
Std Dev	3.247

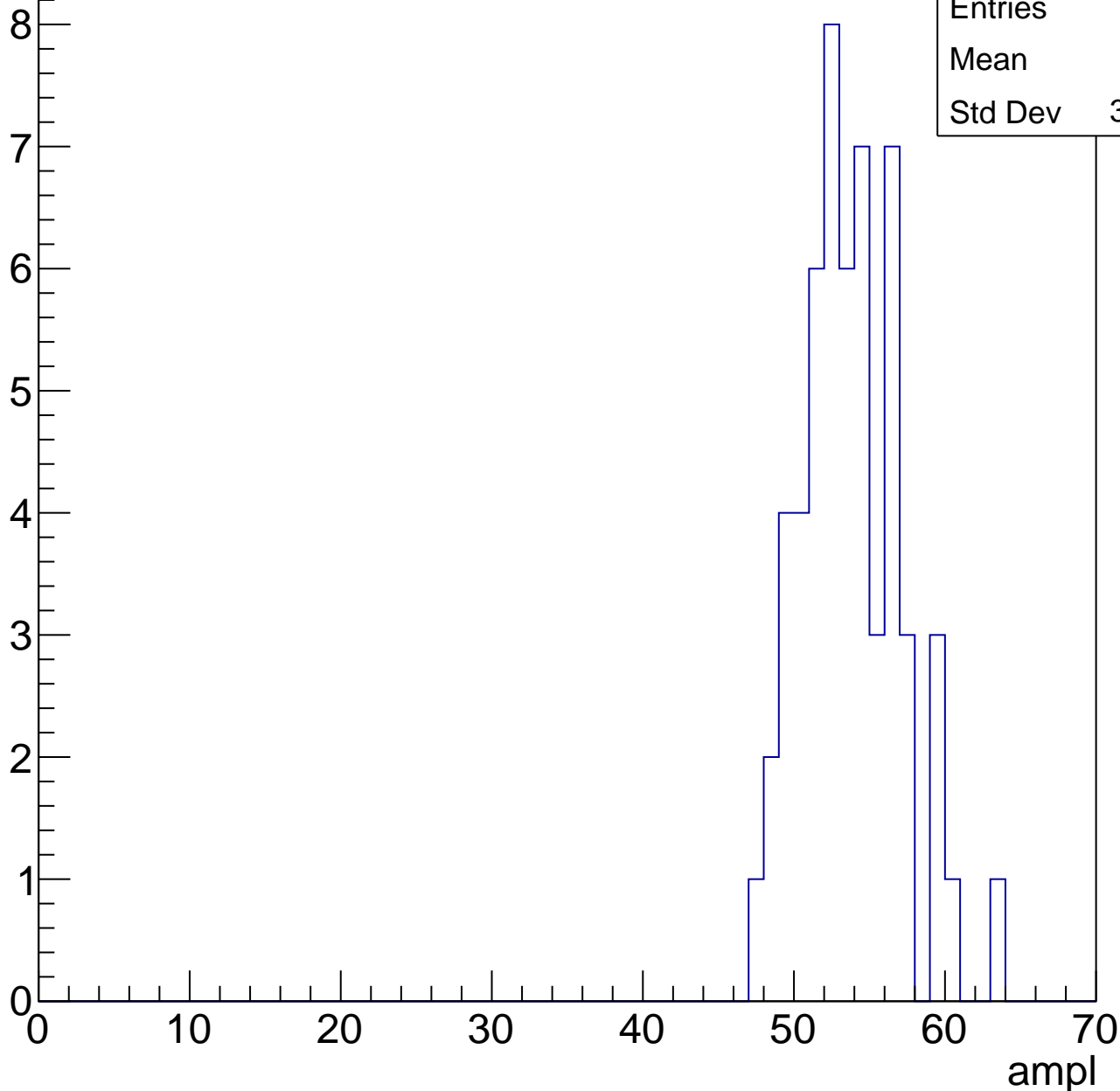


# B0L102S, U17-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	53.3
Std Dev	3.273



# B0L102S, U17-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

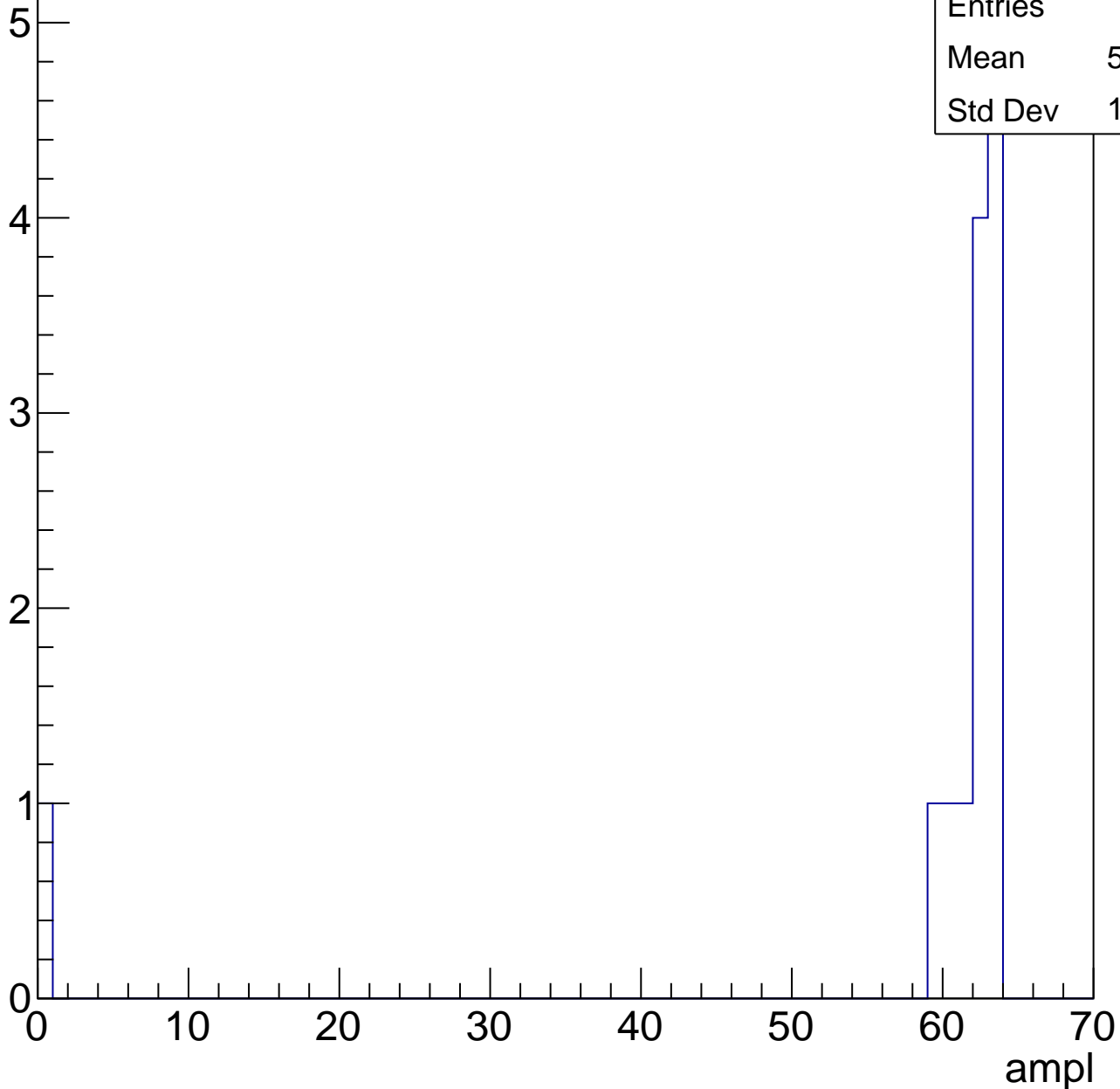
Entries	63
Mean	58.68
Std Dev	2.666

# B0L102S, U17-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	57.15
Std Dev	16.54





# B0L102S, U17-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch51, adc0

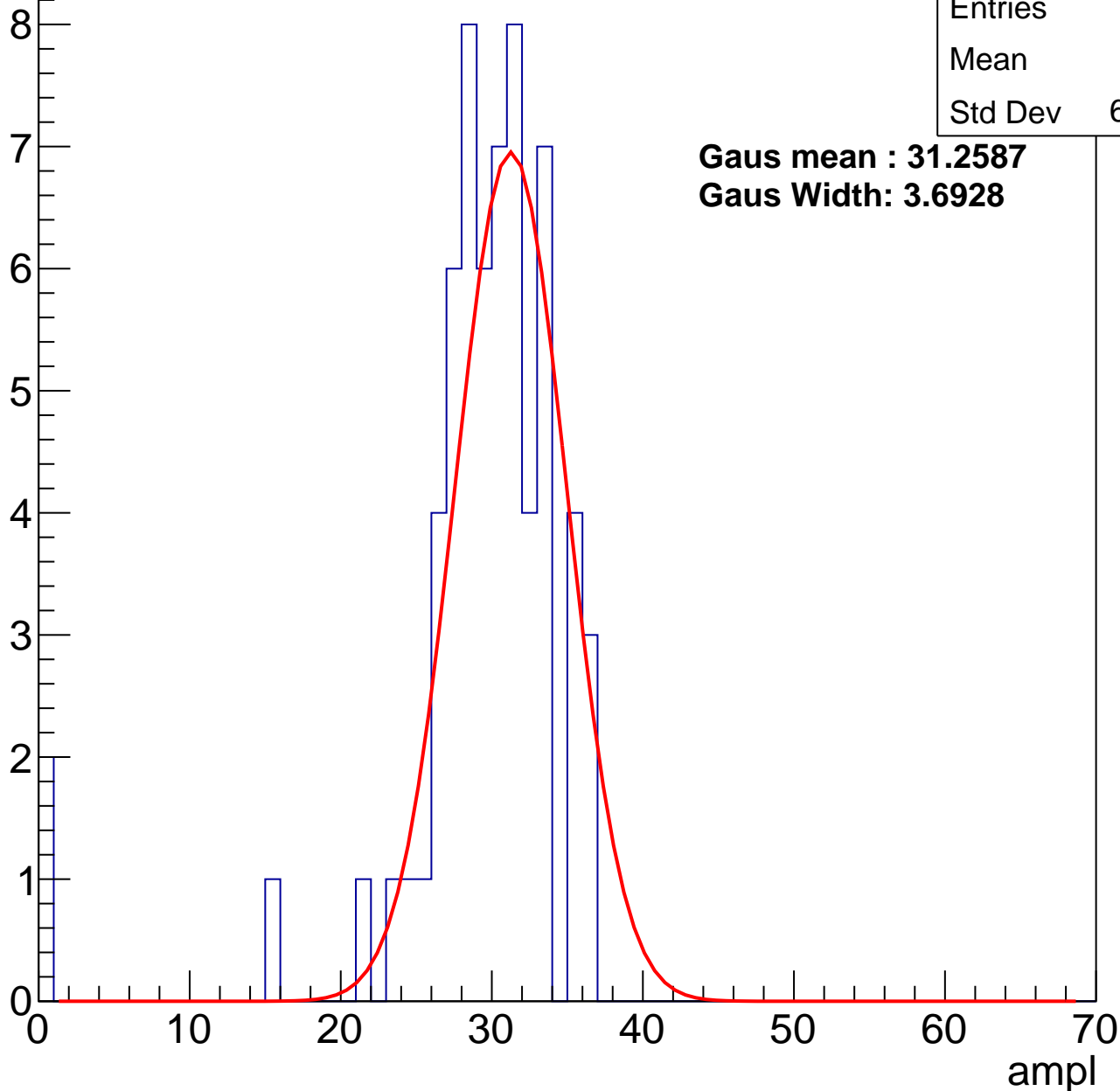
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	28.7
Std Dev	6.324

**Gaus mean : 31.2587**

**Gaus Width: 3.6928**



# B0L102S, U17-ch51, adc1

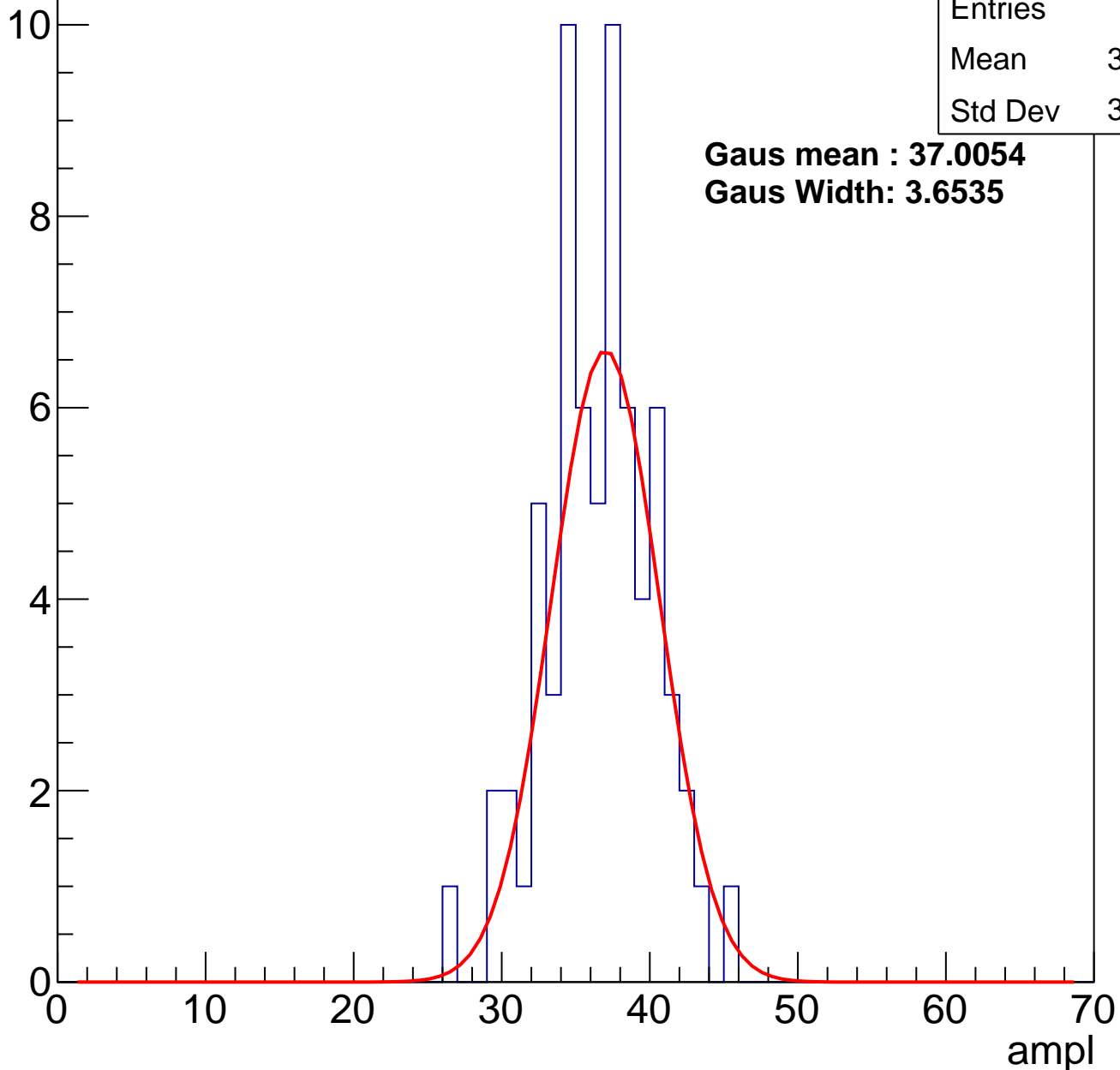
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	36.07
Std Dev	3.619

**Gaus mean : 37.0054**

**Gaus Width: 3.6535**

Entry



# B0L102S, U17-ch51, adc2

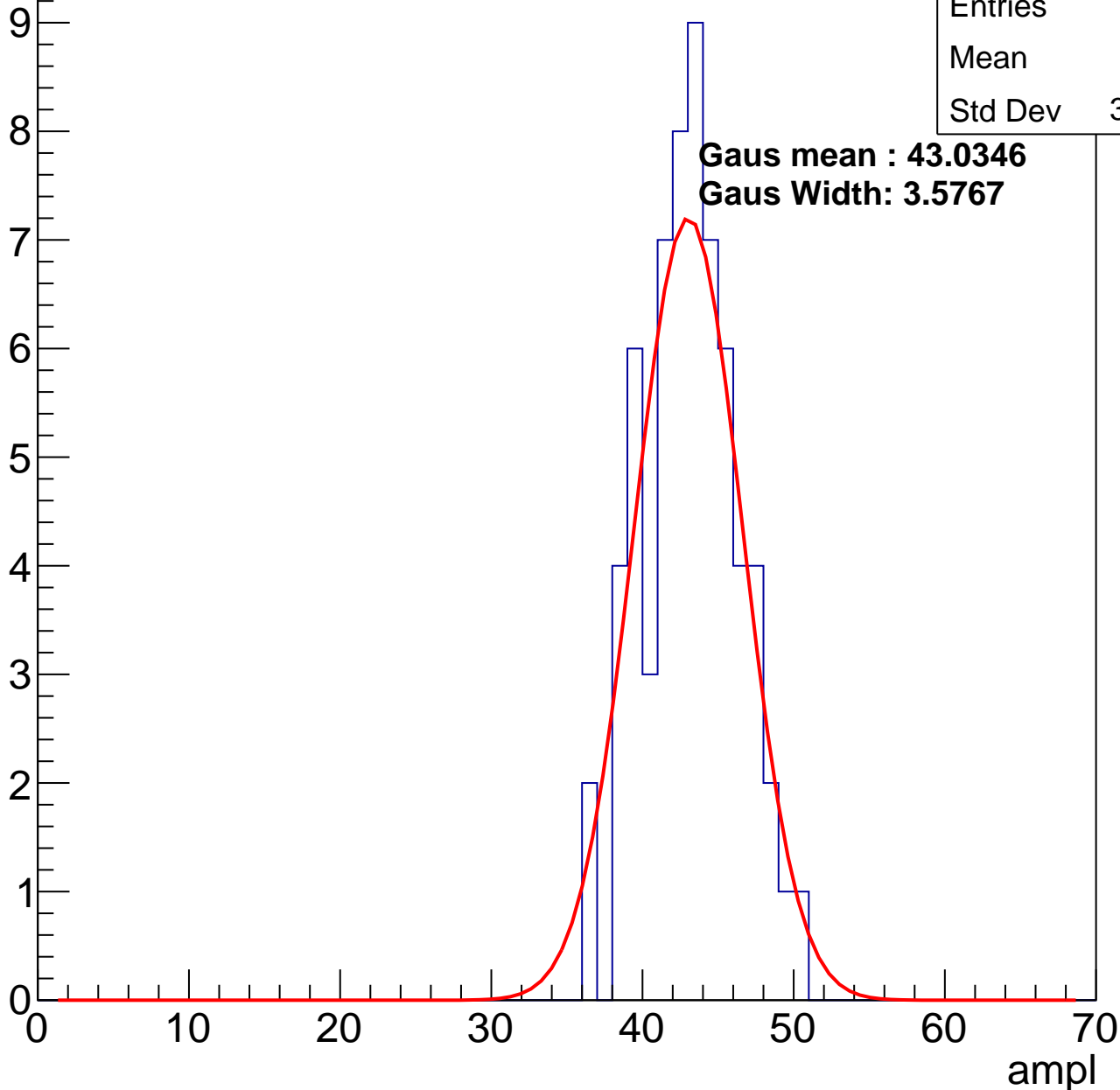
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.7
Std Dev	3.106

**Gaus mean : 43.0346**

**Gaus Width: 3.5767**

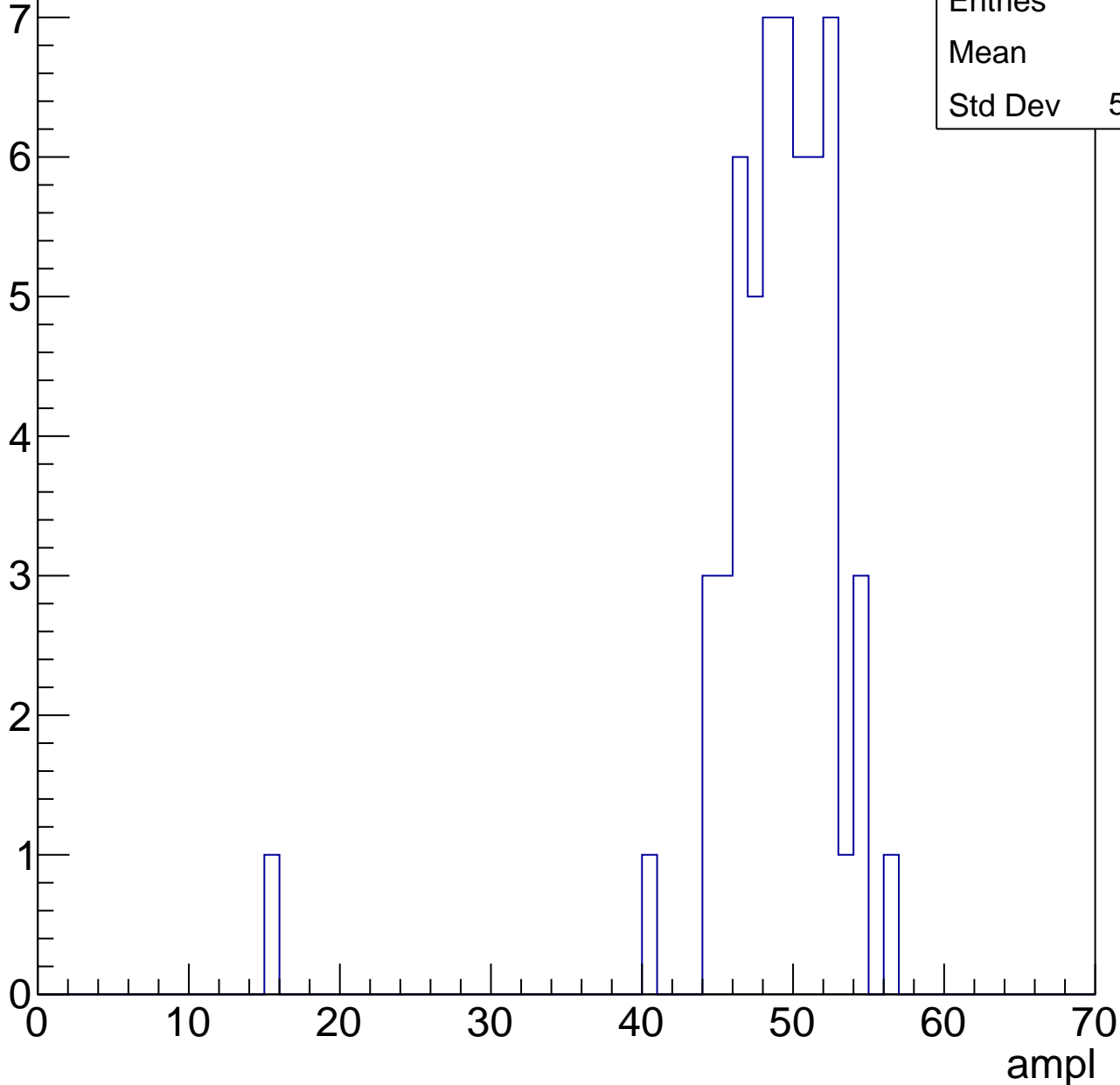


# B0L102S, U17-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.3
Std Dev	5.374

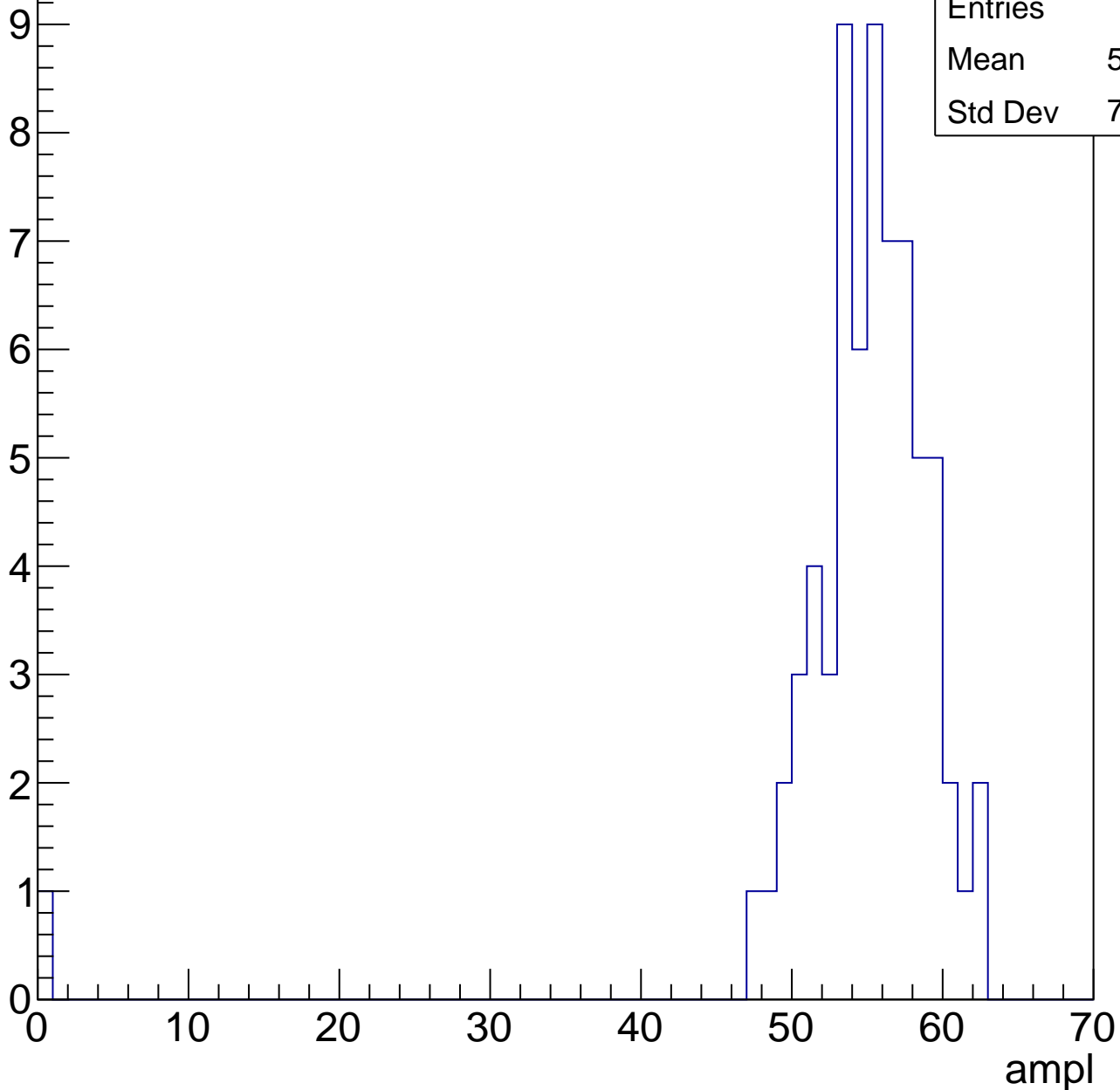


# B0L102S, U17-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	54.12
Std Dev	7.385

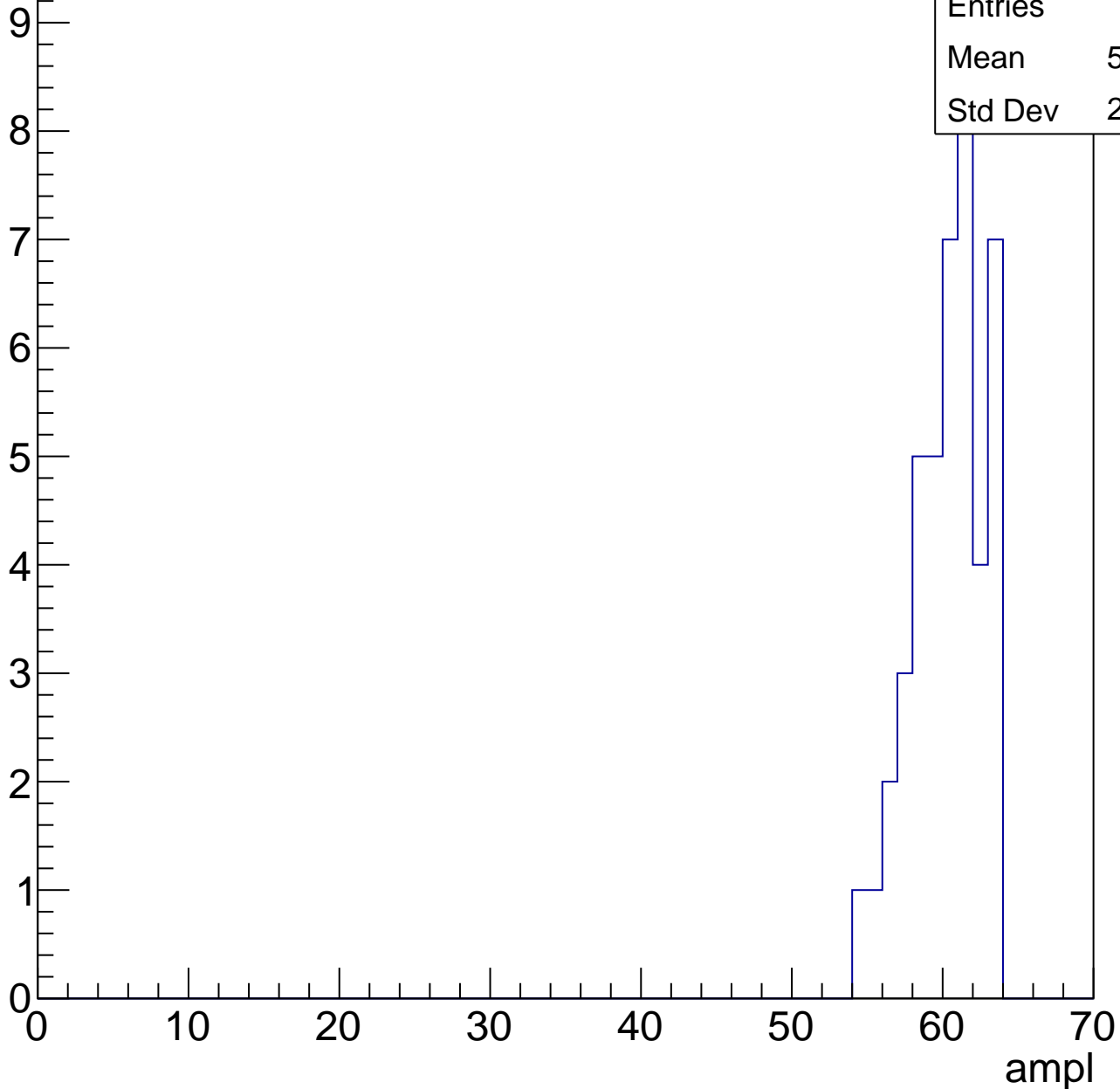


# B0L102S, U17-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

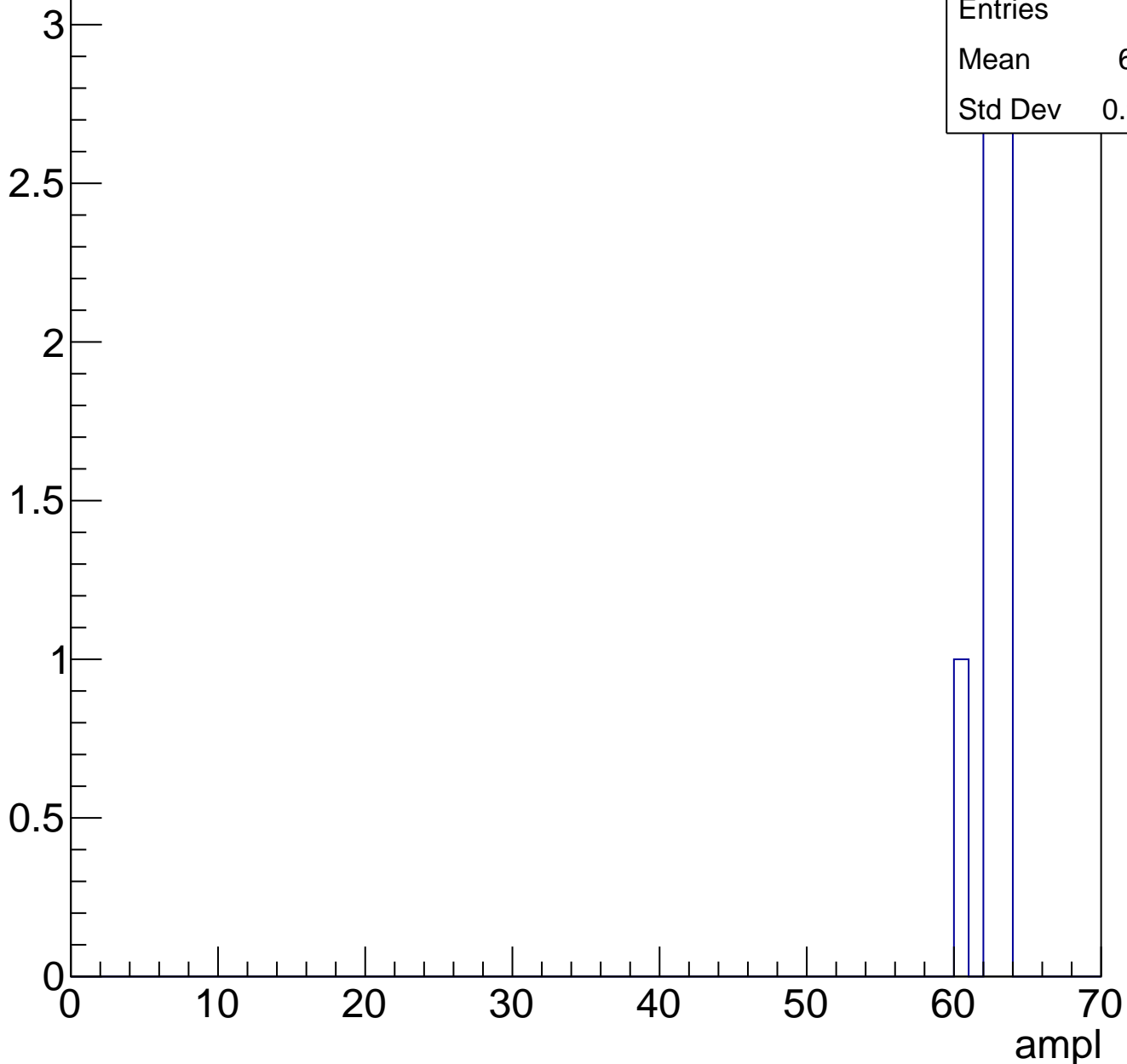
Entries	44
Mean	59.89
Std Dev	2.298



# B0L102S, U17-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch52, adc0

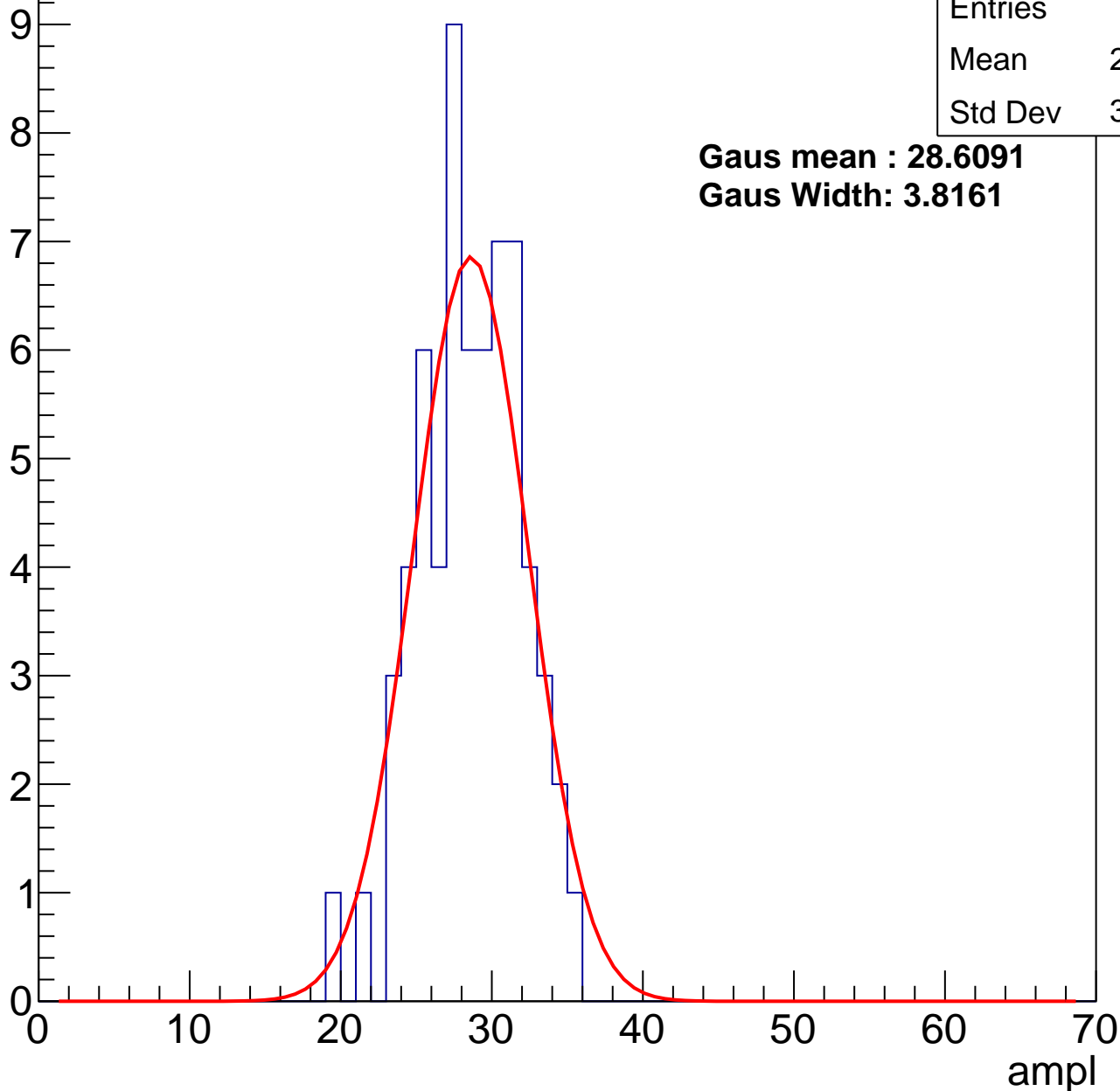
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	28.14
Std Dev	3.316

**Gaus mean : 28.6091**

**Gaus Width: 3.8161**



# B0L102S, U17-ch52, adc1

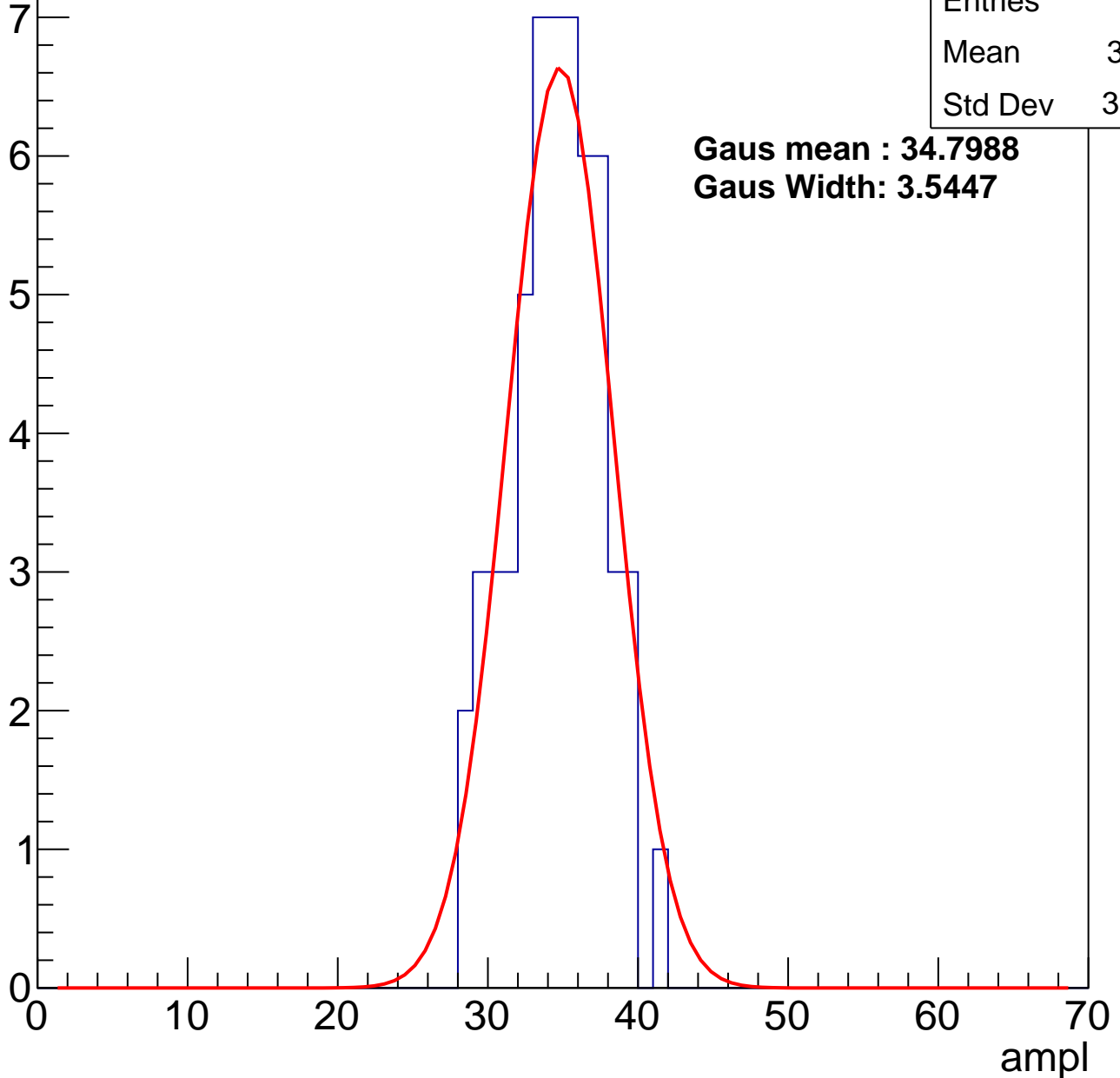
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	34.11
Std Dev	3.004

**Gaus mean : 34.7988**

**Gaus Width: 3.5447**



# B0L102S, U17-ch52, adc2

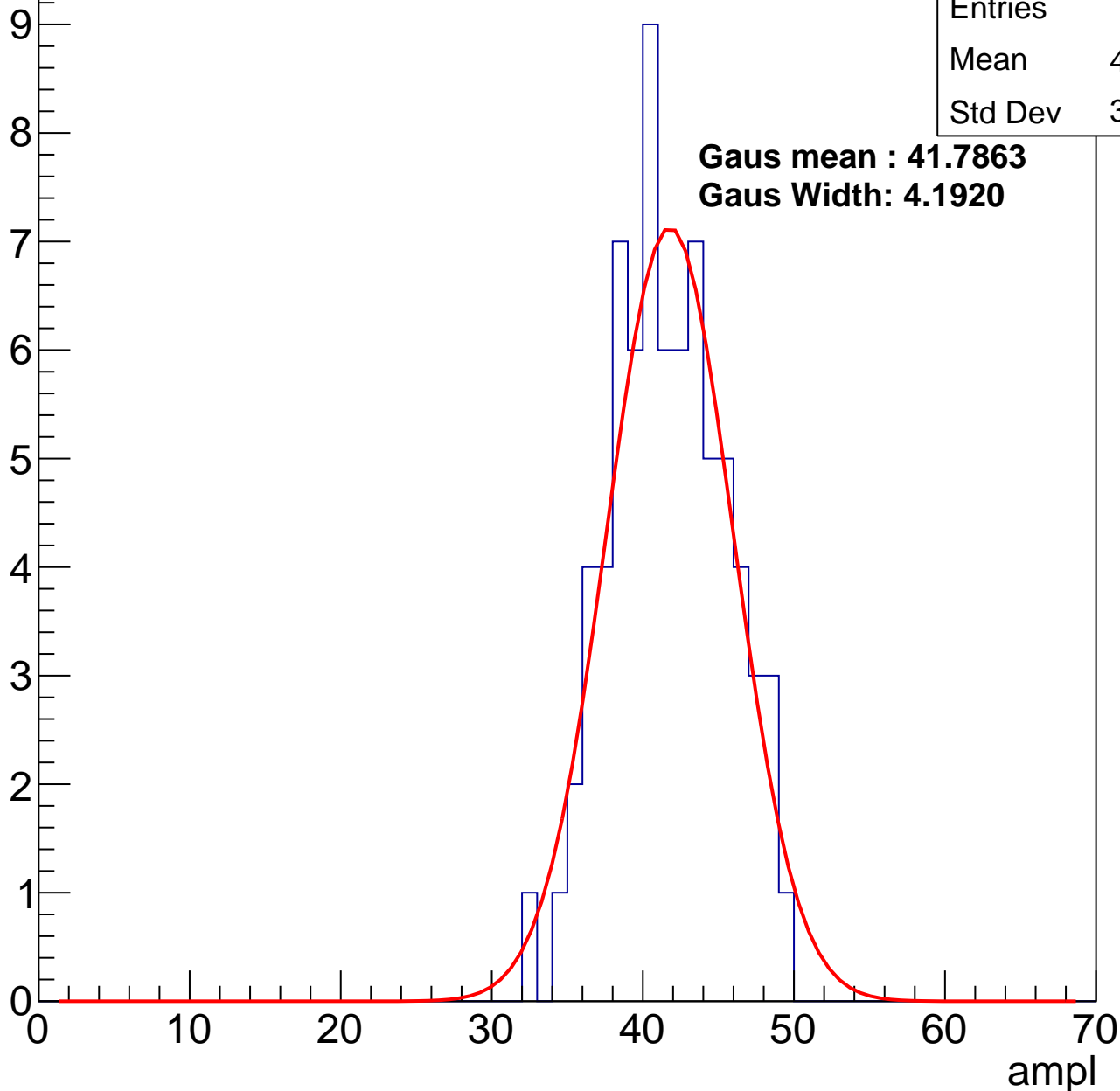
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	41.22
Std Dev	3.743

**Gaus mean : 41.7863**

**Gaus Width: 4.1920**

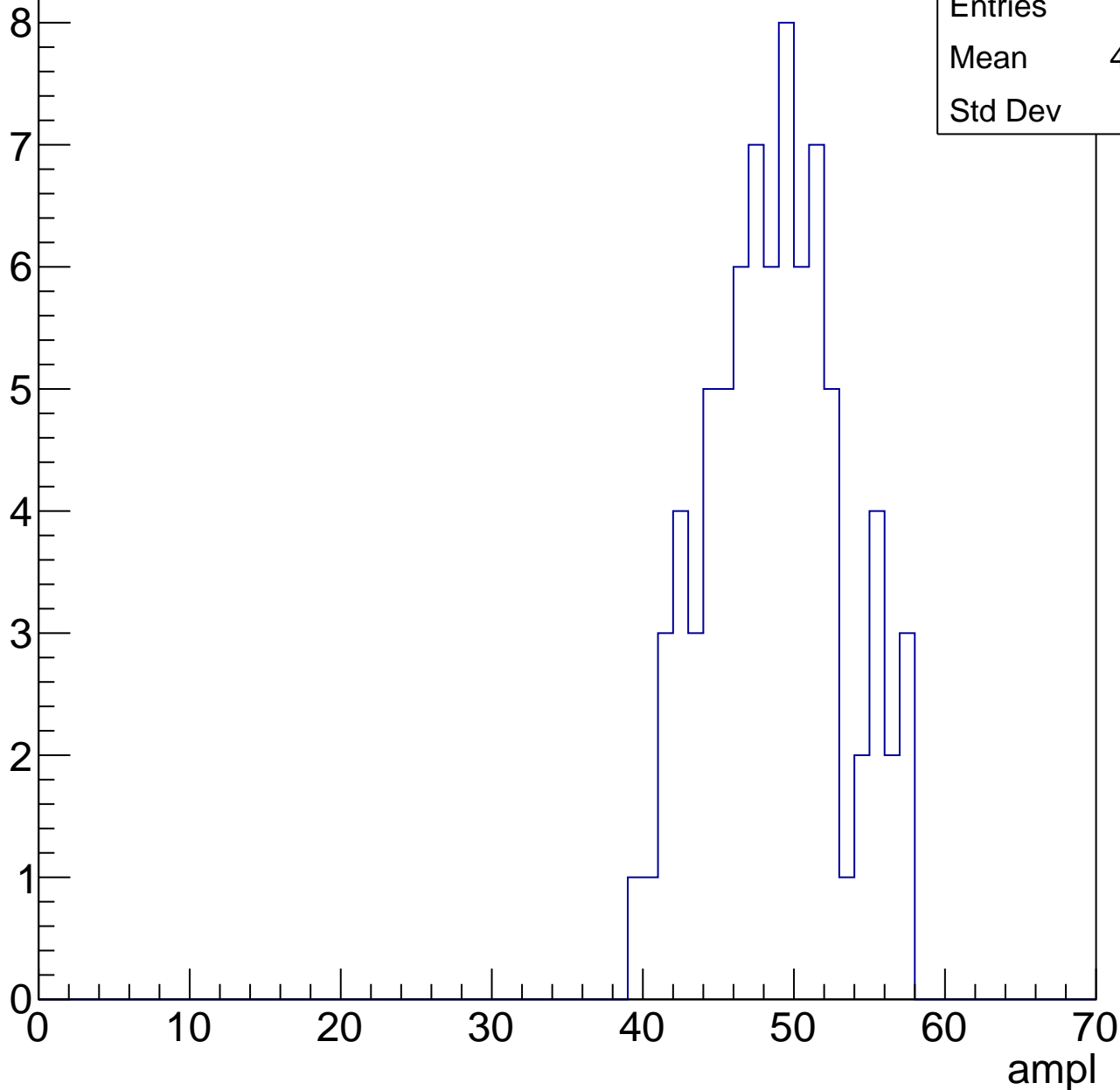


# B0L102S, U17-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	48.23
Std Dev	4.36

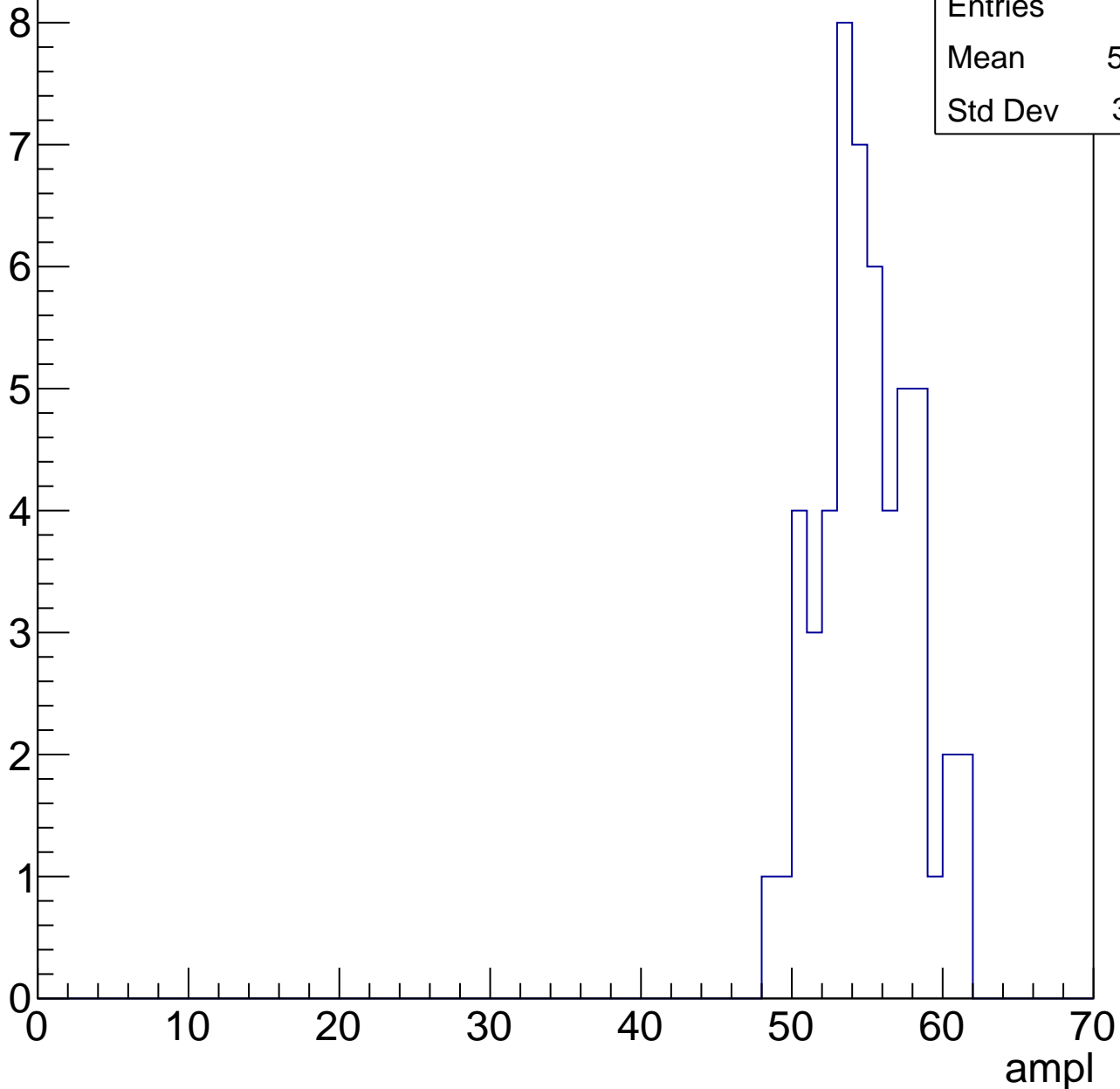


# B0L102S, U17-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	54.53
Std Dev	3.081



# B0L102S, U17-ch52, adc5

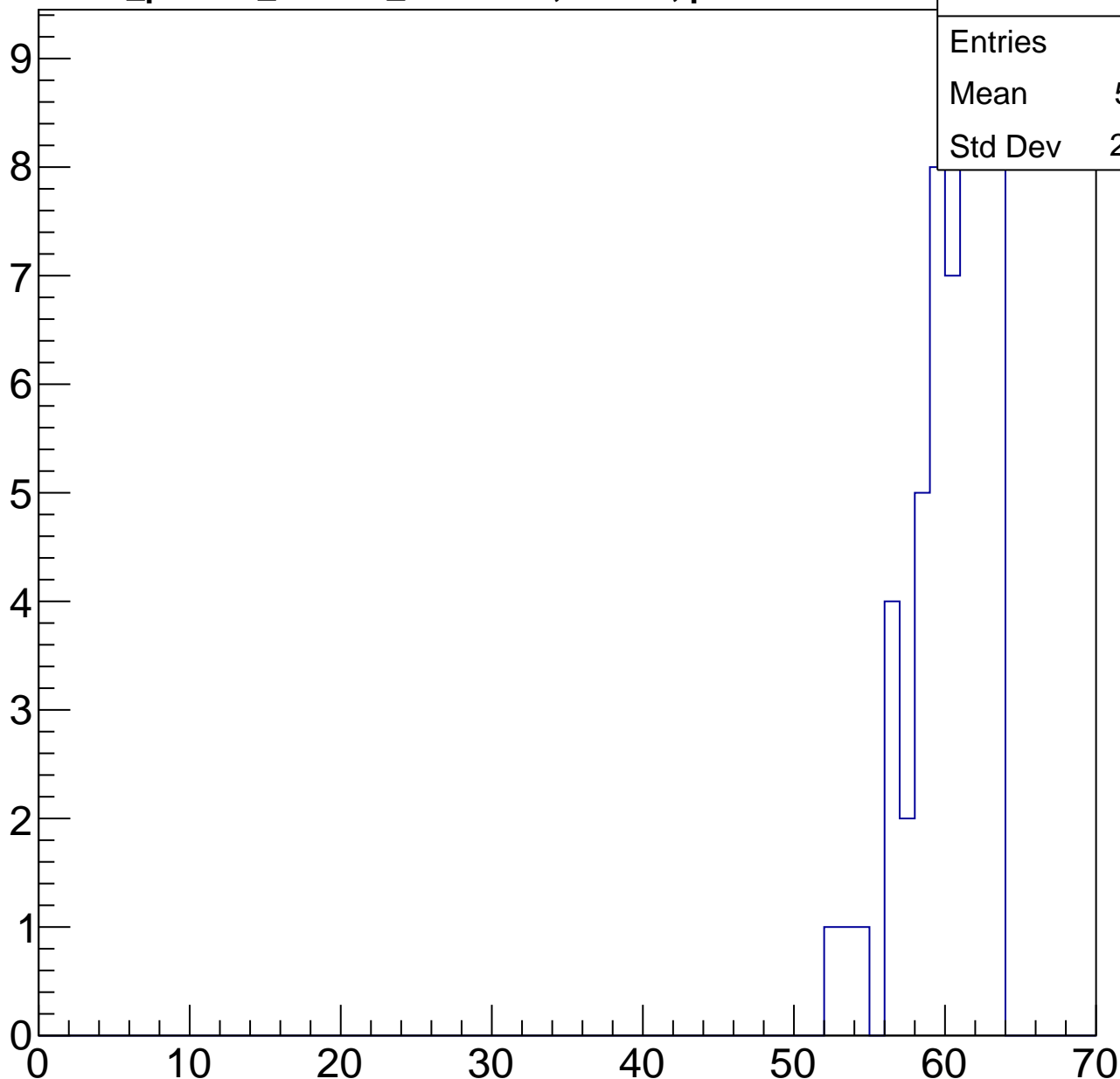
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	59.81
Std Dev	2.646

ampl



# B0L102S, U17-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

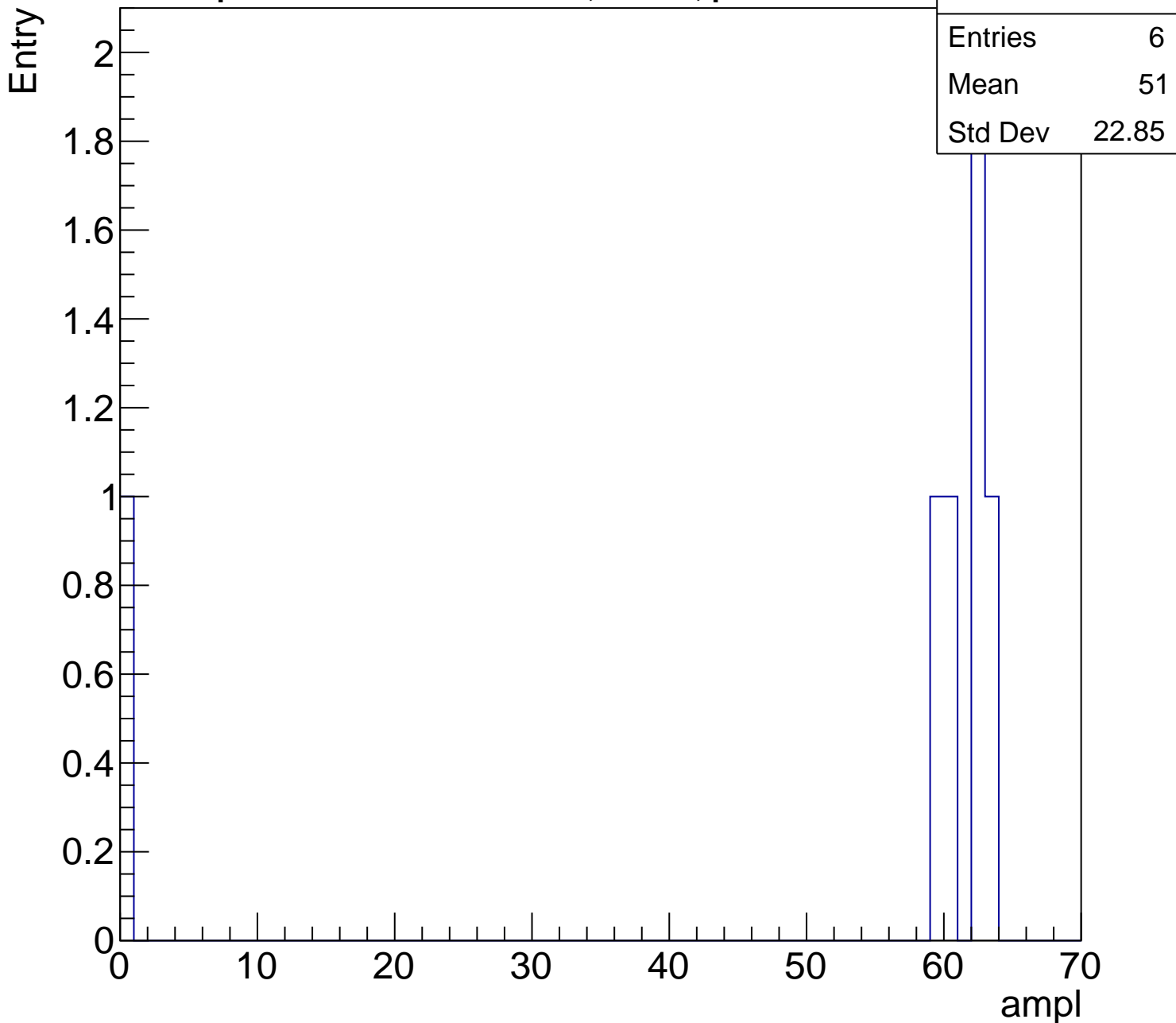
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51
Std Dev	22.85

0 10 20 30 40 50 60 70

ampl





# B0L102S, U17-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch53, adc0

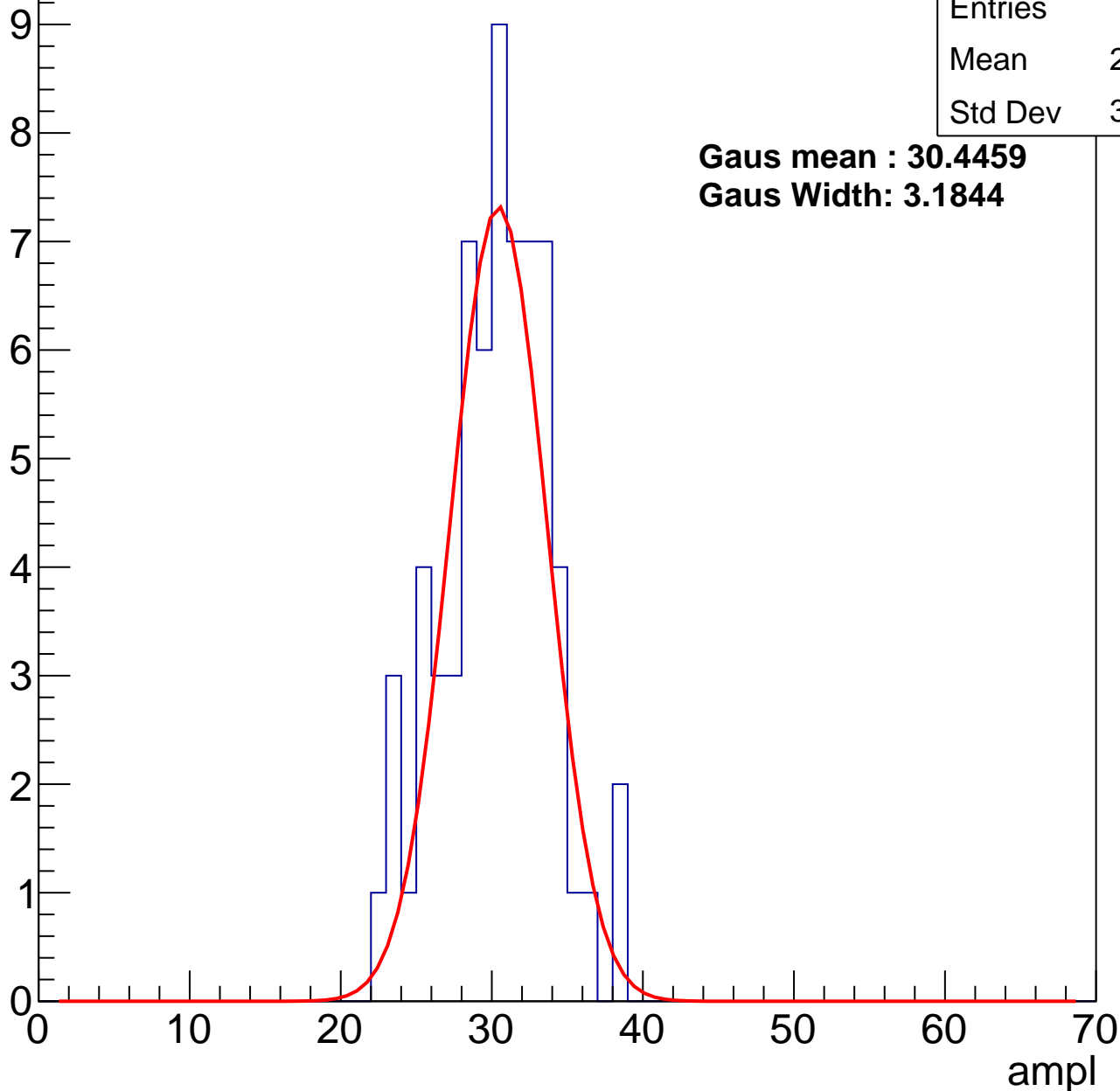
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	29.83
Std Dev	3.493

**Gaus mean : 30.4459**

**Gaus Width: 3.1844**

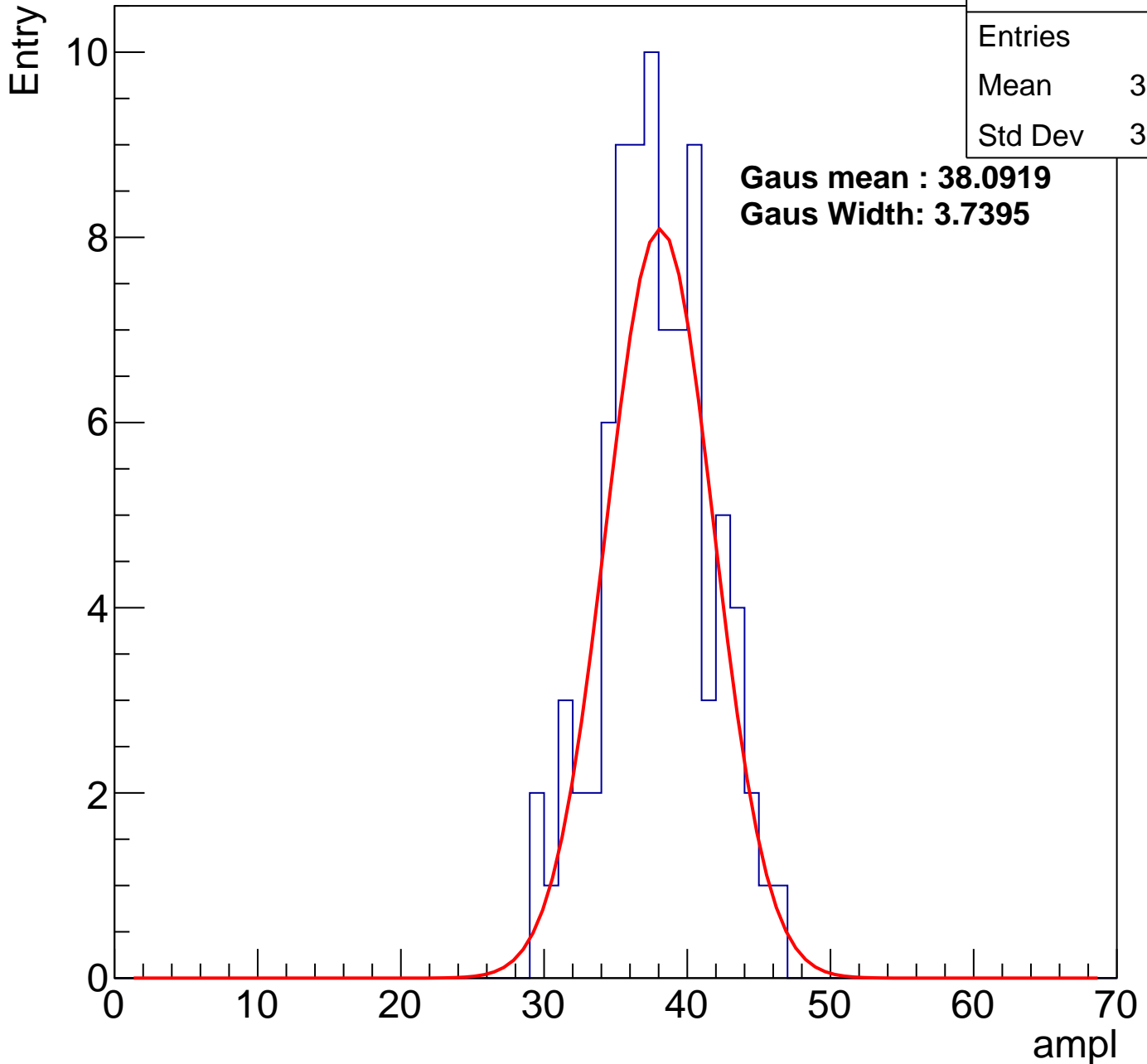


# B0L102S, U17-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	83
Mean	37.43
Std Dev	3.684

**Gaus mean : 38.0919**  
**Gaus Width: 3.7395**



# B0L102S, U17-ch53, adc2

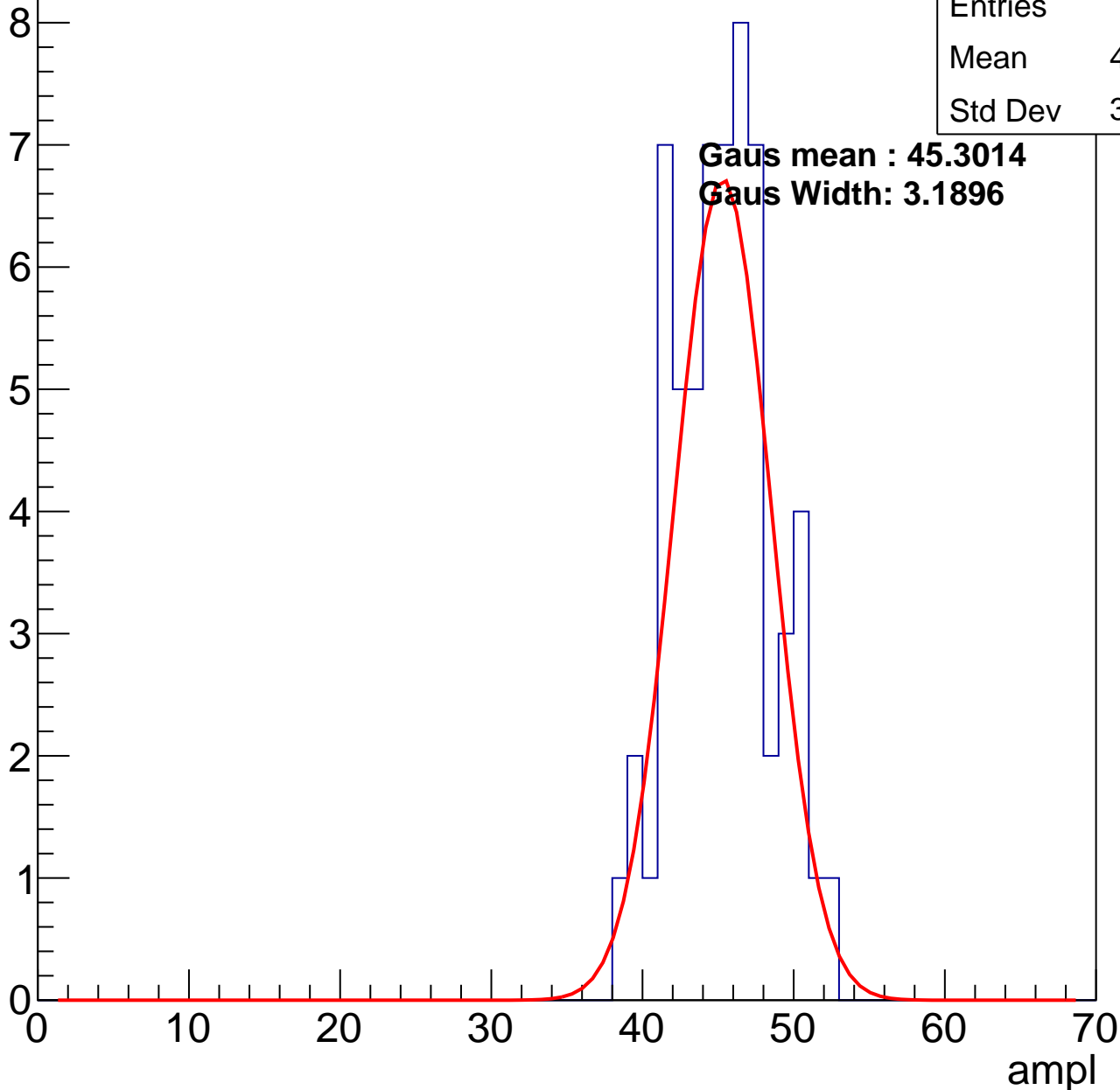
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	44.82
Std Dev	3.175

Gaus mean : 45.3014

Gaus Width: 3.1896

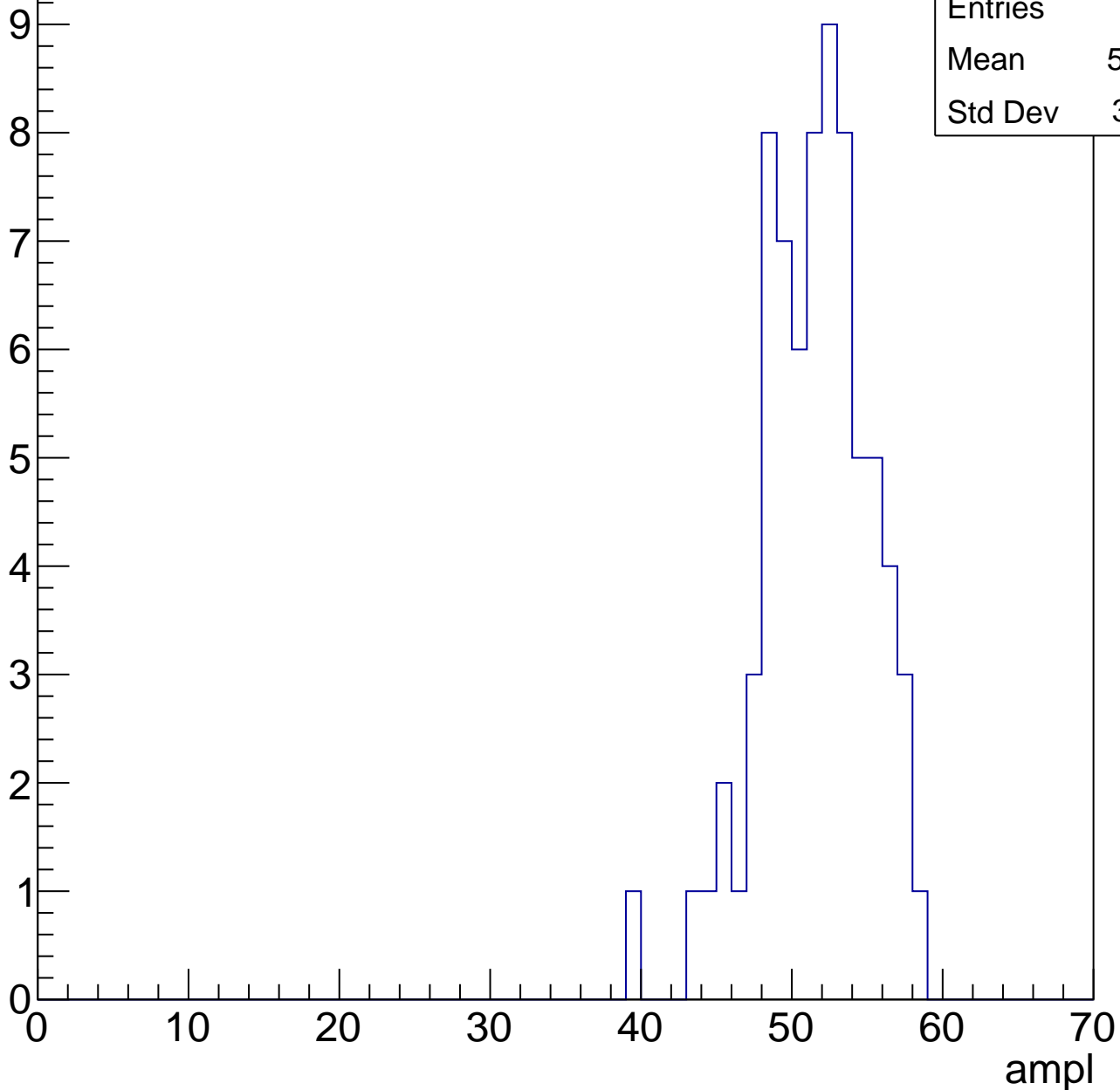


# B0L102S, U17-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

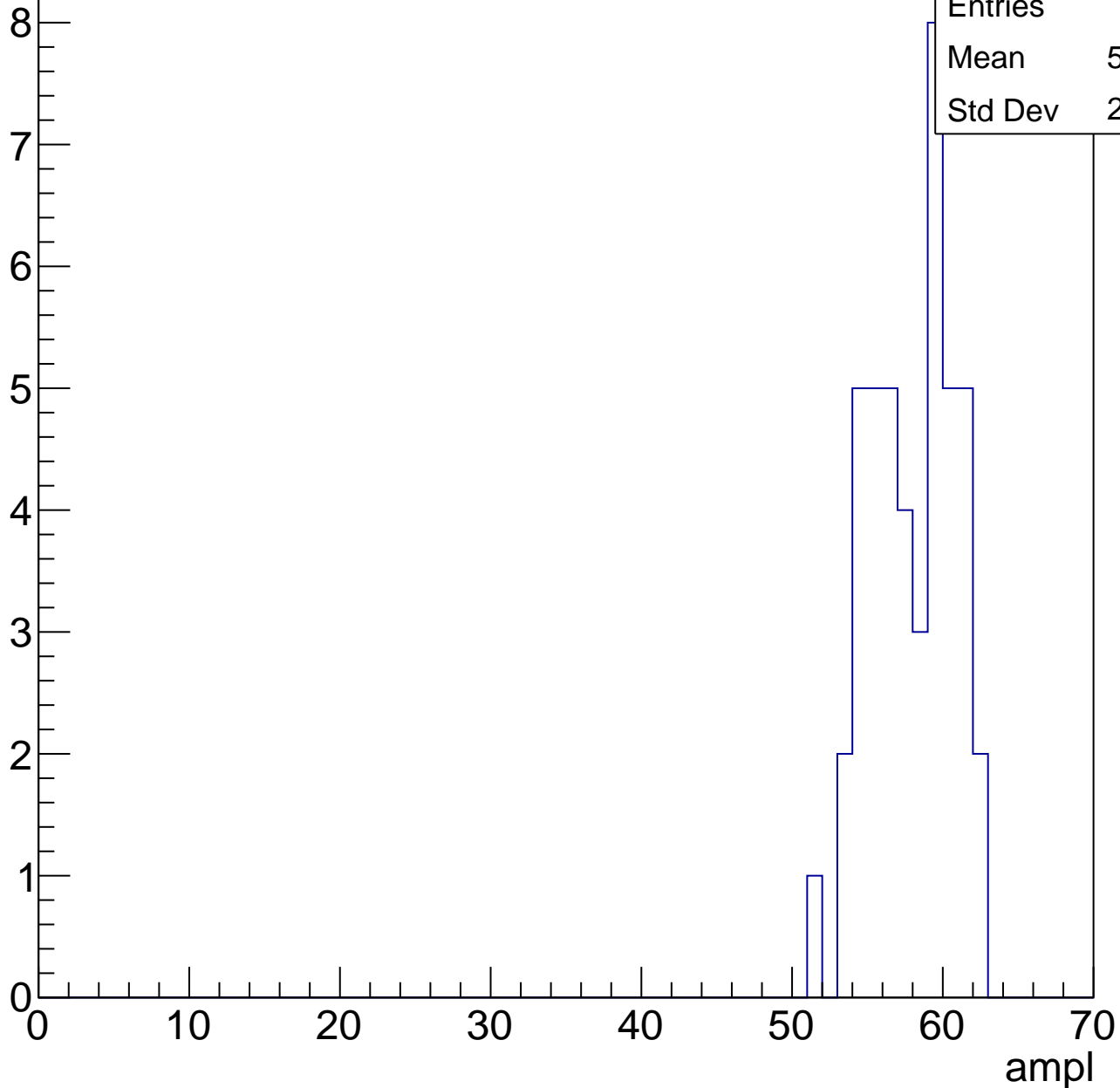
Entries	73
Mean	51.07
Std Dev	3.571



# B0L102S, U17-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

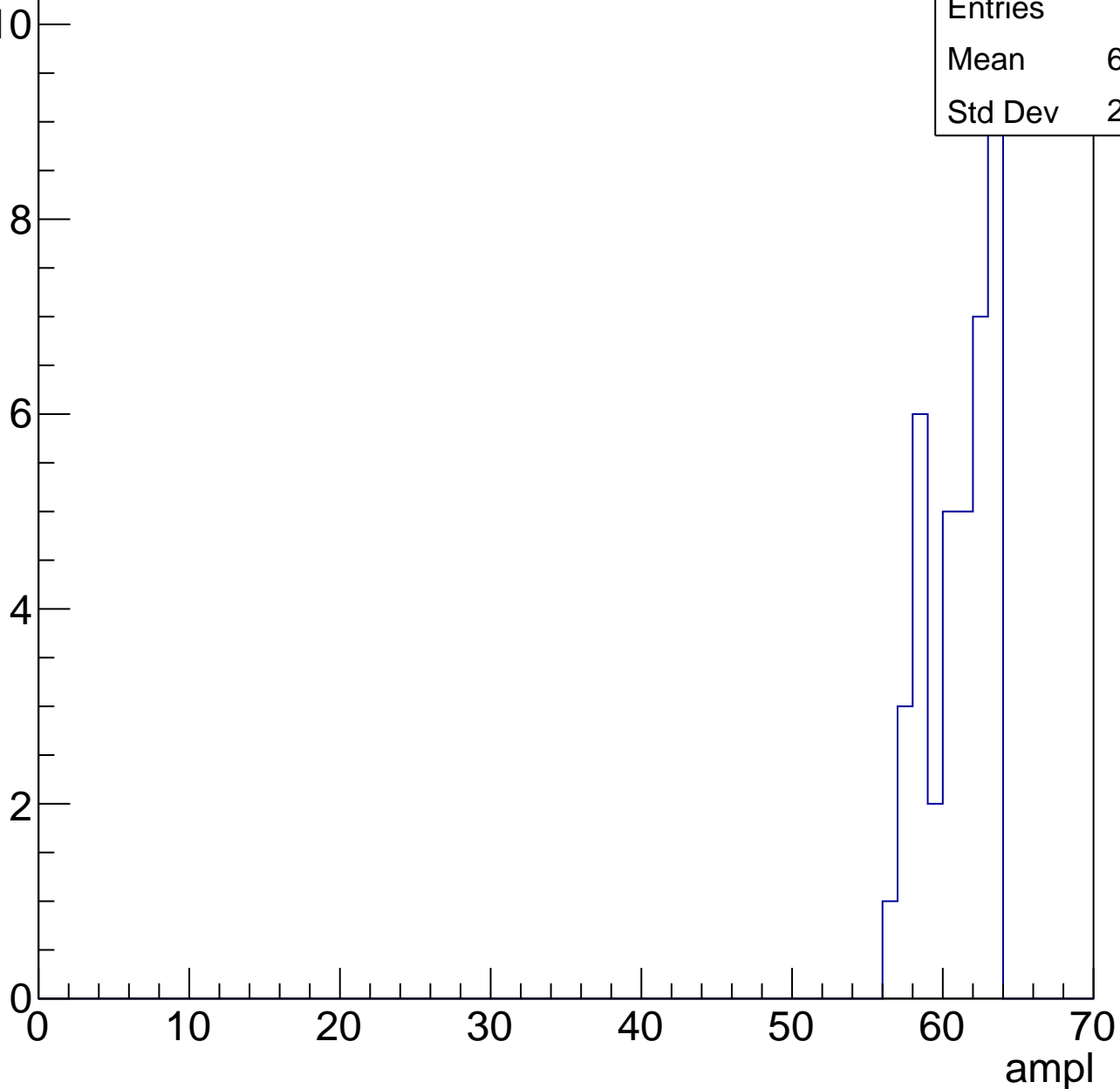


# B0L102S, U17-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

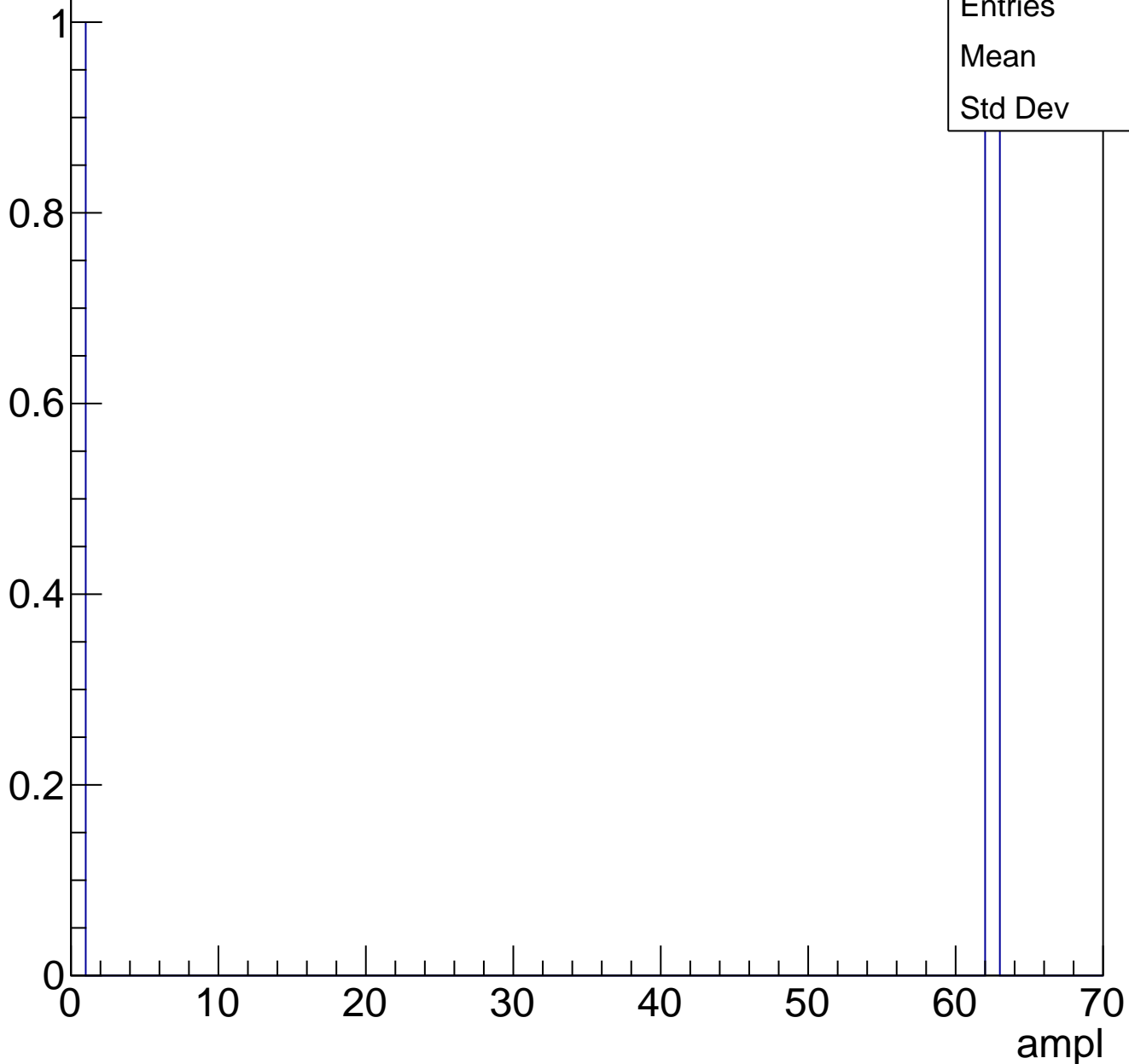
Entries	39
Mean	60.56
Std Dev	2.146



# B0L102S, U17-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	28.45
Std Dev	4.615

**Gaus mean : 28.8564**

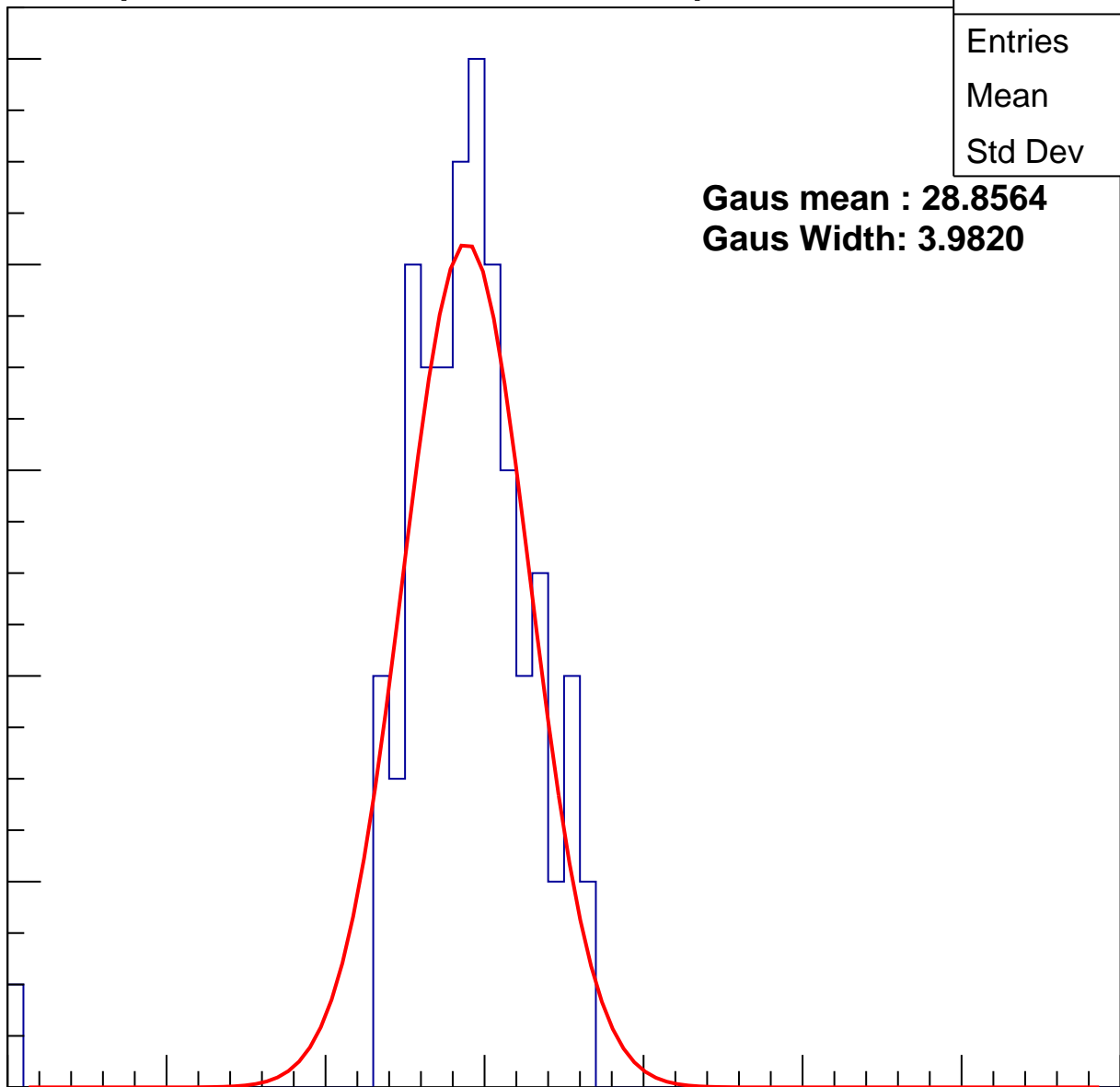
**Gaus Width: 3.9820**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch54, adc1

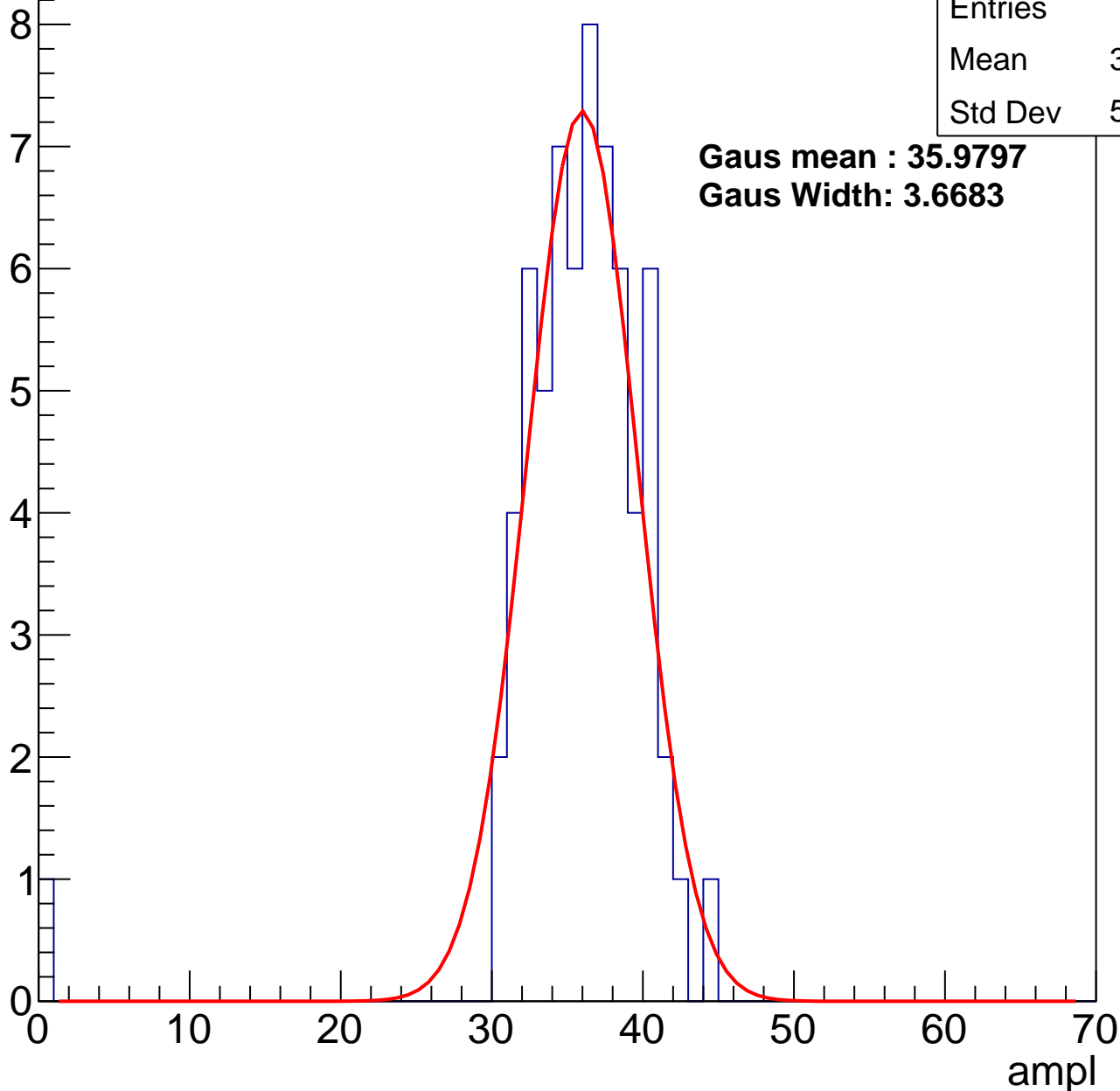
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	35.27
Std Dev	5.395

**Gaus mean : 35.9797**

**Gaus Width: 3.6683**



# B0L102S, U17-ch54, adc2

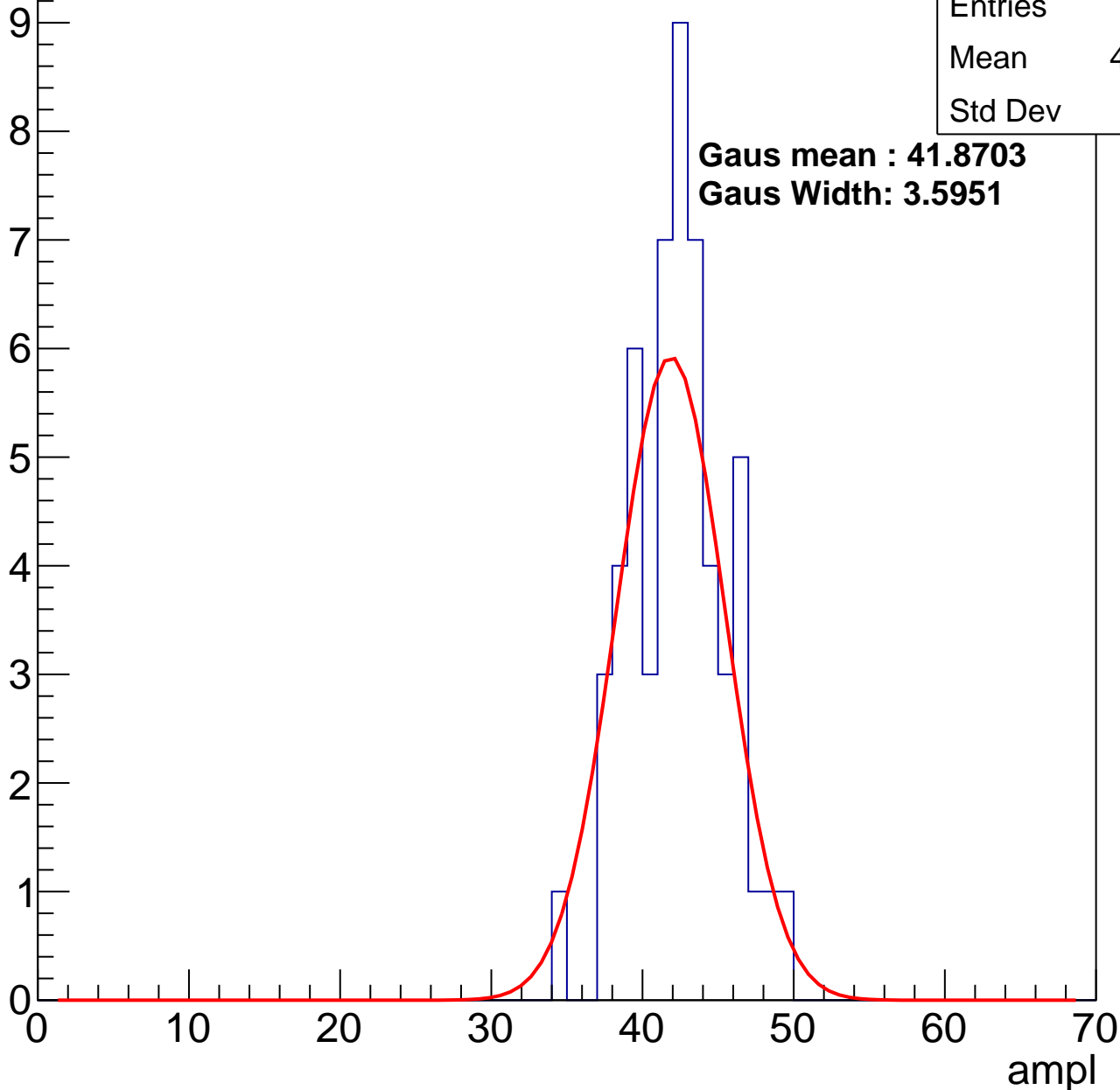
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.85
Std Dev	3.06

**Gaus mean : 41.8703**

**Gaus Width: 3.5951**

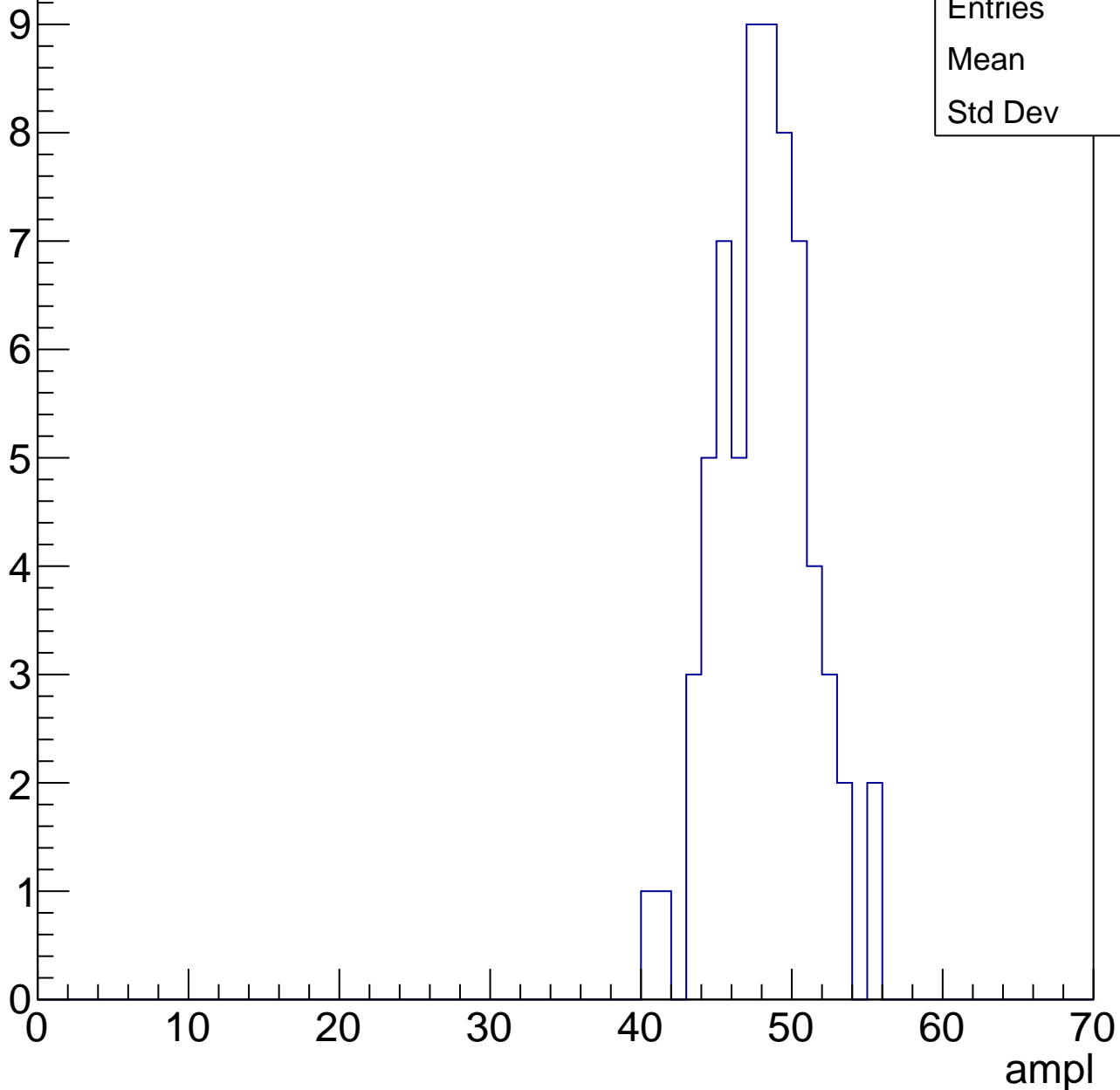


# B0L102S, U17-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.7
Std Dev	3.07

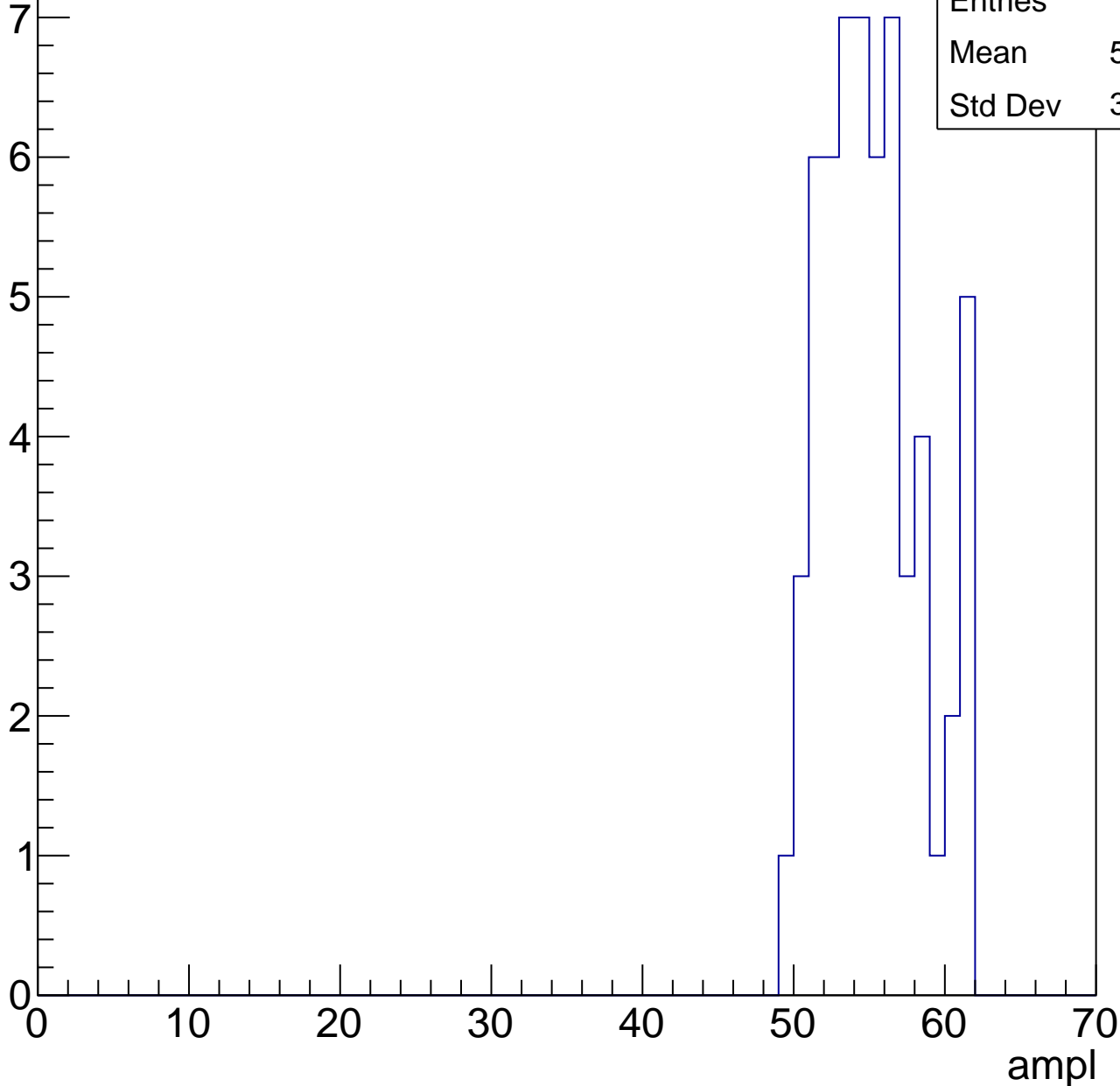


# B0L102S, U17-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

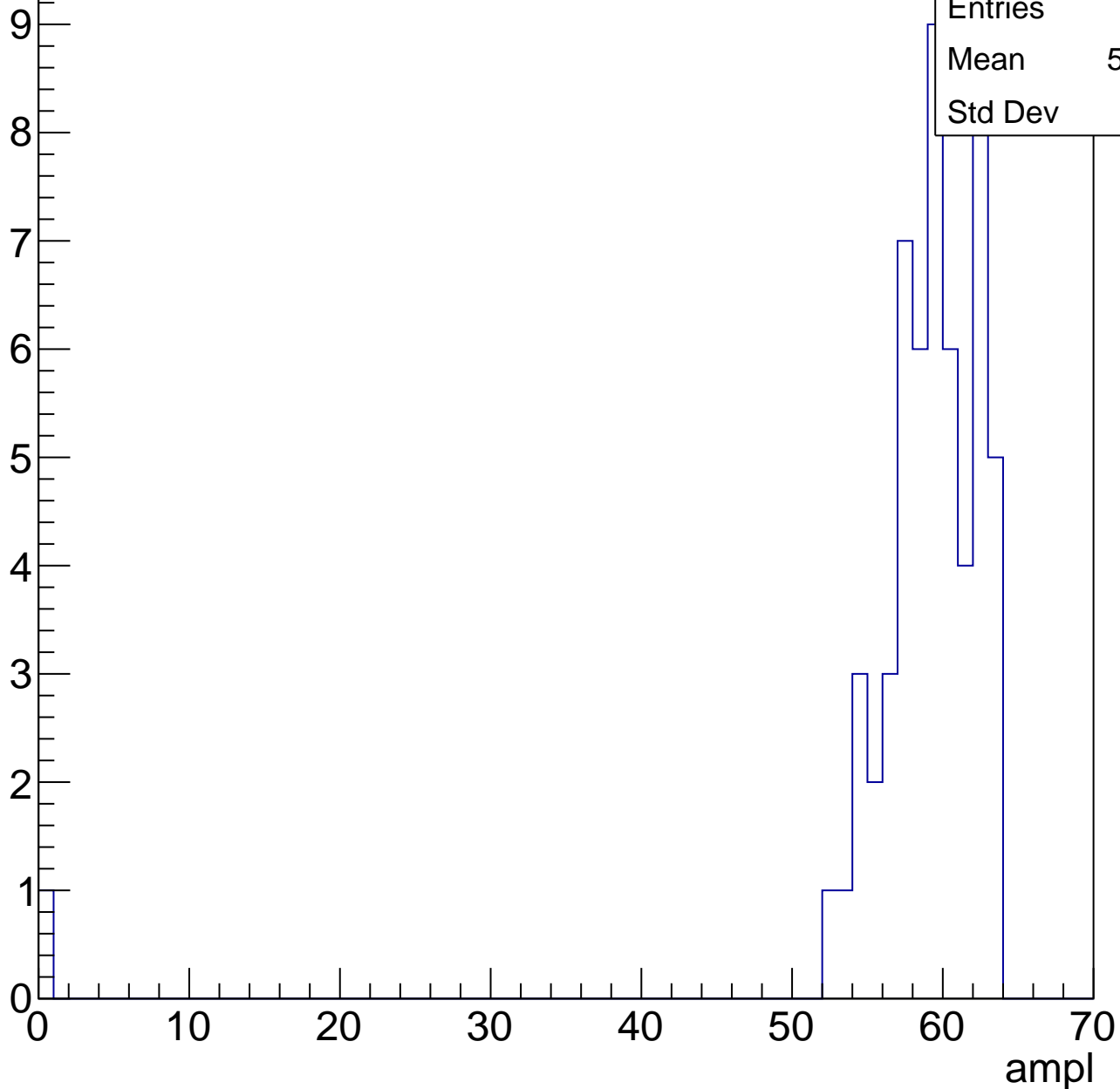
Entries	58
Mean	54.74
Std Dev	3.198



# B0L102S, U17-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

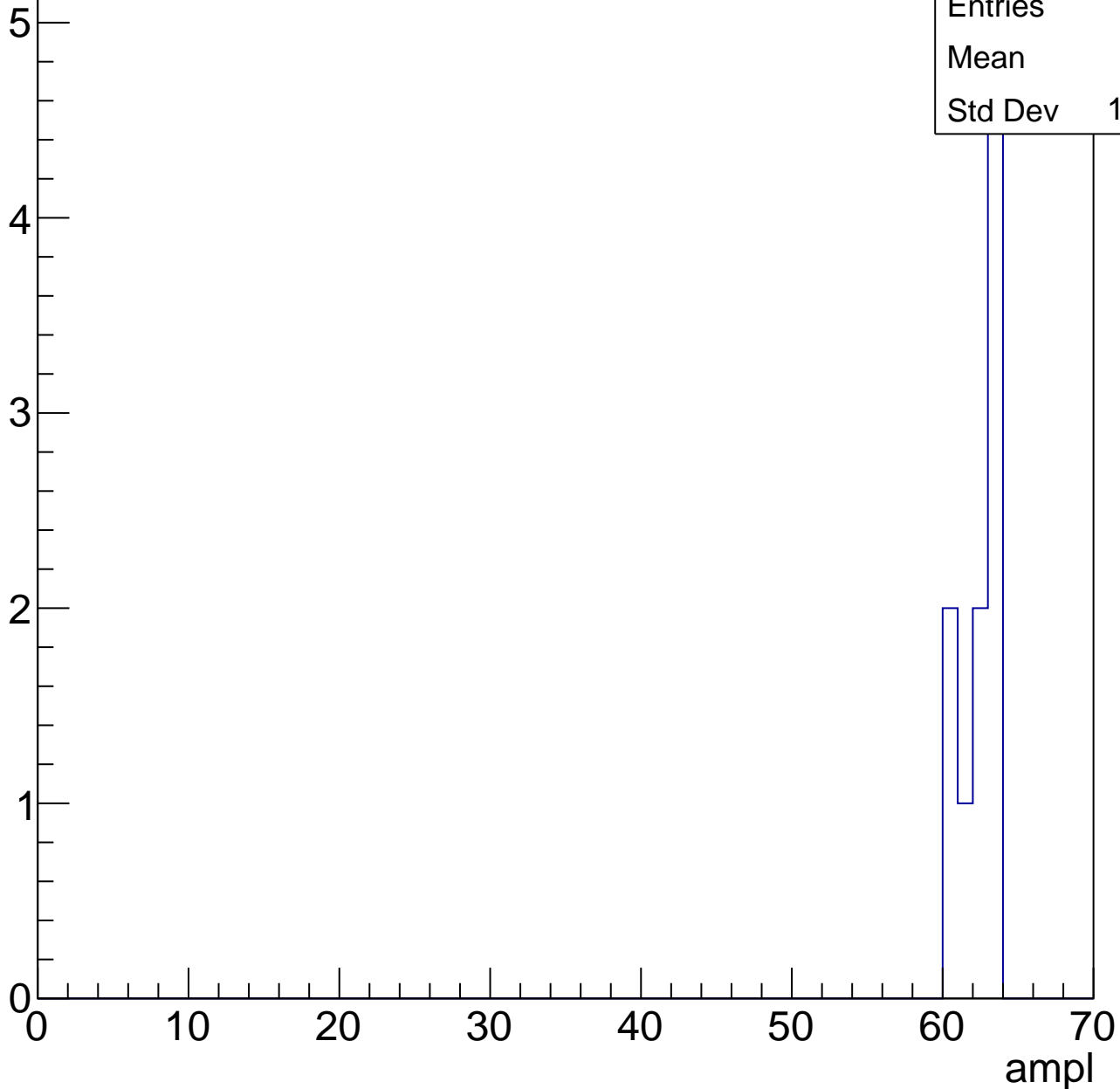


# B0L102S, U17-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	10
Mean	62
Std Dev	1.183





# B0L102S, U17-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

# B0L102S, U17-ch55, adc0

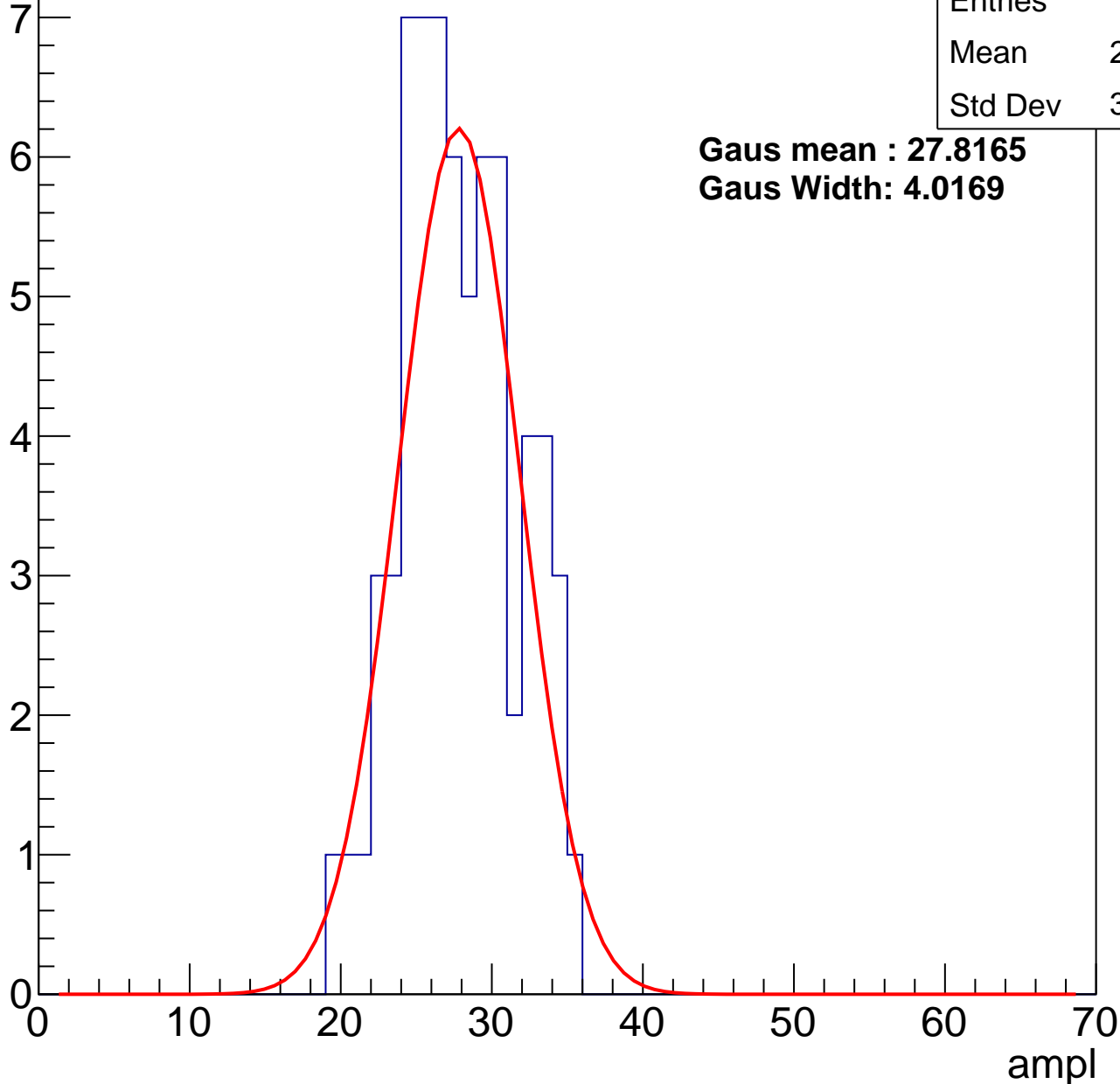
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	27.39
Std Dev	3.725

**Gaus mean : 27.8165**

**Gaus Width: 4.0169**



# B0L102S, U17-ch55, adc1

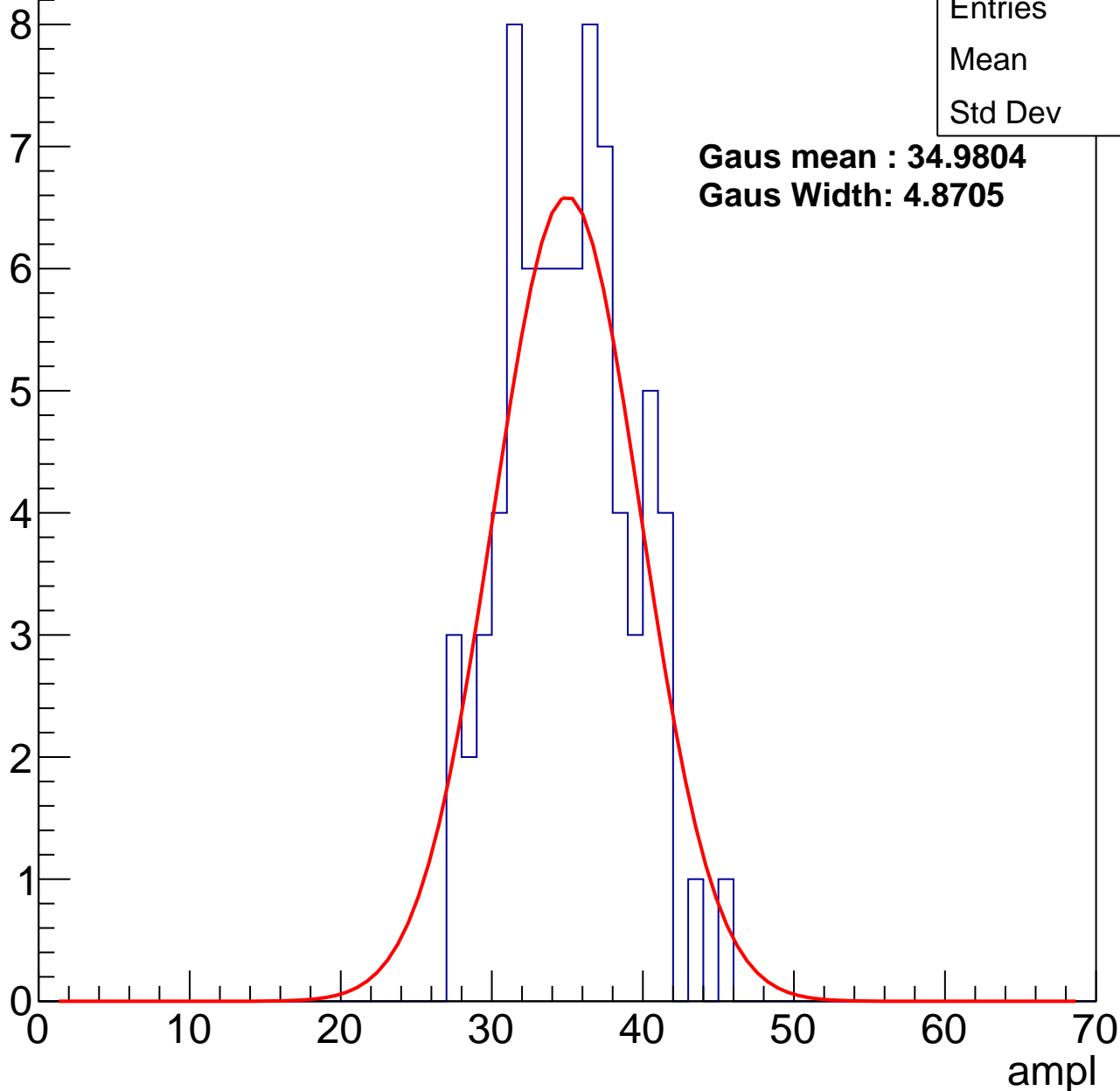
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	34.6
Std Dev	4.03

**Gaus mean : 34.9804**

**Gaus Width: 4.8705**



# B0L102S, U17-ch55, adc2

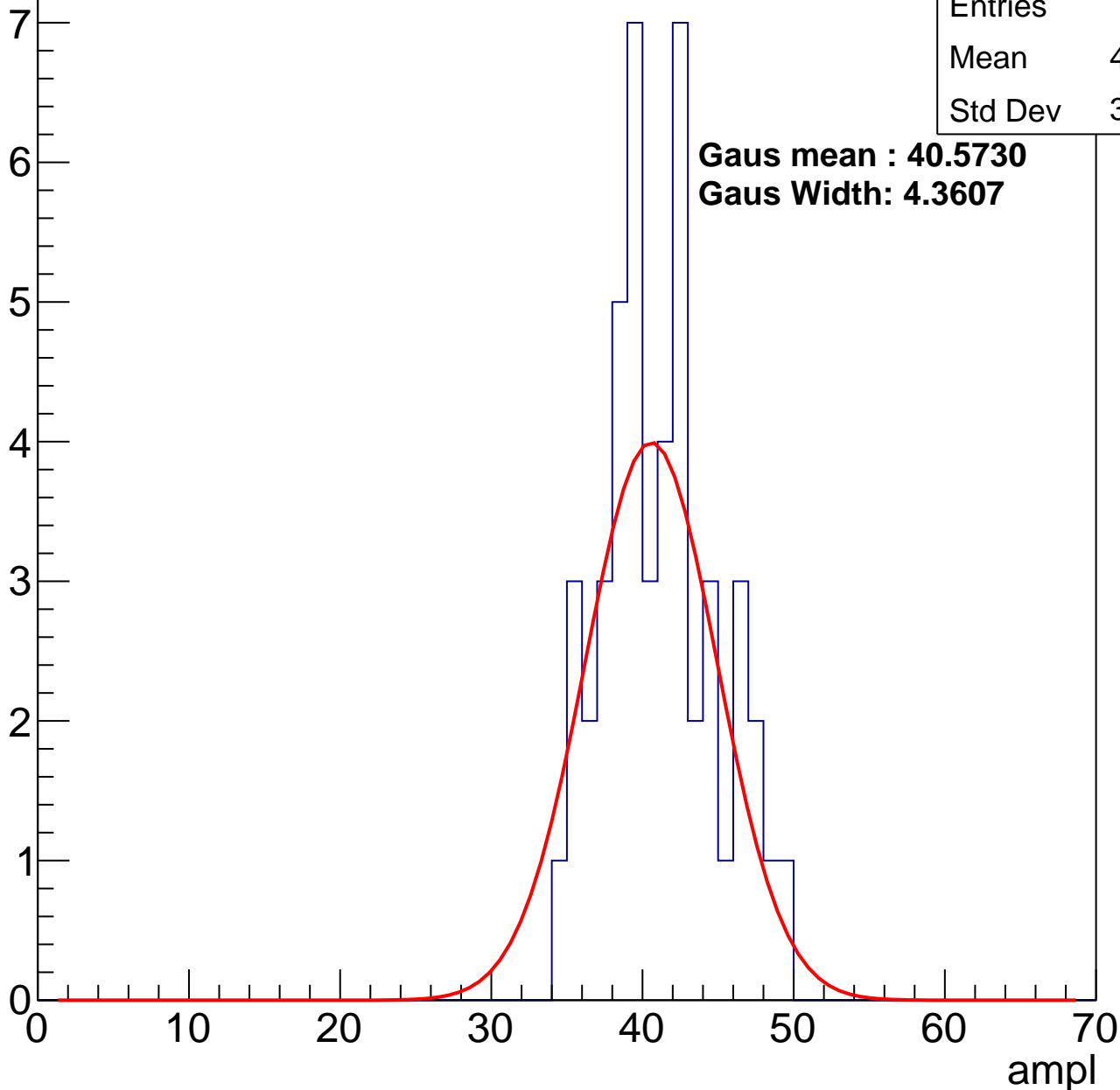
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	40.73
Std Dev	3.667

**Gaus mean : 40.5730**

**Gaus Width: 4.3607**

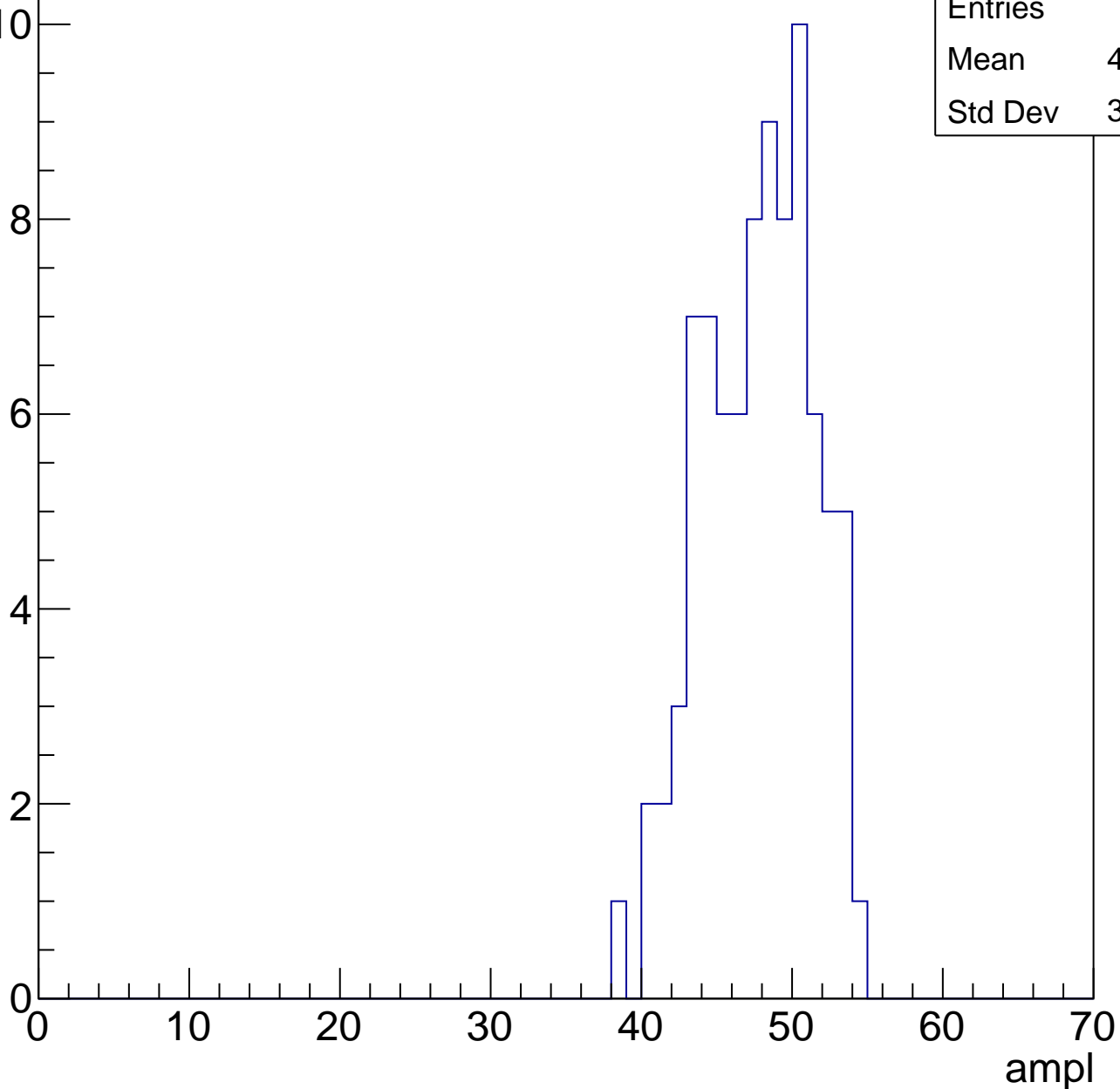


# B0L102S, U17-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	86
Mean	47.28
Std Dev	3.585

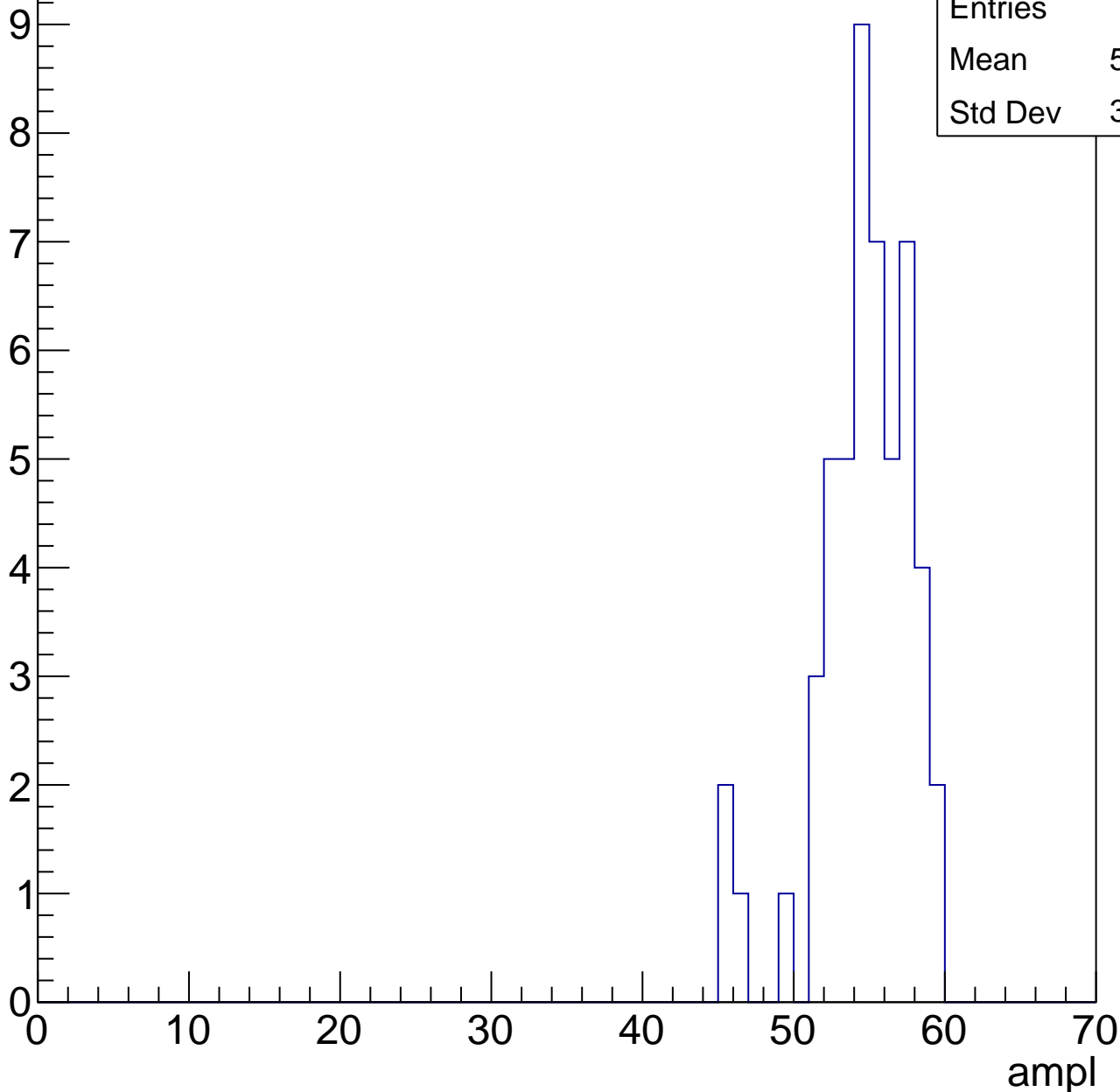


# B0L102S, U17-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	54.18
Std Dev	3.148



# B0L102S, U17-ch55, adc5

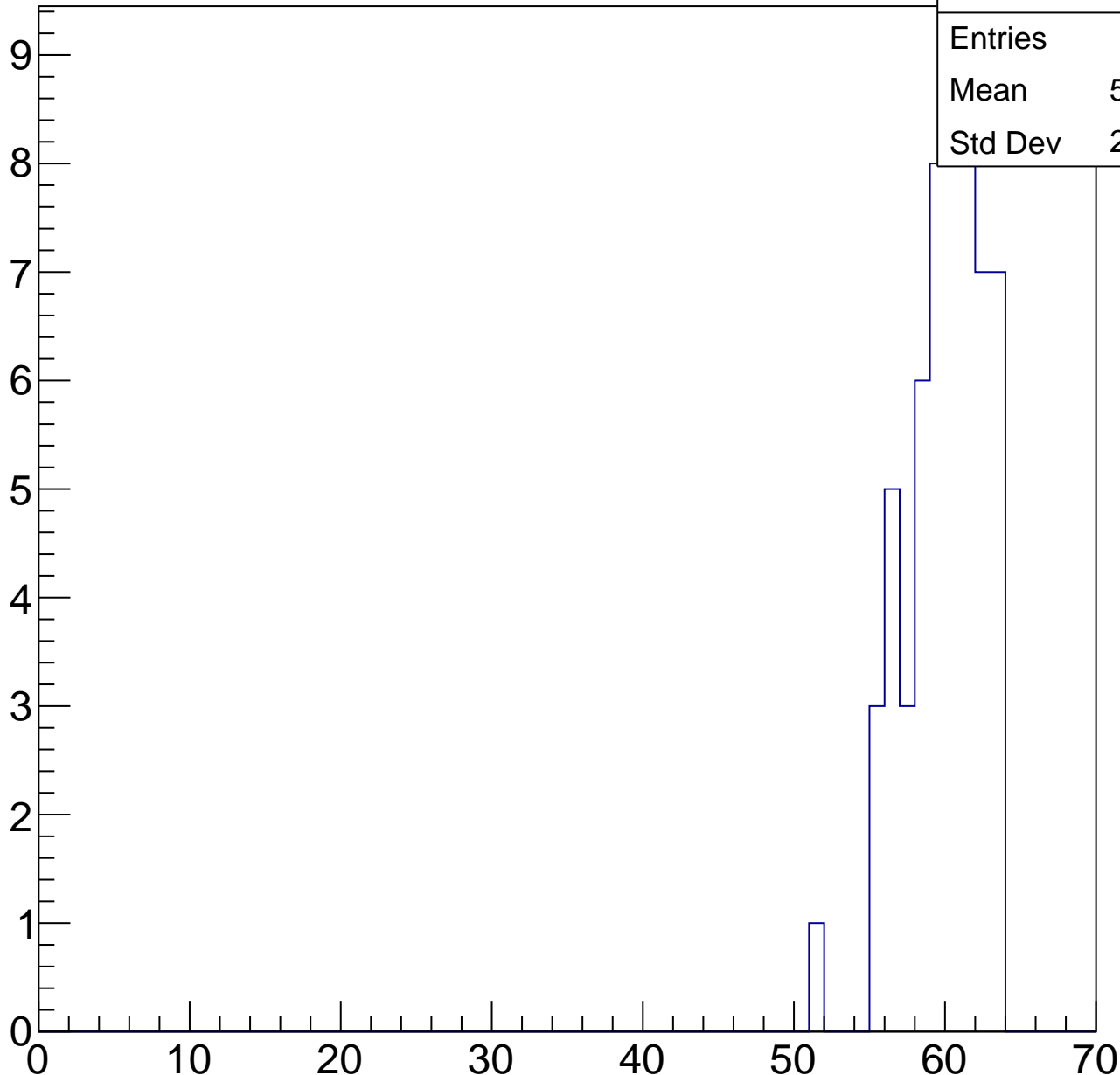
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	59.47
Std Dev	2.576

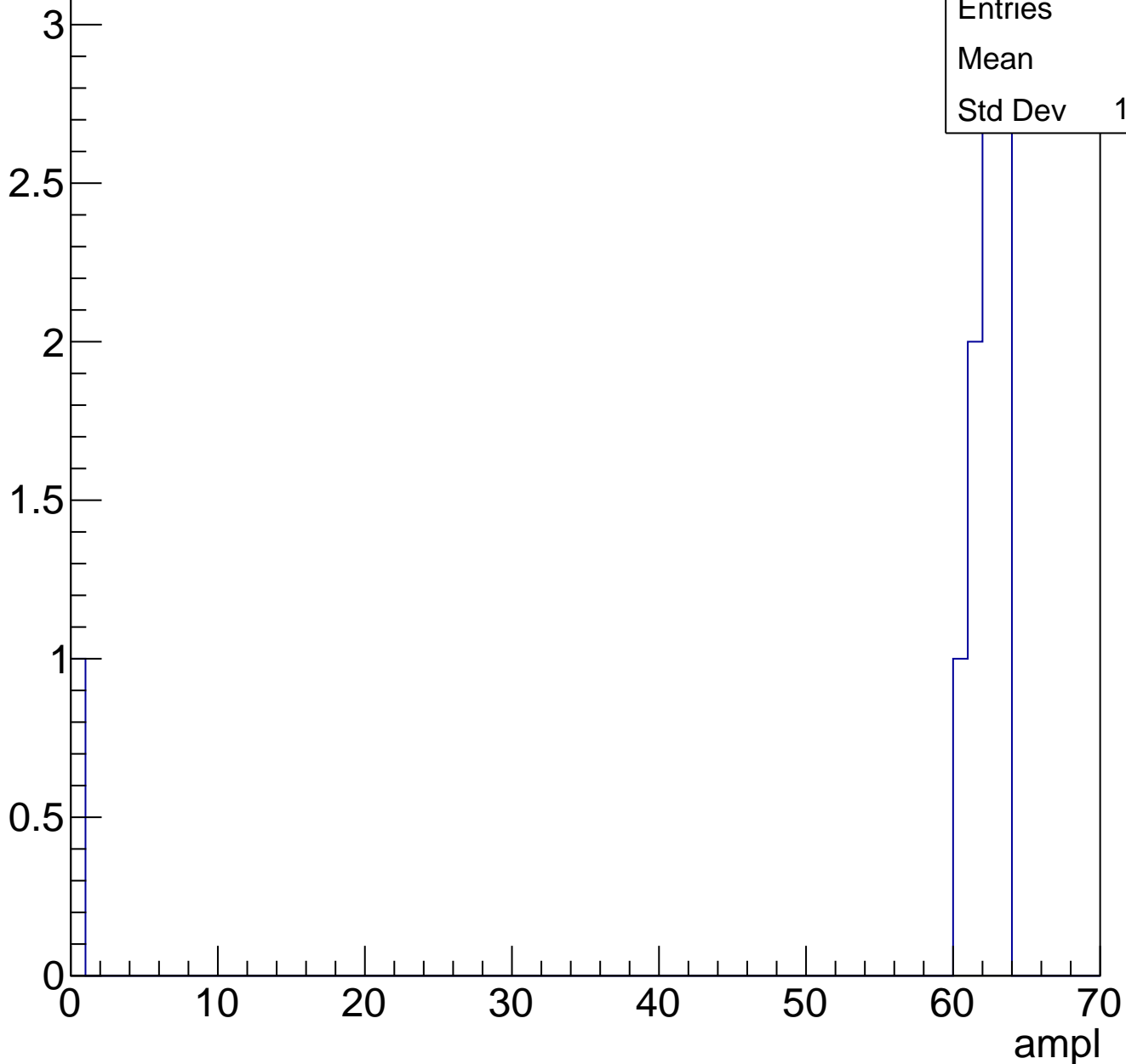
ampl



# B0L102S, U17-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch56, adc0

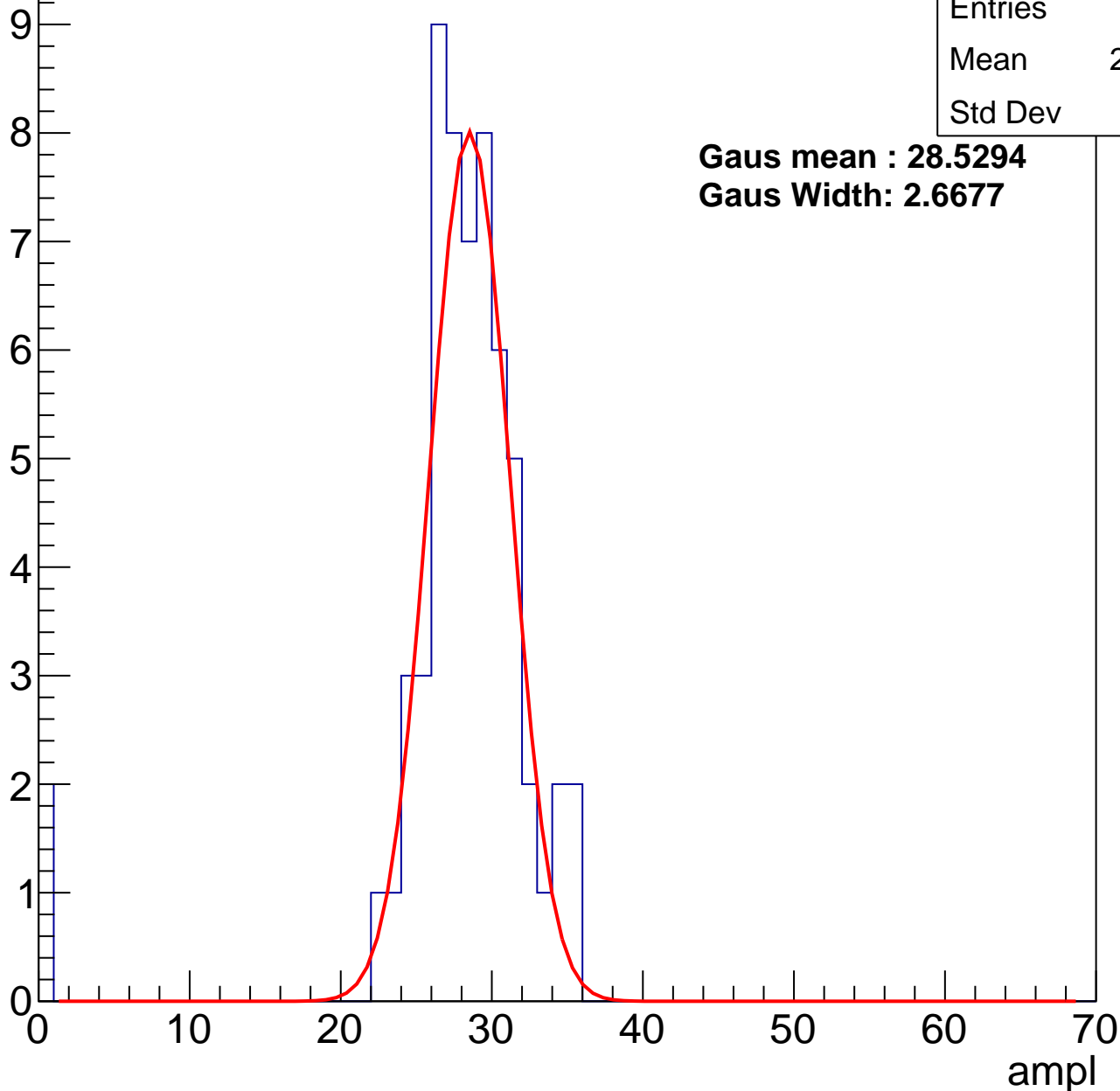
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	27.33
Std Dev	5.81

**Gaus mean : 28.5294**

**Gaus Width: 2.6677**



# B0L102S, U17-ch56, adc1

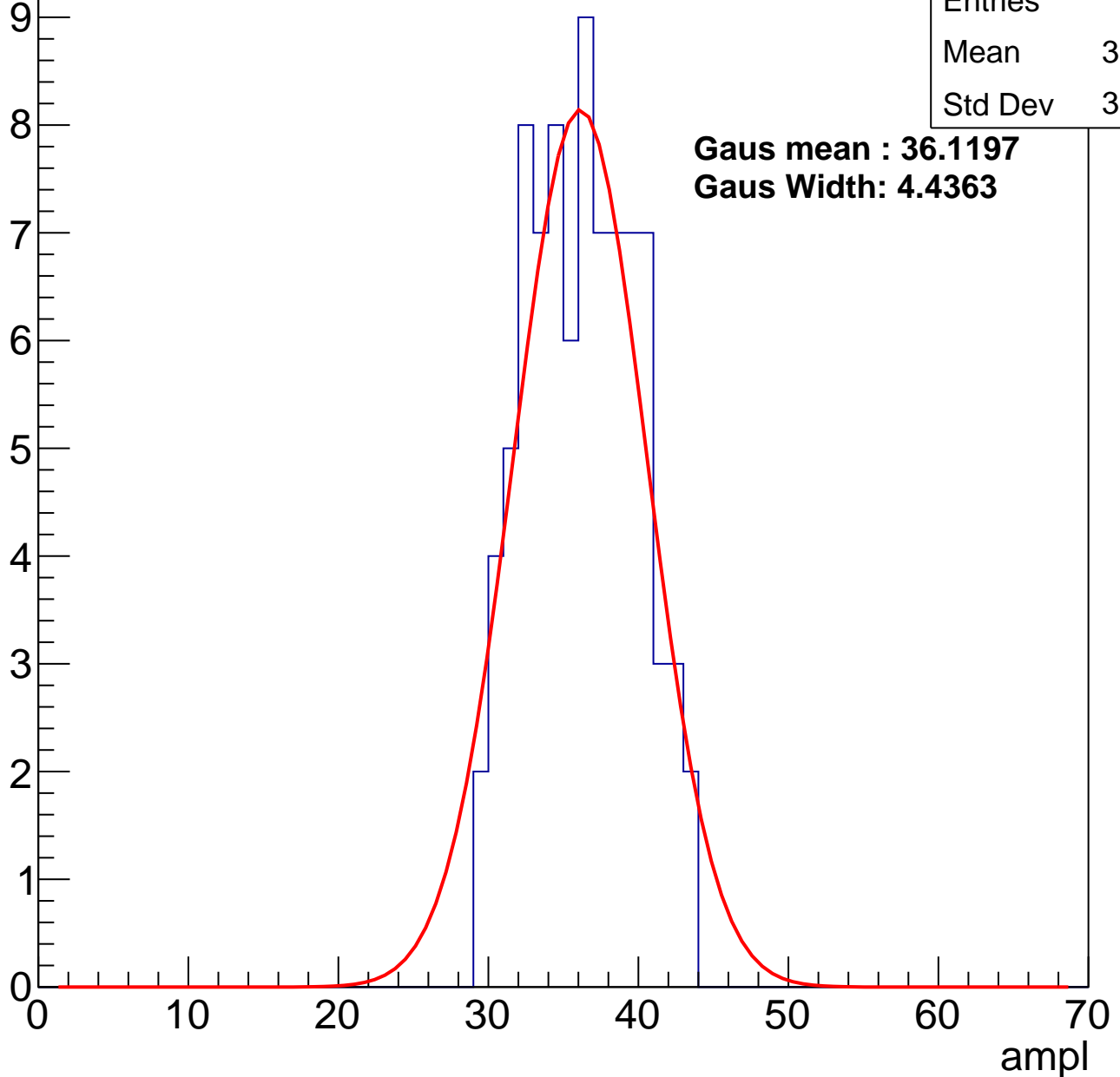
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	35.75
Std Dev	3.568

**Gaus mean : 36.1197**

**Gaus Width: 4.4363**



# B0L102S, U17-ch56, adc2

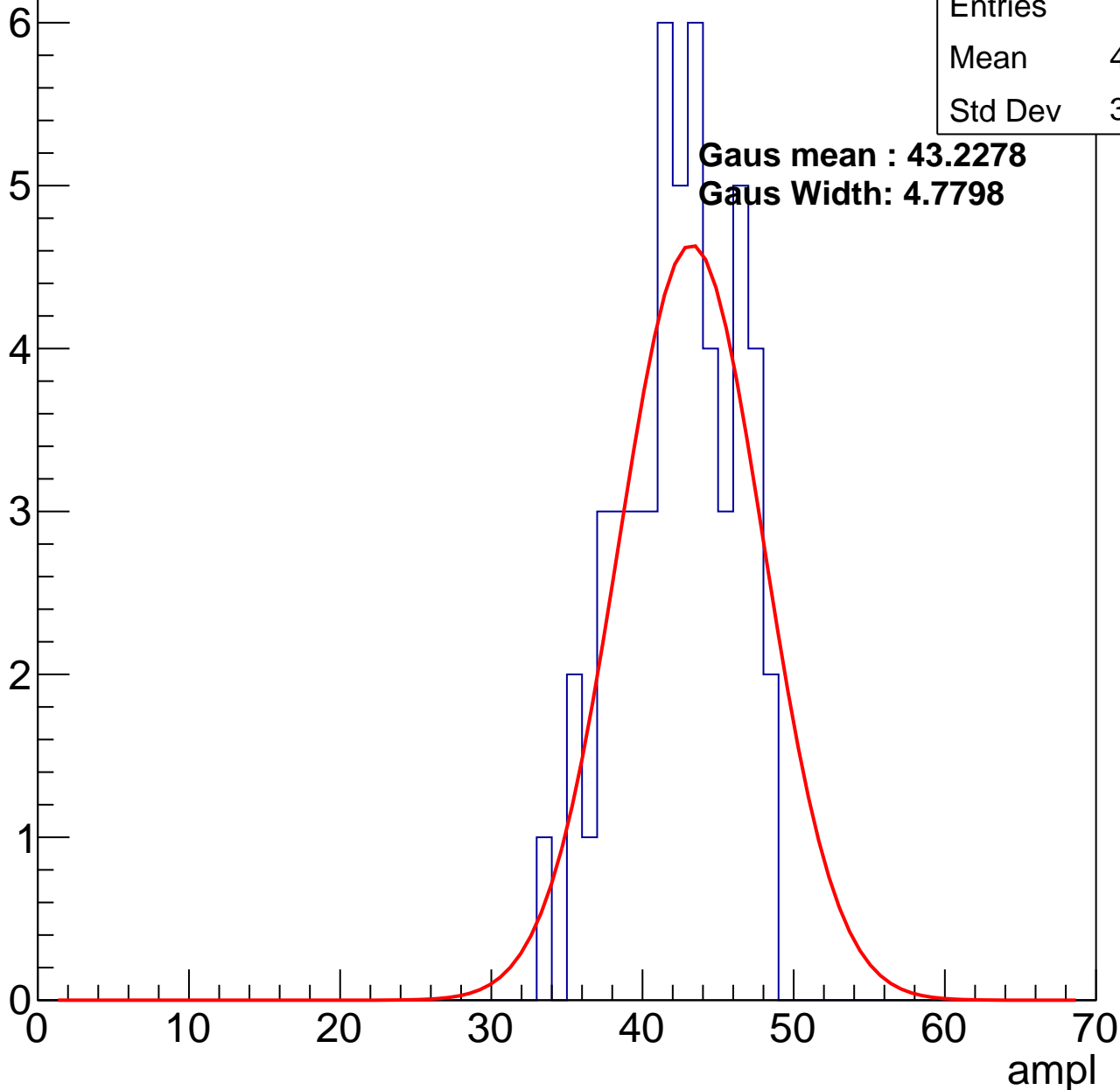
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	41.96
Std Dev	3.662

**Gaus mean : 43.2278**

**Gaus Width: 4.7798**

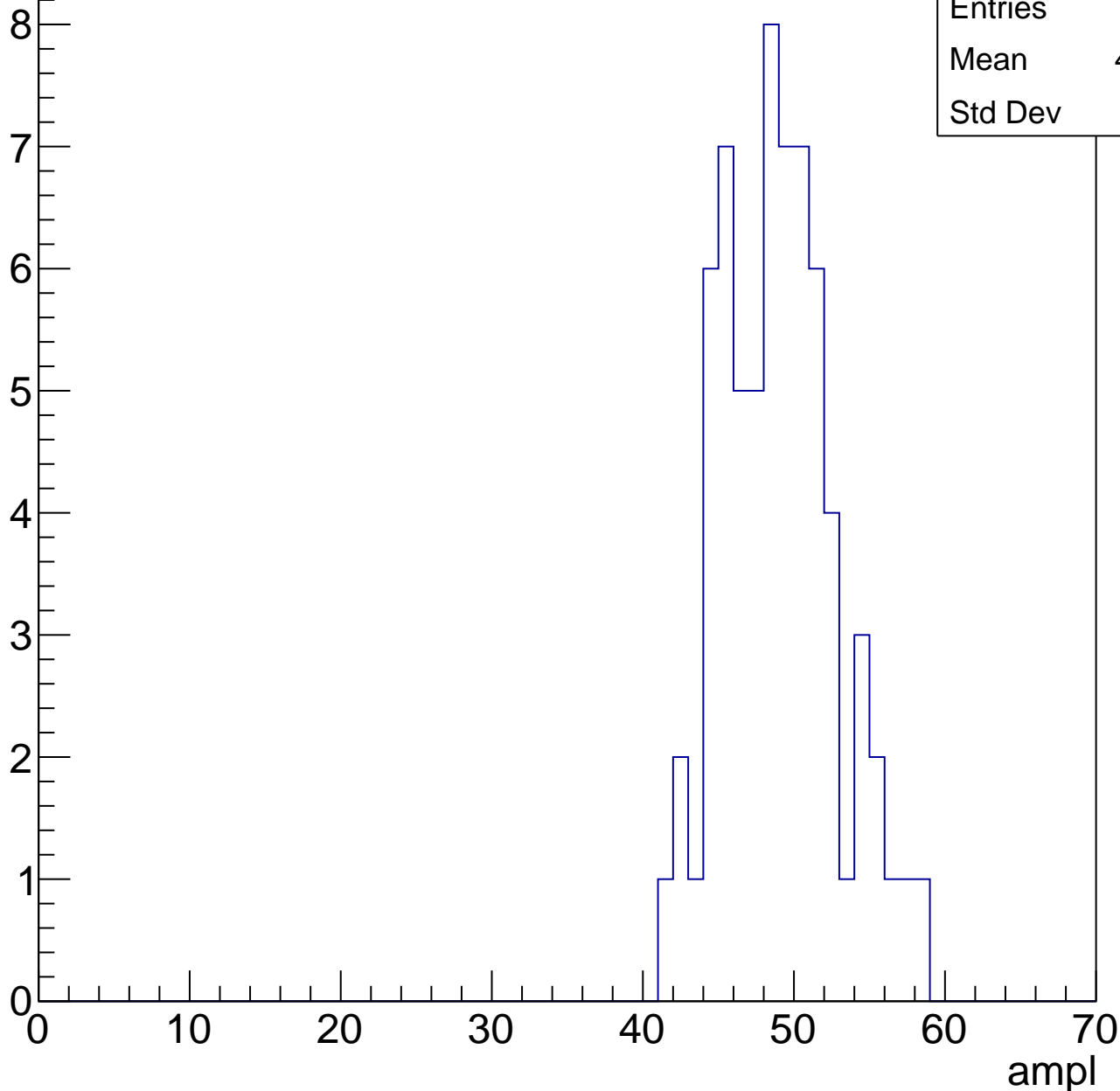


# B0L102S, U17-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	48.51
Std Dev	3.72

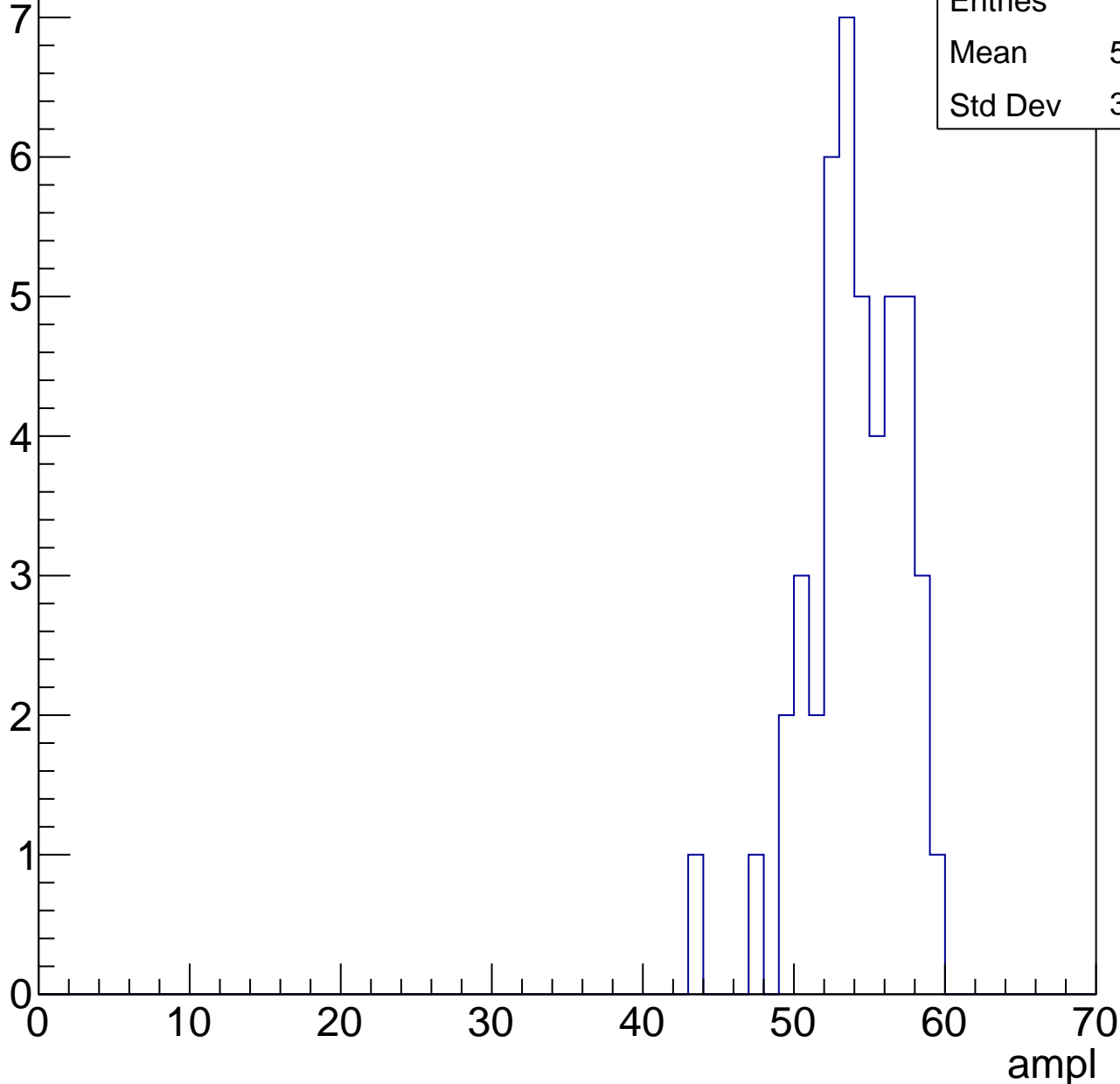


# B0L102S, U17-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

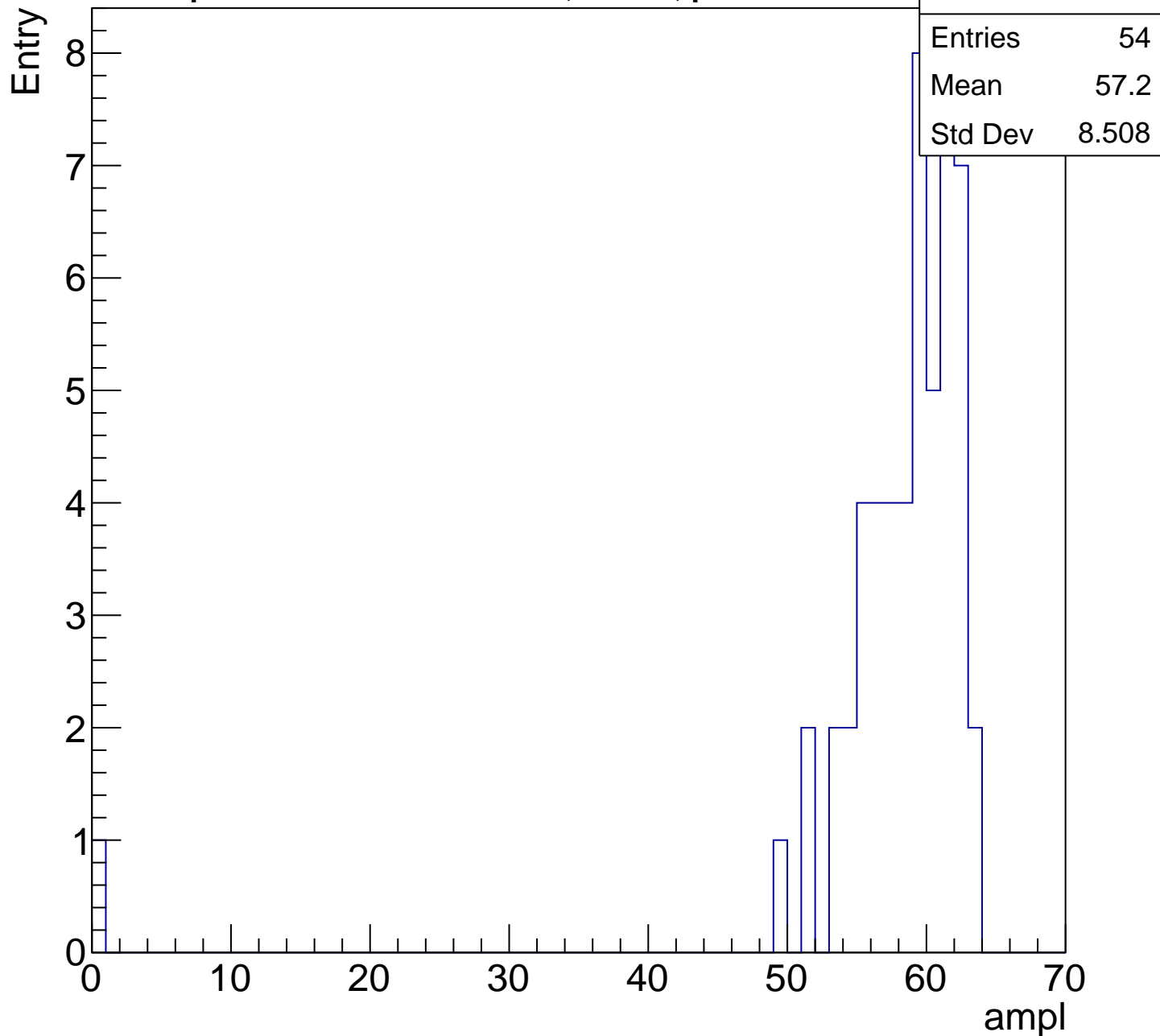
Entry

Entries	45
Mean	53.58
Std Dev	3.166



# B0L102S, U17-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

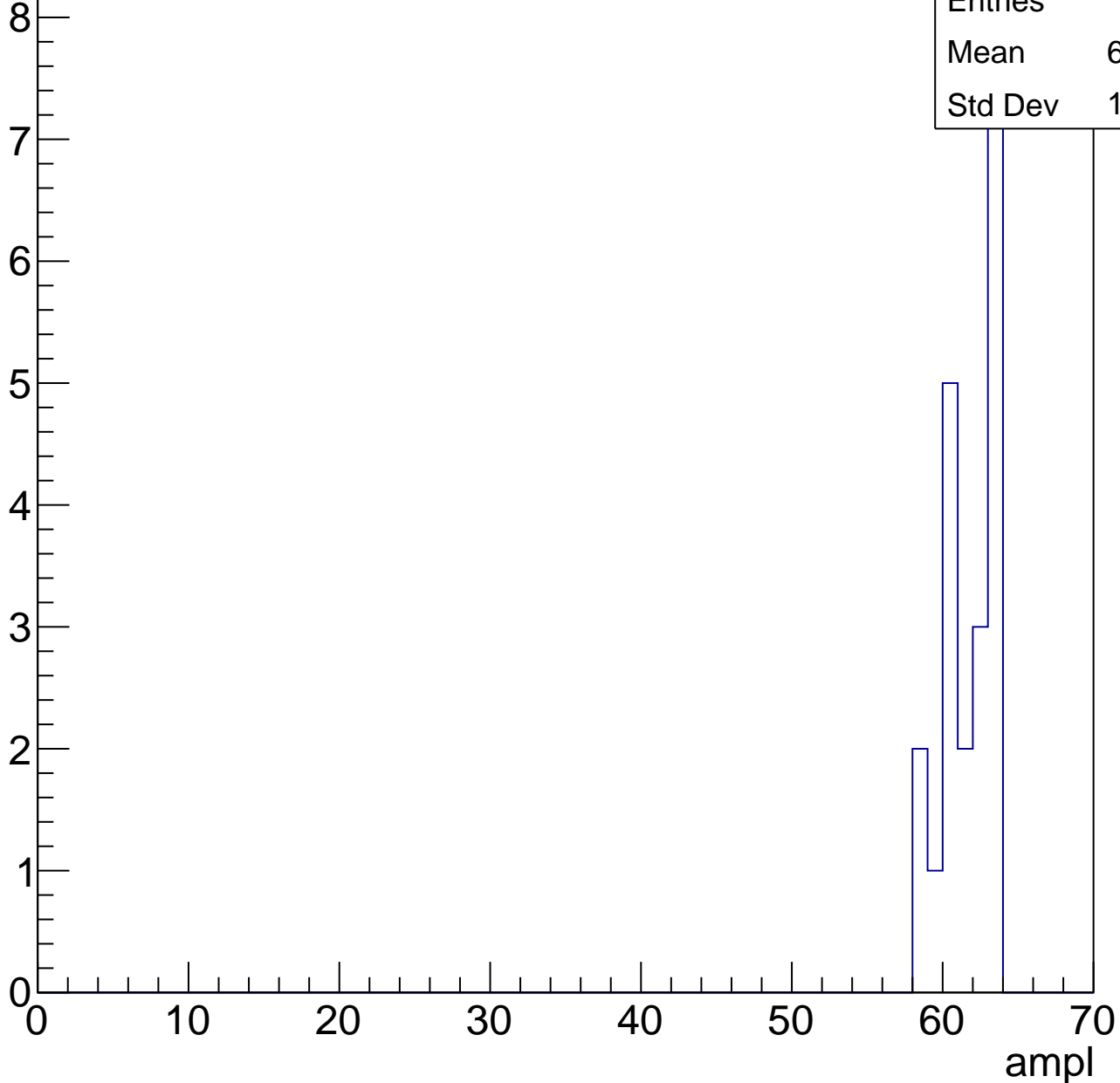


# B0L102S, U17-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	21
Mean	61.29
Std Dev	1.694





# B0L102S, U17-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch57, adc0

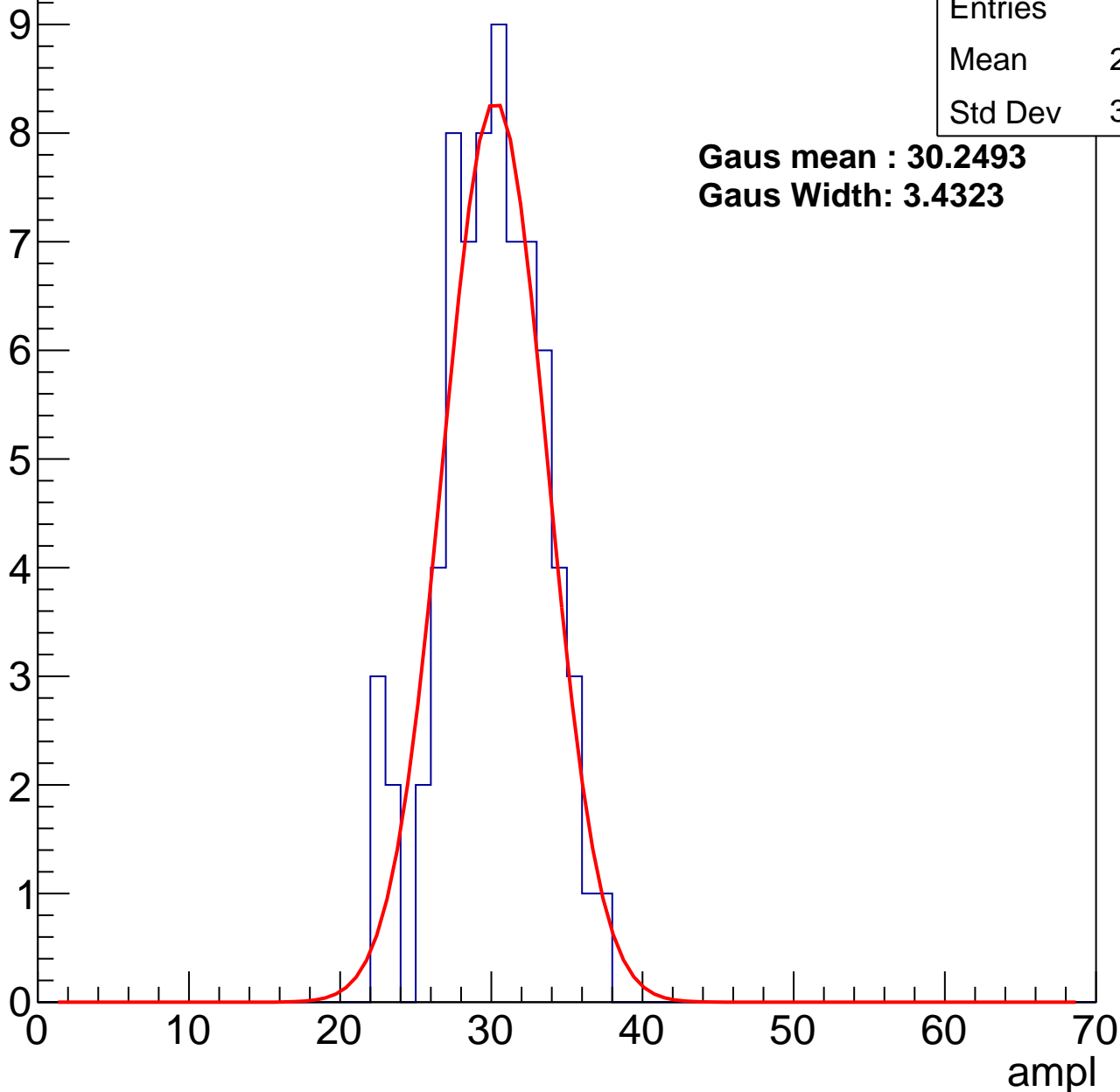
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	29.62
Std Dev	3.364

**Gaus mean : 30.2493**

**Gaus Width: 3.4323**



# B0L102S, U17-ch57, adc1

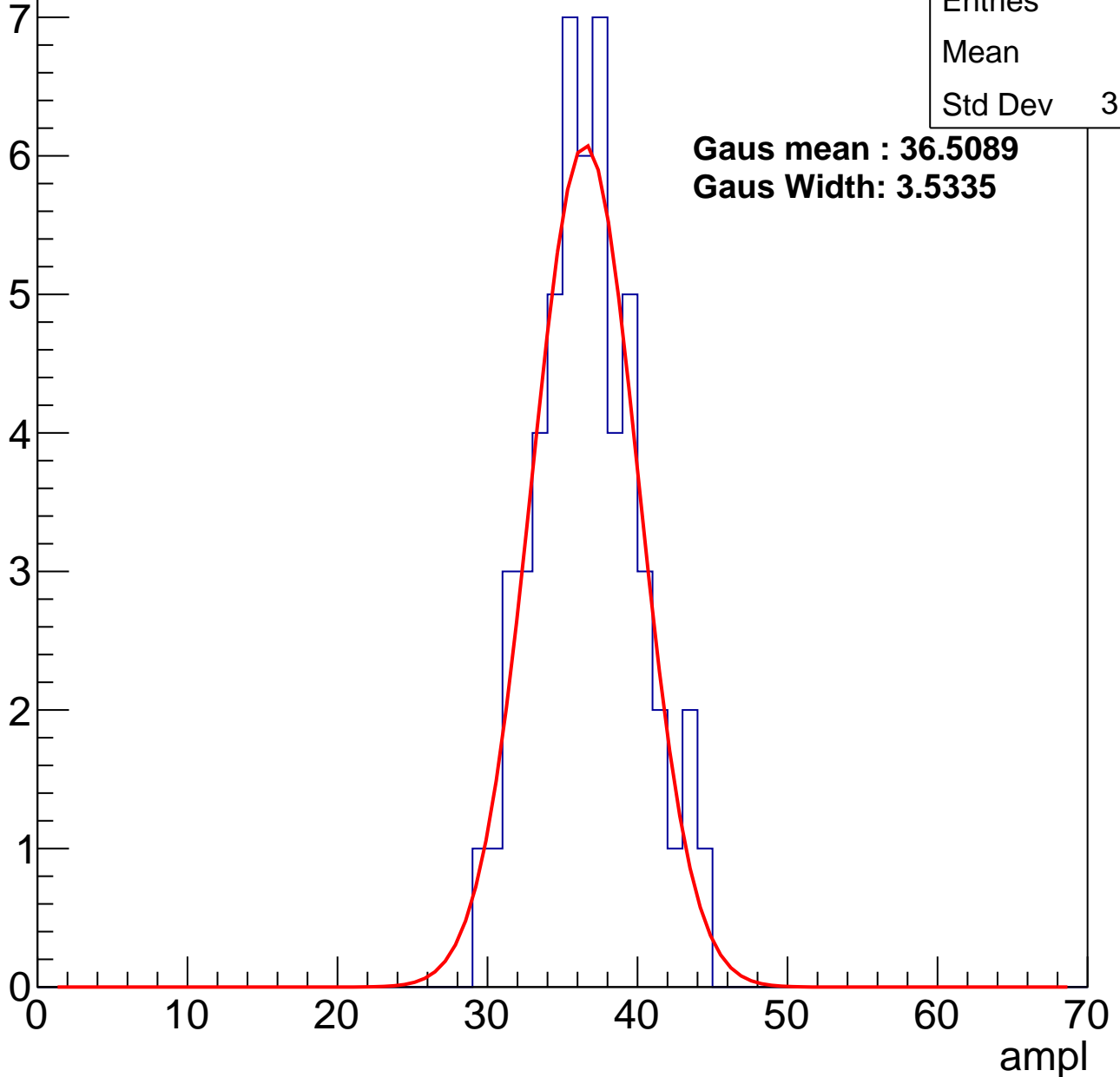
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	36.2
Std Dev	3.392

**Gaus mean : 36.5089**

**Gaus Width: 3.5335**



# B0L102S, U17-ch57, adc2

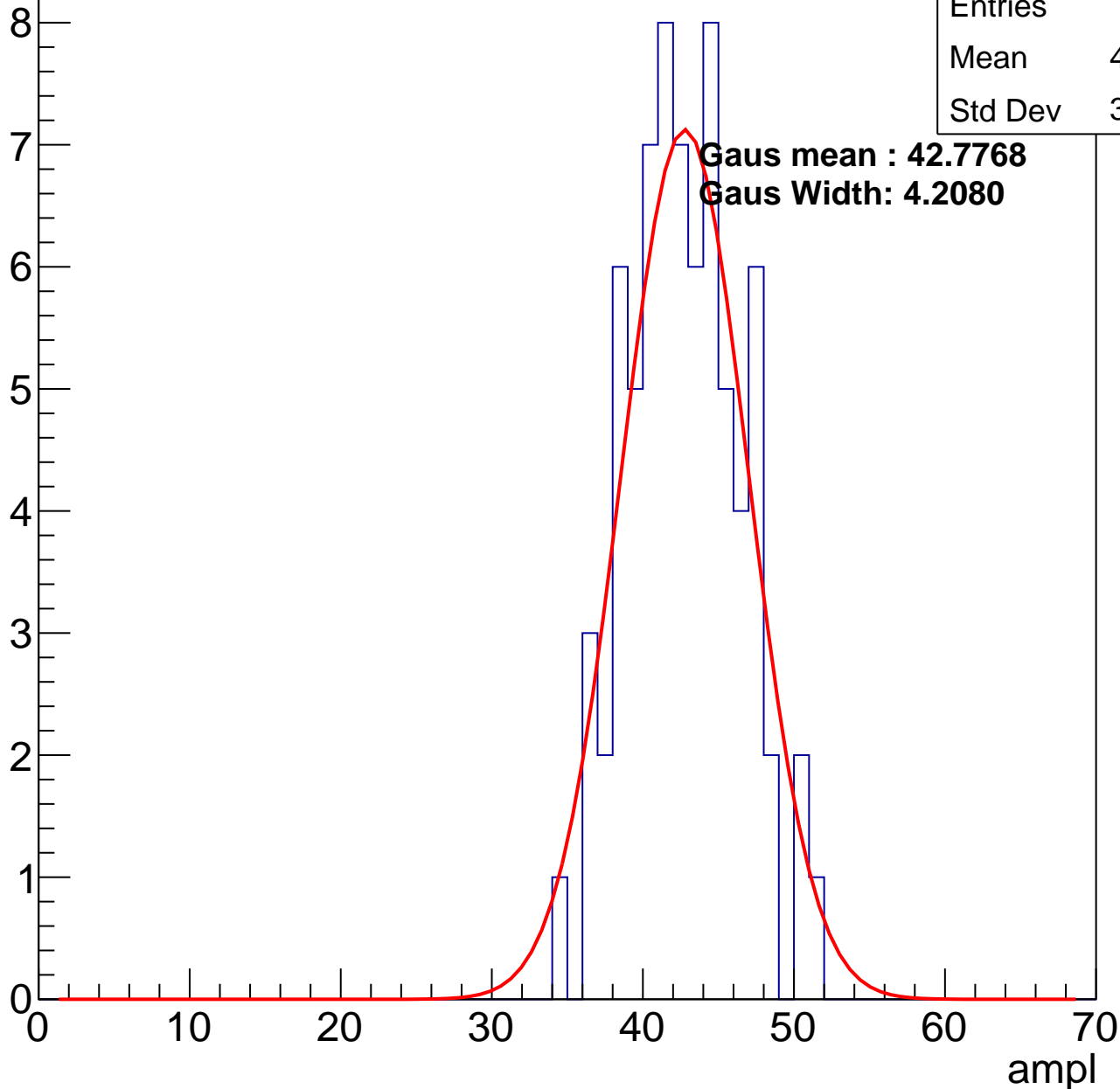
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	42.32
Std Dev	3.645

**Gaus mean : 42.7768**

**Gaus Width: 4.2080**

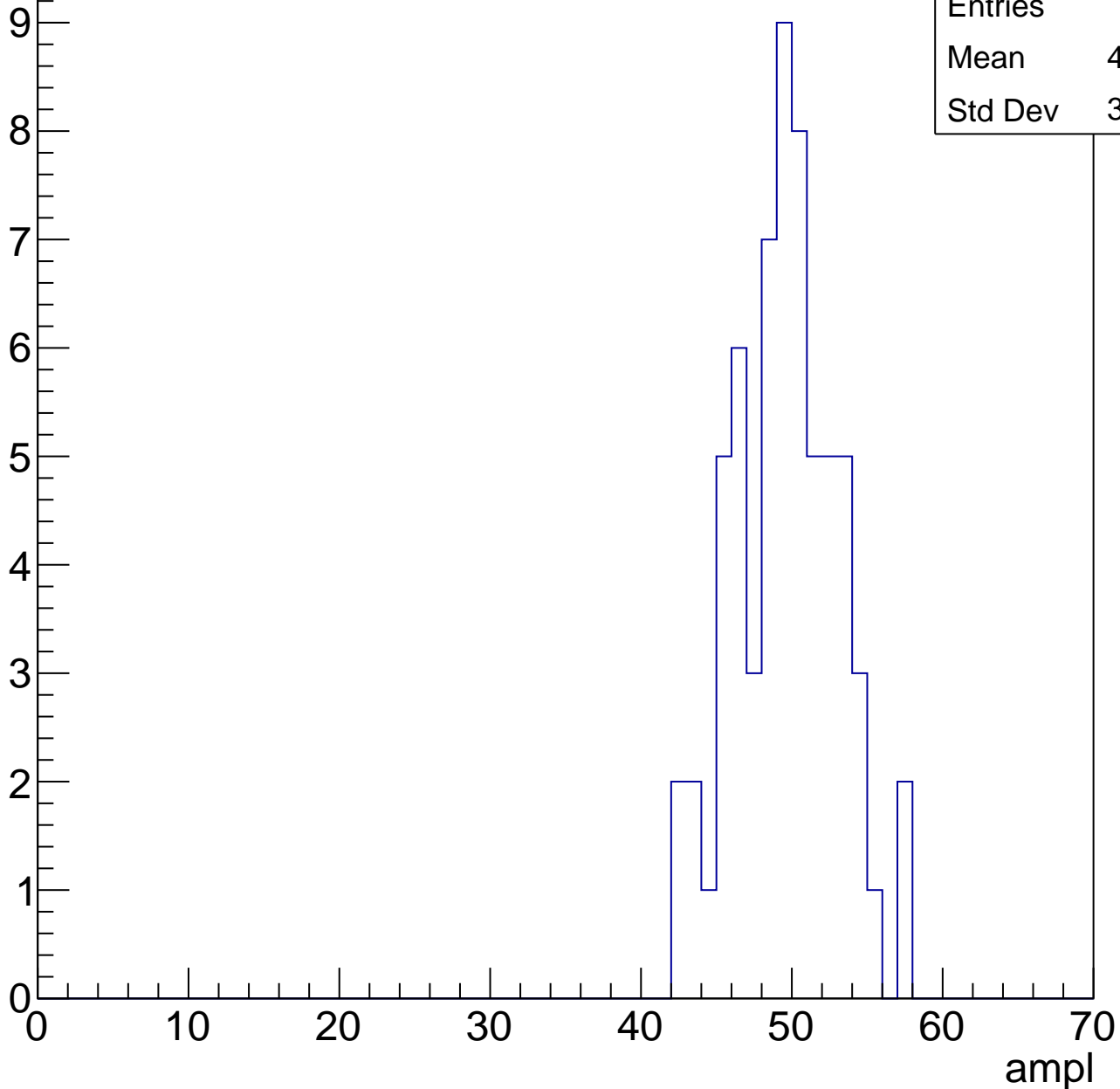


# B0L102S, U17-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	49.12
Std Dev	3.398

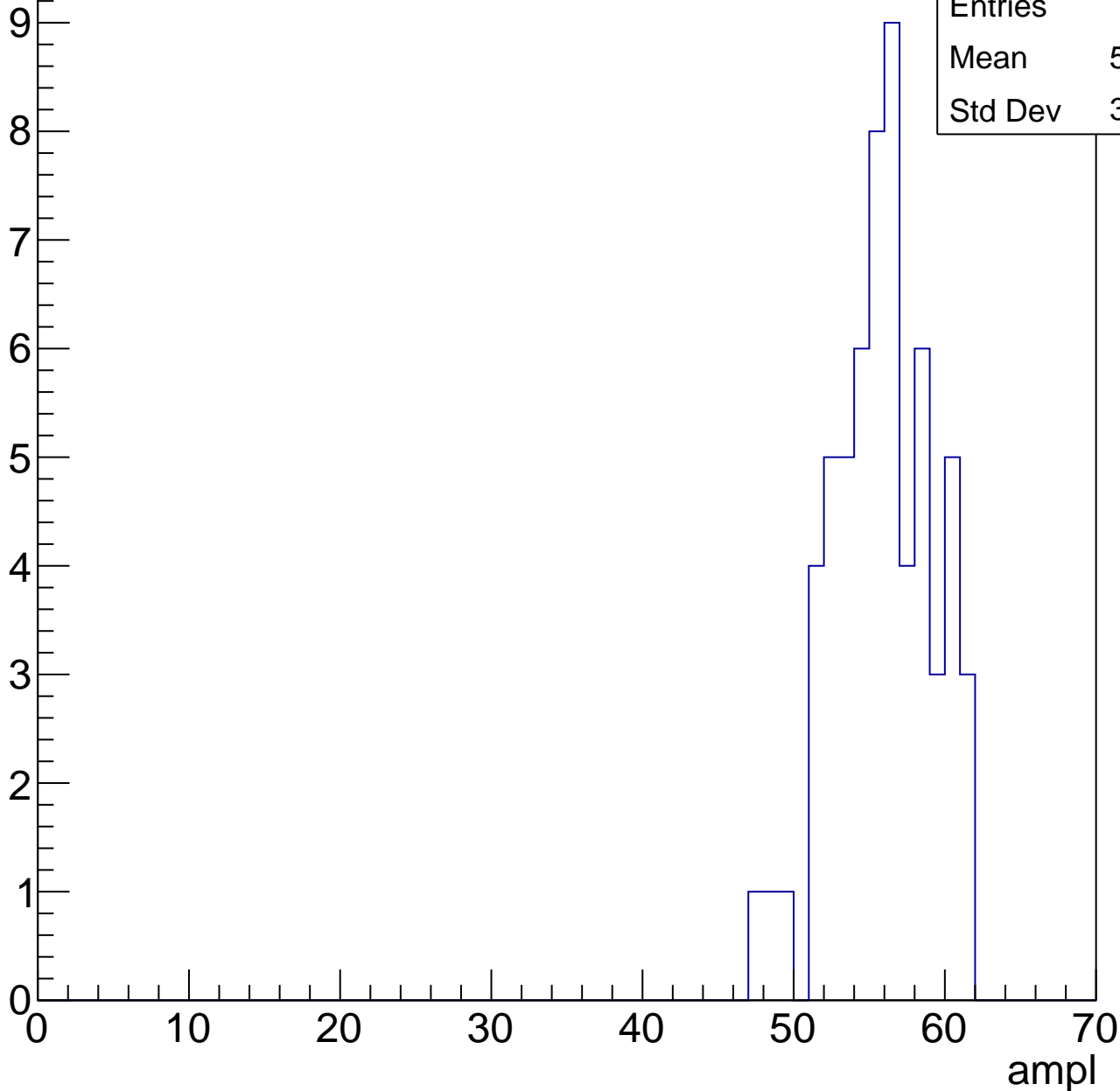


# B0L102S, U17-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.36
Std Dev	3.229

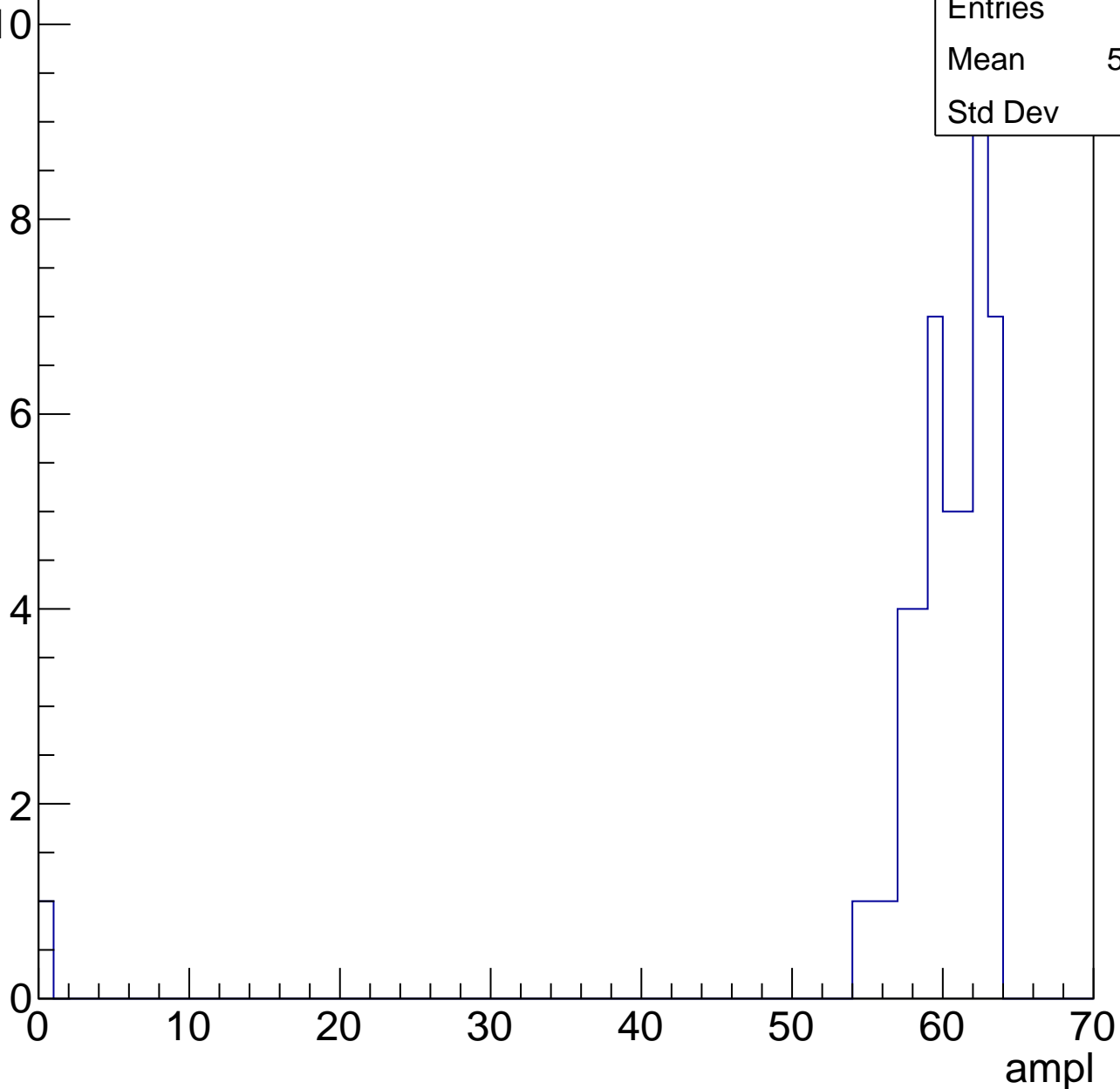


# B0L102S, U17-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	58.78
Std Dev	9.06



# B0L102S, U17-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

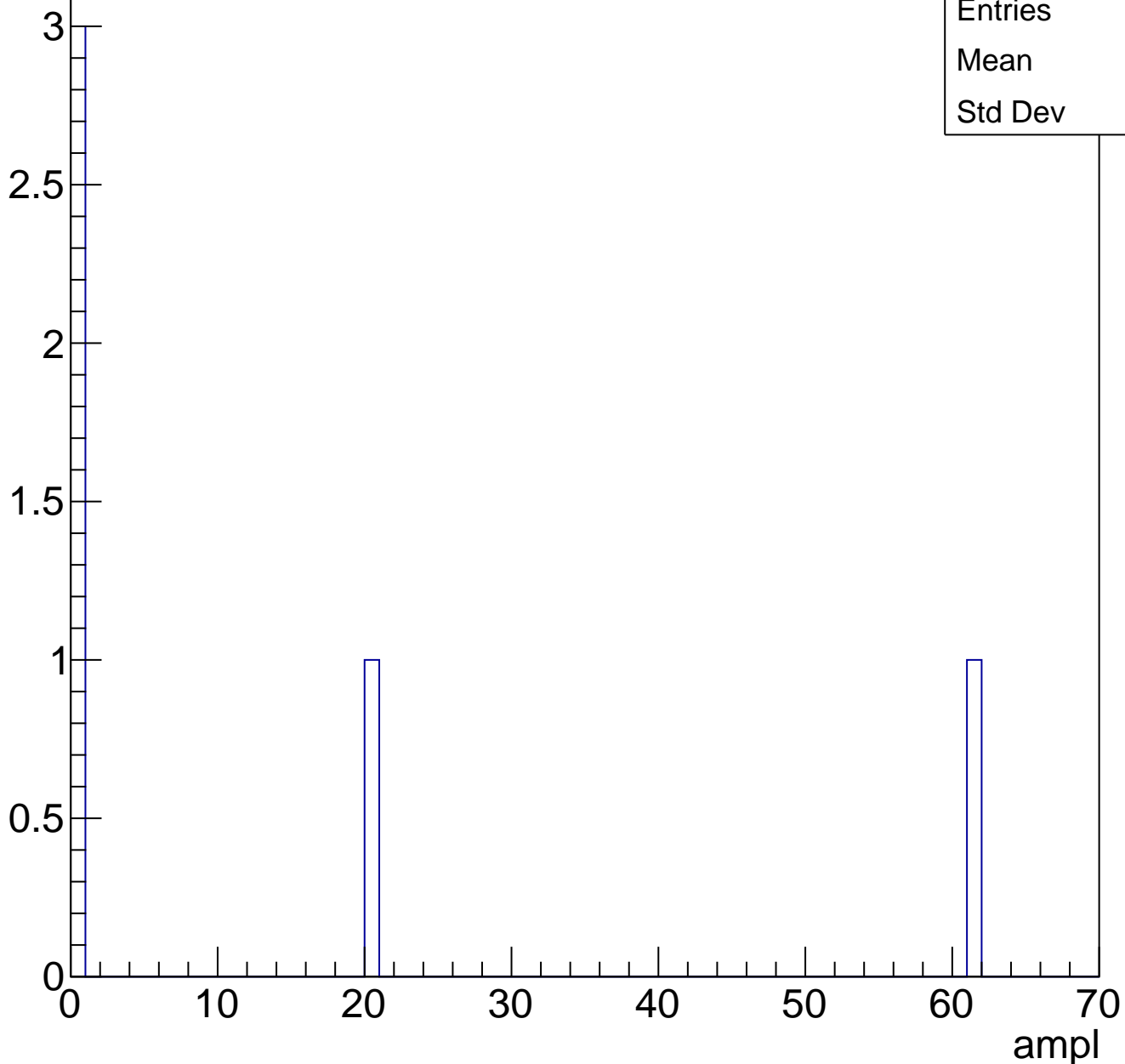




# B0L102S, U17-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	5
Mean	16.2
Std Dev	23.7

# B0L102S, U17-ch58, adc0

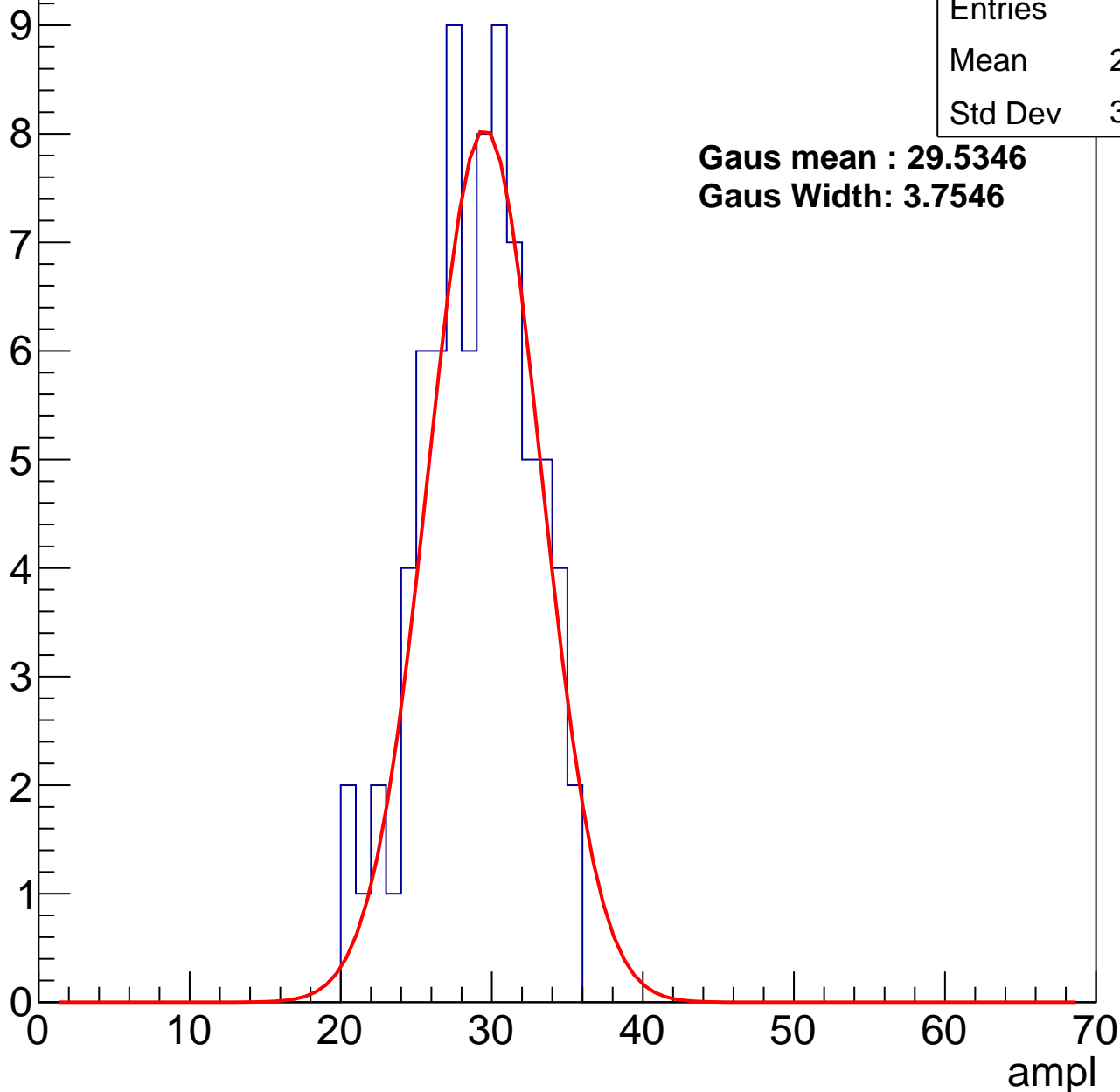
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.45
Std Dev	3.537

**Gaus mean : 29.5346**

**Gaus Width: 3.7546**



# B0L102S, U17-ch58, adc1

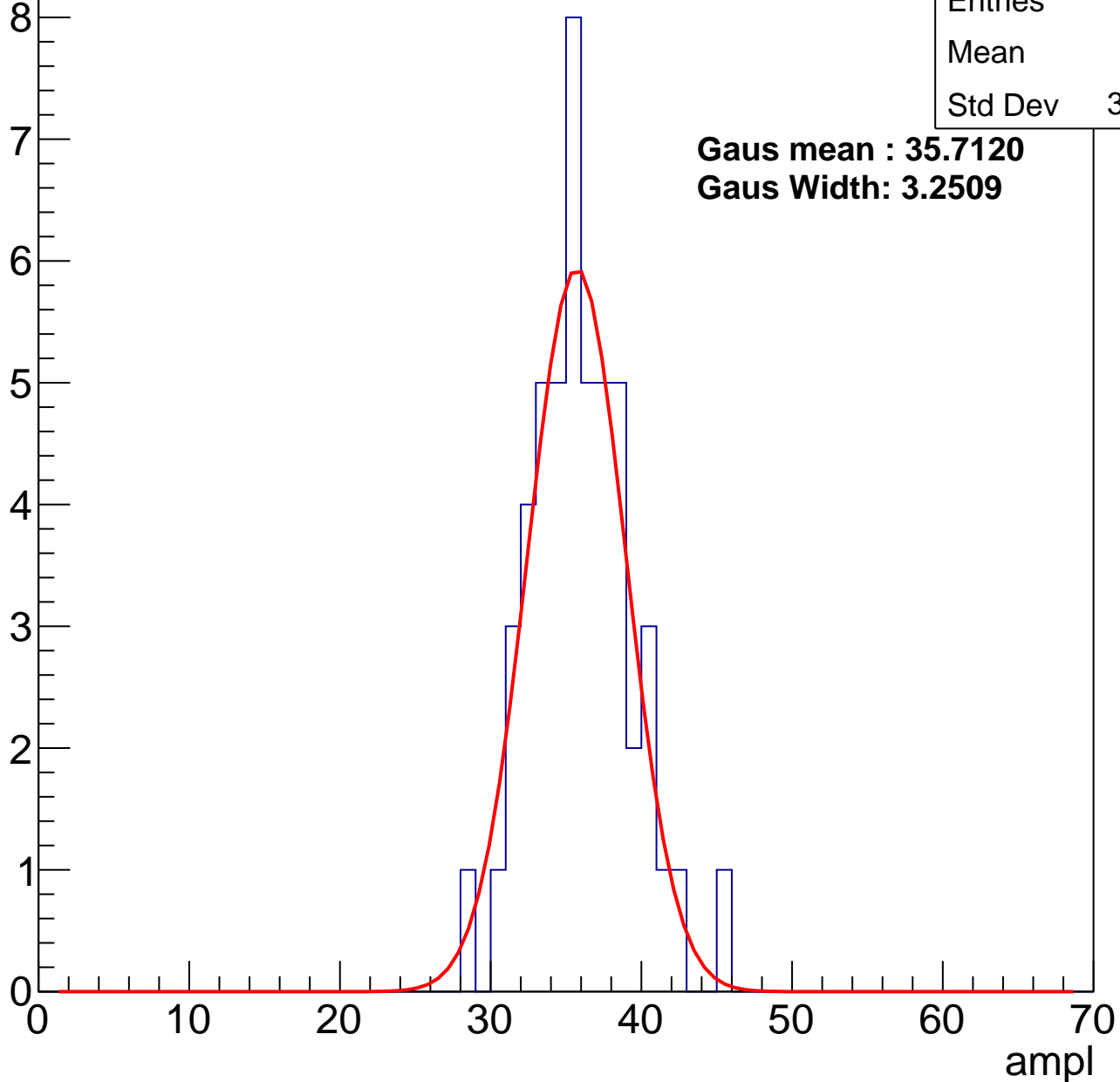
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	35.5
Std Dev	3.263

**Gaus mean : 35.7120**

**Gaus Width: 3.2509**



# B0L102S, U17-ch58, adc2

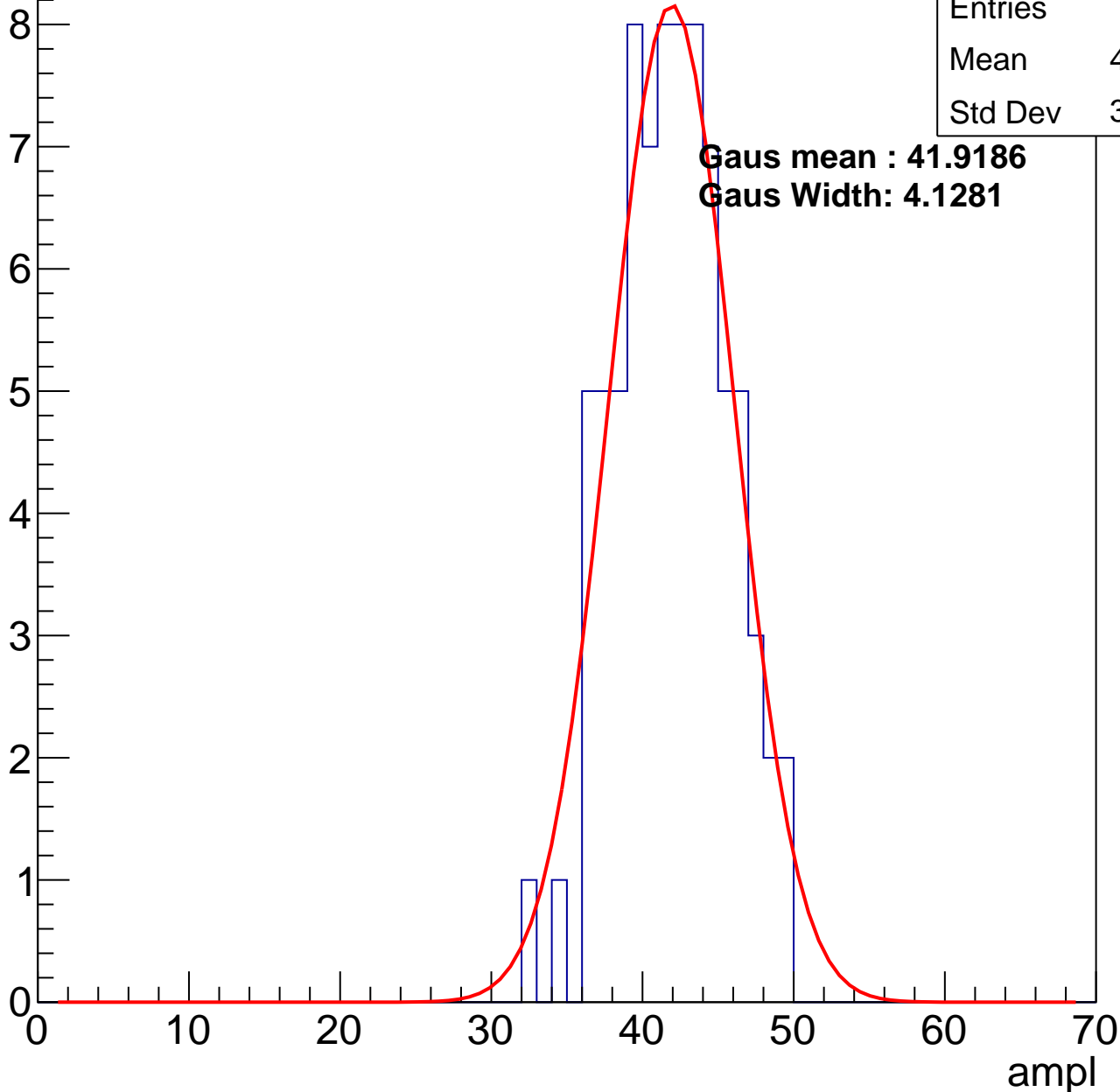
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	41.49
Std Dev	3.609

**Gaus mean : 41.9186**

**Gaus Width: 4.1281**

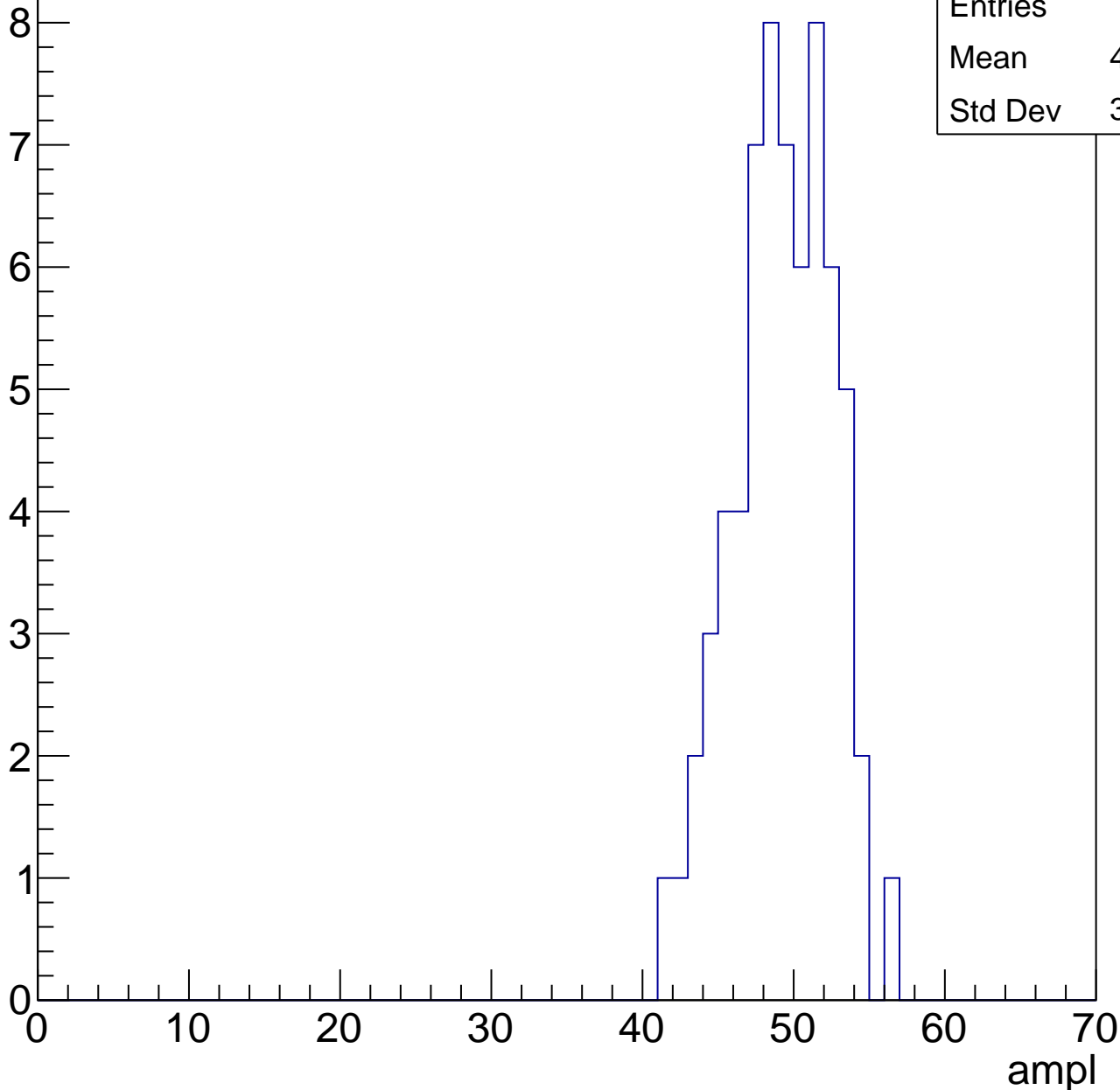


# B0L102S, U17-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

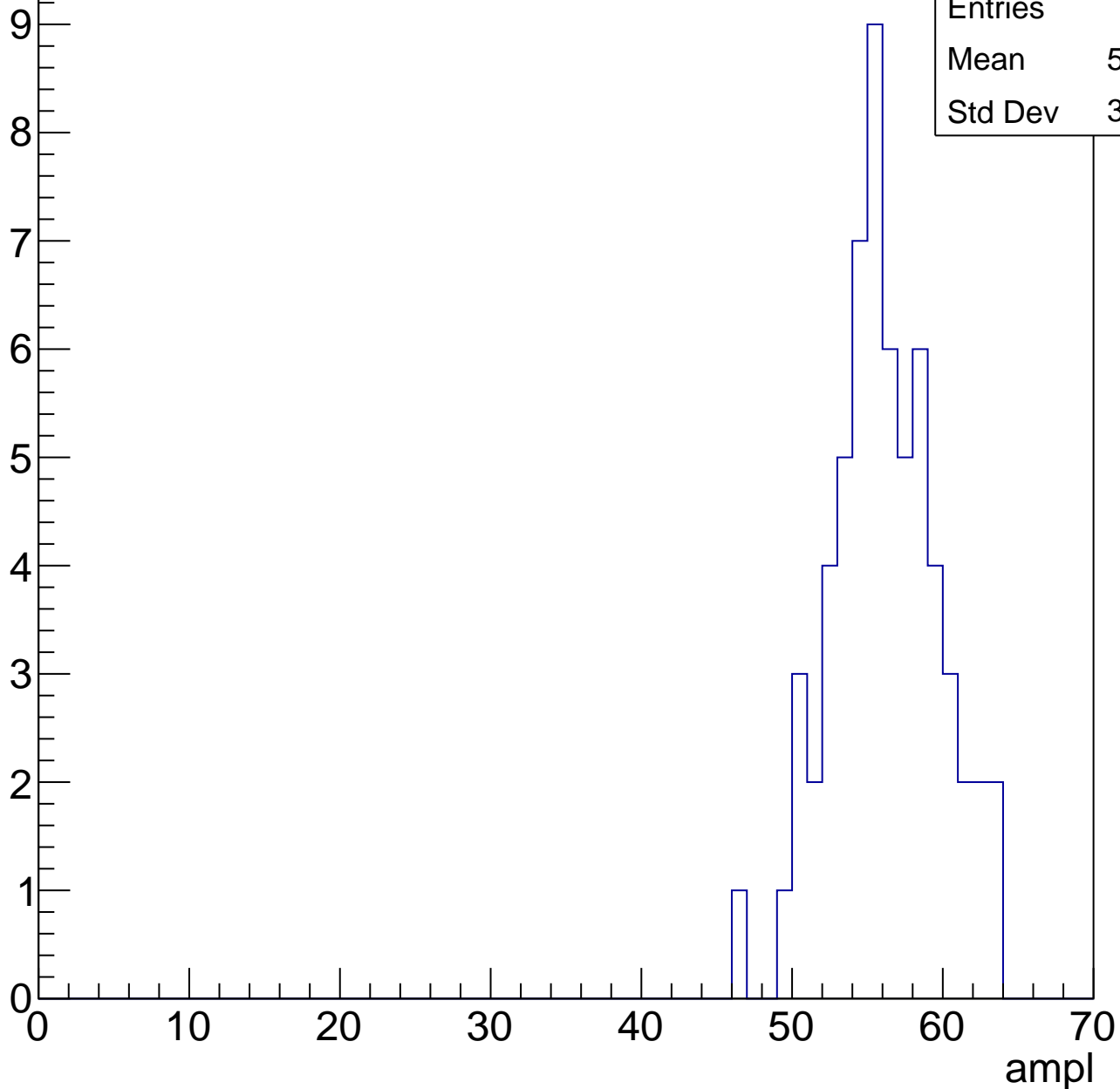
Entries	65
Mean	48.77
Std Dev	3.195



# B0L102S, U17-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

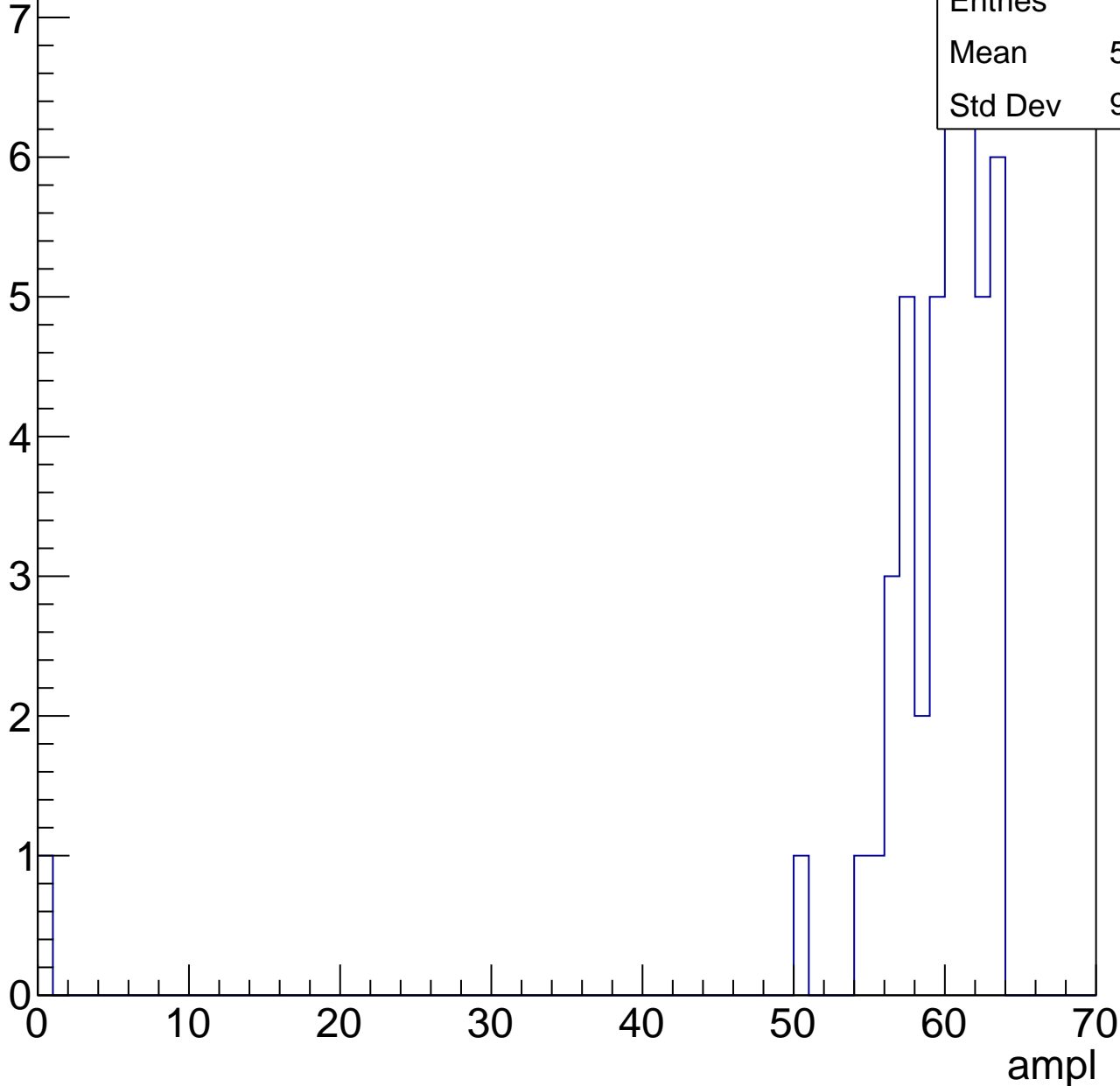


# B0L102S, U17-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.14
Std Dev	9.287



# B0L102S, U17-ch58, adc6

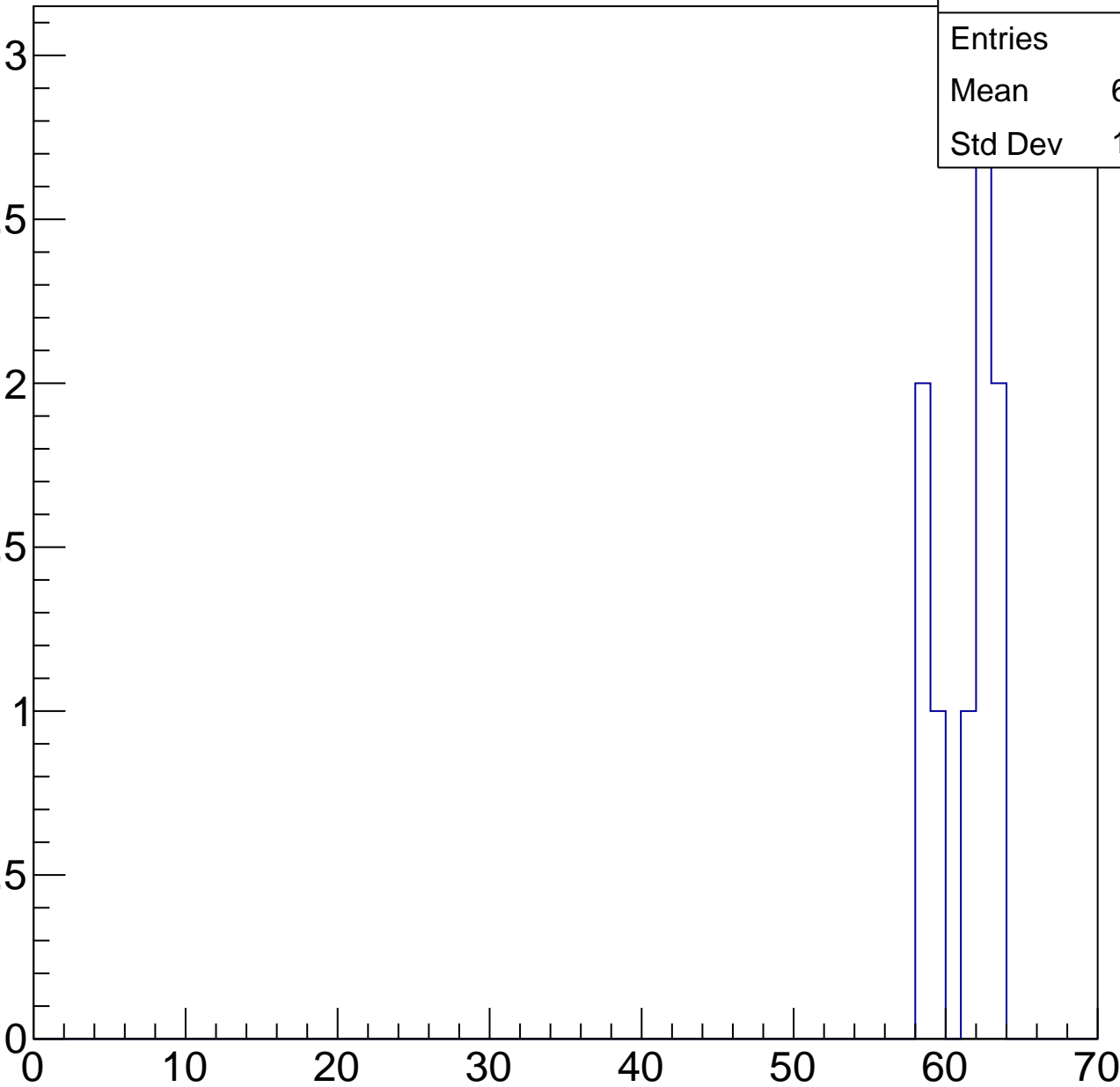
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	60.89
Std Dev	1.912

ampl





# B0L102S, U17-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	52
Mean	28.27
Std Dev	5.133

**Gaus mean : 29.3309**

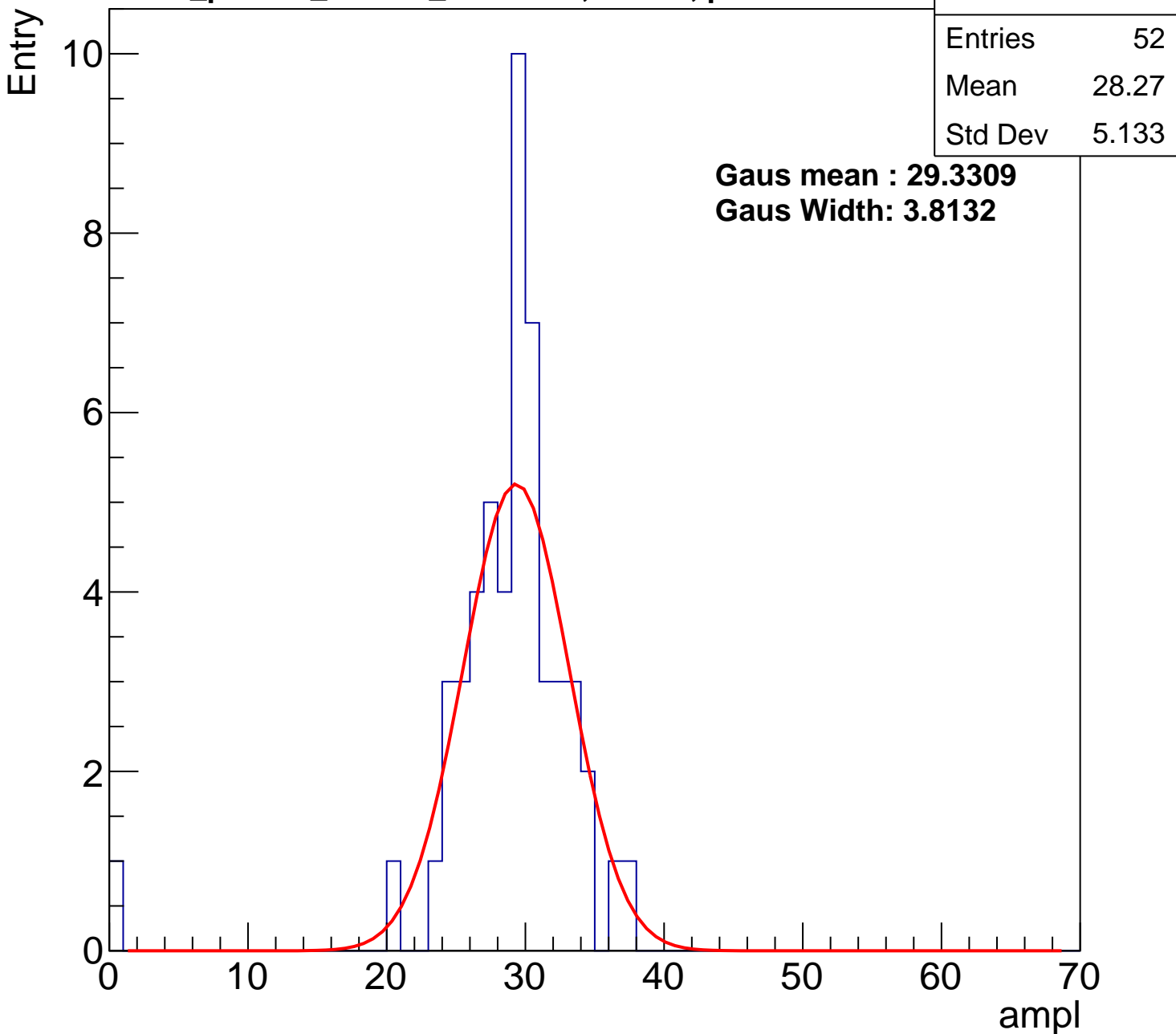
**Gaus Width: 3.8132**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch59, adc1

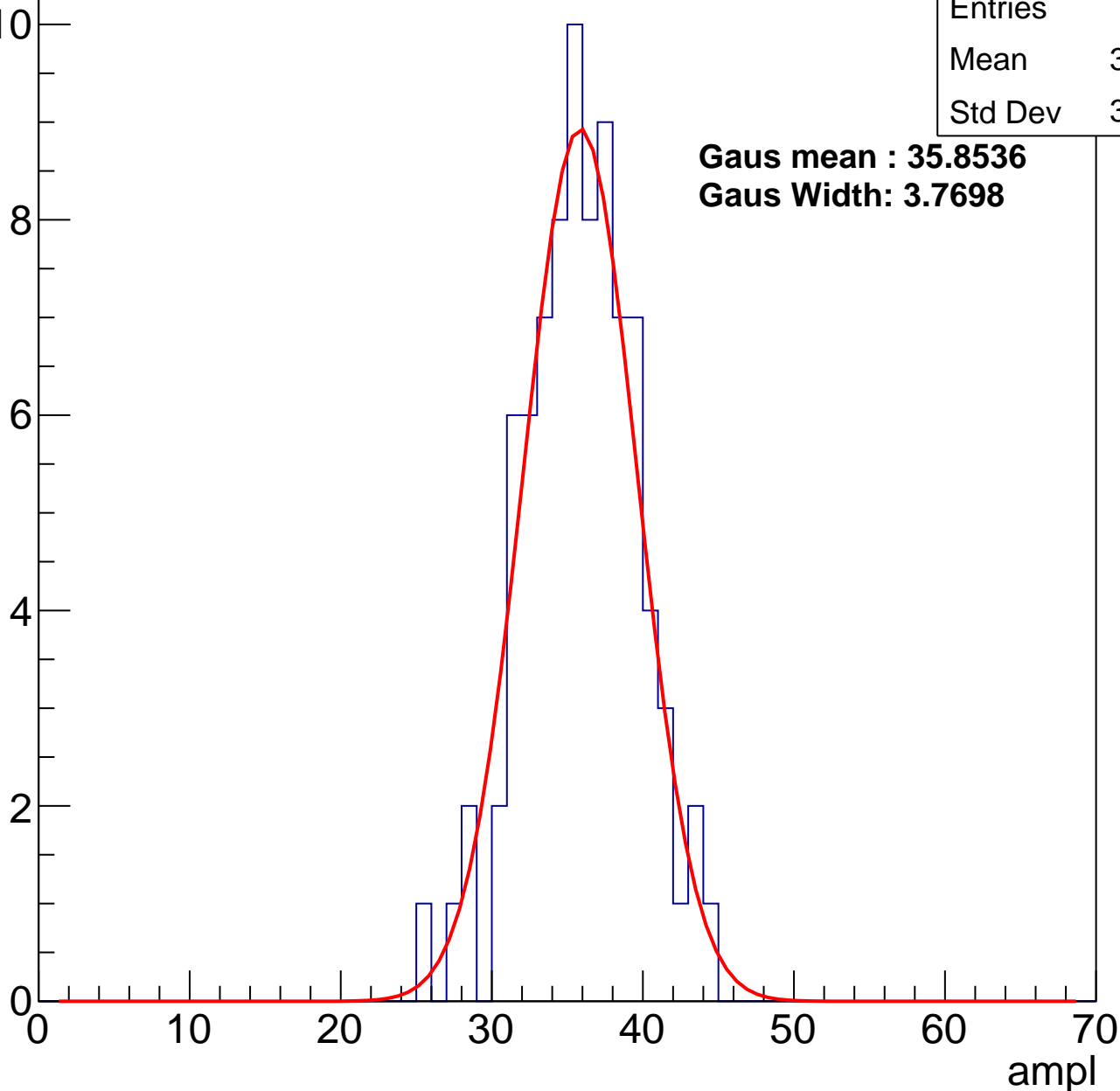
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	35.46
Std Dev	3.702

**Gaus mean : 35.8536**

**Gaus Width: 3.7698**



# B0L102S, U17-ch59, adc2

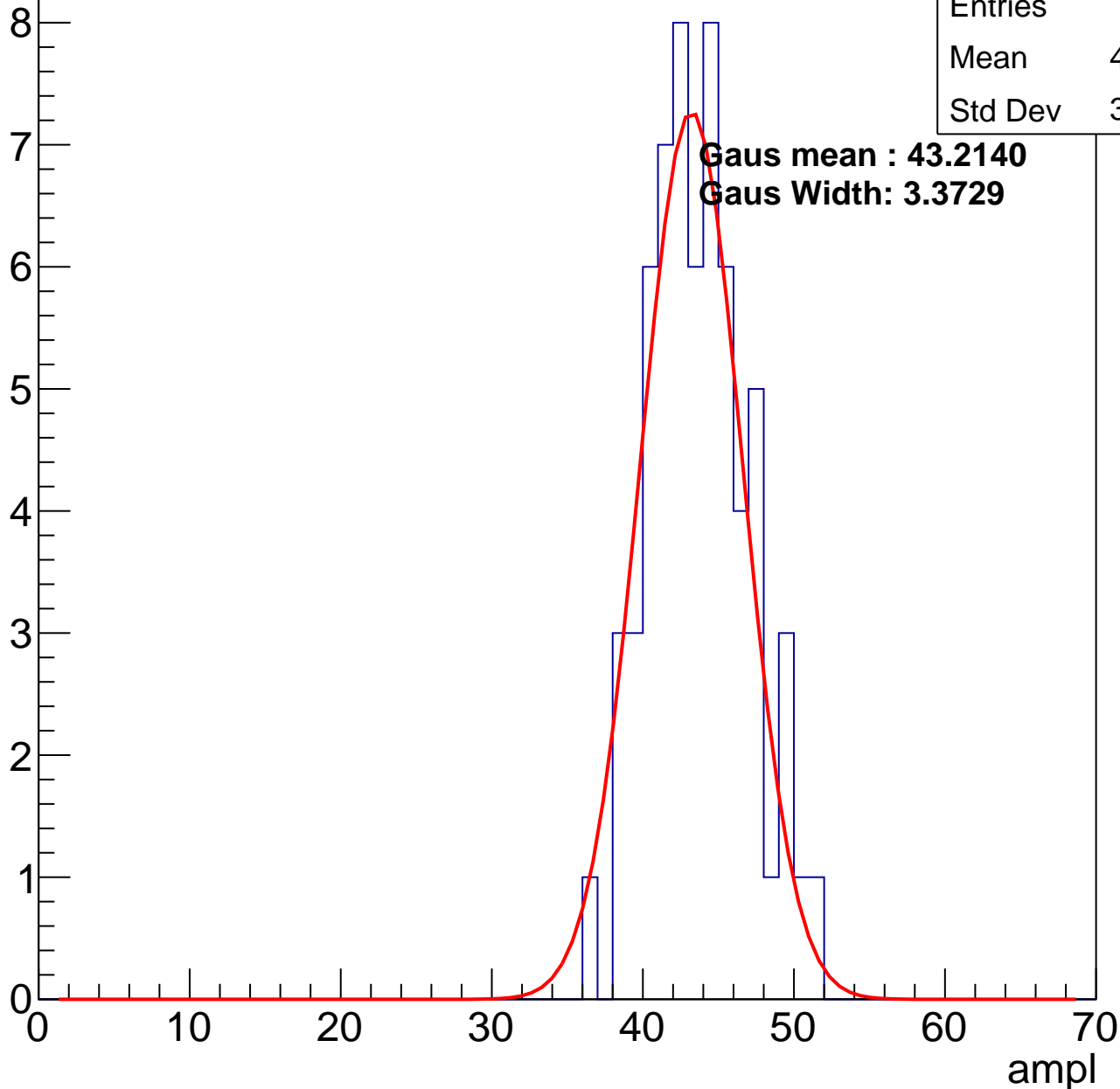
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.25
Std Dev	3.217

**Gaus mean : 43.2140**

**Gaus Width: 3.3729**

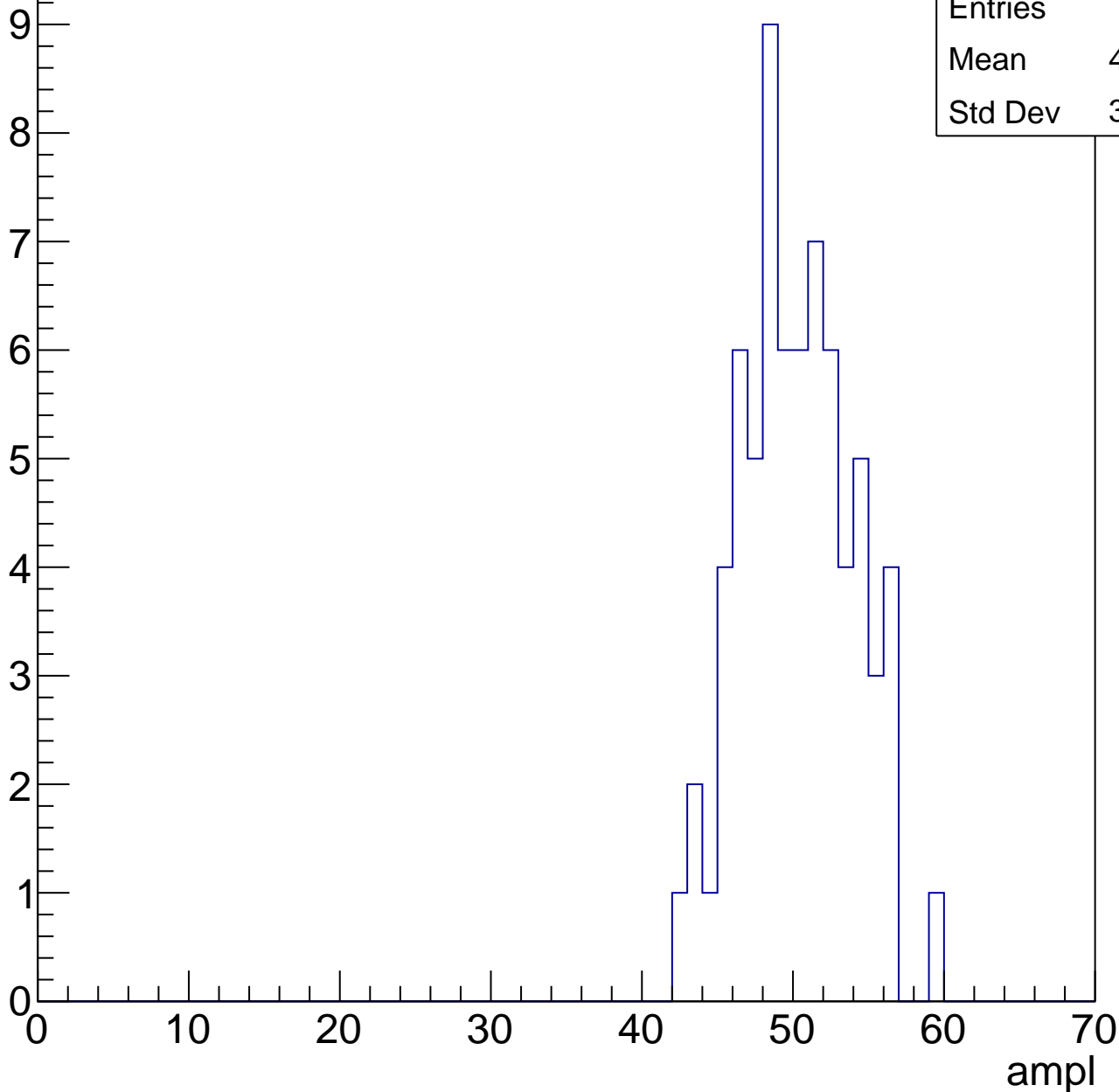


# B0L102S, U17-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.83
Std Dev	3.637



# B0L102S, U17-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

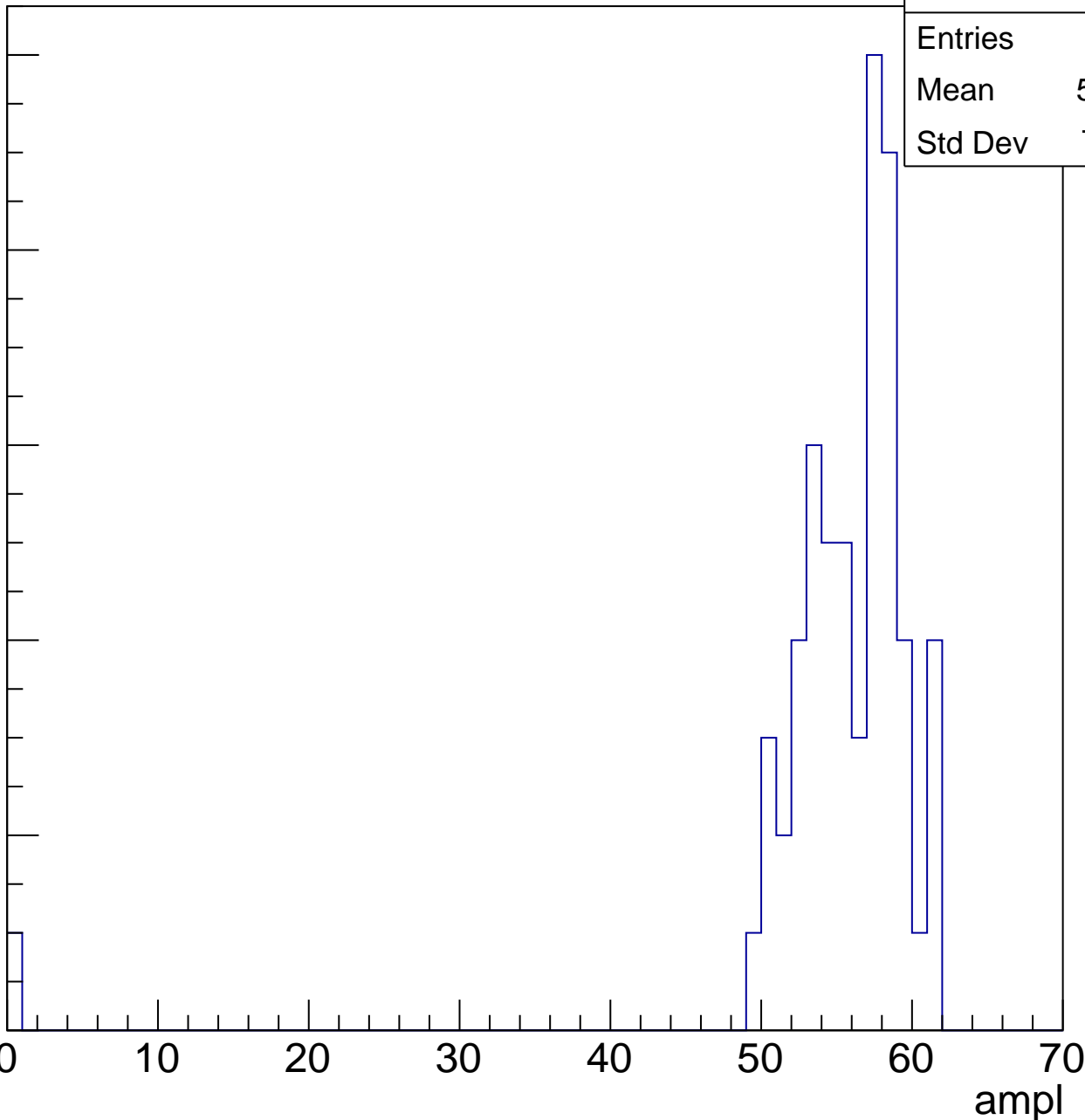
40

50

60

ampl

Entries	58
Mean	54.69
Std Dev	7.861



# B0L102S, U17-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	60.42
Std Dev	2.202

# B0L102S, U17-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch60, adc0

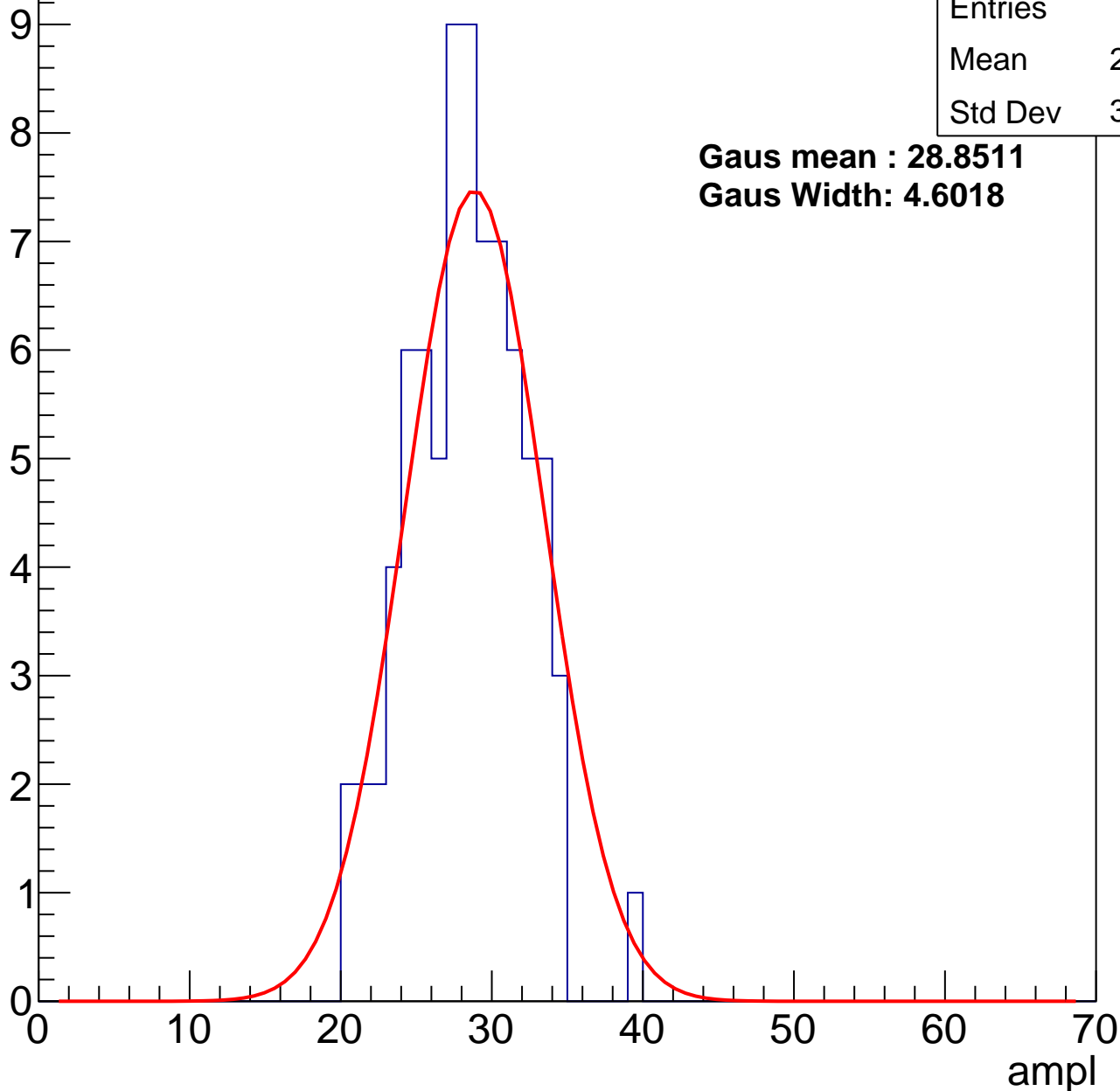
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	27.87
Std Dev	3.729

**Gaus mean : 28.8511**

**Gaus Width: 4.6018**



# B0L102S, U17-ch60, adc1

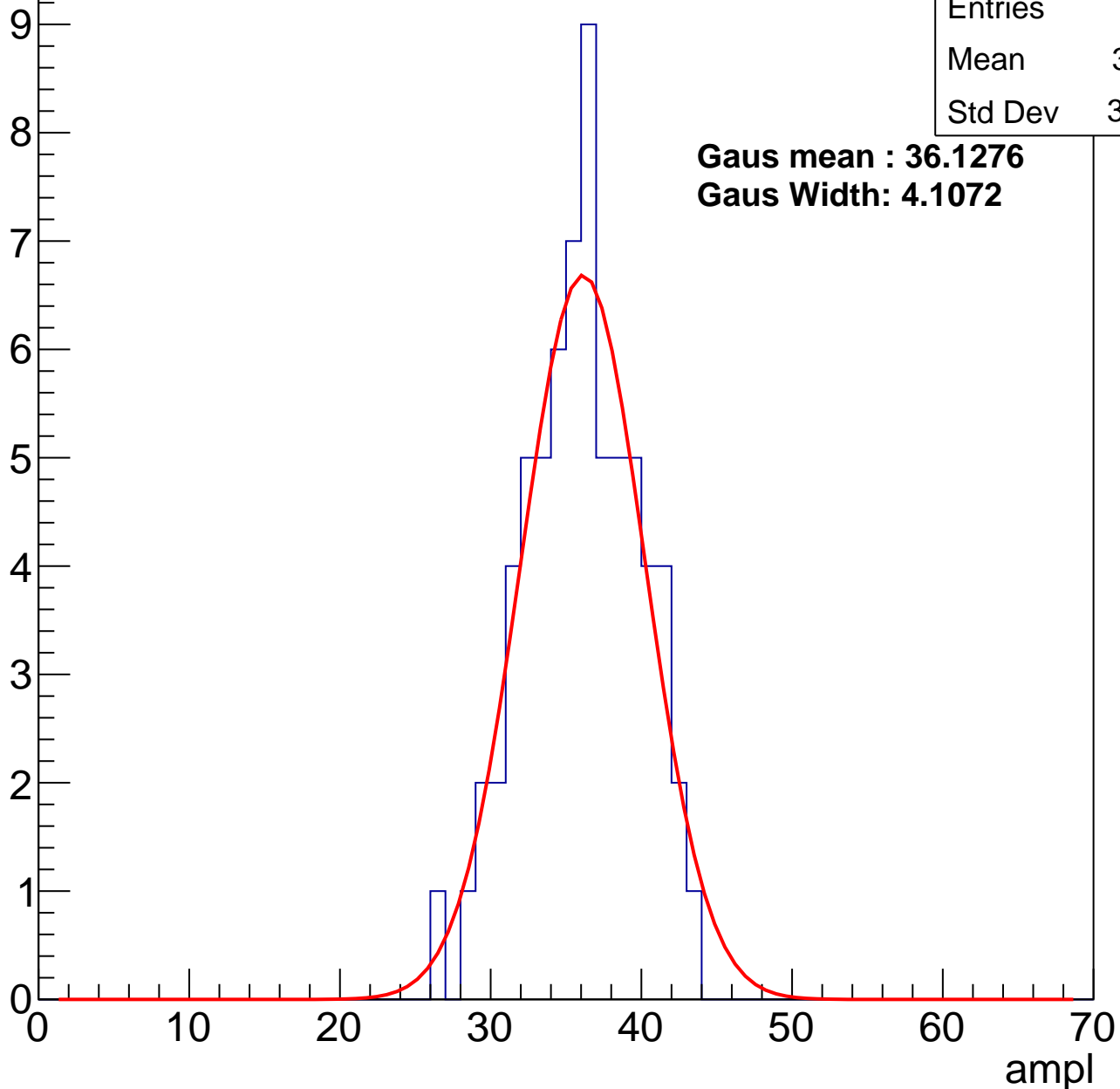
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	35.51
Std Dev	3.696

**Gaus mean : 36.1276**

**Gaus Width: 4.1072**



# B0L102S, U17-ch60, adc2

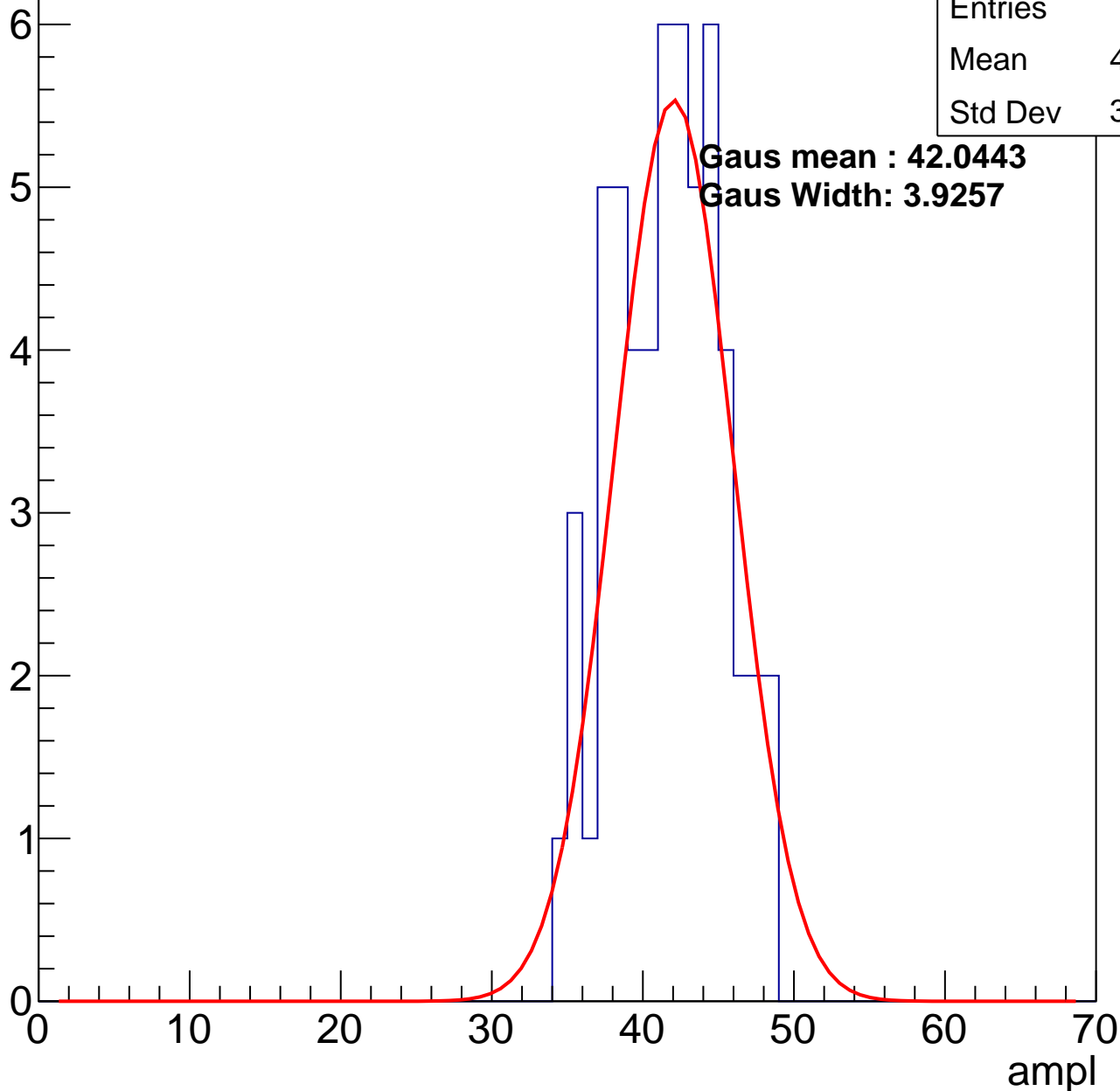
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	41.16
Std Dev	3.509

**Gaus mean : 42.0443**

**Gaus Width: 3.9257**

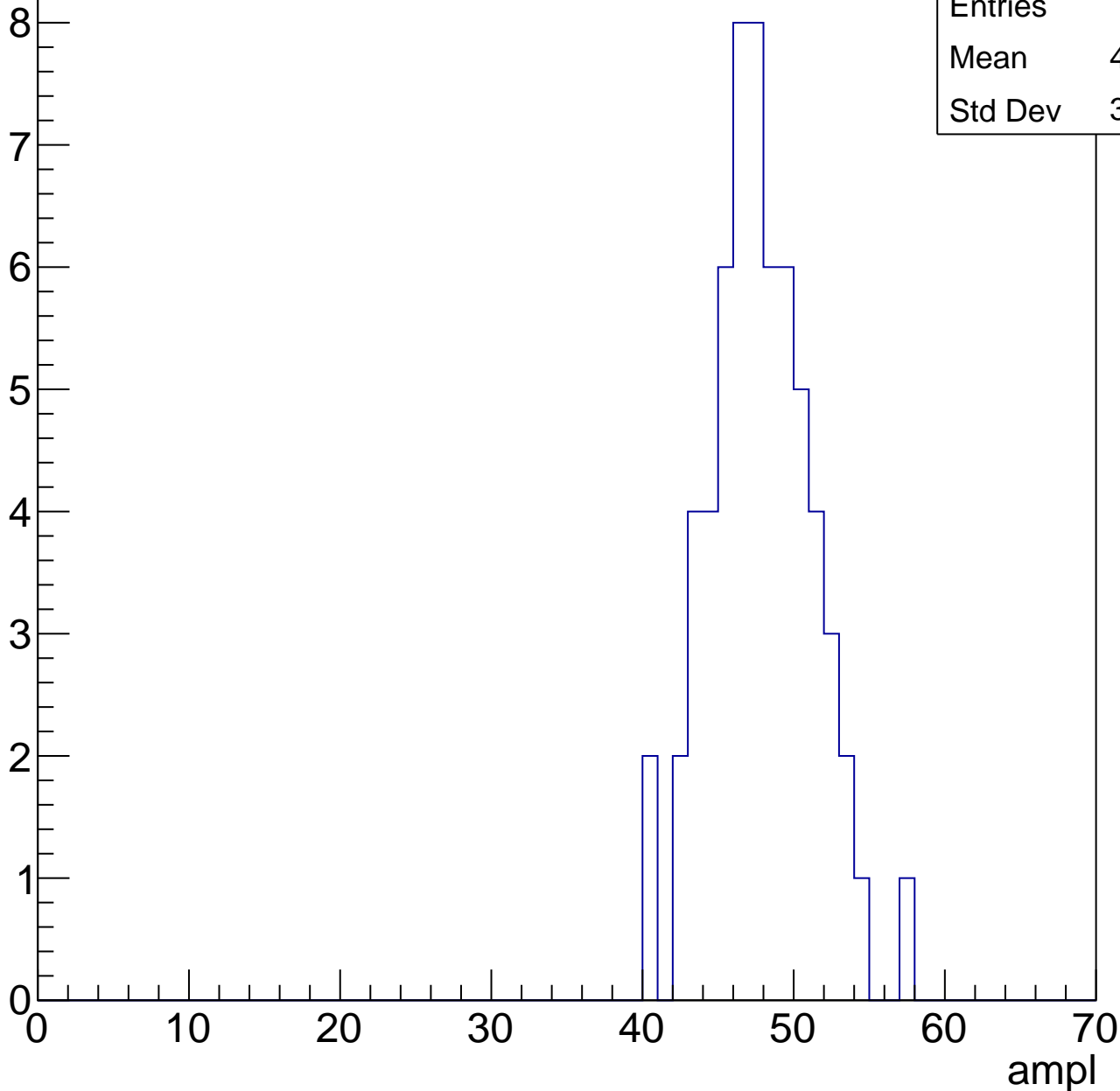


# B0L102S, U17-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	47.34
Std Dev	3.379

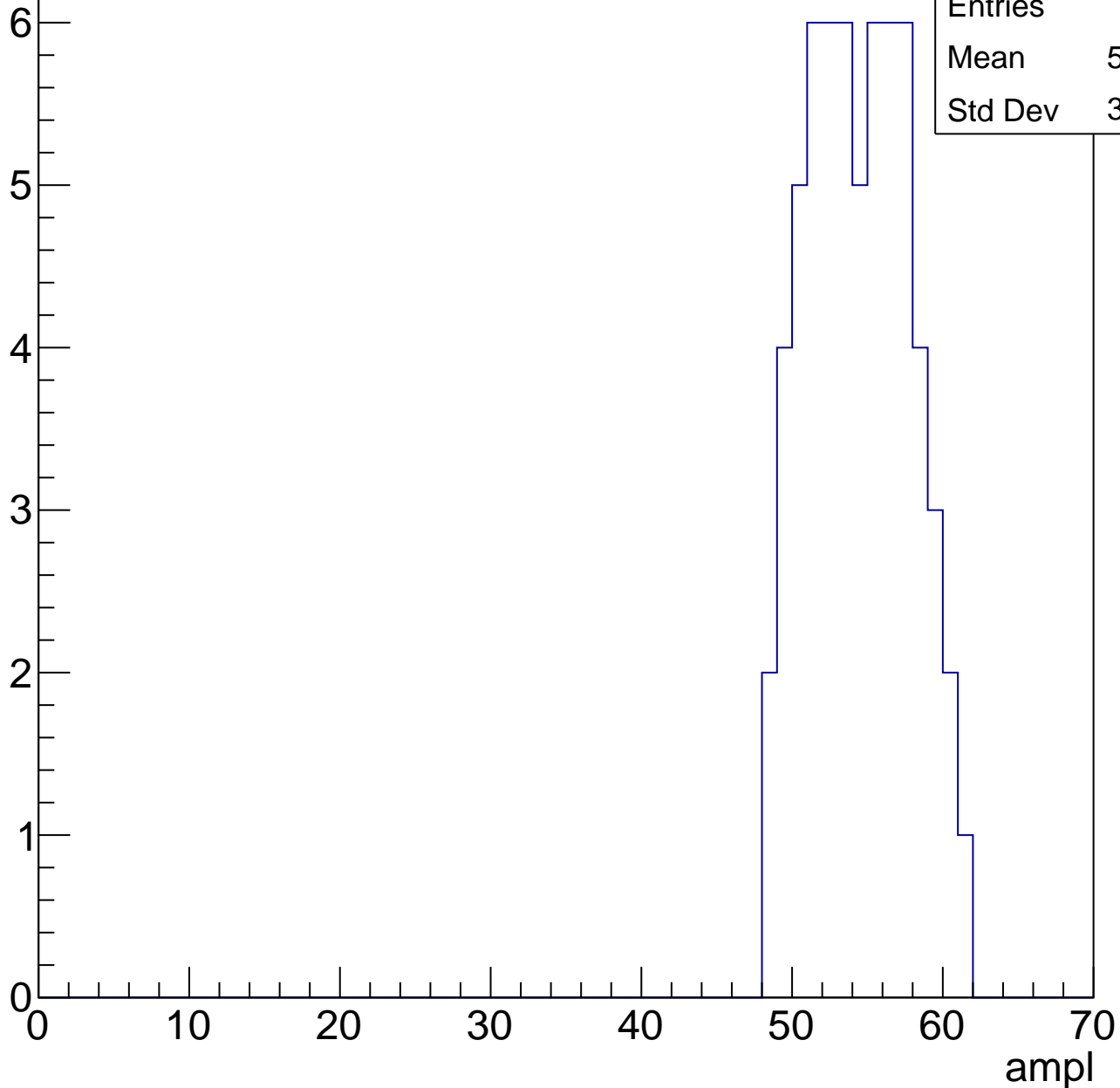


# B0L102S, U17-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	53.97
Std Dev	3.312

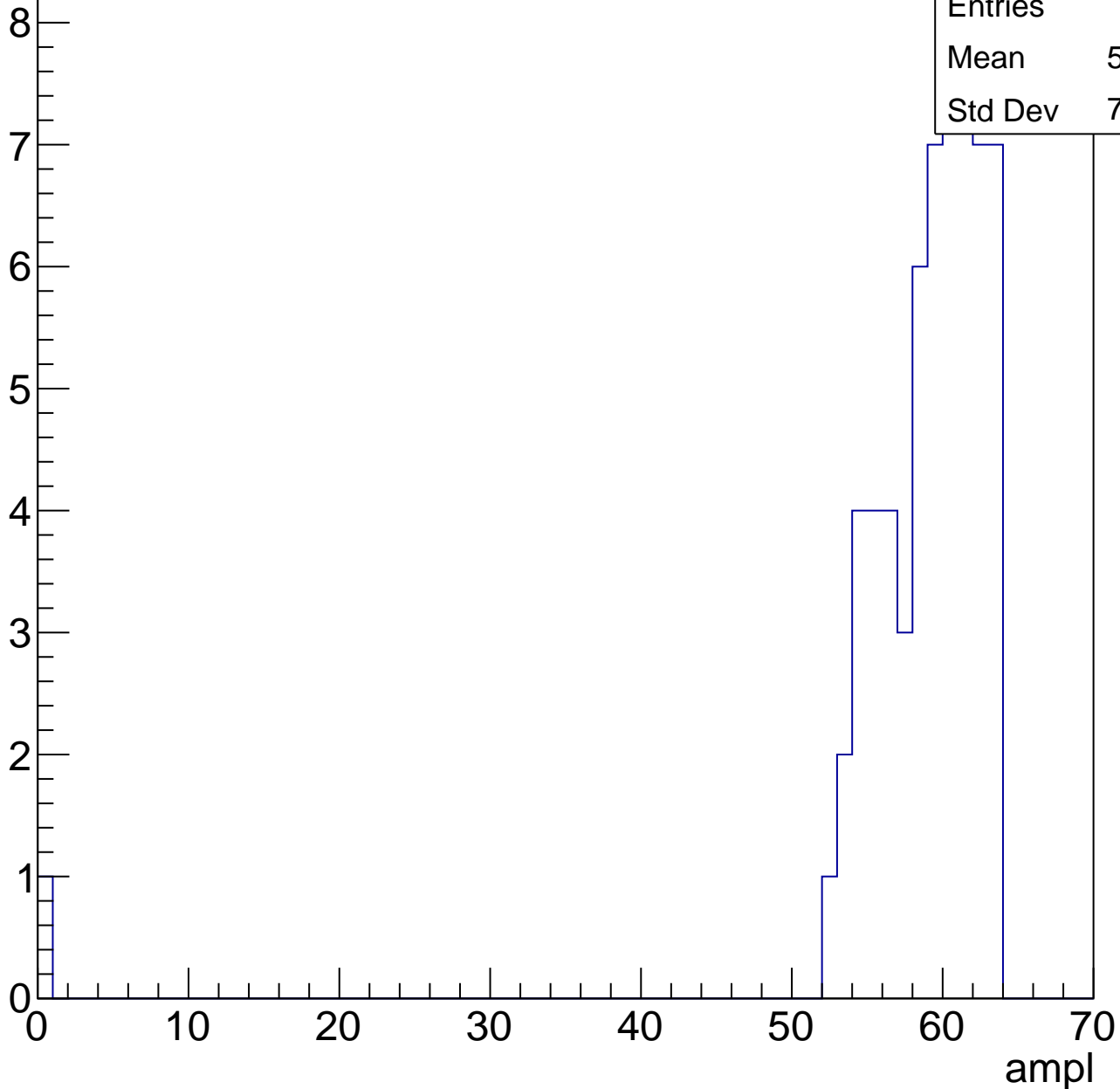


# B0L102S, U17-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	57.95
Std Dev	7.999



# B0L102S, U17-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch61, adc0

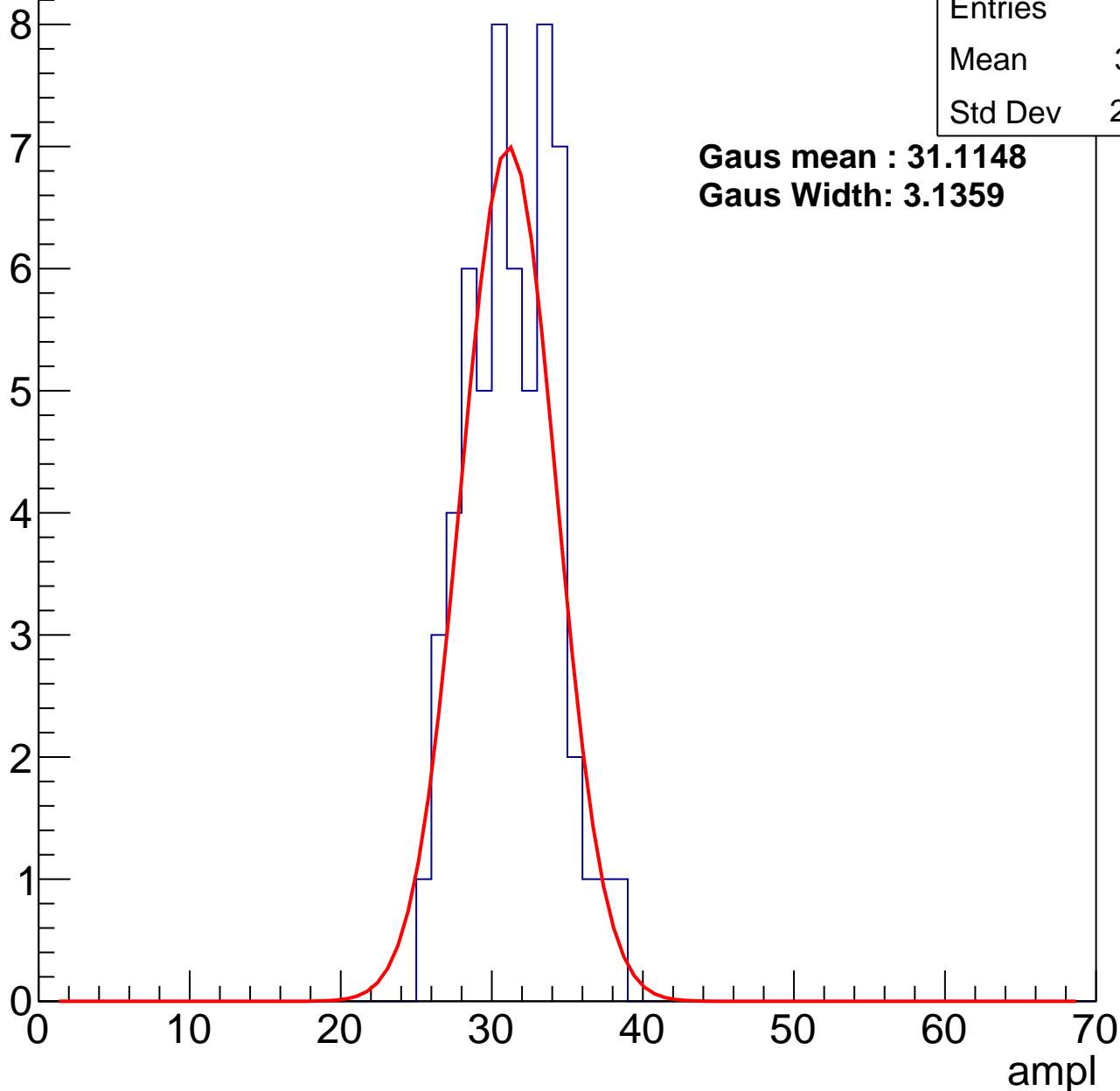
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	30.91
Std Dev	2.932

**Gaus mean : 31.1148**

**Gaus Width: 3.1359**



# B0L102S, U17-ch61, adc1

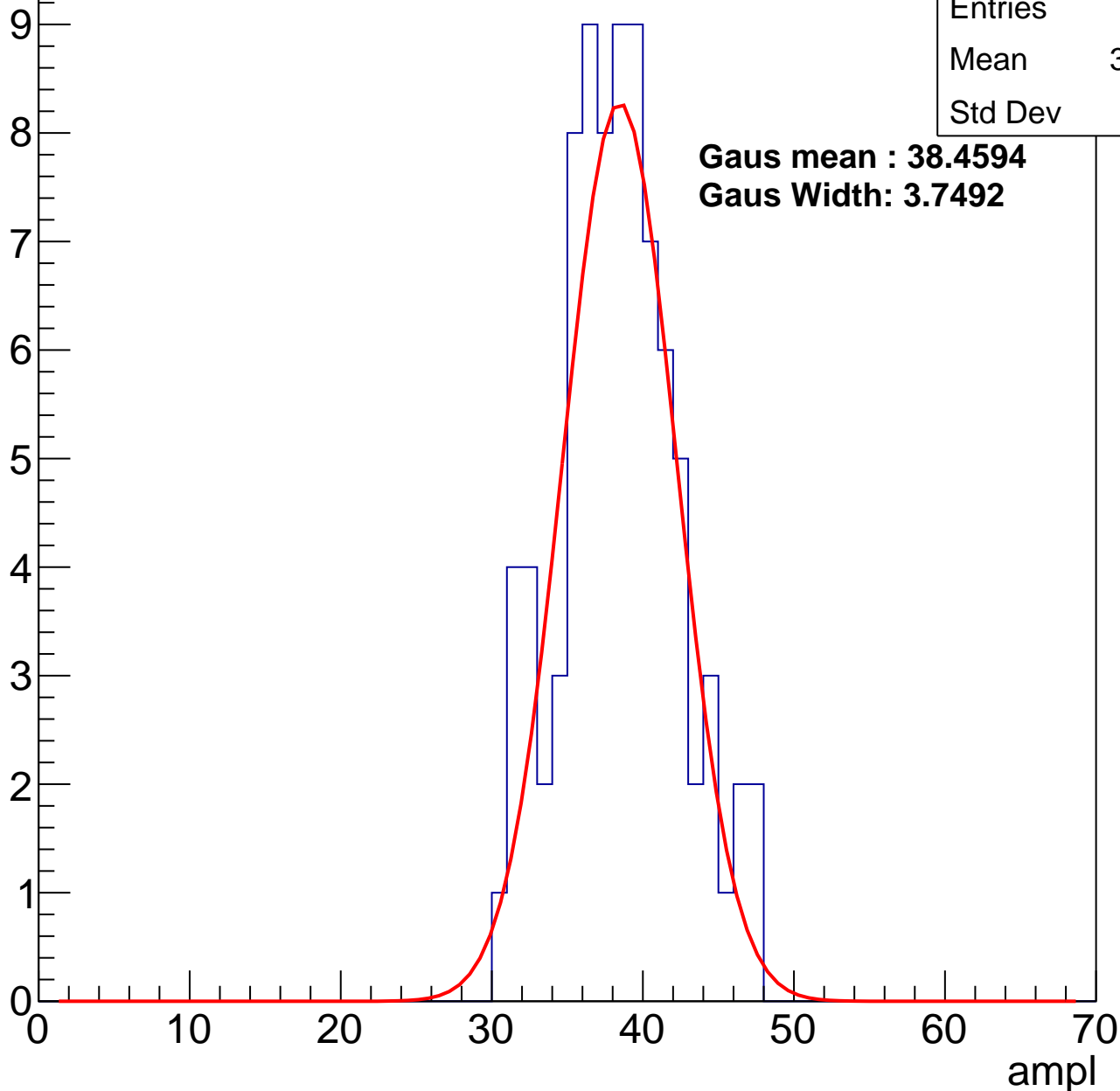
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	37.98
Std Dev	3.89

**Gaus mean : 38.4594**

**Gaus Width: 3.7492**



# B0L102S, U17-ch61, adc2

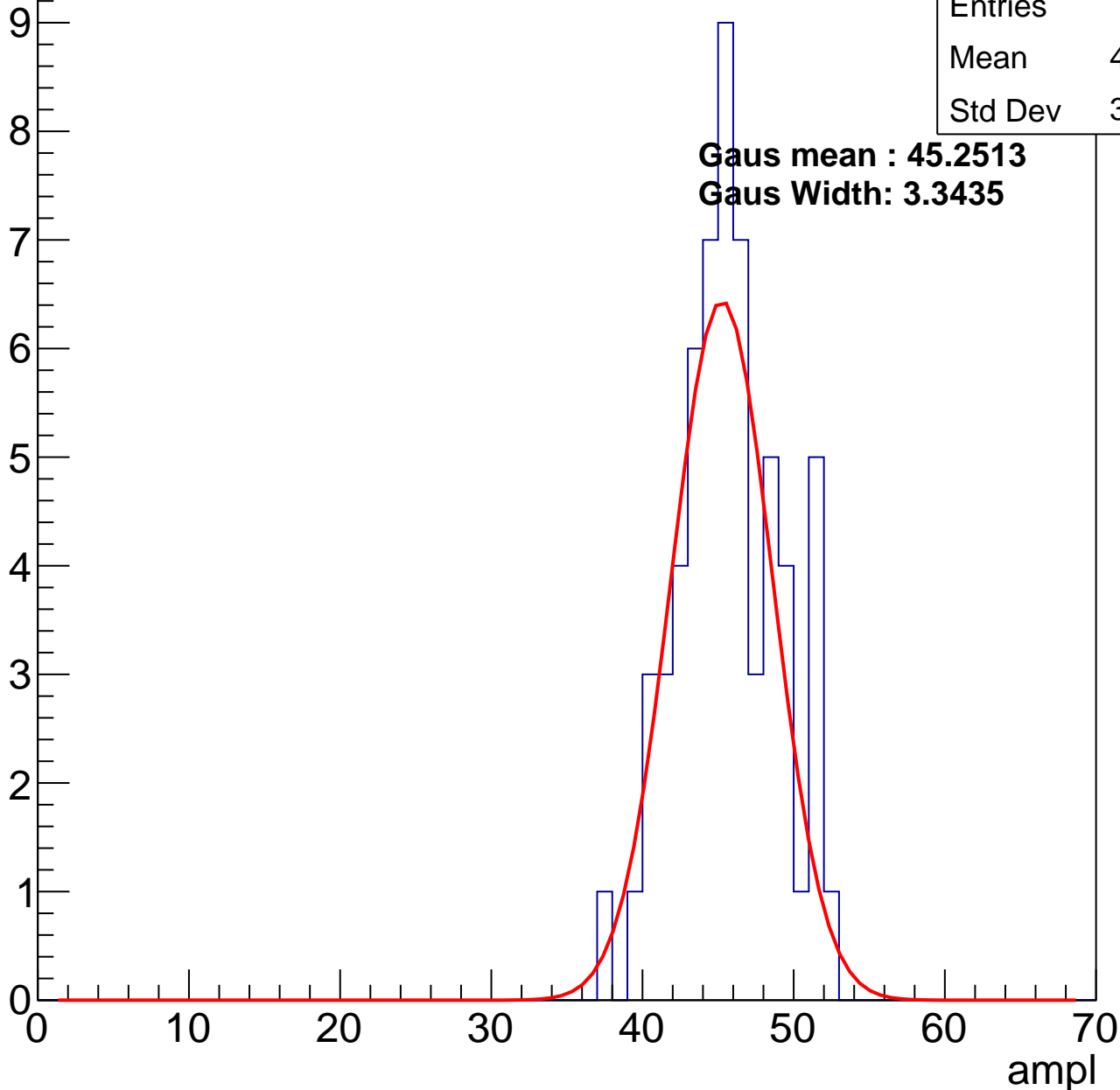
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	45.23
Std Dev	3.338

**Gaus mean : 45.2513**

**Gaus Width: 3.3435**

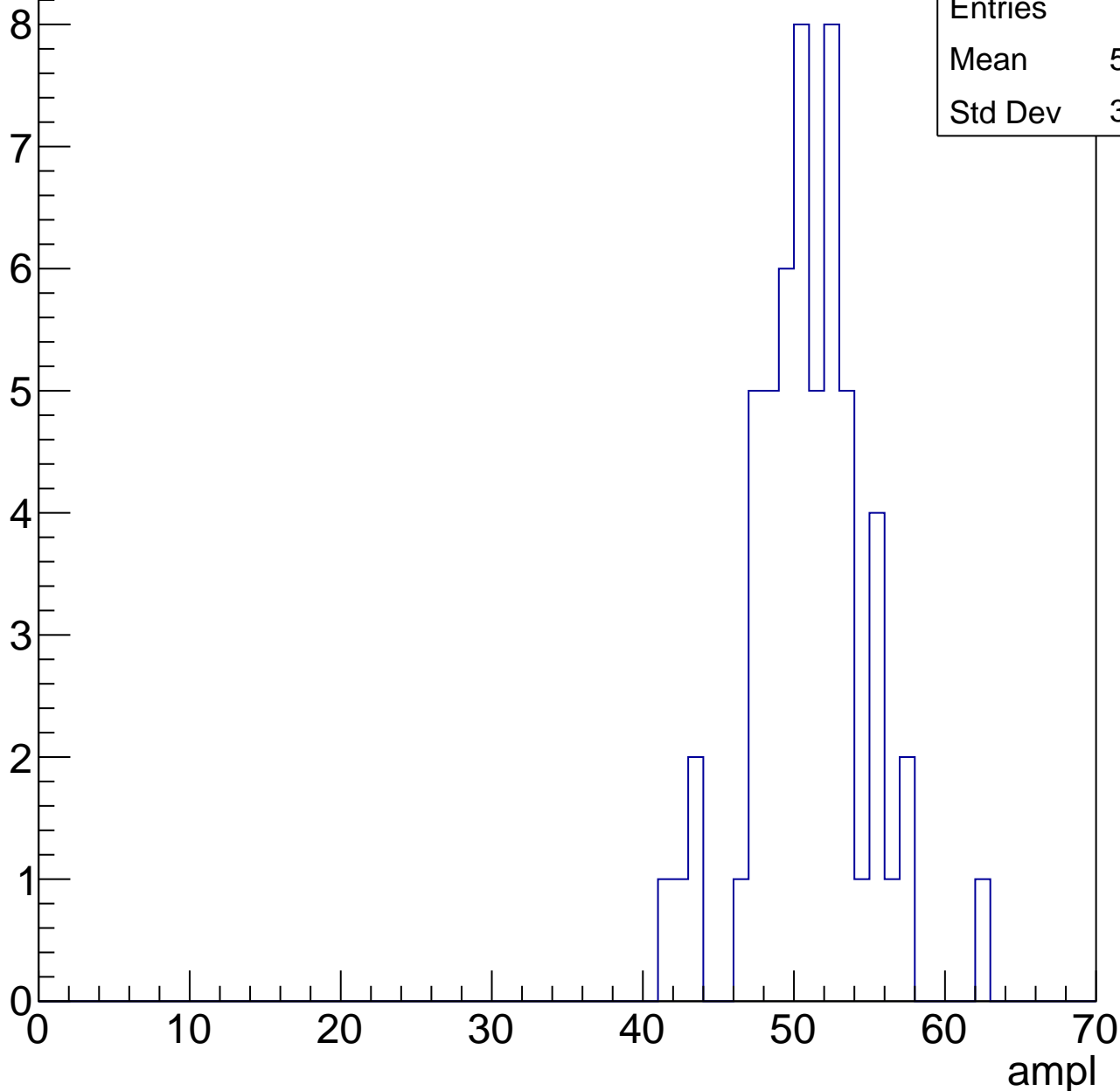


# B0L102S, U17-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

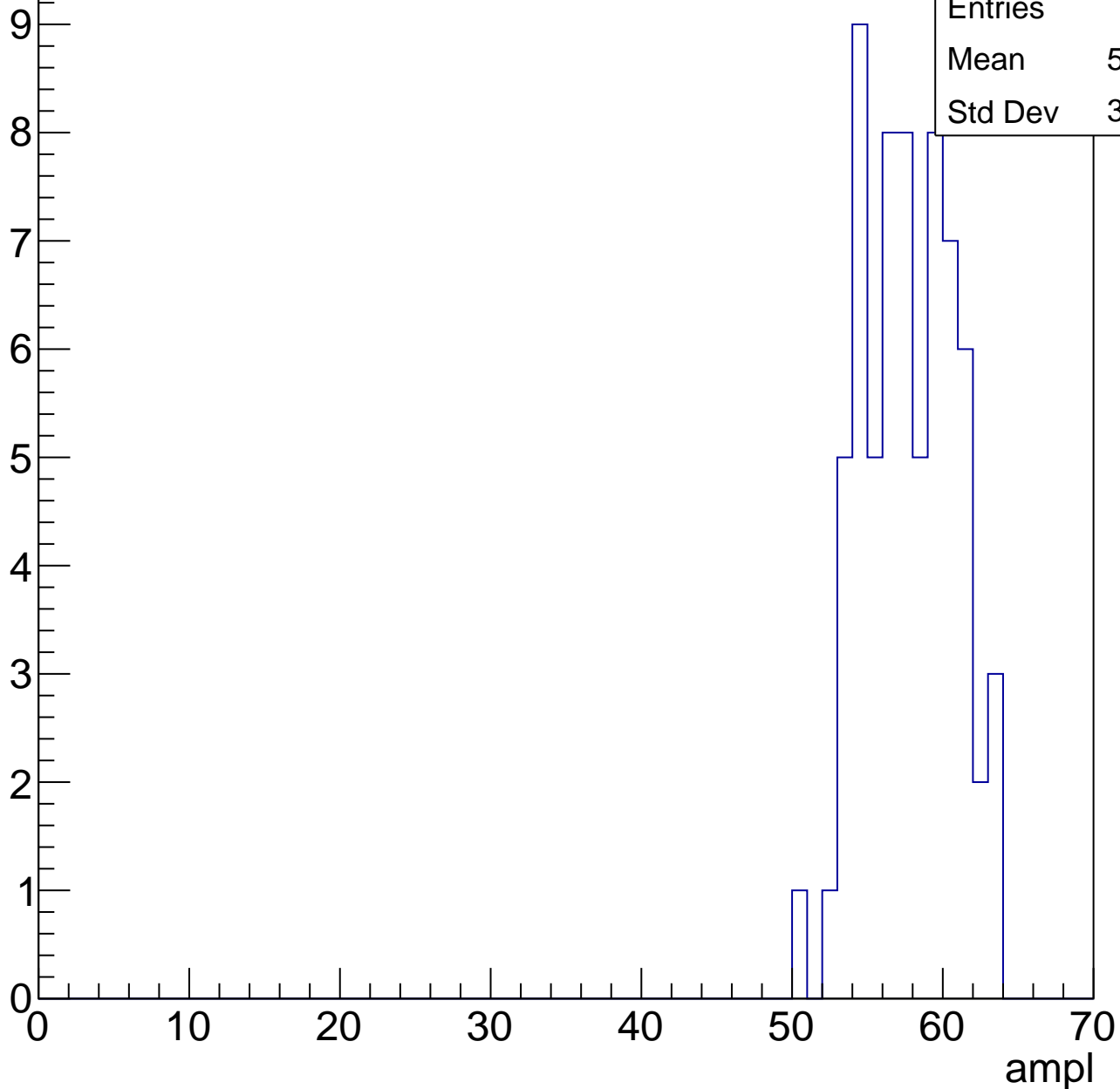
Entries	56
Mean	50.46
Std Dev	3.765



# B0L102S, U17-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

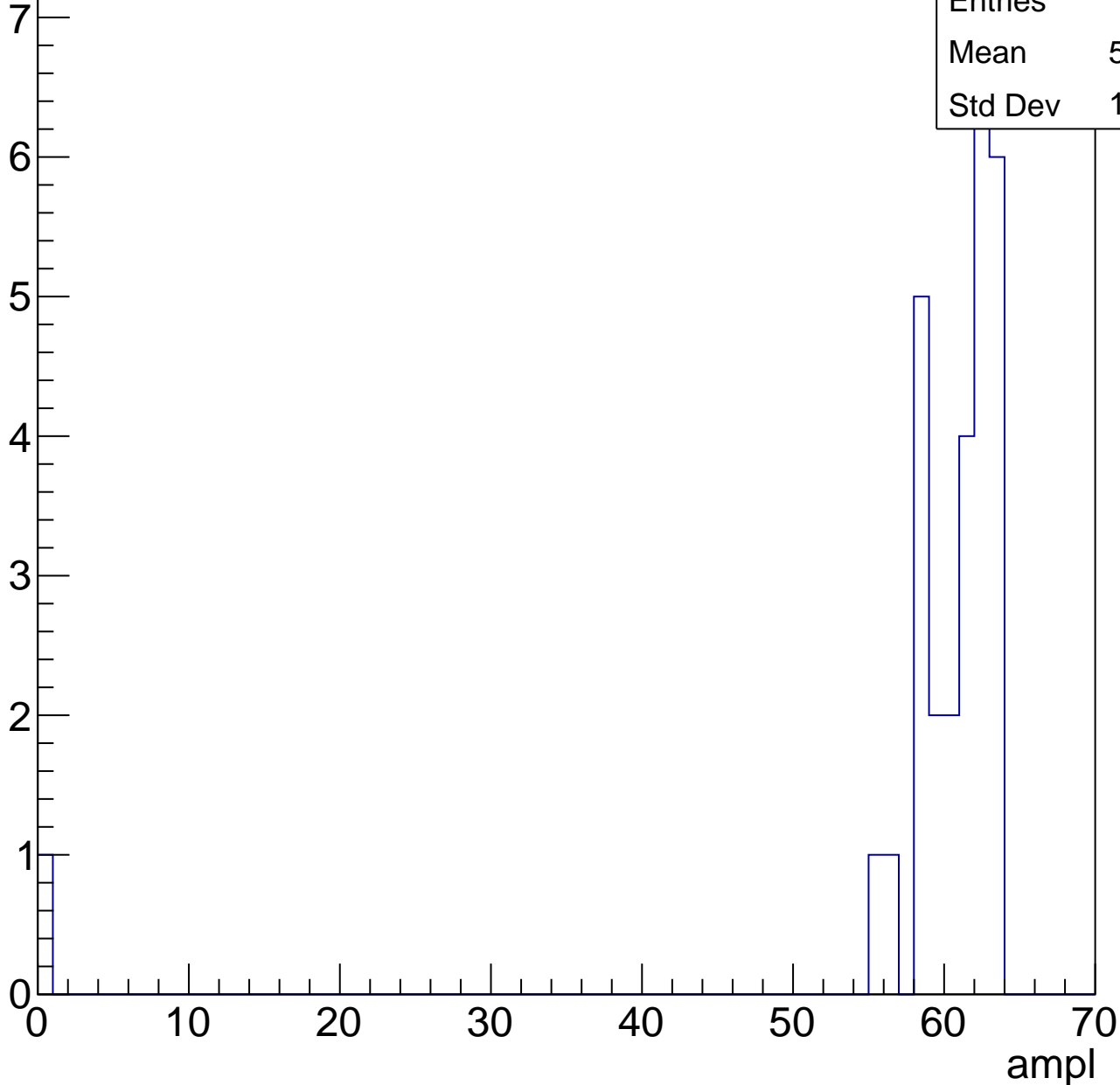


# B0L102S, U17-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	29
Mean	58.45
Std Dev	11.26



# B0L102S, U17-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch62, adc0

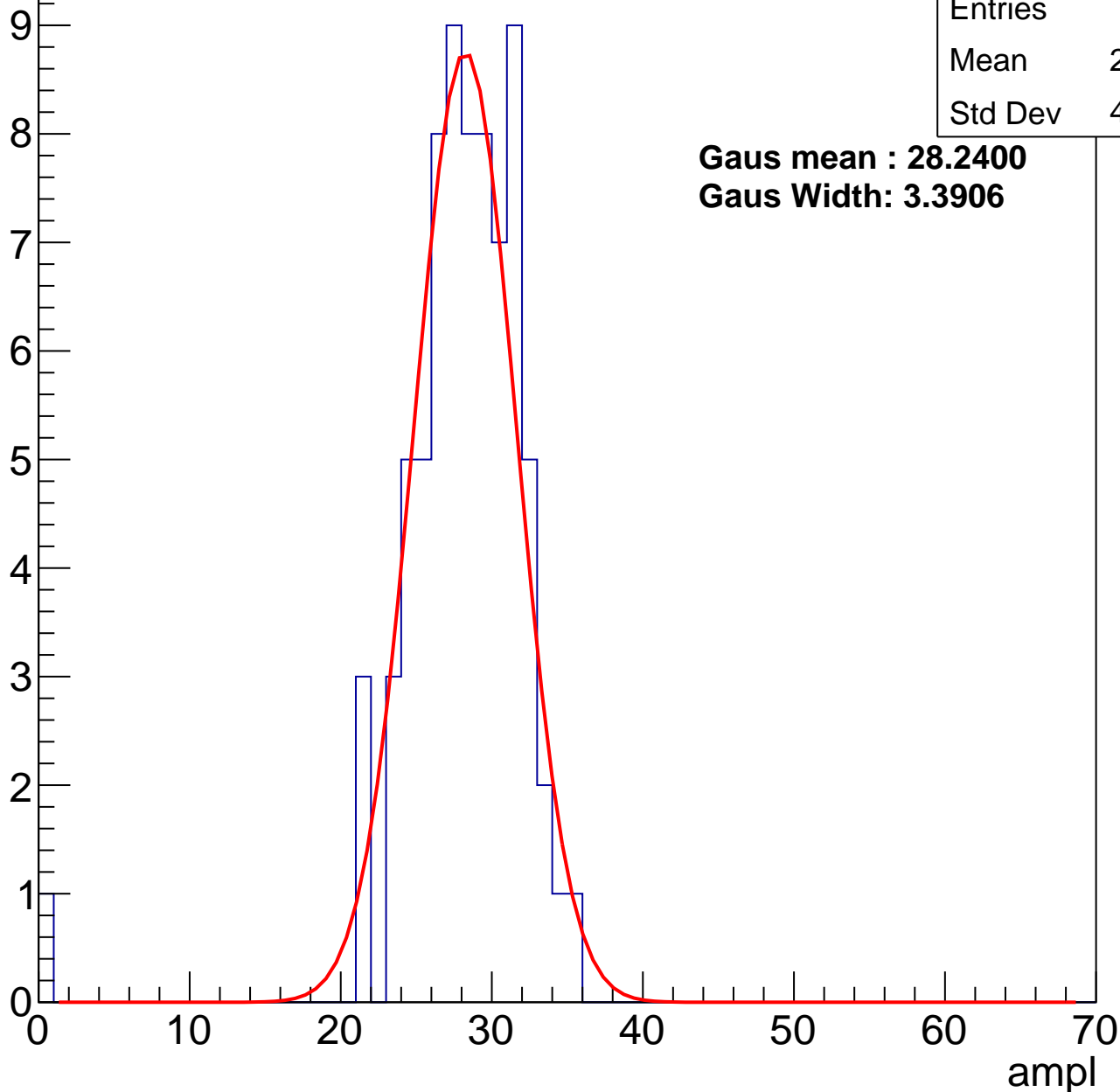
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	27.57
Std Dev	4.458

**Gaus mean : 28.2400**

**Gaus Width: 3.3906**



# B0L102S, U17-ch62, adc1

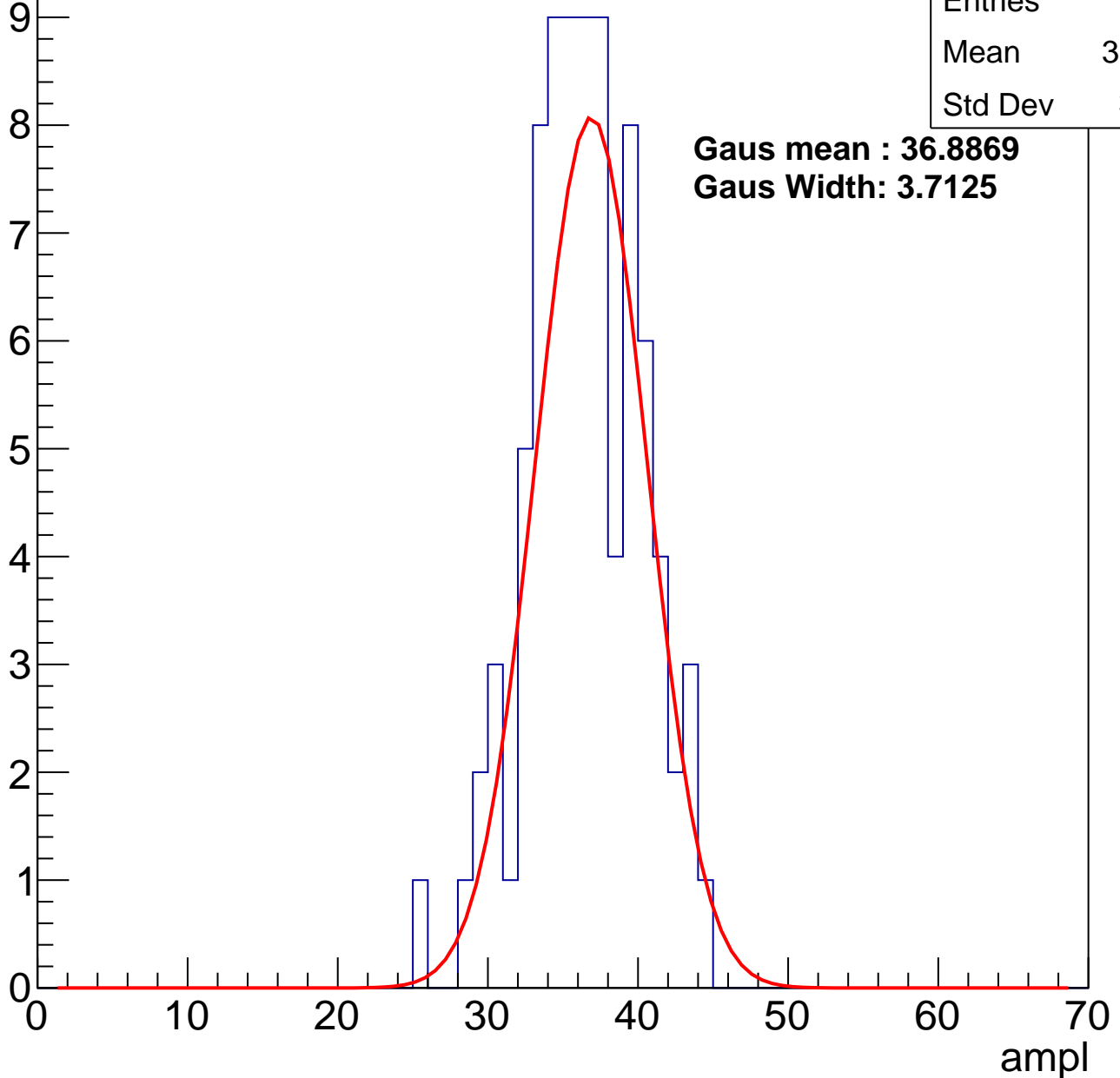
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	35.99
Std Dev	3.74

**Gaus mean : 36.8869**

**Gaus Width: 3.7125**



# B0L102S, U17-ch62, adc2

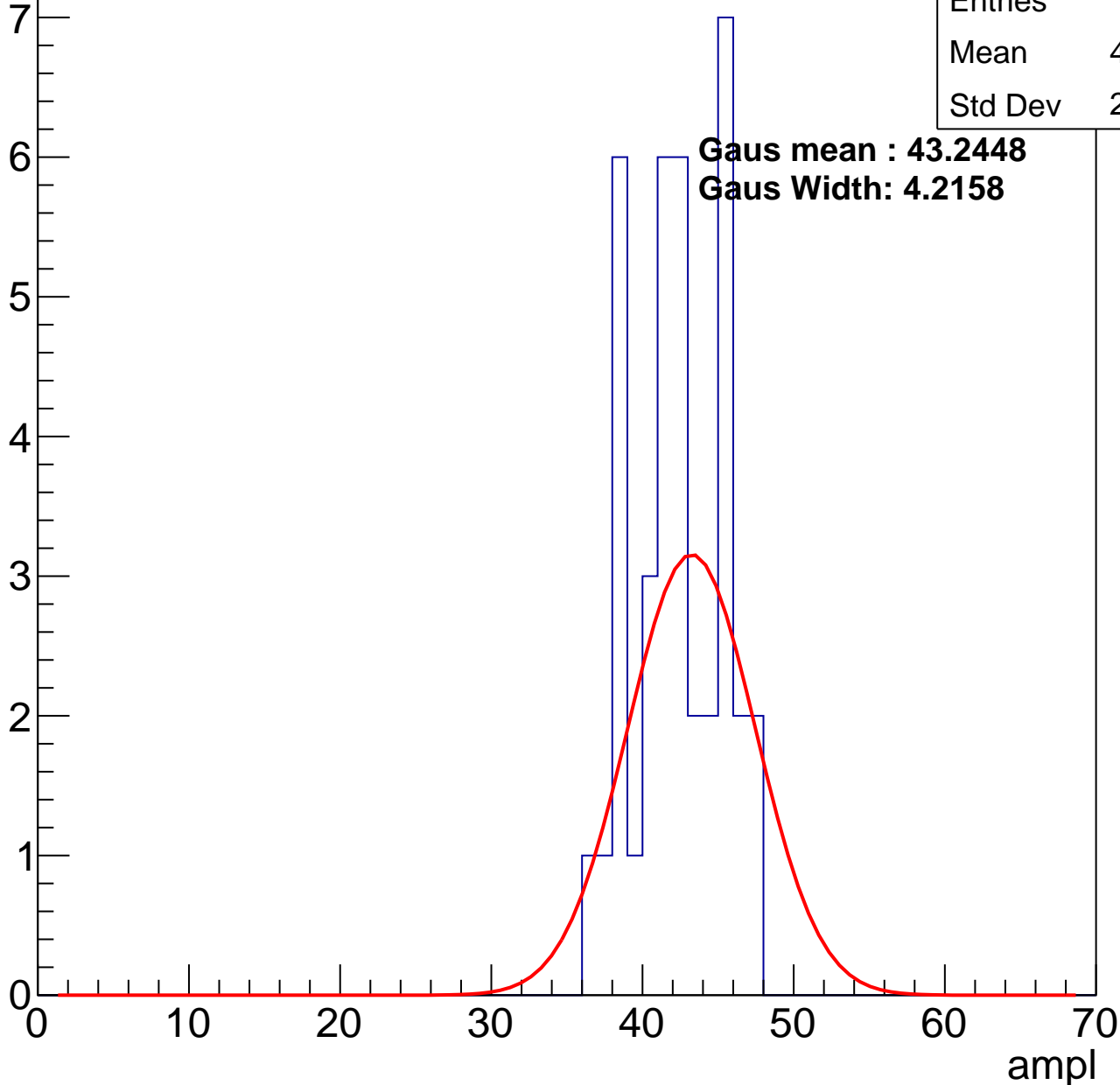
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	41.87
Std Dev	2.945

**Gaus mean : 43.2448**

**Gaus Width: 4.2158**

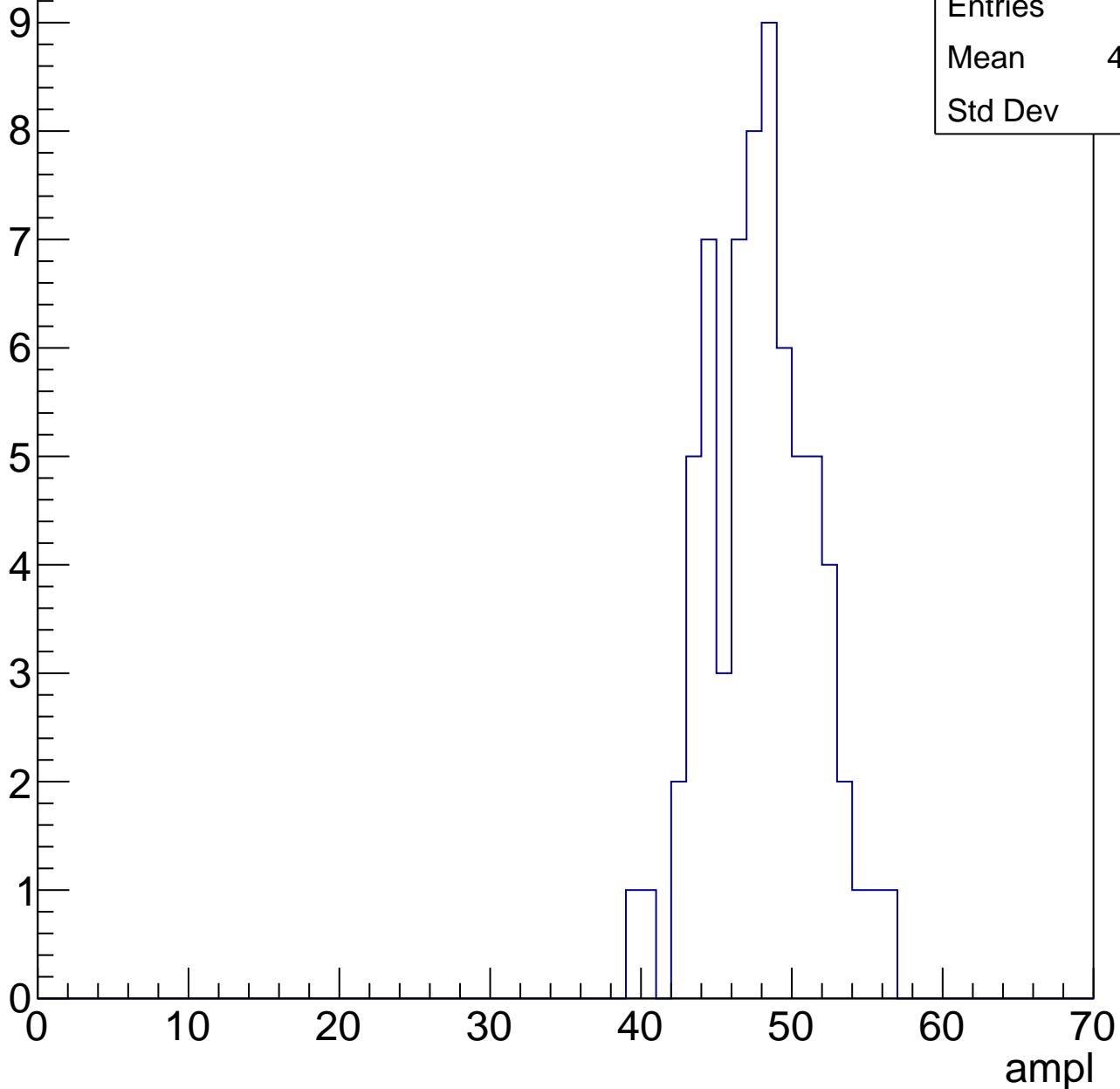


# B0L102S, U17-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	47.49
Std Dev	3.5

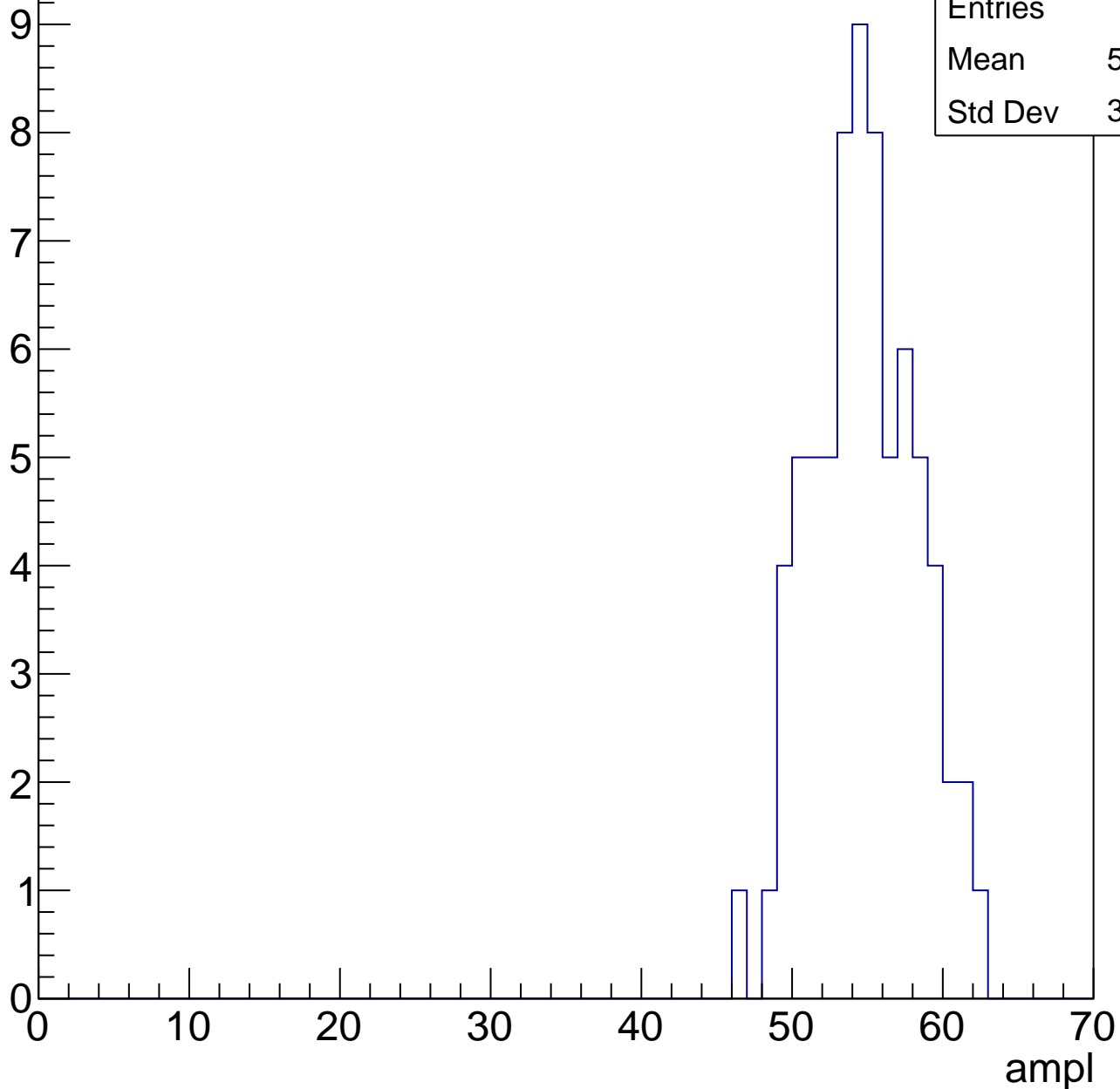


# B0L102S, U17-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	54.32
Std Dev	3.443

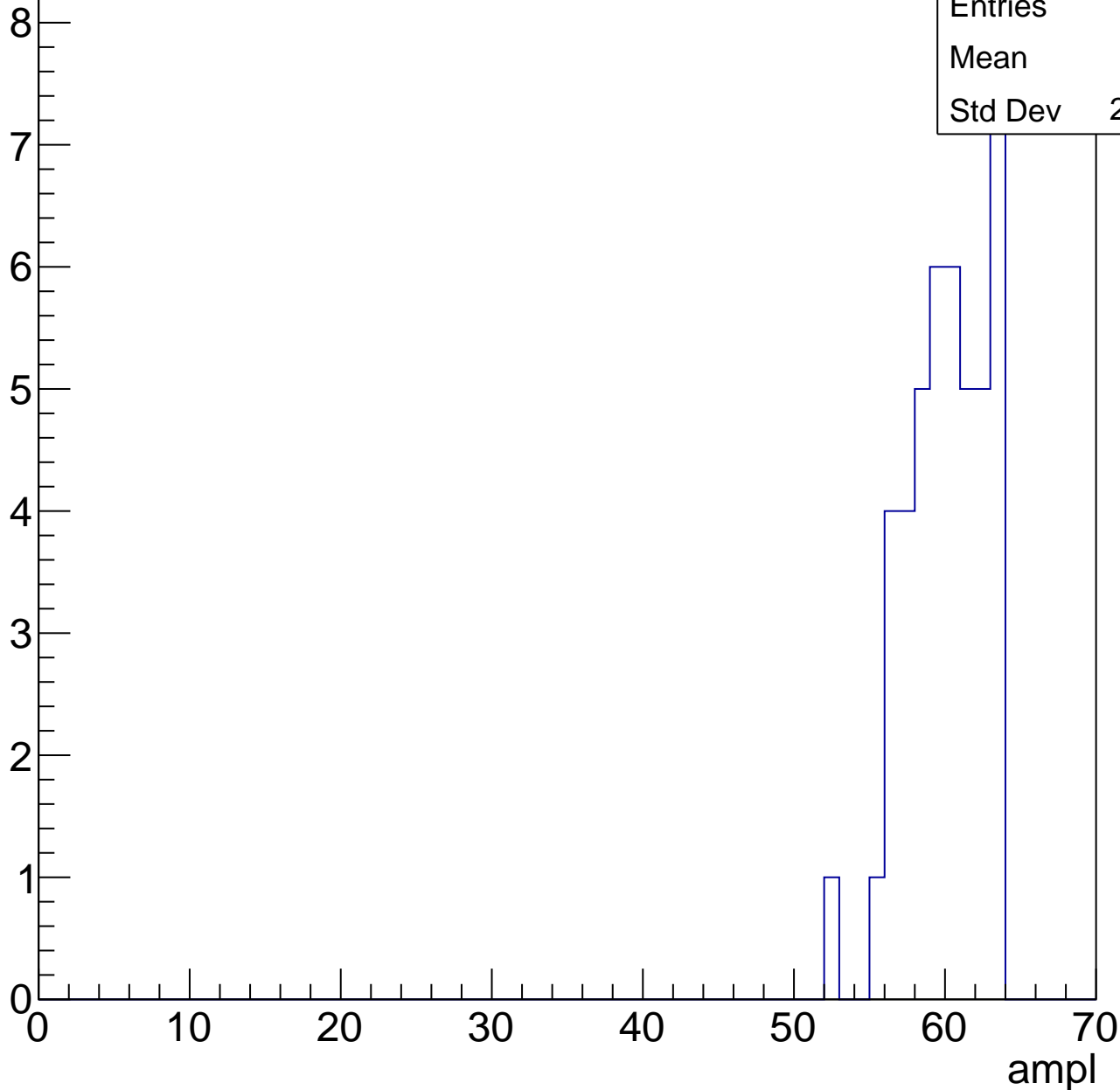


# B0L102S, U17-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

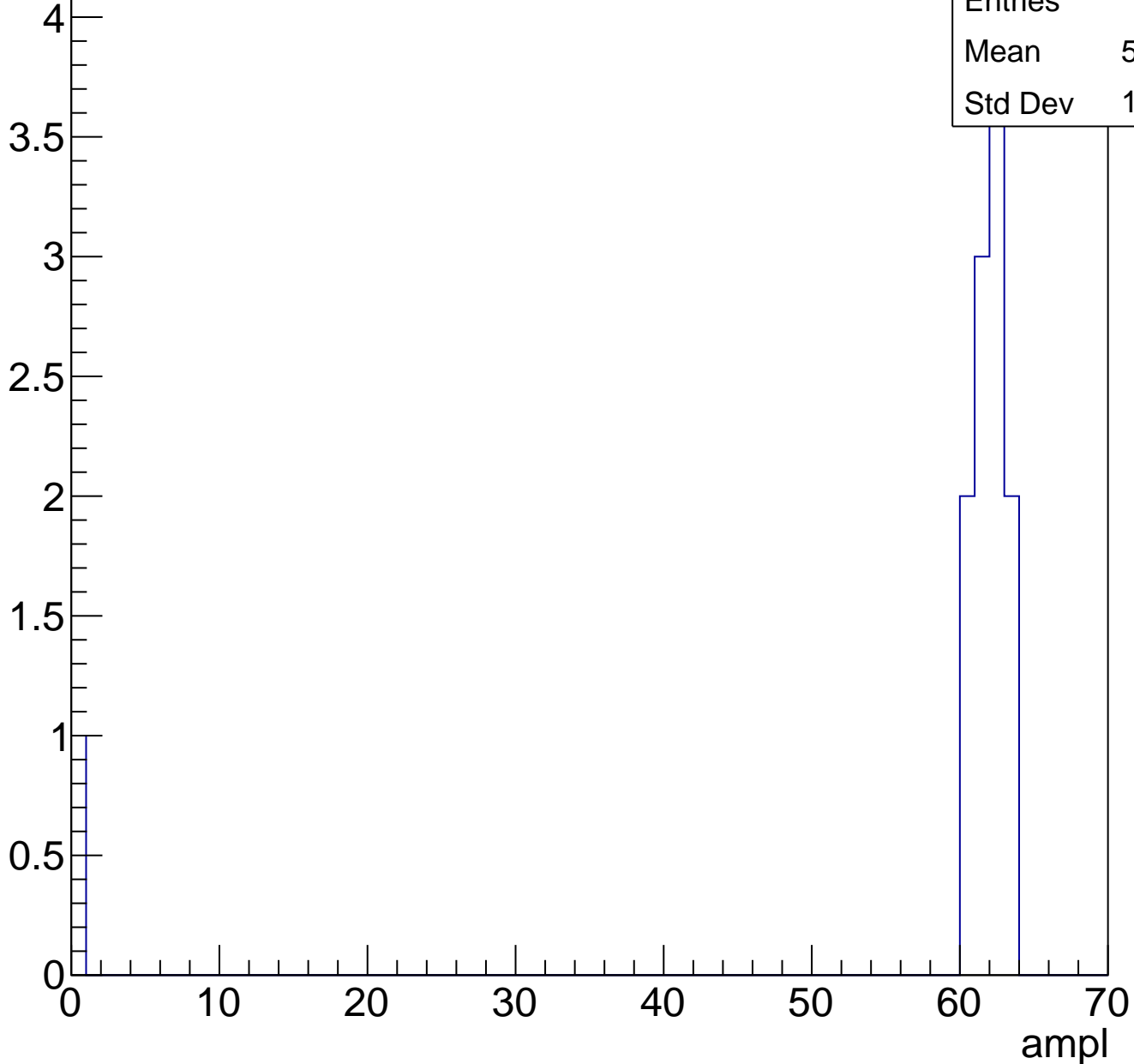
Entries	45
Mean	59.6
Std Dev	2.603



# B0L102S, U17-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



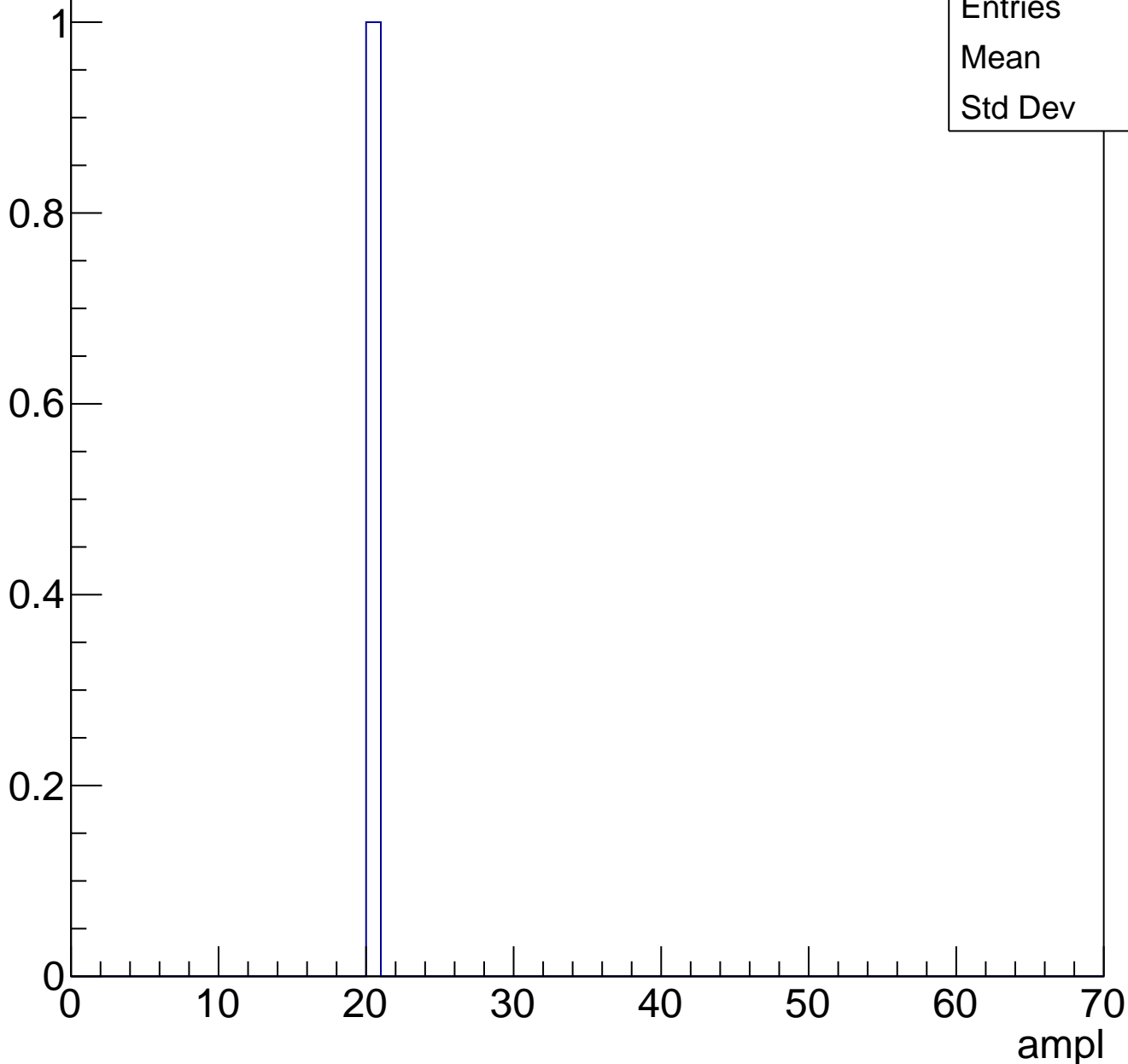
Entries	12
Mean	56.42
Std Dev	17.04



# B0L102S, U17-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	76
Mean	29.5
Std Dev	5.882

**Gaus mean : 30.8467**

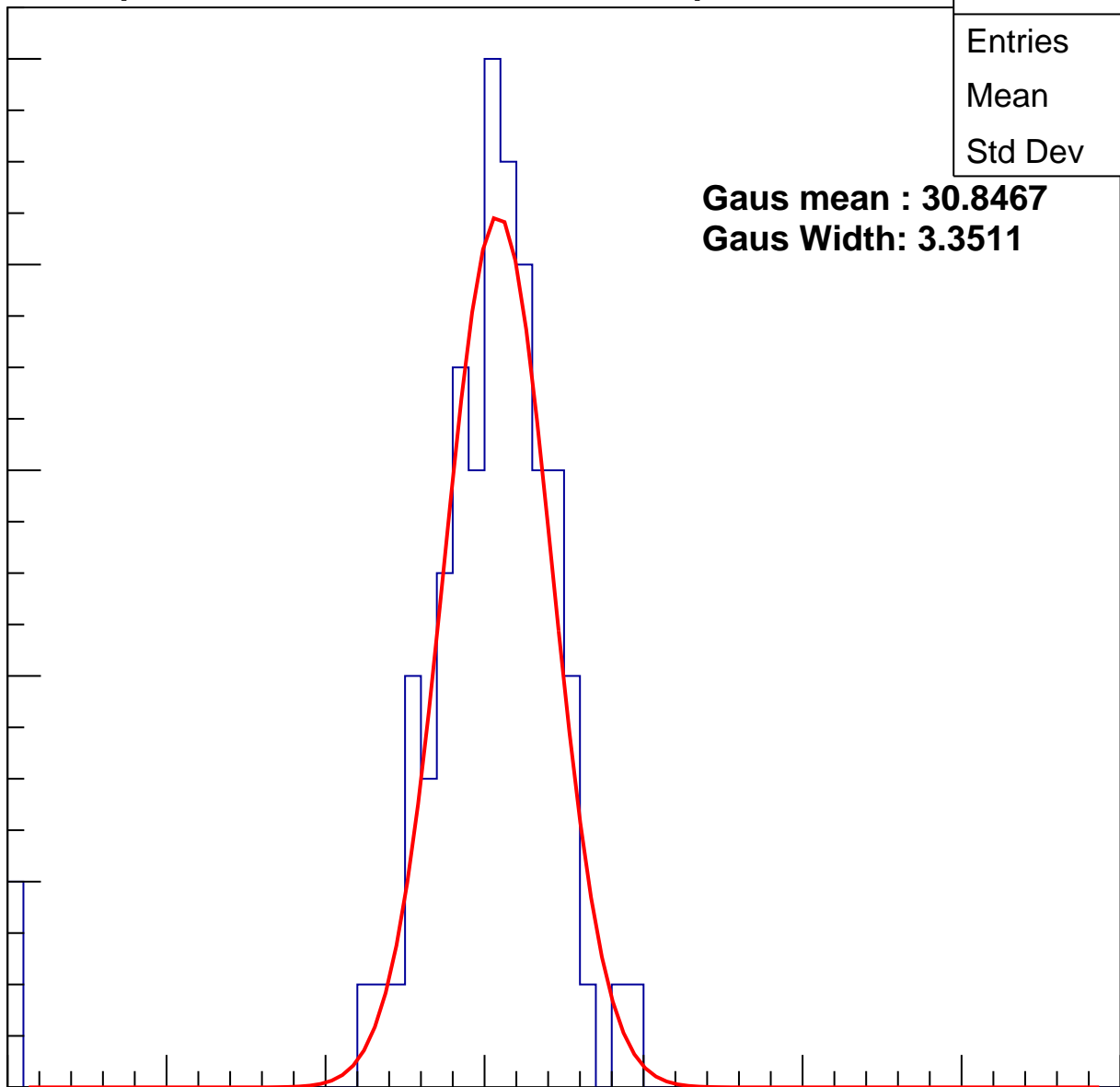
**Gaus Width: 3.3511**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch63, adc1

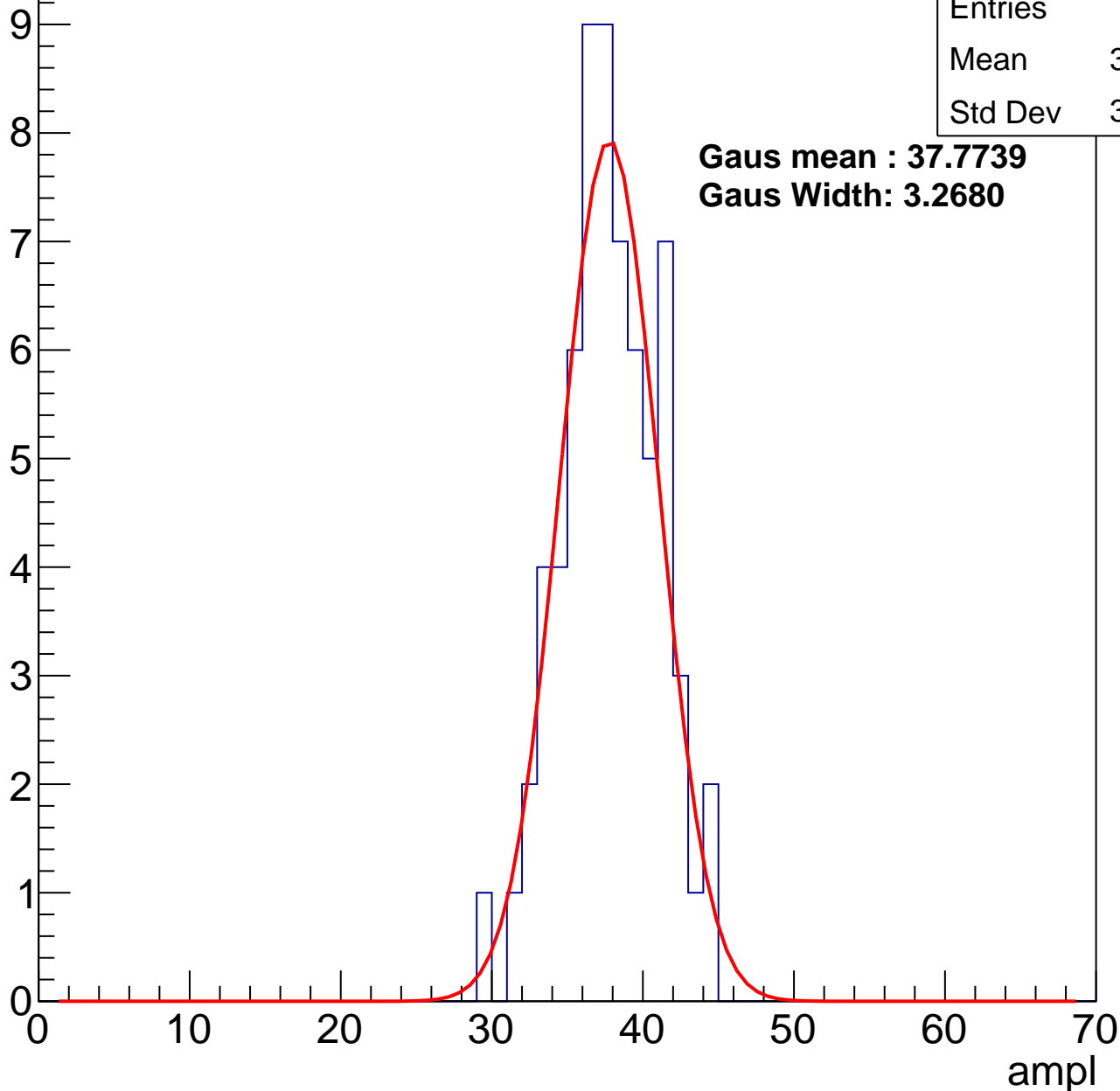
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	37.36
Std Dev	3.166

**Gaus mean : 37.7739**

**Gaus Width: 3.2680**



# B0L102S, U17-ch63, adc2

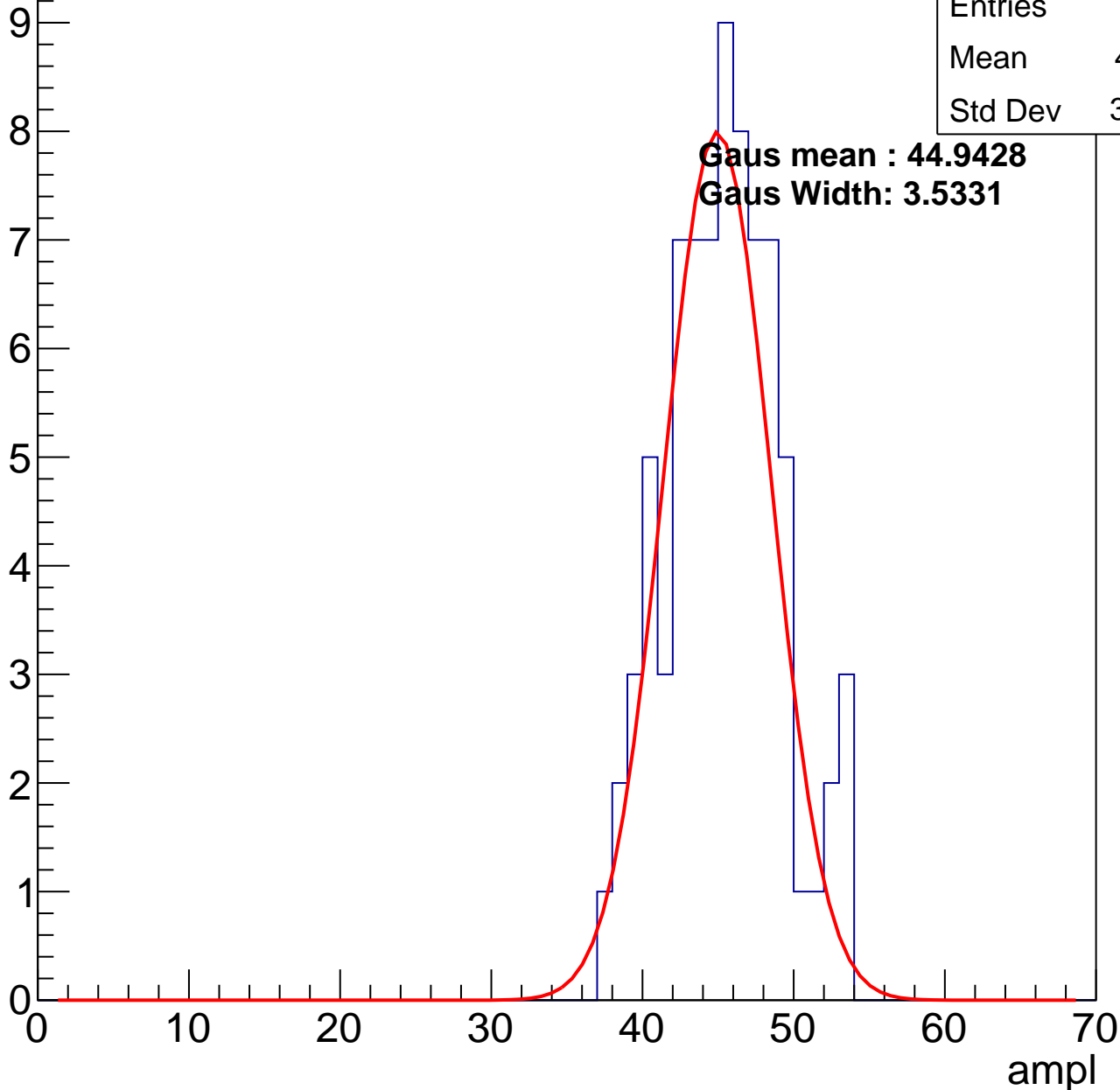
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	44.91
Std Dev	3.704

**Gaus mean : 44.9428**

**Gaus Width: 3.5331**

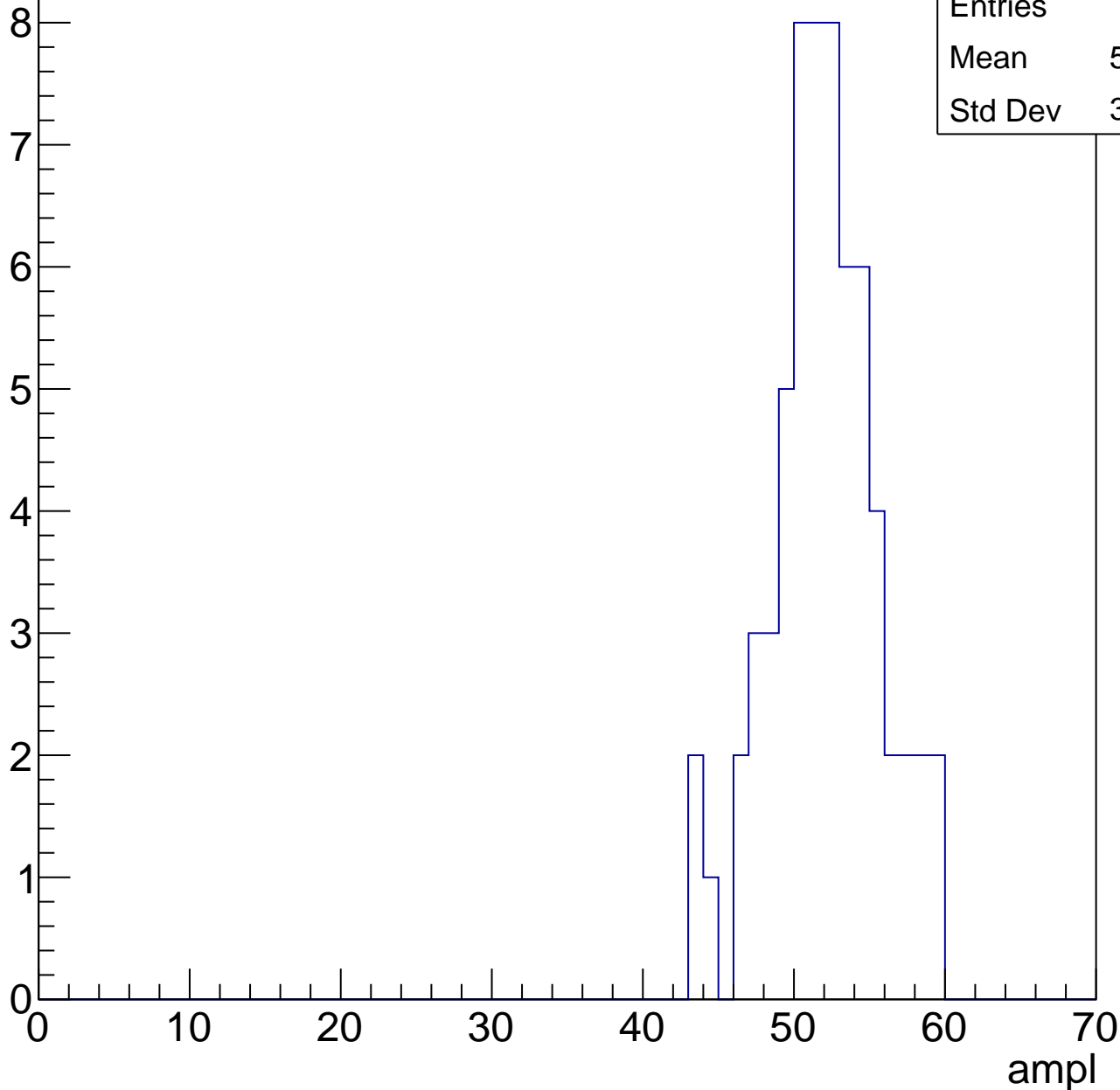


# B0L102S, U17-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	51.53
Std Dev	3.557

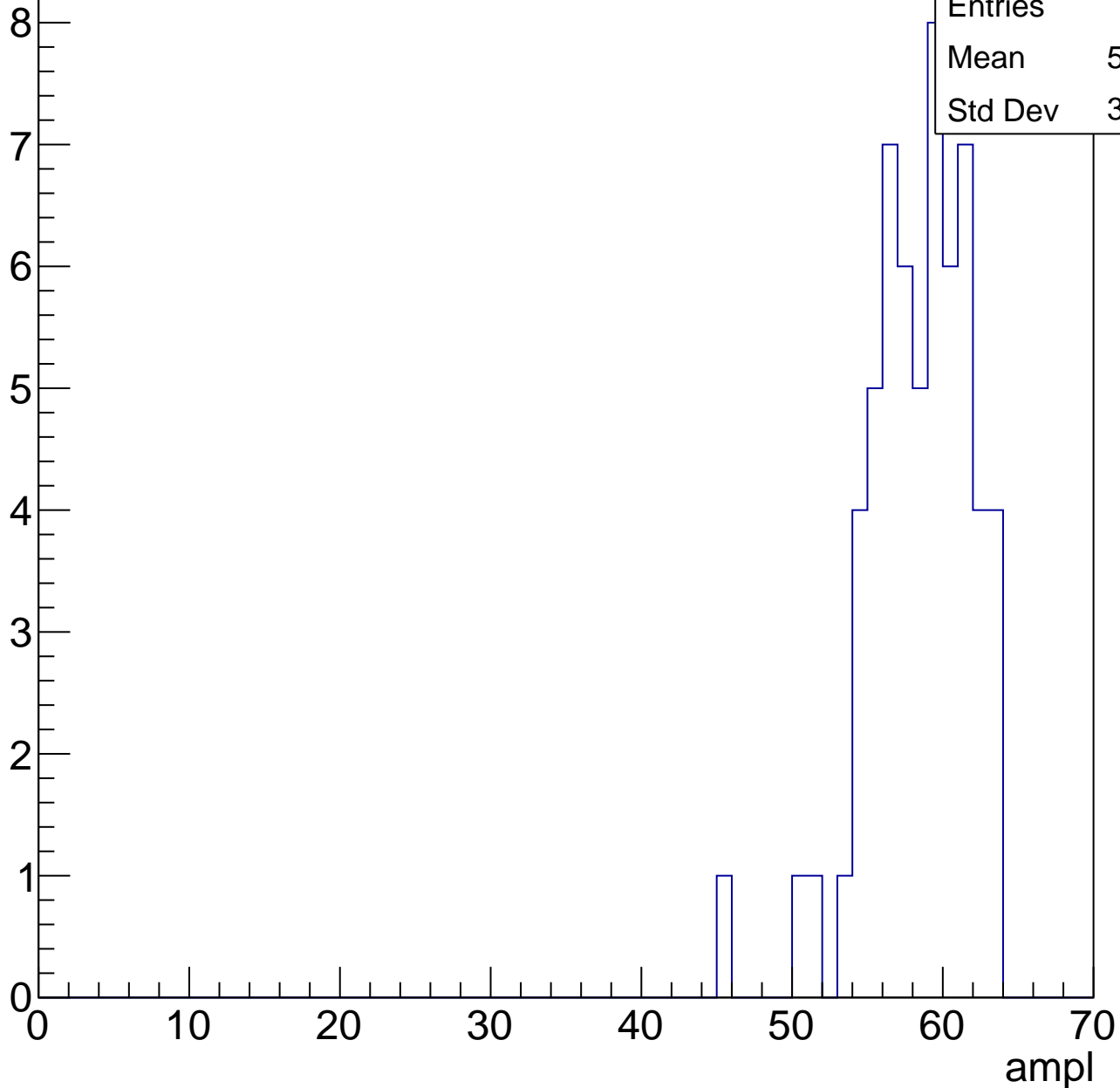


# B0L102S, U17-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	57.88
Std Dev	3.436

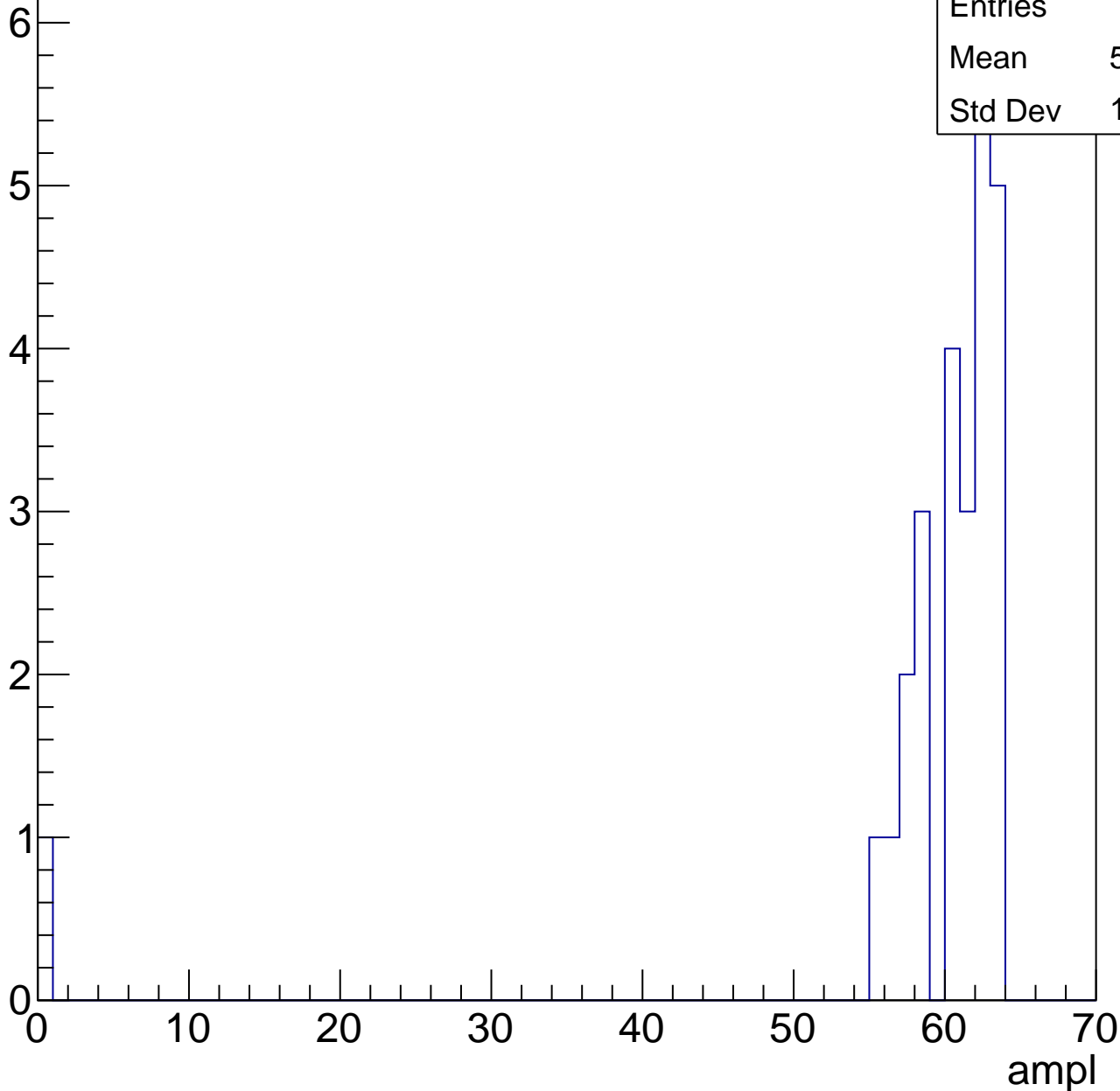


# B0L102S, U17-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	26
Mean	58.04
Std Dev	11.84



# B0L102S, U17-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L102S, U17-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch64, adc0

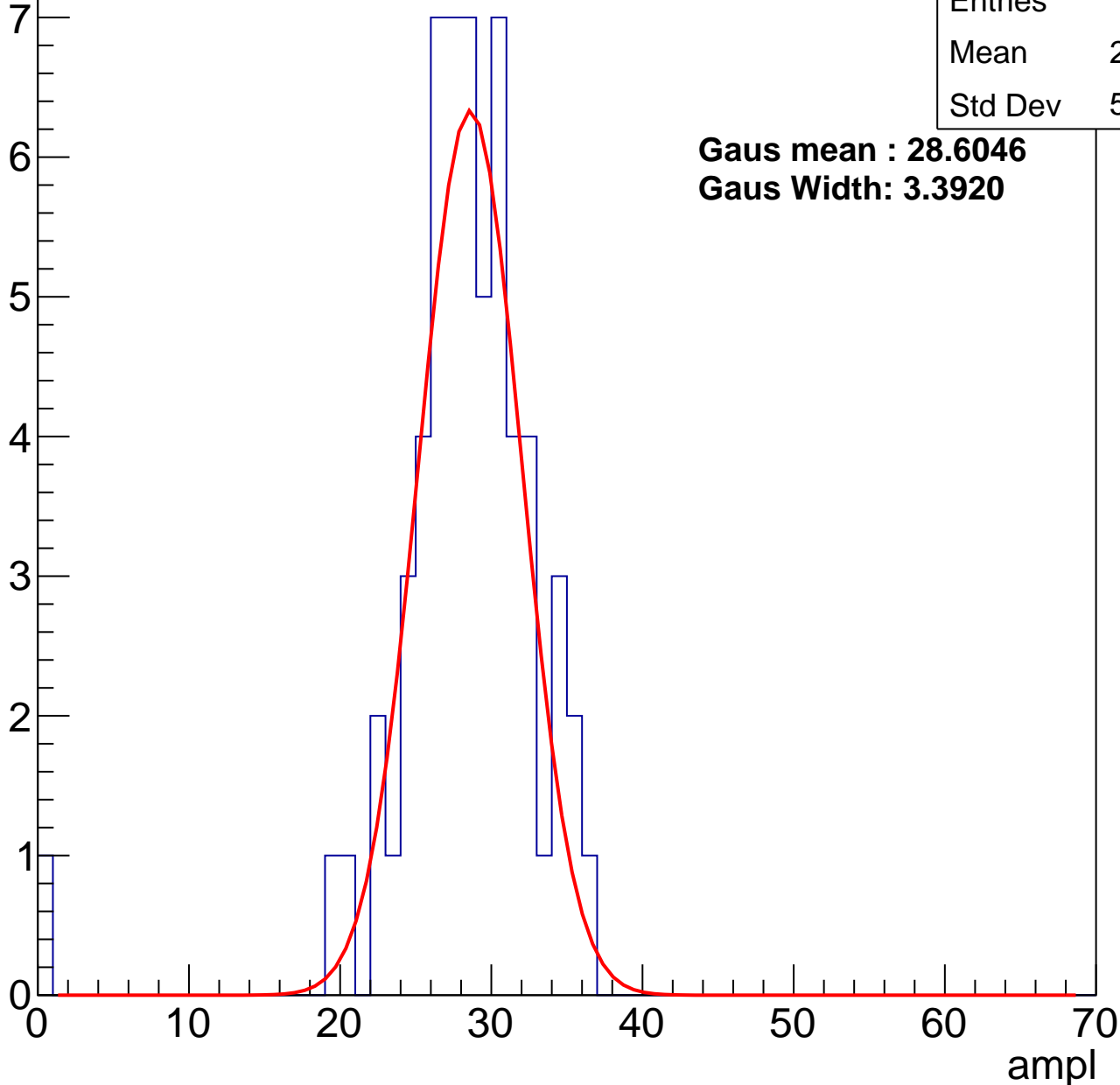
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	27.75
Std Dev	5.072

**Gaus mean : 28.6046**

**Gaus Width: 3.3920**



# B0L102S, U17-ch64, adc1

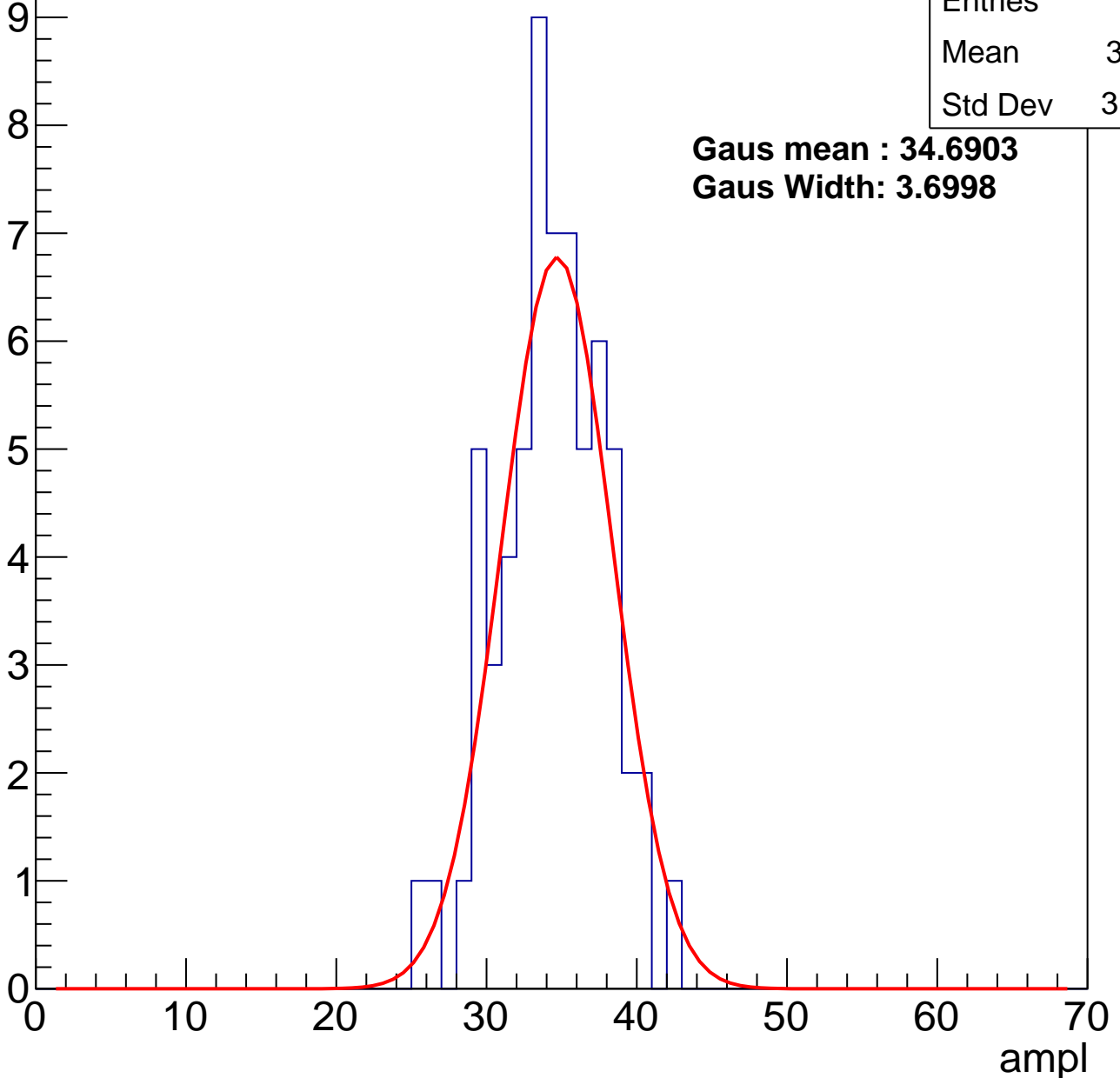
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	33.91
Std Dev	3.458

**Gaus mean : 34.6903**

**Gaus Width: 3.6998**



# B0L102S, U17-ch64, adc2

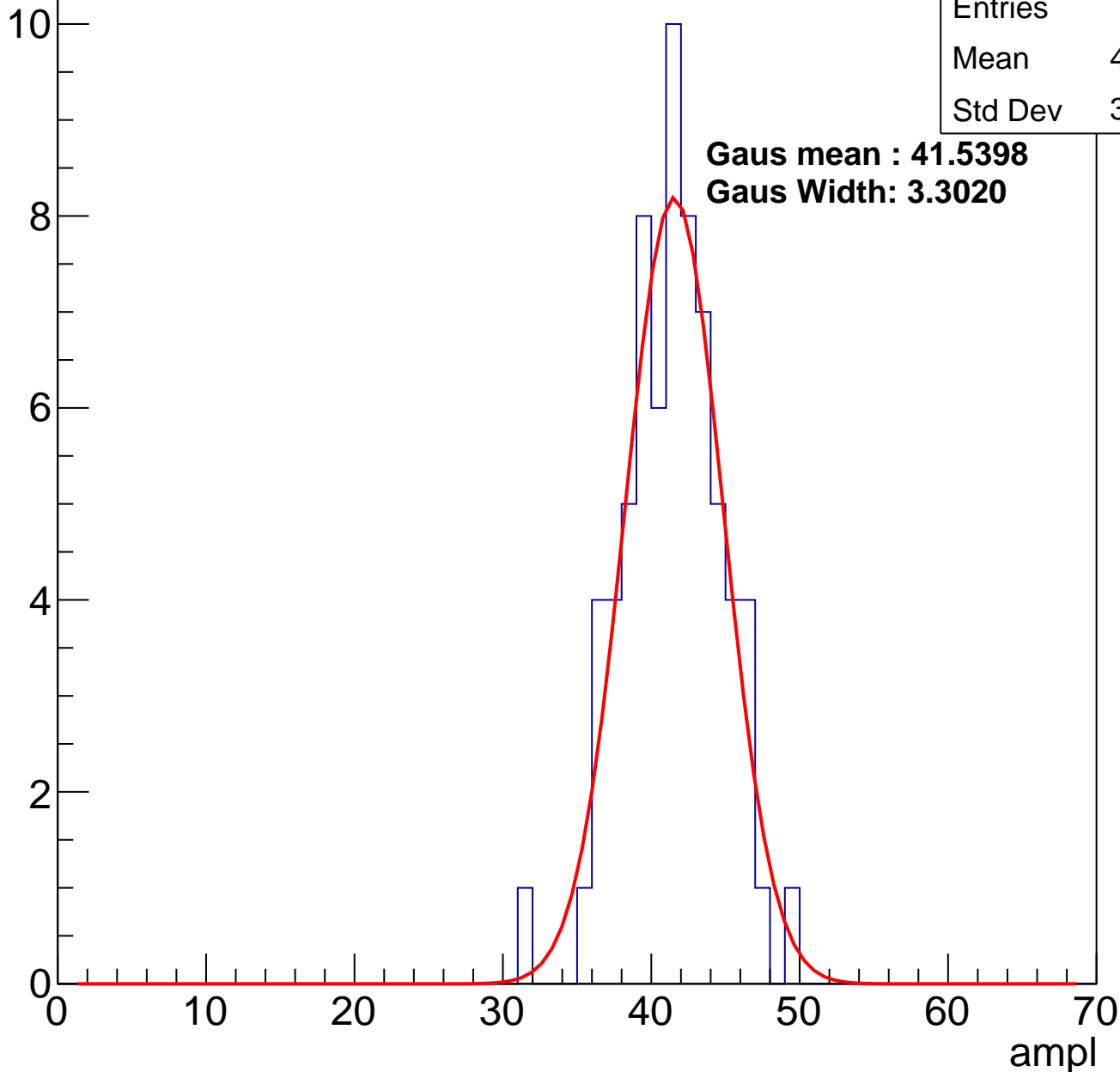
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	69
Mean	40.97
Std Dev	3.248

**Gaus mean : 41.5398**

**Gaus Width: 3.3020**

Entry



# B0L102S, U17-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	62
Mean	47.58
Std Dev	3.324

Entry

10

8

6

4

2

0

0

10

20

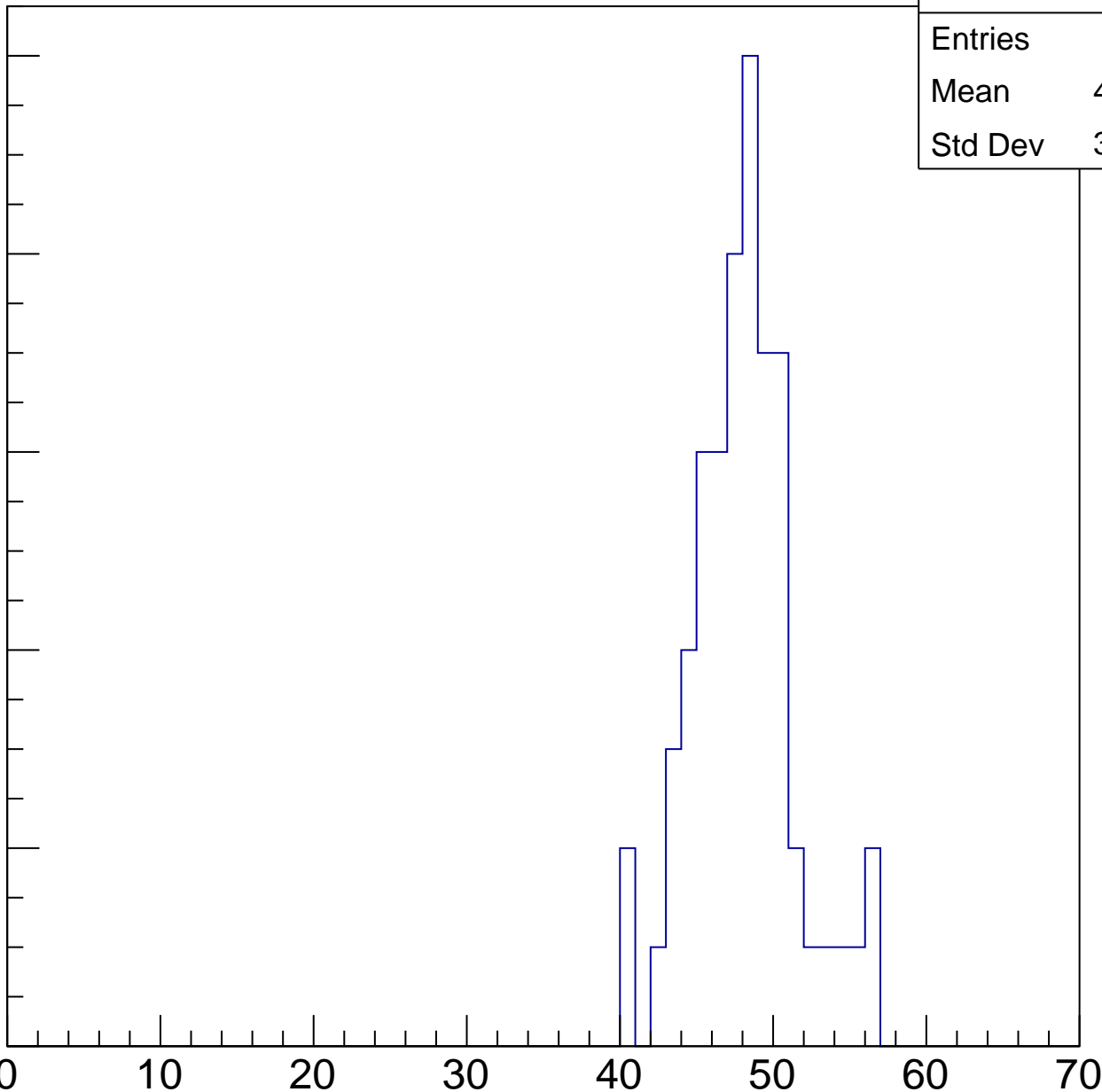
30

40

50

60

ampl

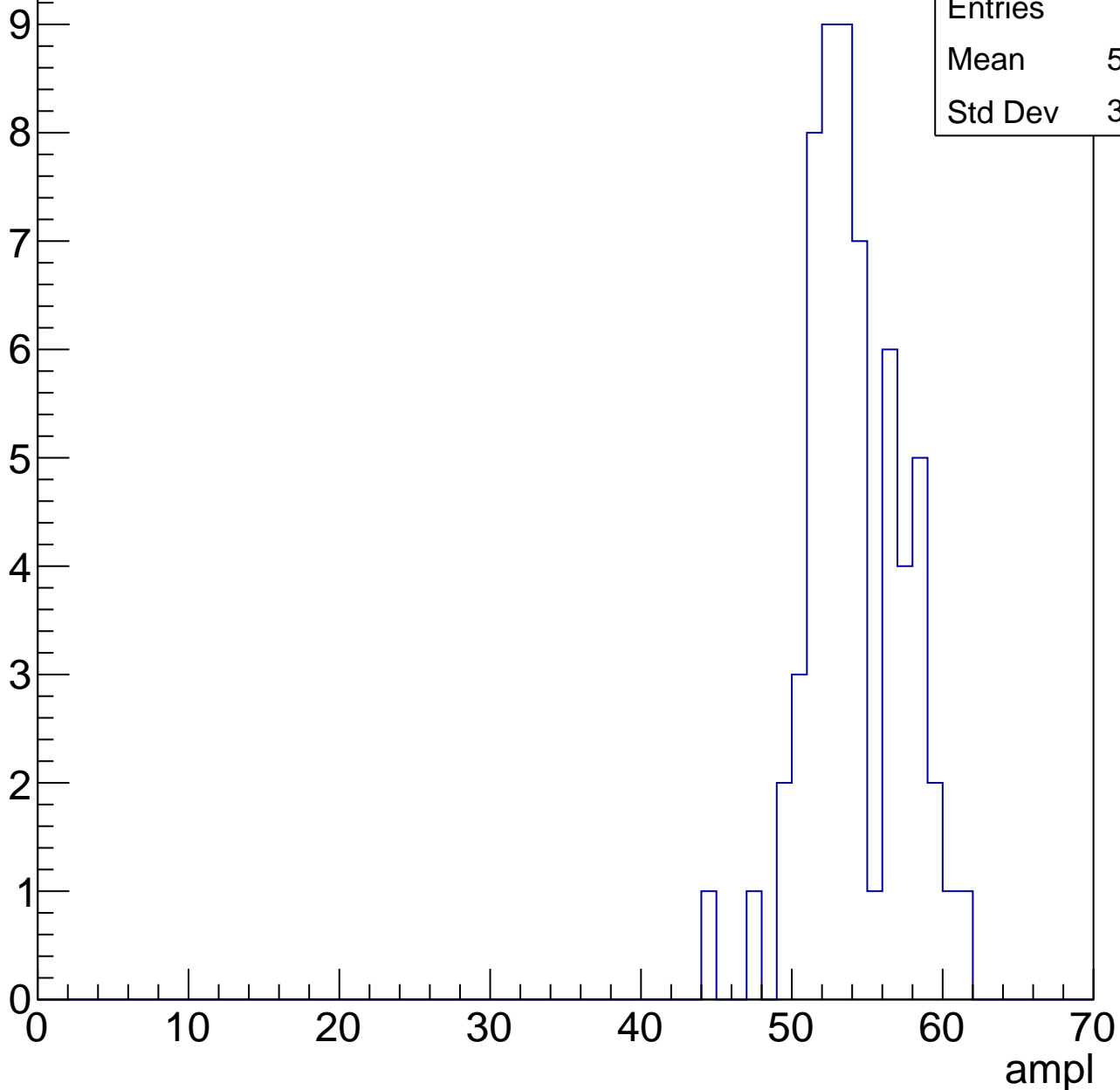


# B0L102S, U17-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

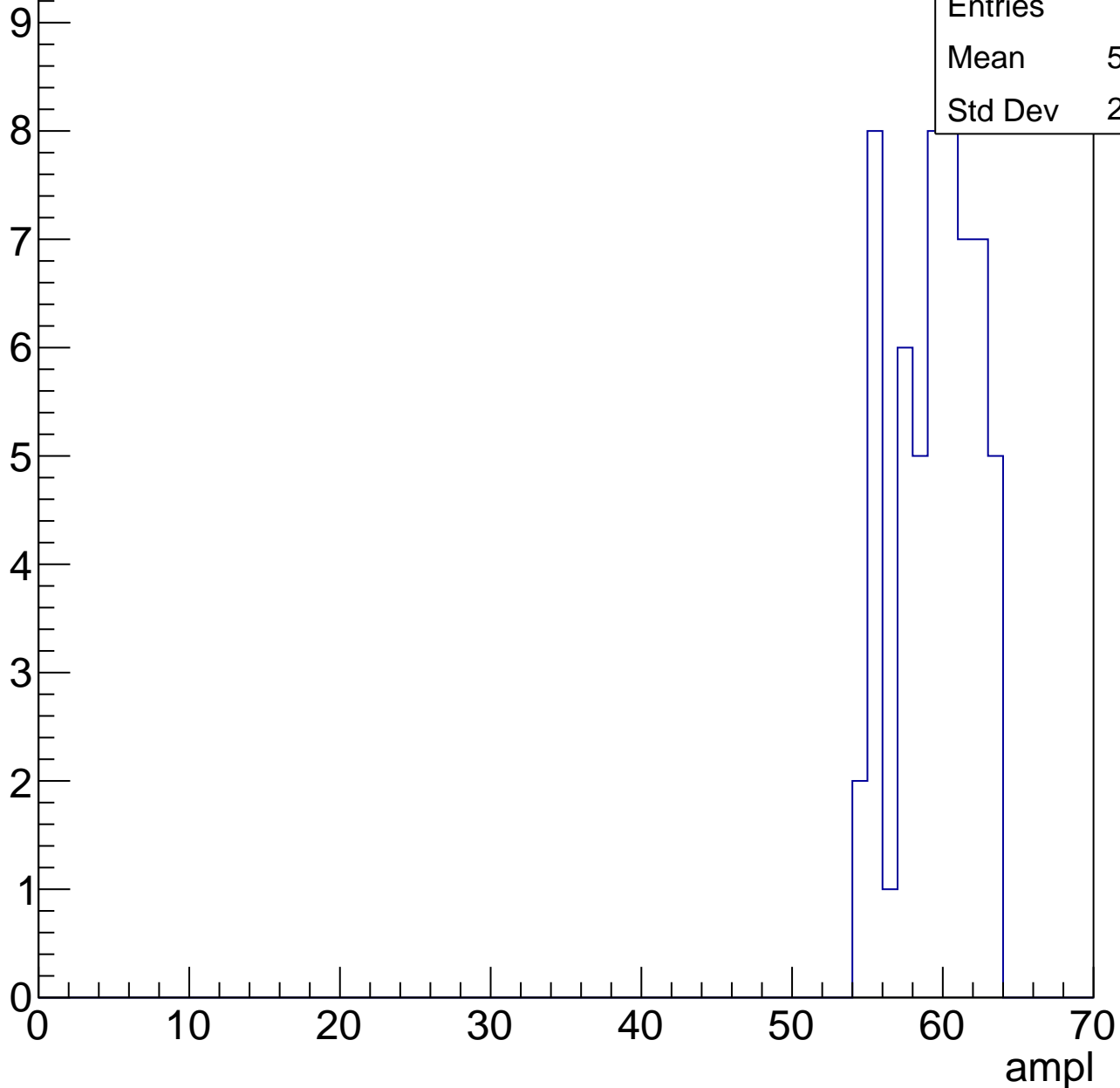
Entries	60
Mean	53.63
Std Dev	3.245



# B0L102S, U17-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

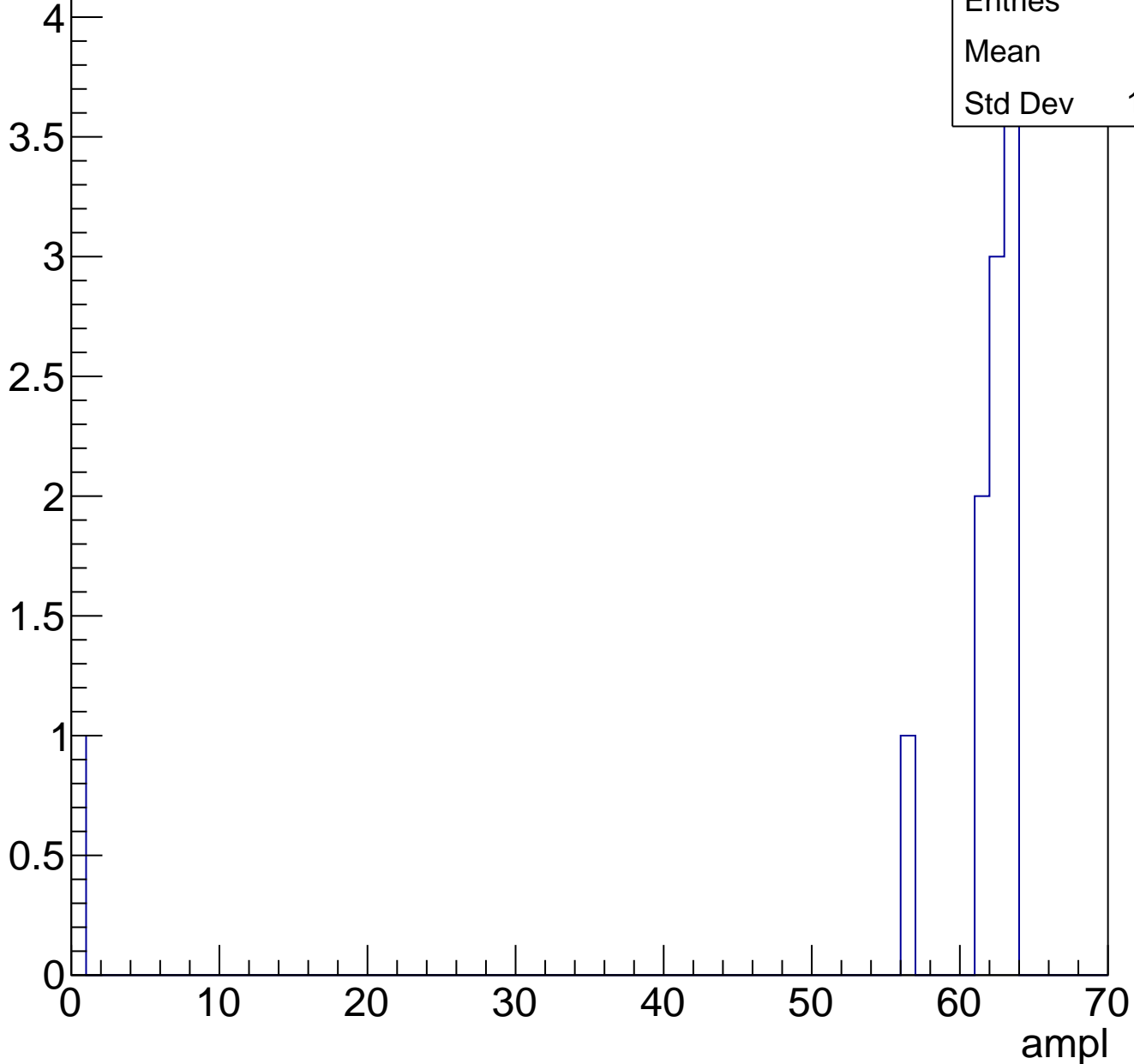
Entry



# B0L102S, U17-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0

# B0L102S, U17-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	81
Mean	26.91
Std Dev	6.428

**Gaus mean : 28.0334**

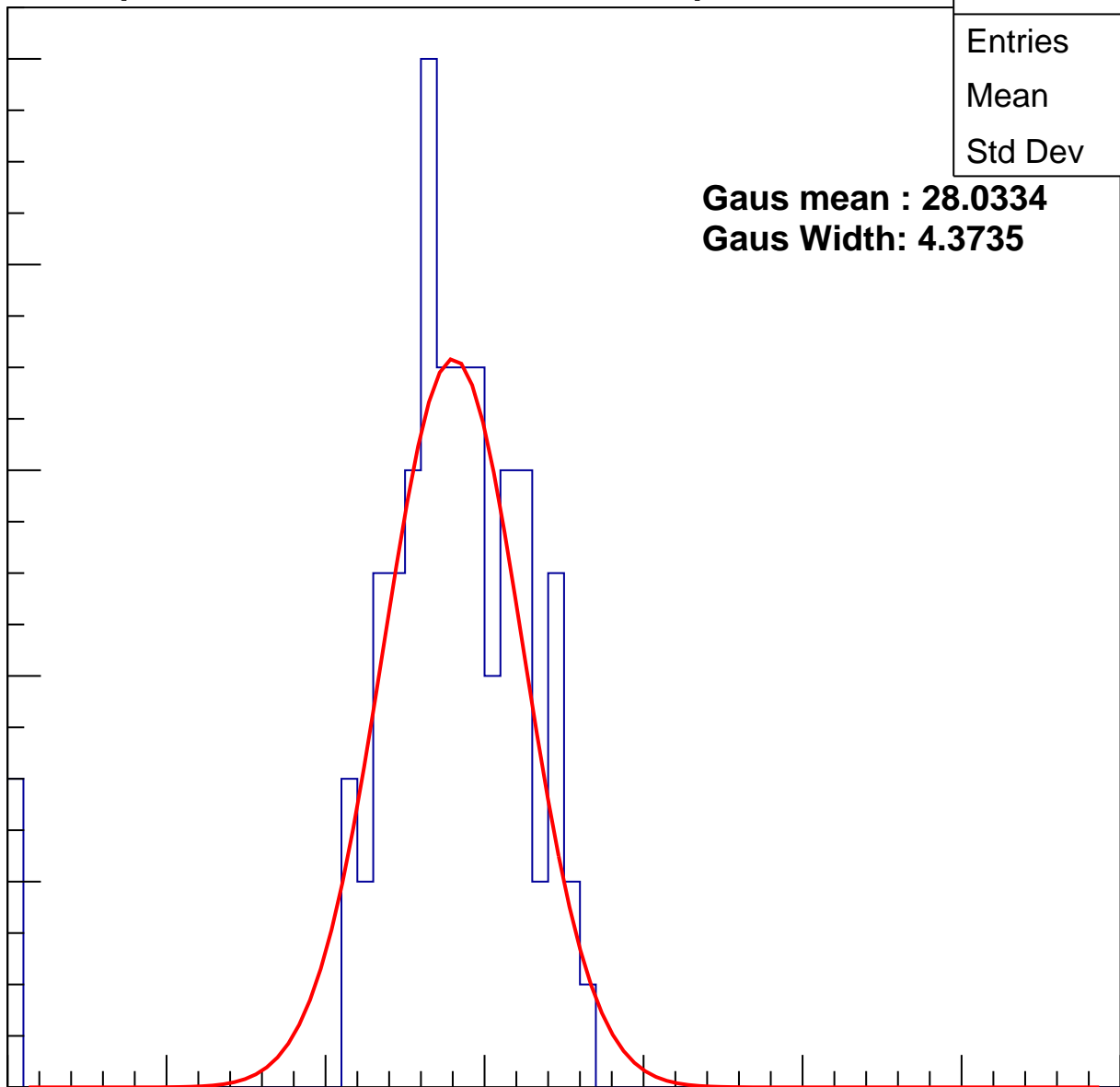
**Gaus Width: 4.3735**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



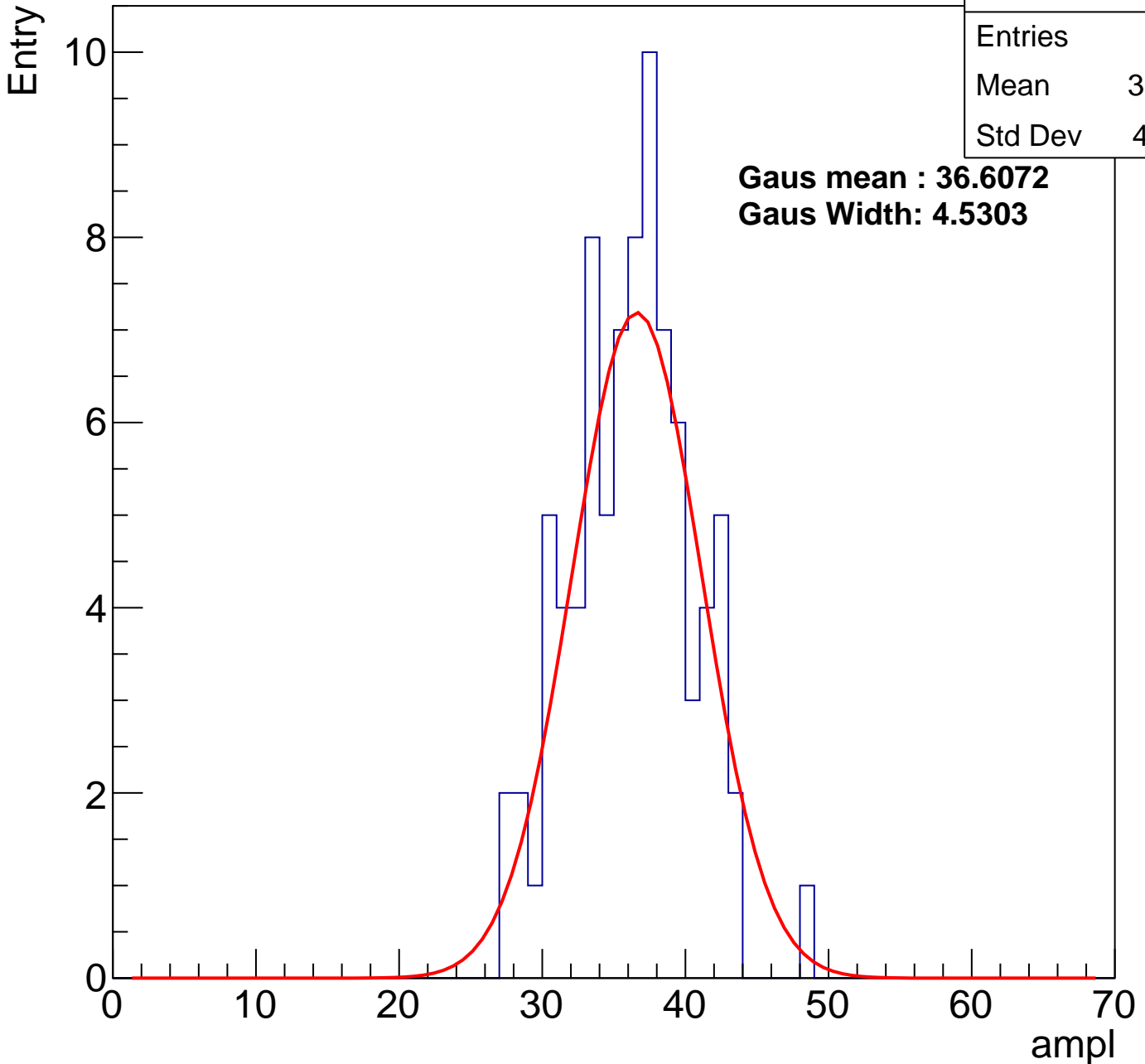
# B0L102S, U17-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	84
Mean	35.79
Std Dev	4.141

**Gaus mean : 36.6072**

**Gaus Width: 4.5303**



# B0L102S, U17-ch65, adc2

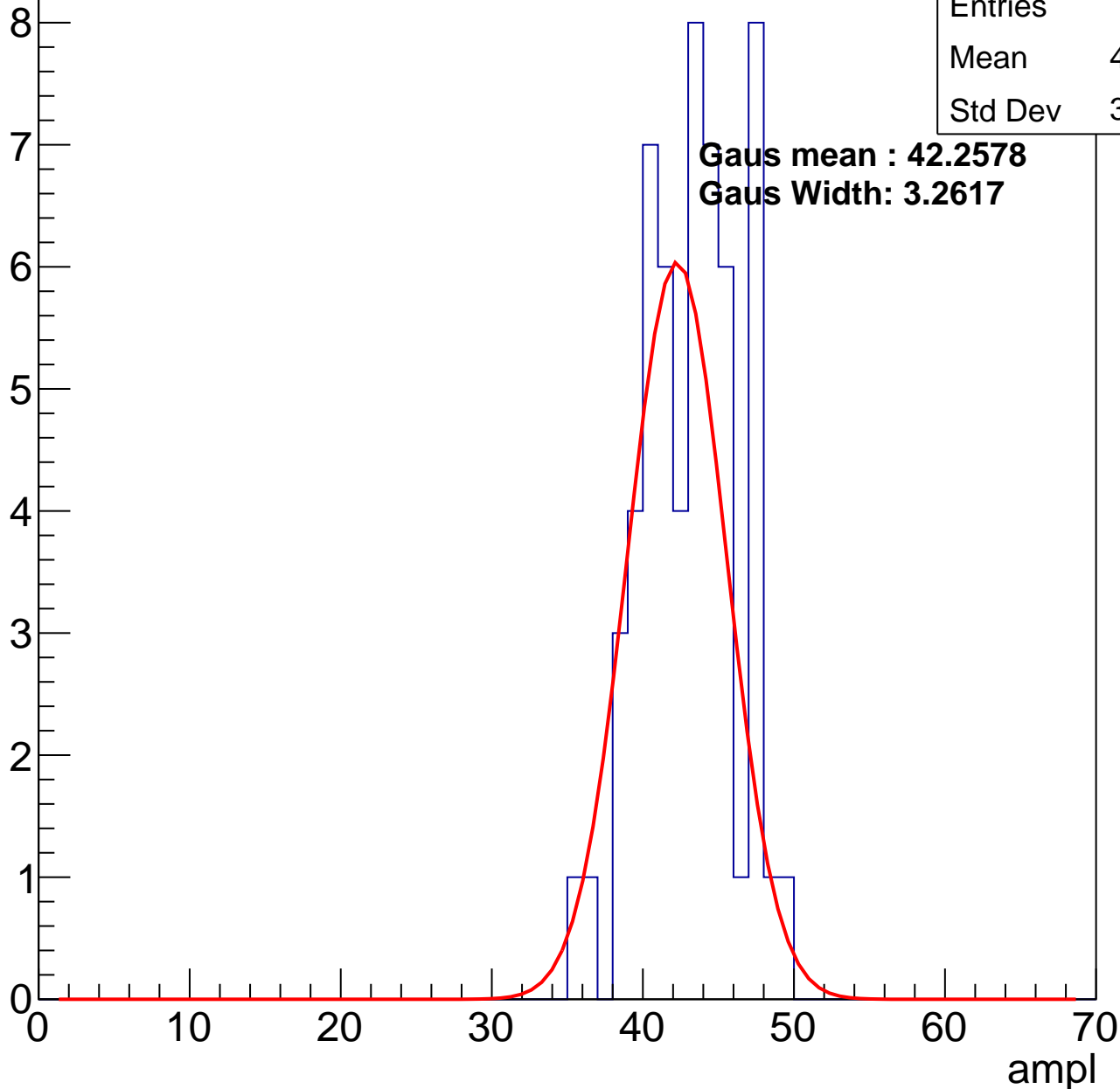
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	42.69
Std Dev	3.125

**Gaus mean : 42.2578**

**Gaus Width: 3.2617**

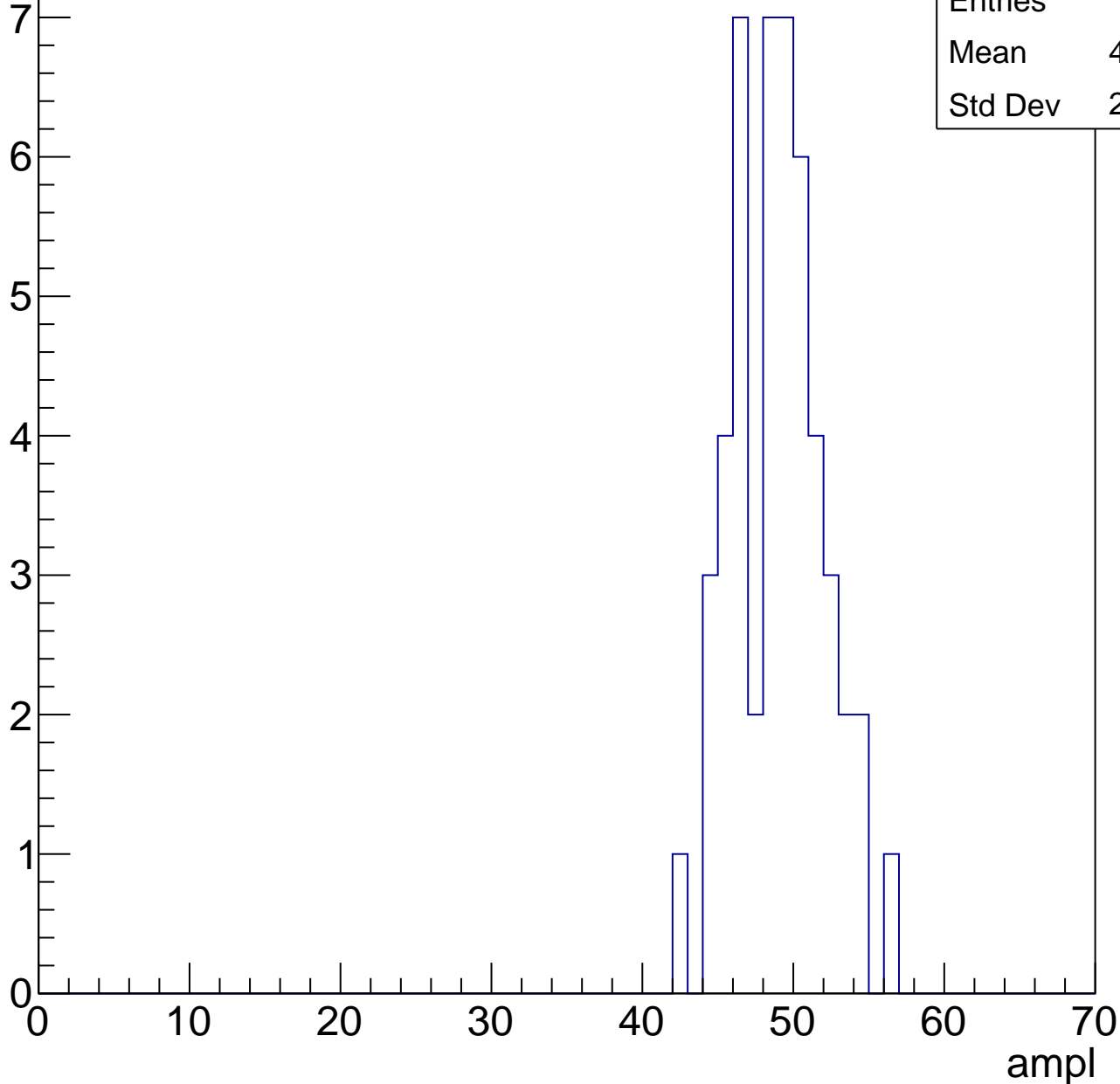


# B0L102S, U17-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

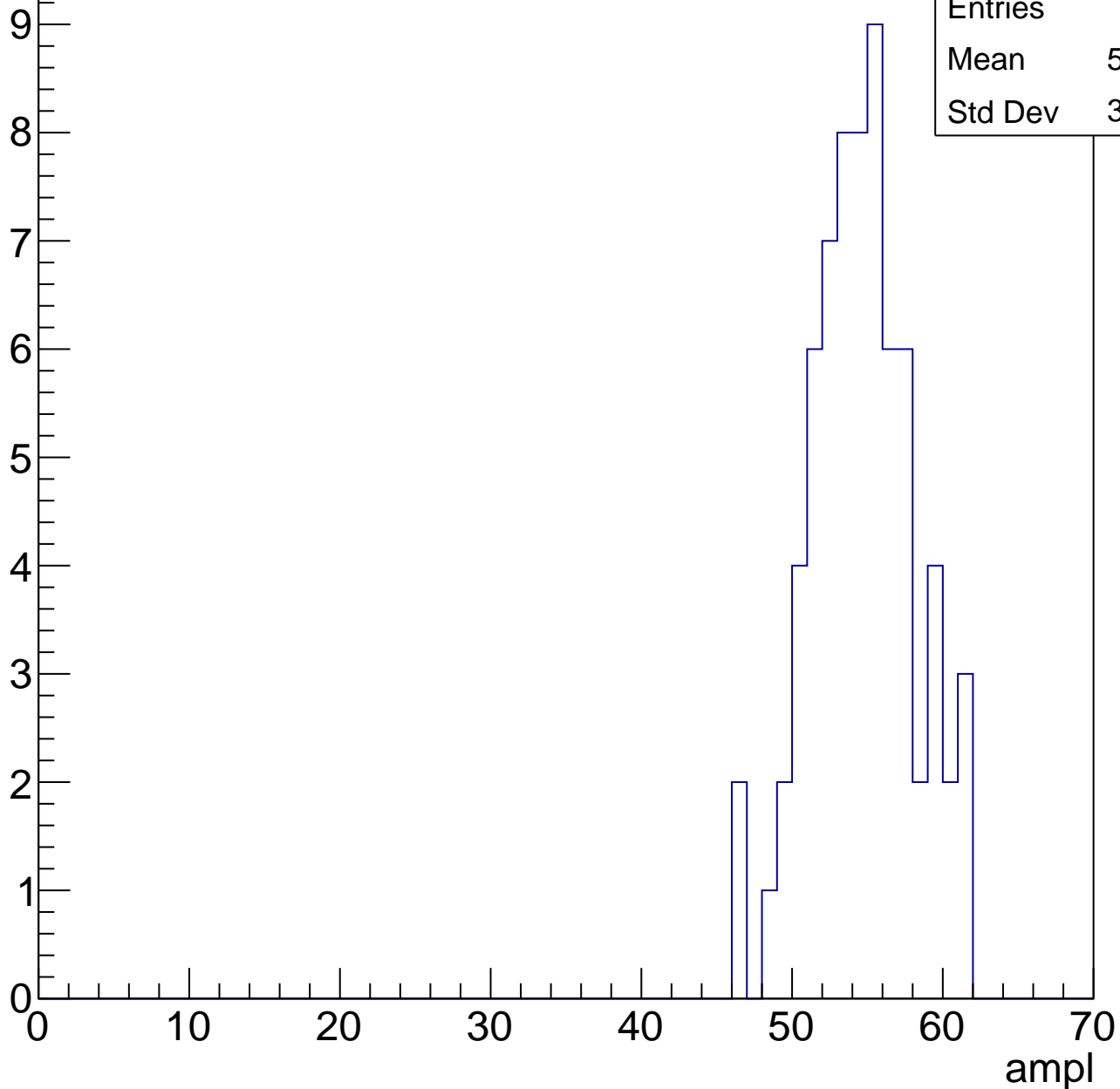
Entries	49
Mean	48.55
Std Dev	2.983



# B0L102S, U17-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

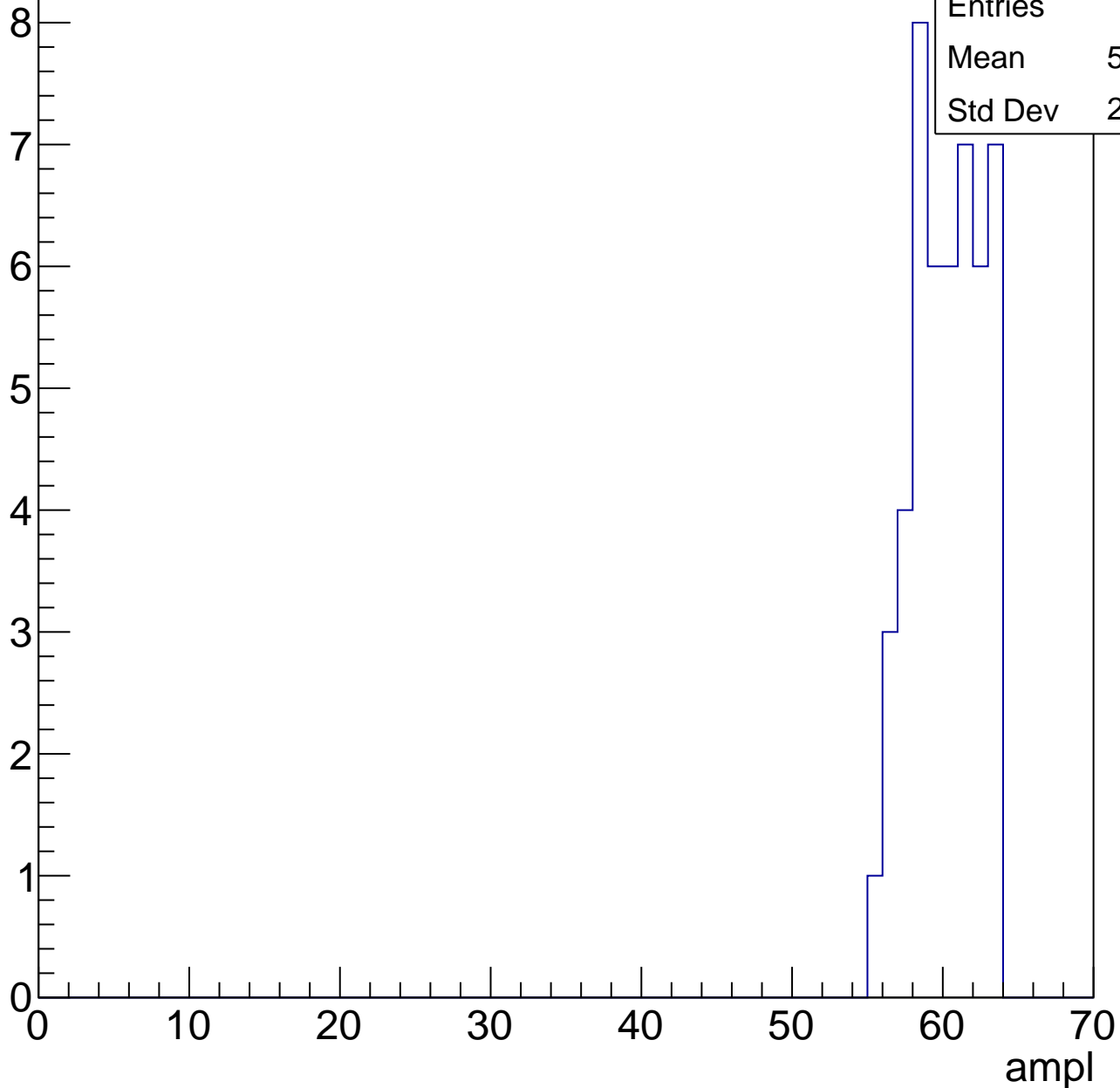
Entry



# B0L102S, U17-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

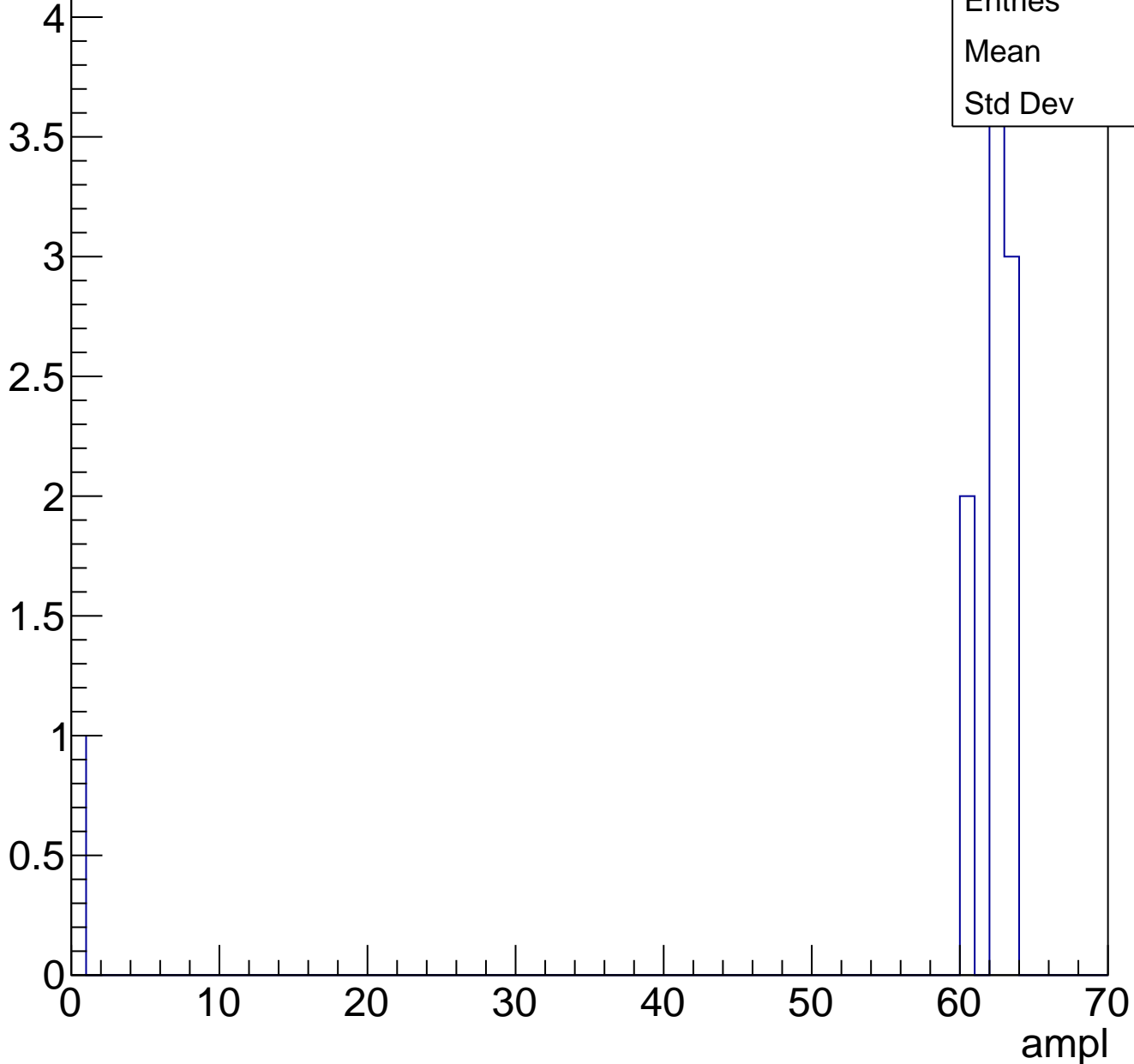


Entries	48
Mean	59.77
Std Dev	2.229

# B0L102S, U17-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch66, adc0

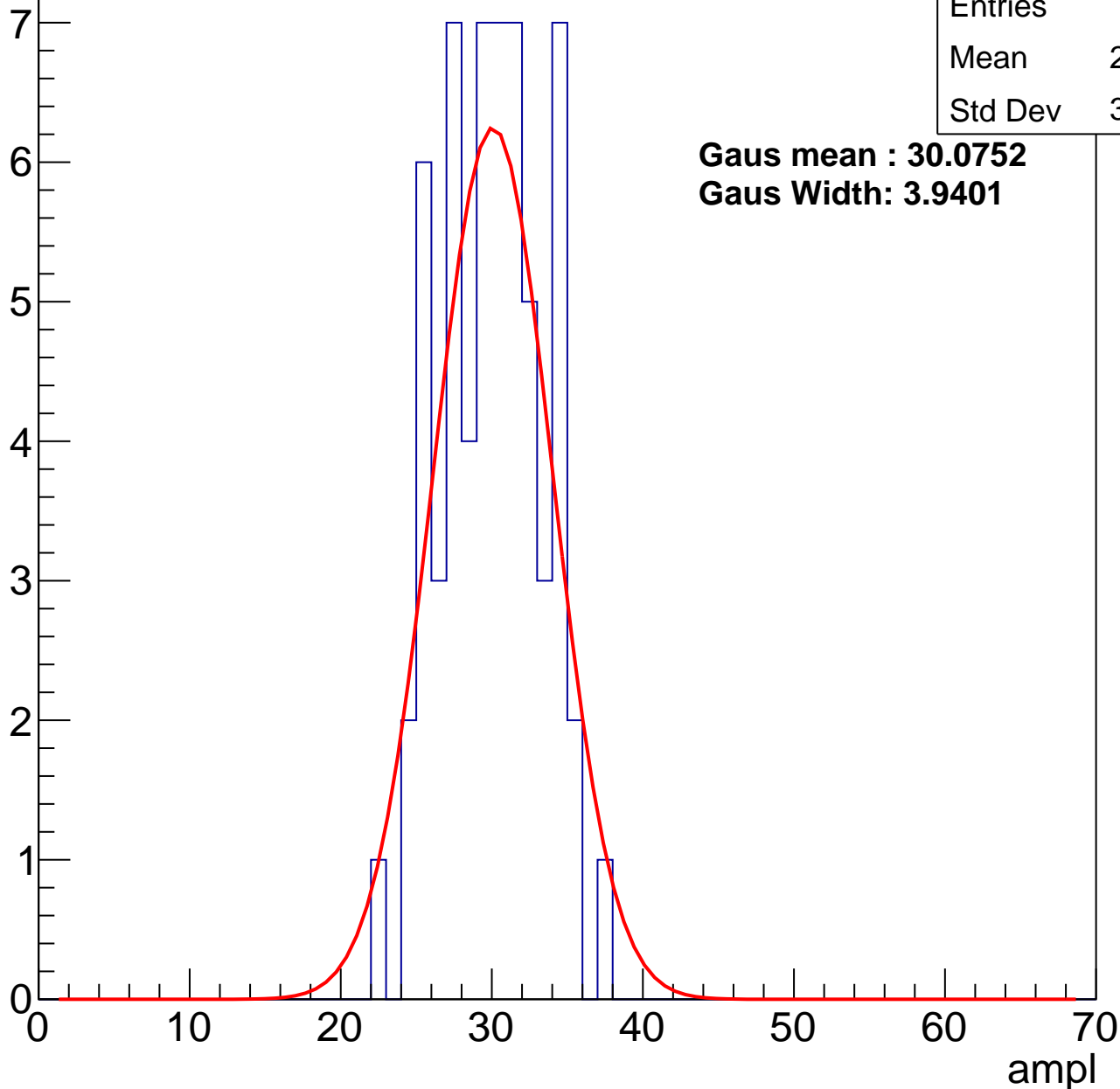
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	29.56
Std Dev	3.295

**Gaus mean : 30.0752**

**Gaus Width: 3.9401**



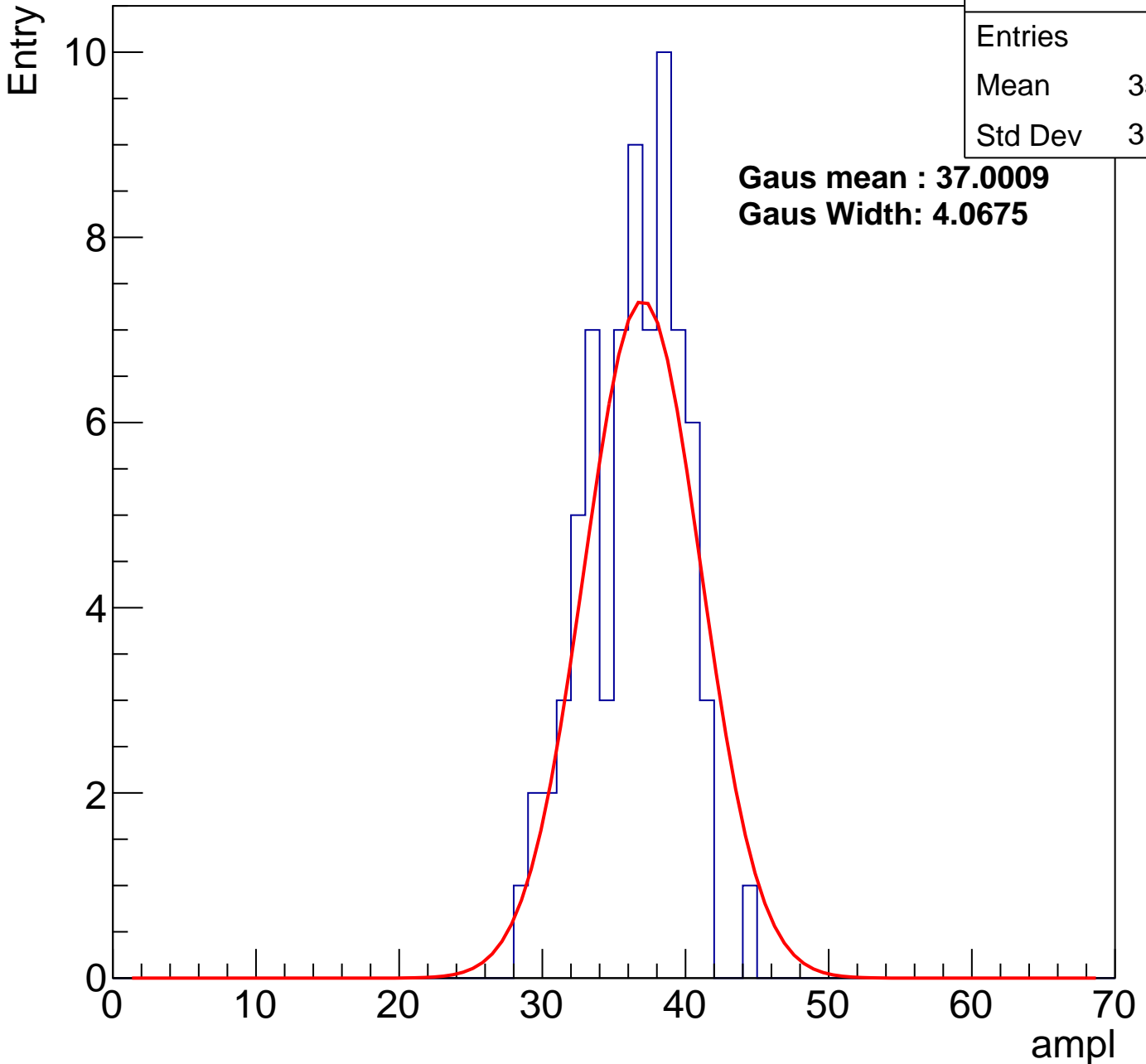
# B0L102S, U17-ch66, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	73
Mean	35.89
Std Dev	3.342

**Gaus mean : 37.0009**

**Gaus Width: 4.0675**



# B0L102S, U17-ch66, adc2

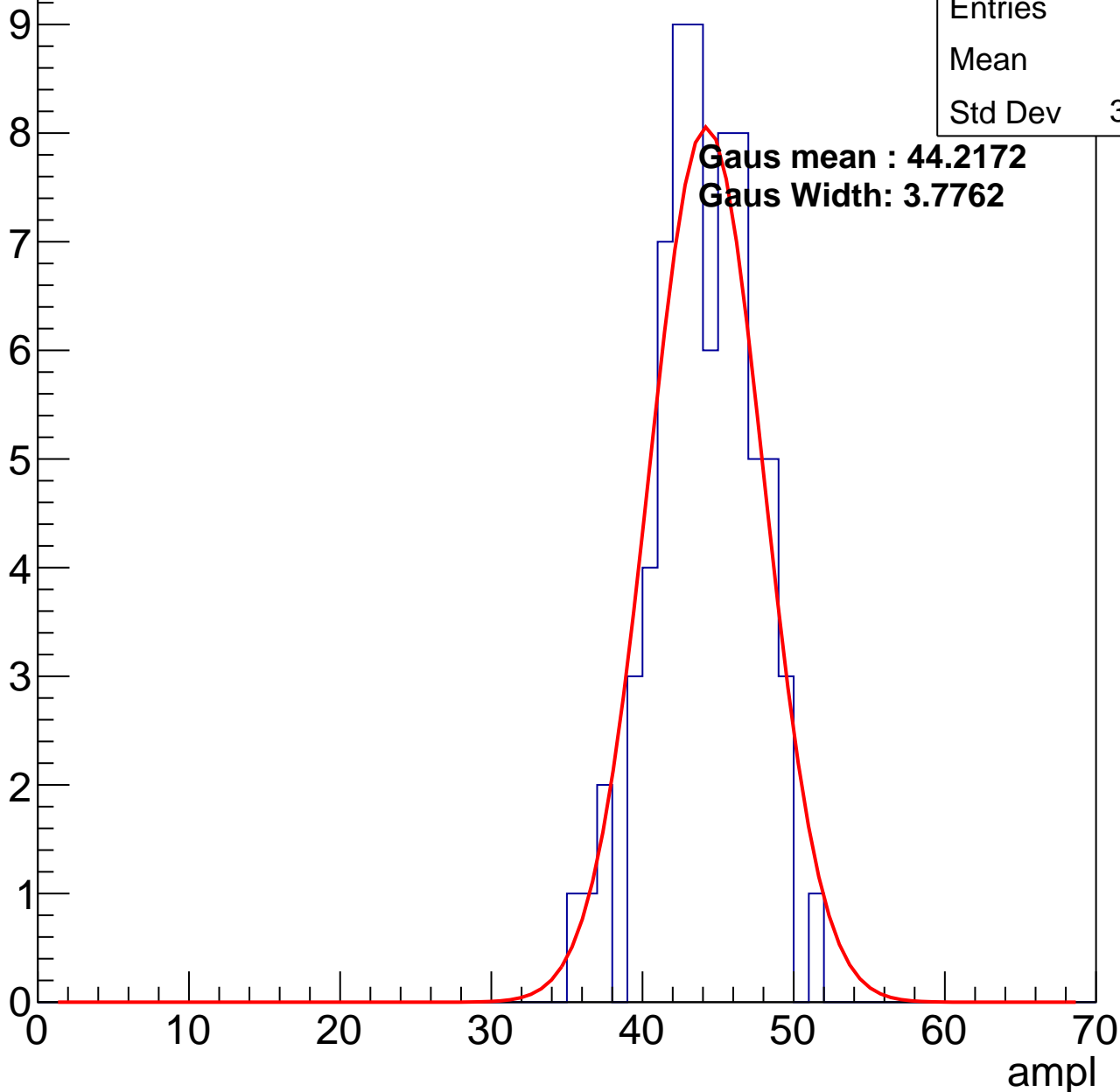
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	43.6
Std Dev	3.265

**Gaus mean : 44.2172**

**Gaus Width: 3.7762**

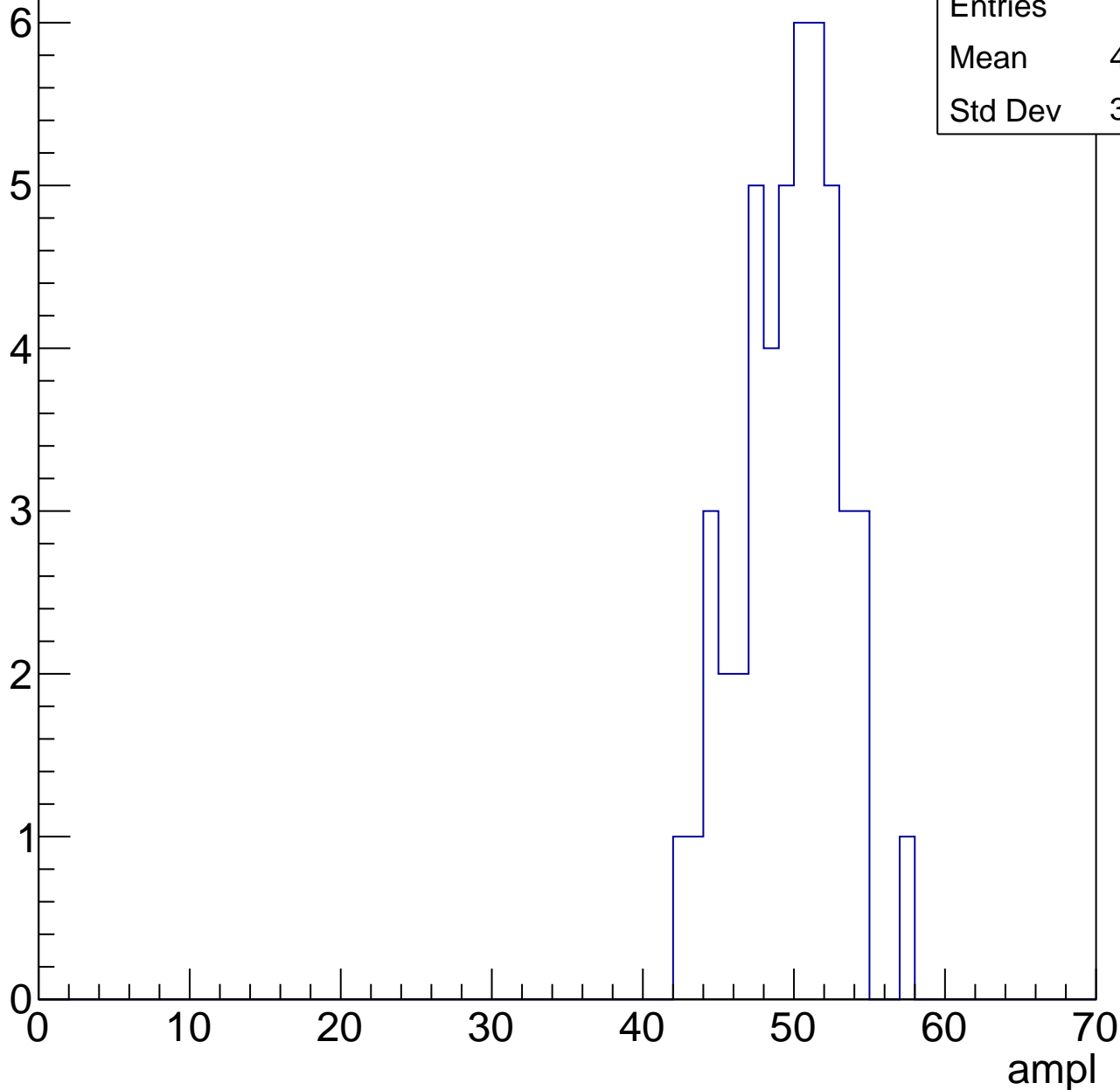


# B0L102S, U17-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	49.26
Std Dev	3.239

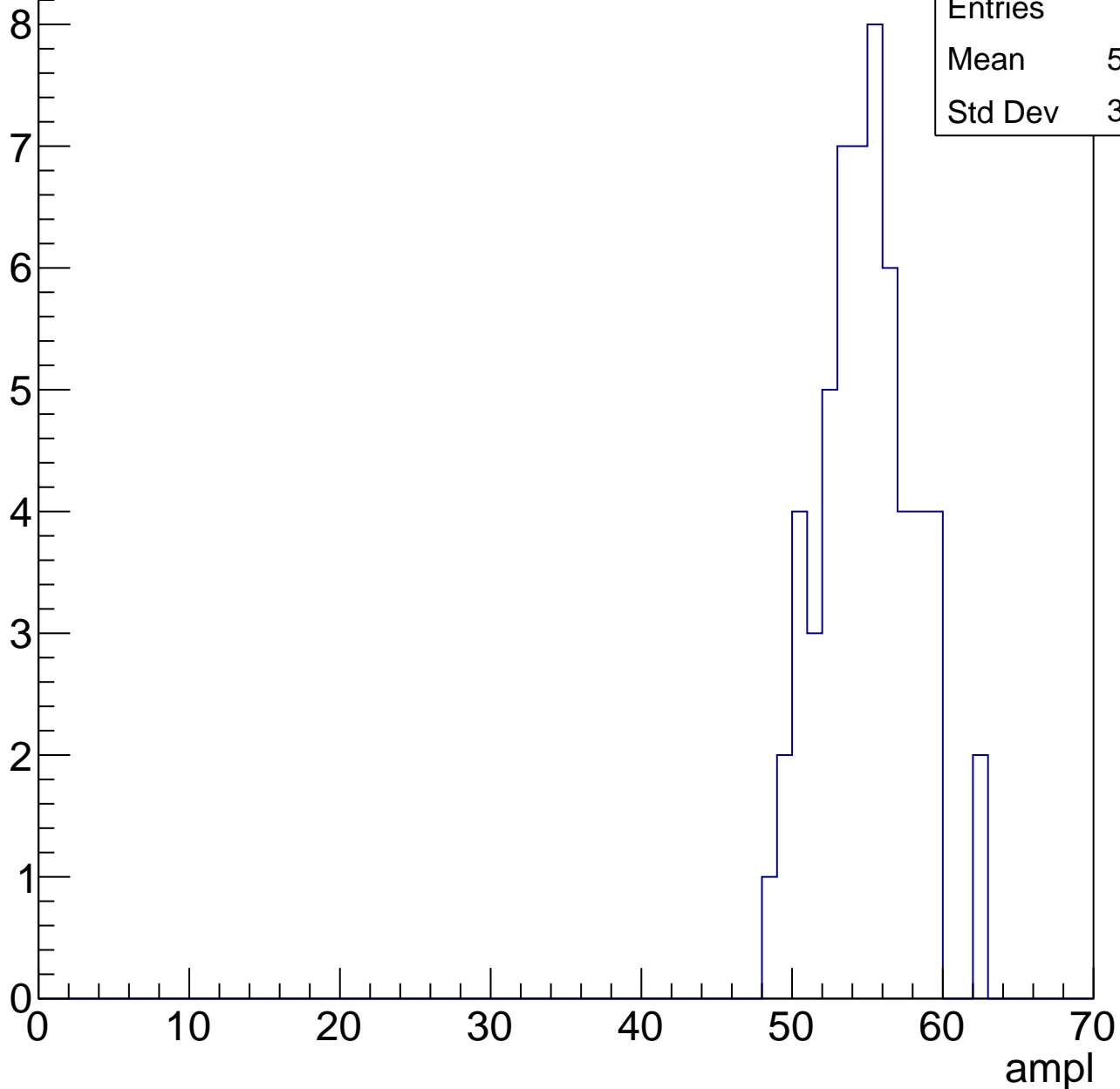


# B0L102S, U17-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

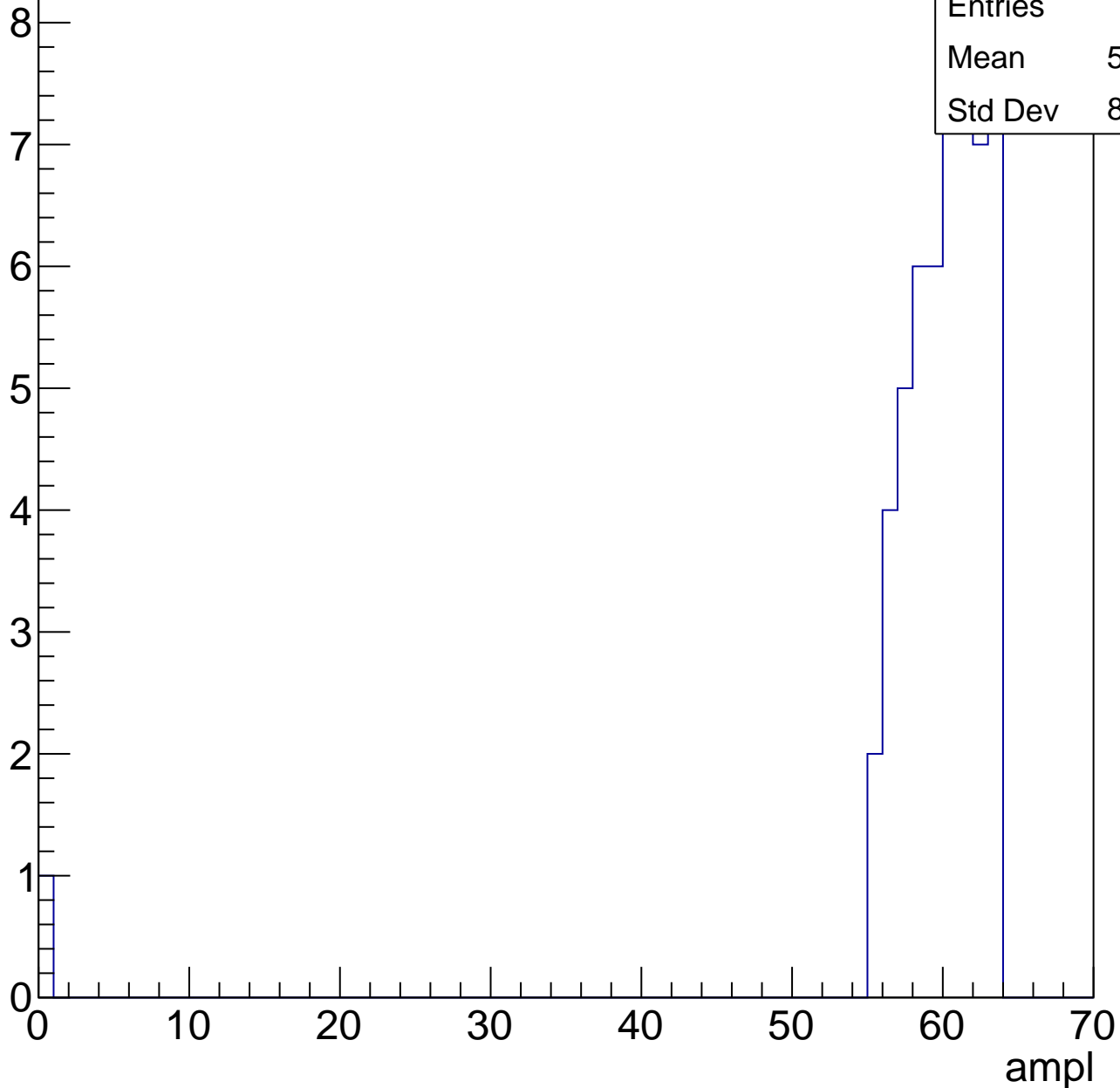
Entries	57
Mean	54.46
Std Dev	3.112



# B0L102S, U17-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	1.258

ampl

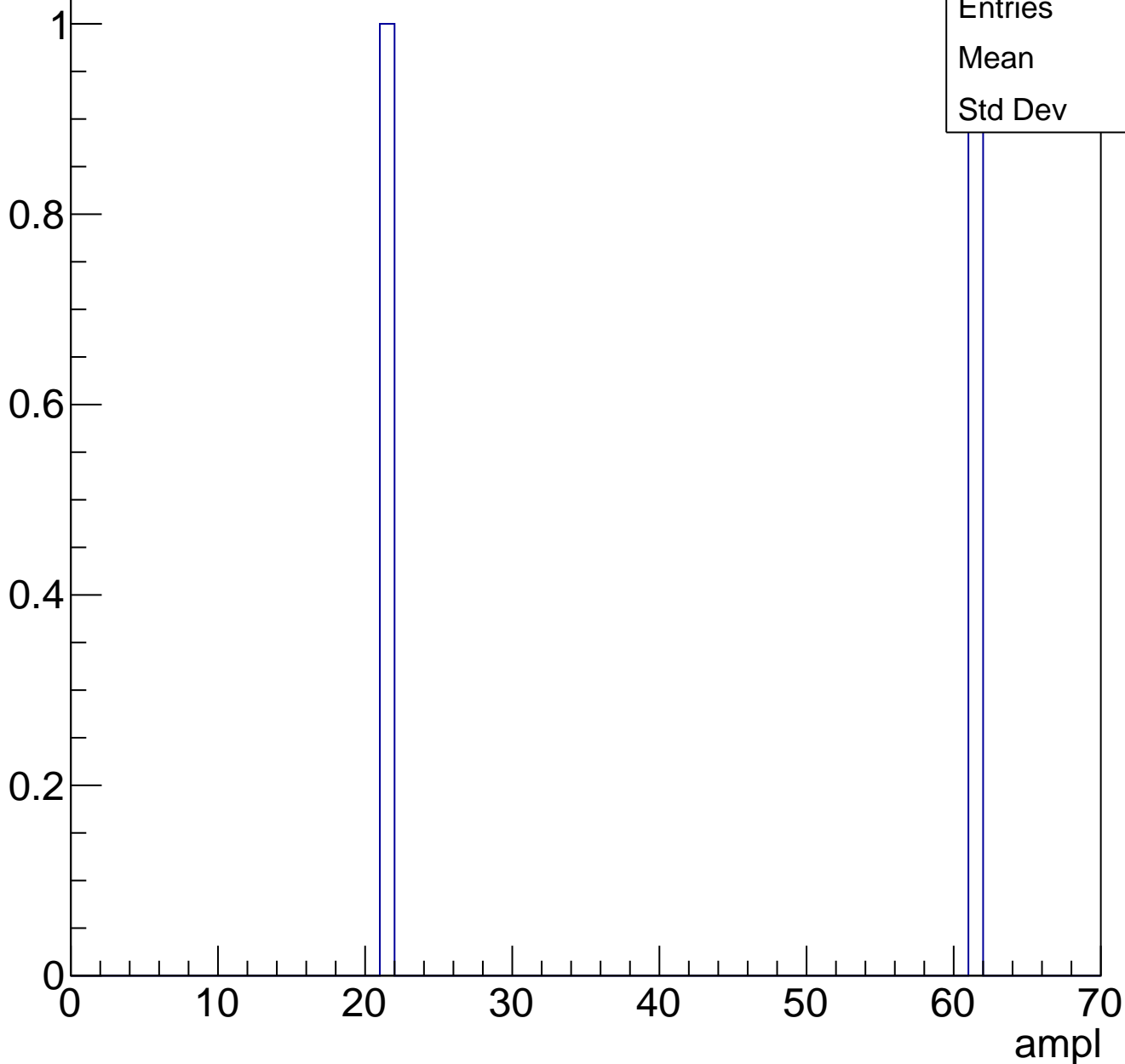
0 10 20 30 40 50 60 70



# B0L102S, U17-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	82
Mean	28.63
Std Dev	3.601

**Gaus mean : 28.7163**

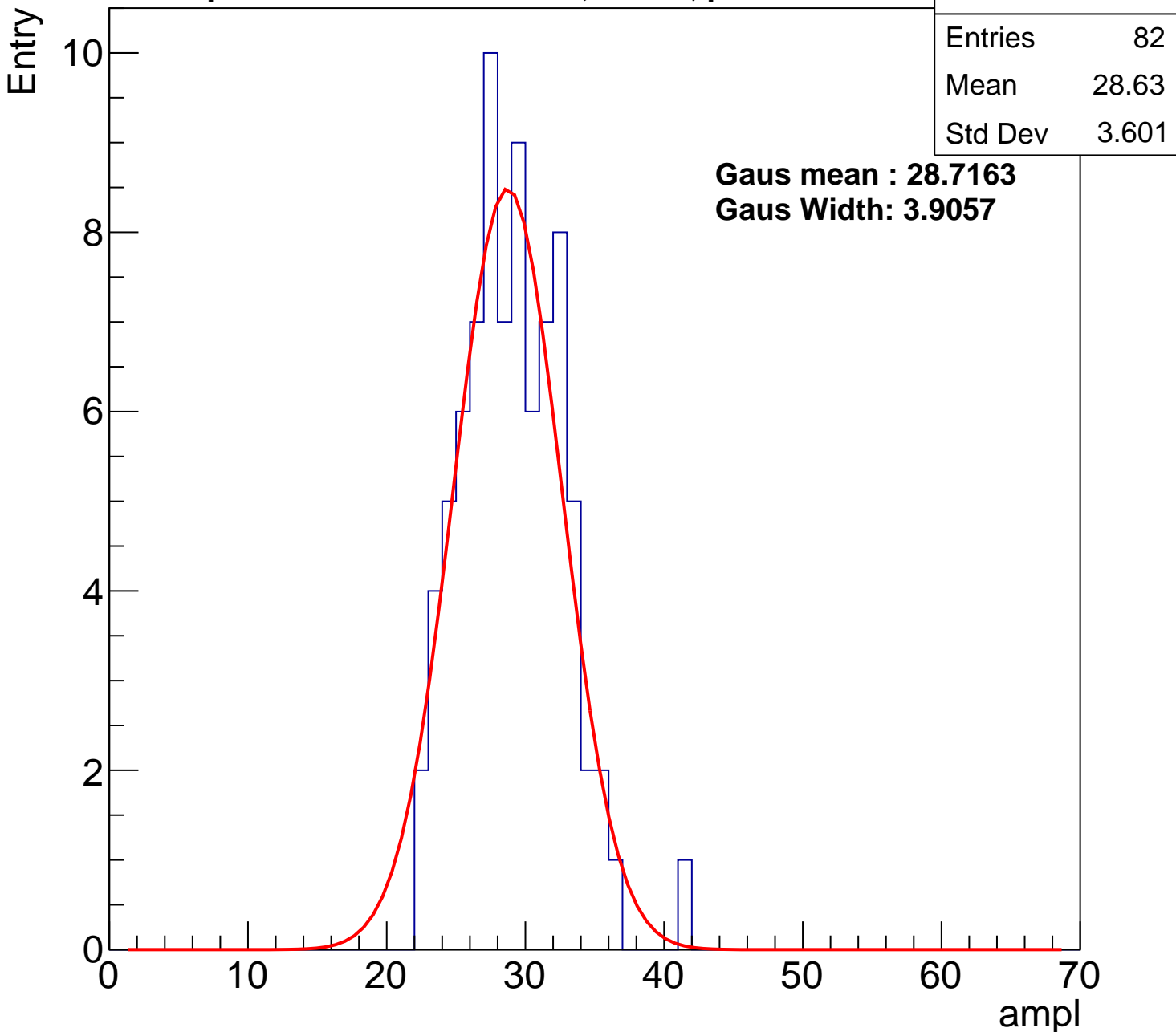
**Gaus Width: 3.9057**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch67, adc1

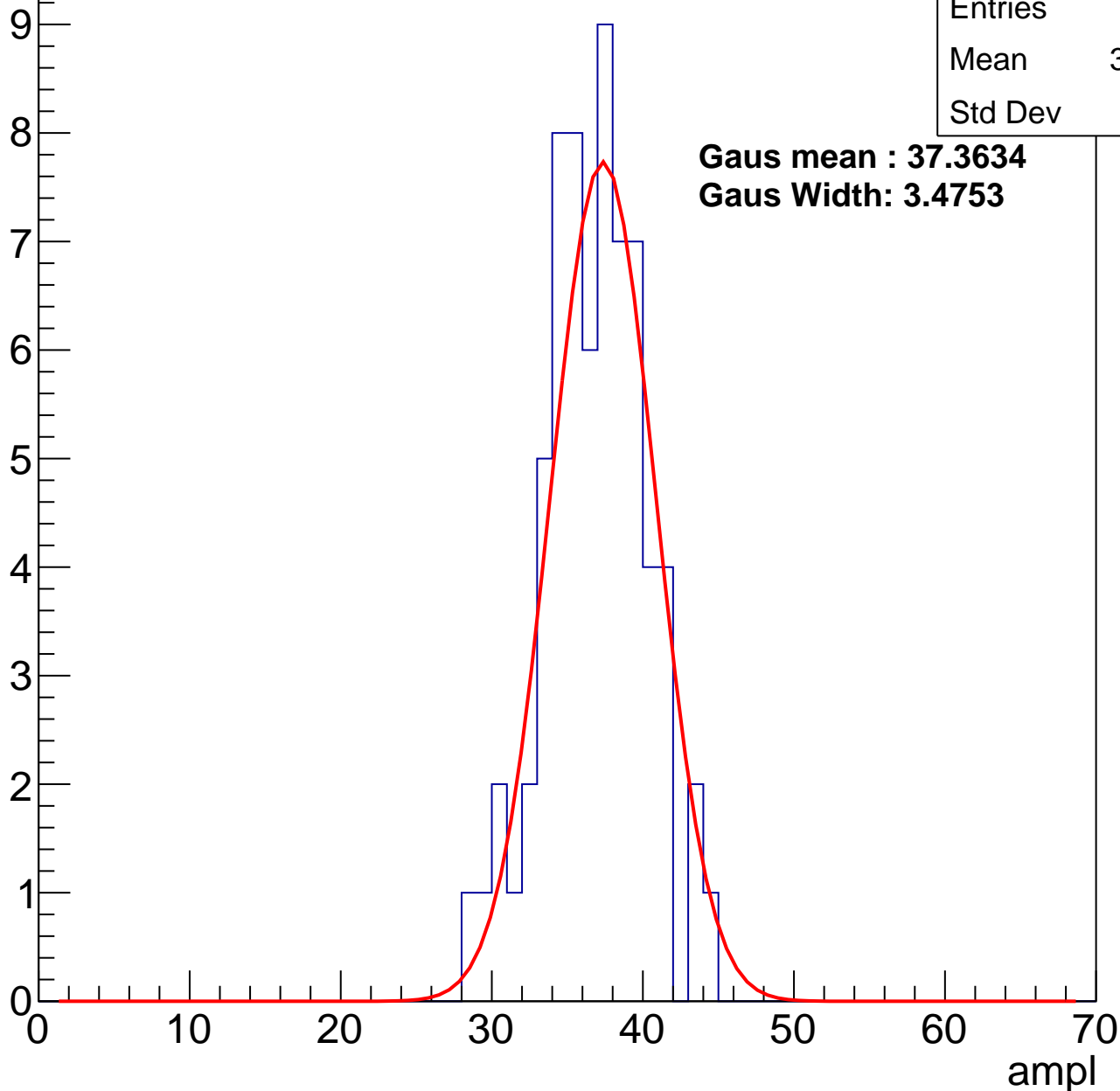
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.34
Std Dev	3.31

**Gaus mean : 37.3634**

**Gaus Width: 3.4753**



# B0L102S, U17-ch67, adc2

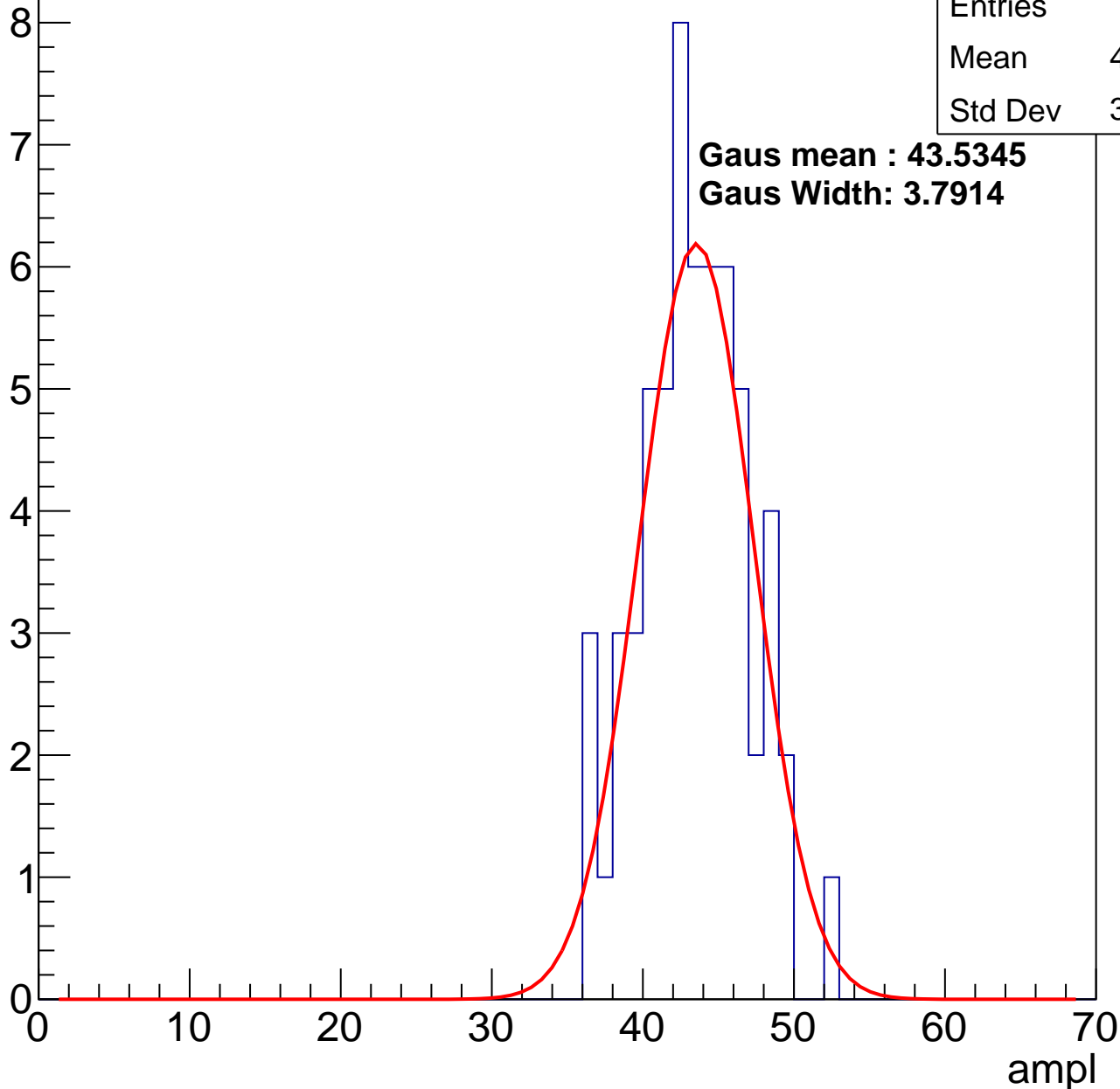
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.92
Std Dev	3.509

**Gaus mean : 43.5345**

**Gaus Width: 3.7914**

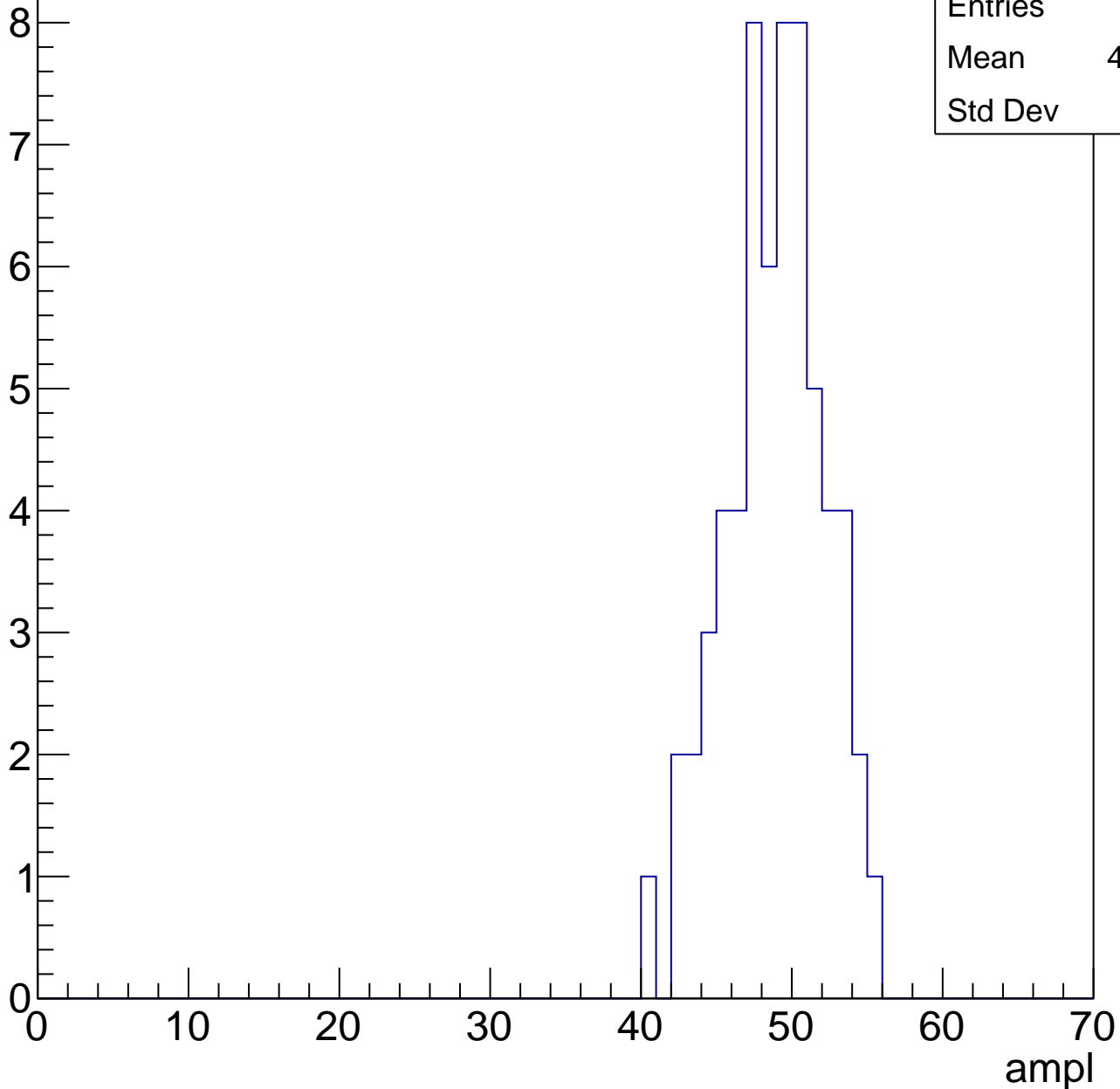


# B0L102S, U17-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	48.39
Std Dev	3.25

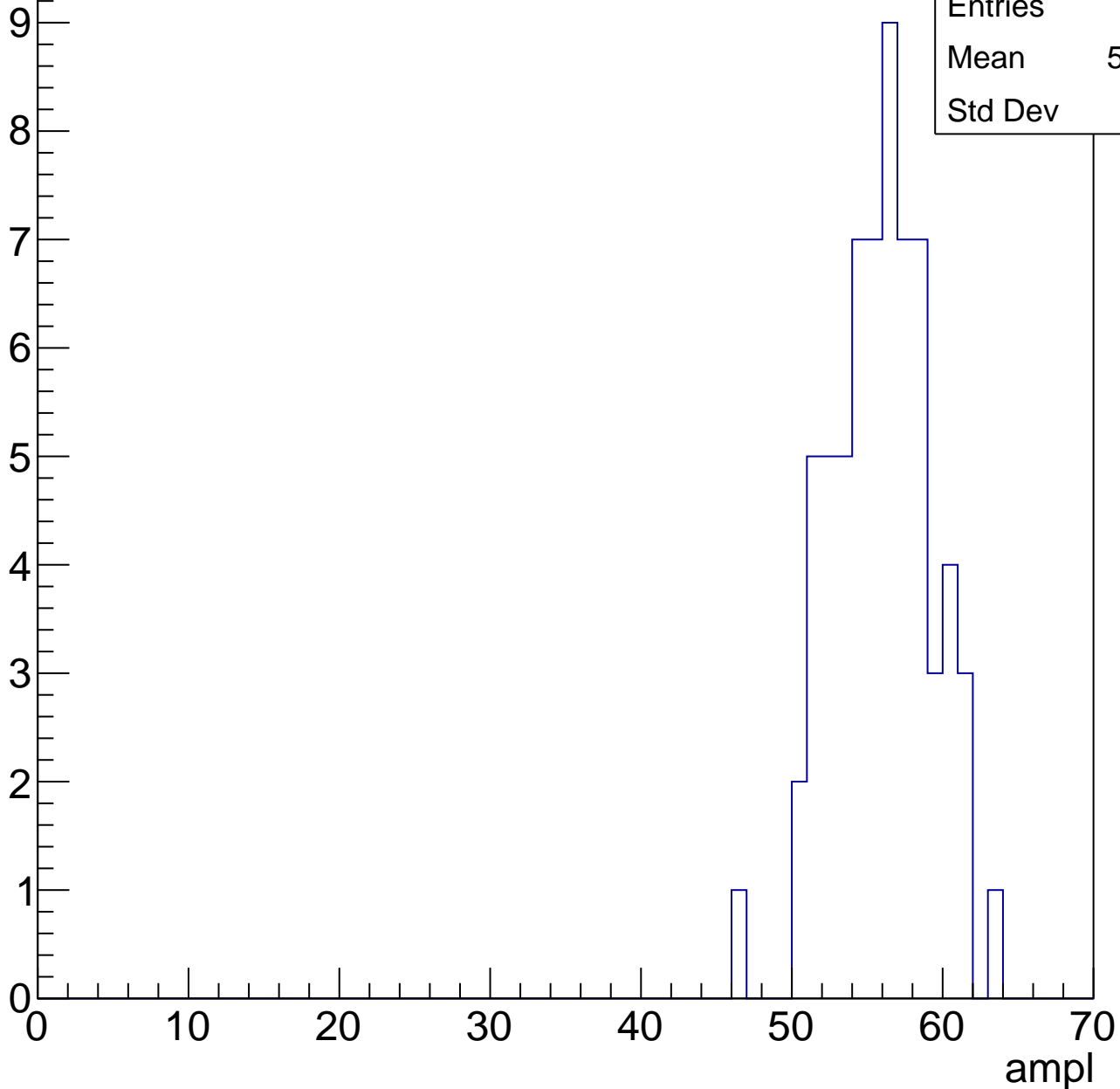


# B0L102S, U17-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	55.47
Std Dev	3.23

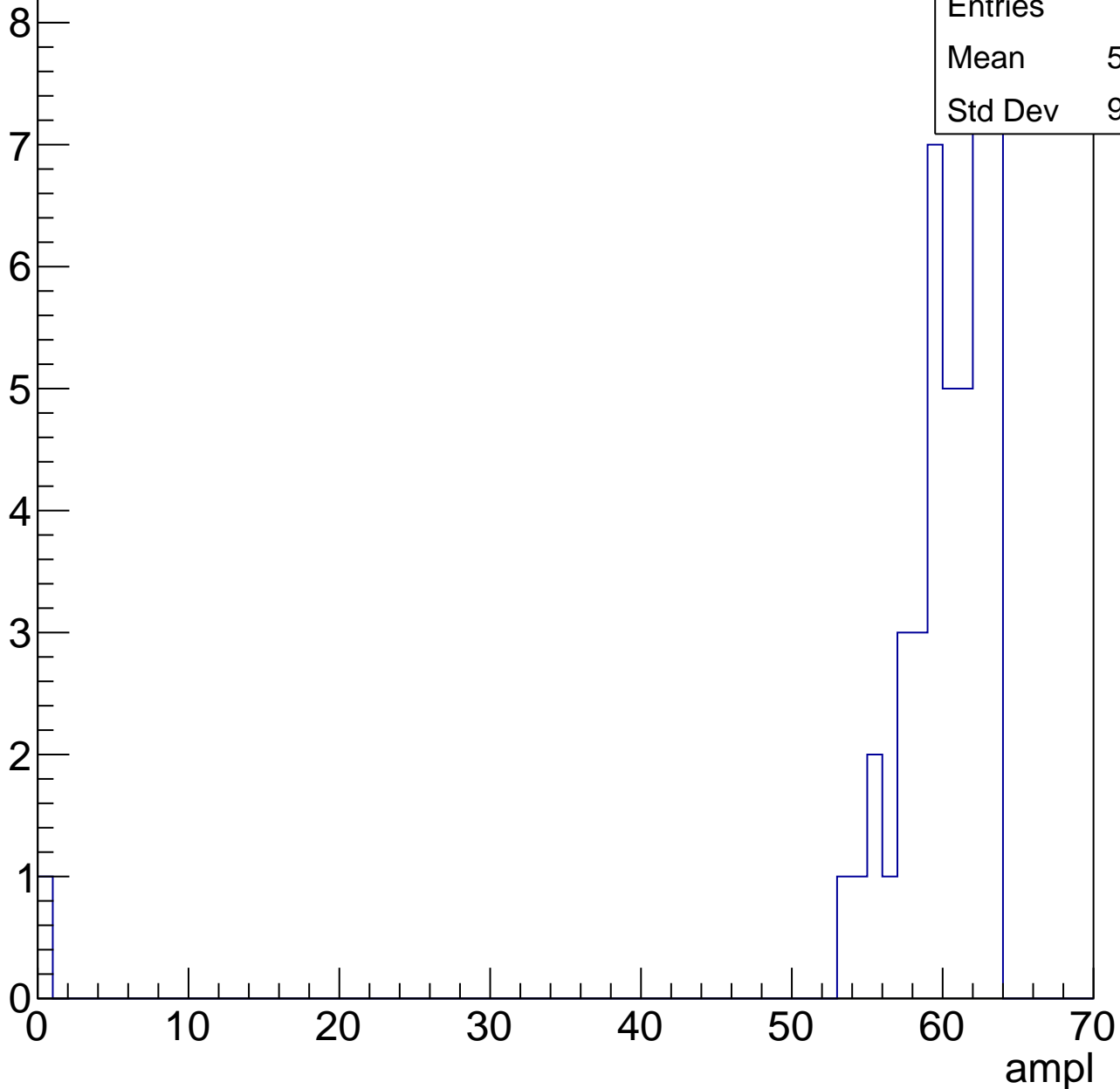


# B0L102S, U17-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	58.58
Std Dev	9.207



# B0L102S, U17-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch68, adc0

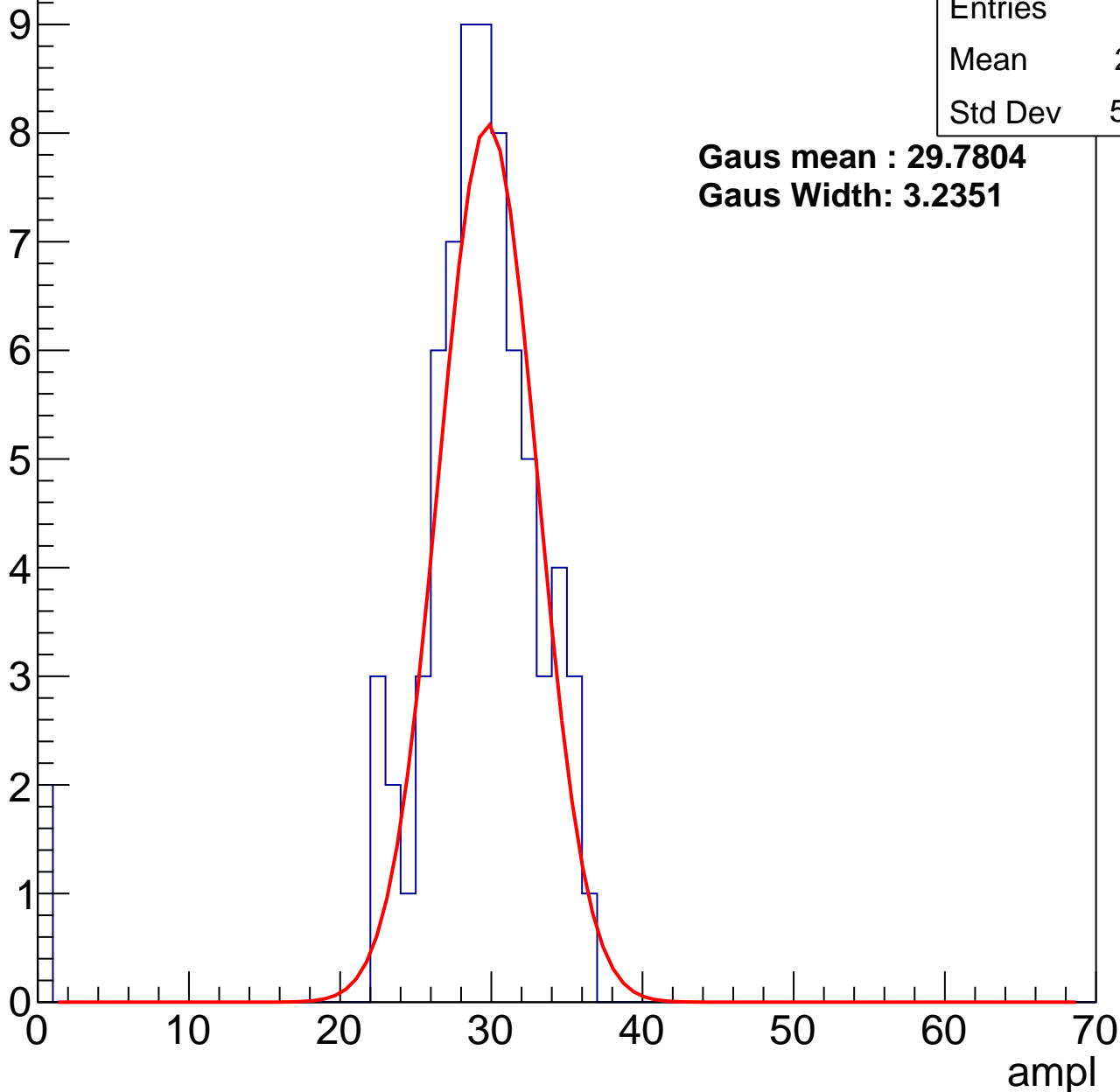
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	28.21
Std Dev	5.776

**Gaus mean : 29.7804**

**Gaus Width: 3.2351**



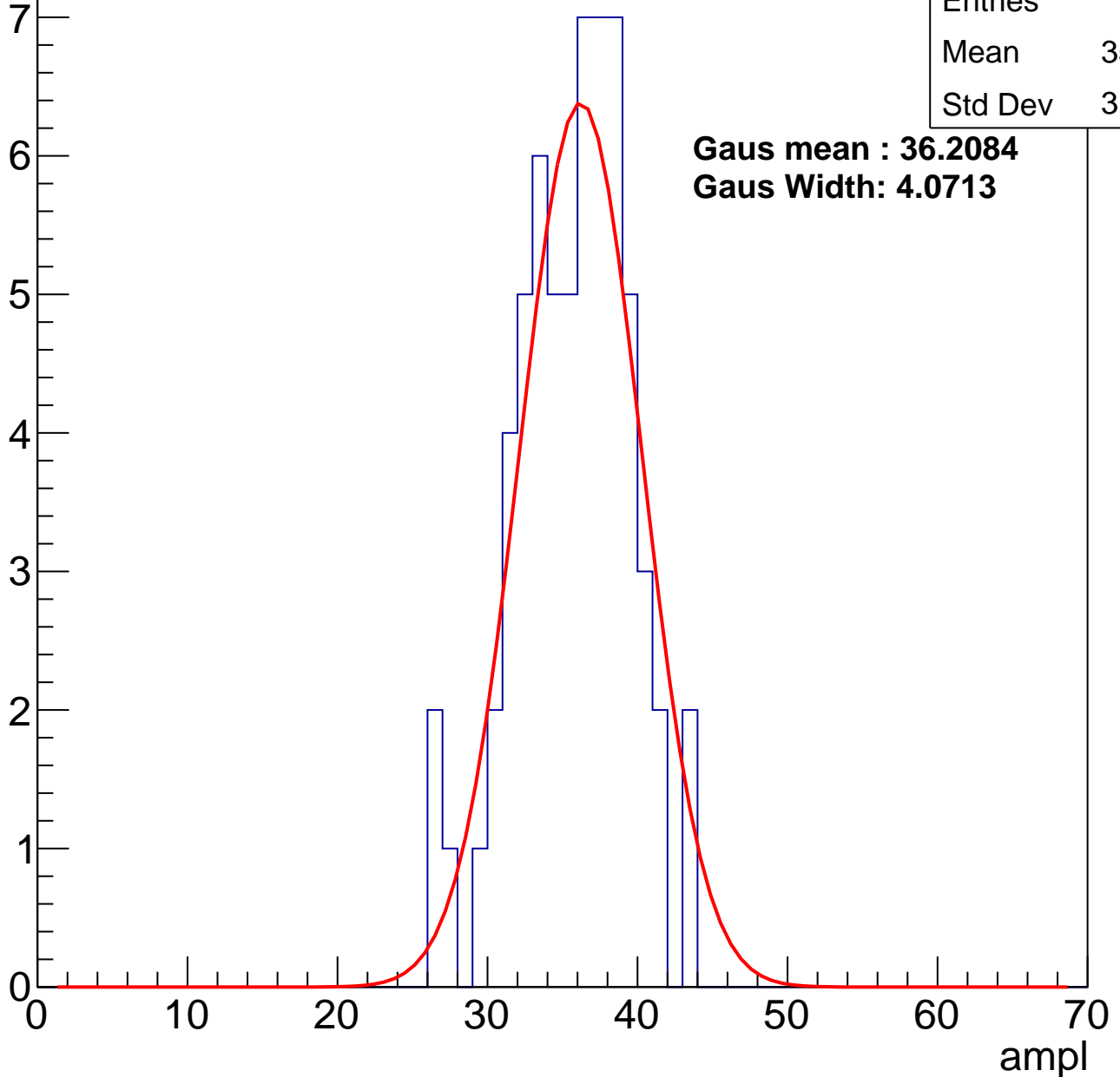
# B0L102S, U17-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.23
Std Dev	3.749

**Gaus mean : 36.2084**  
**Gaus Width: 4.0713**



# B0L102S, U17-ch68, adc2

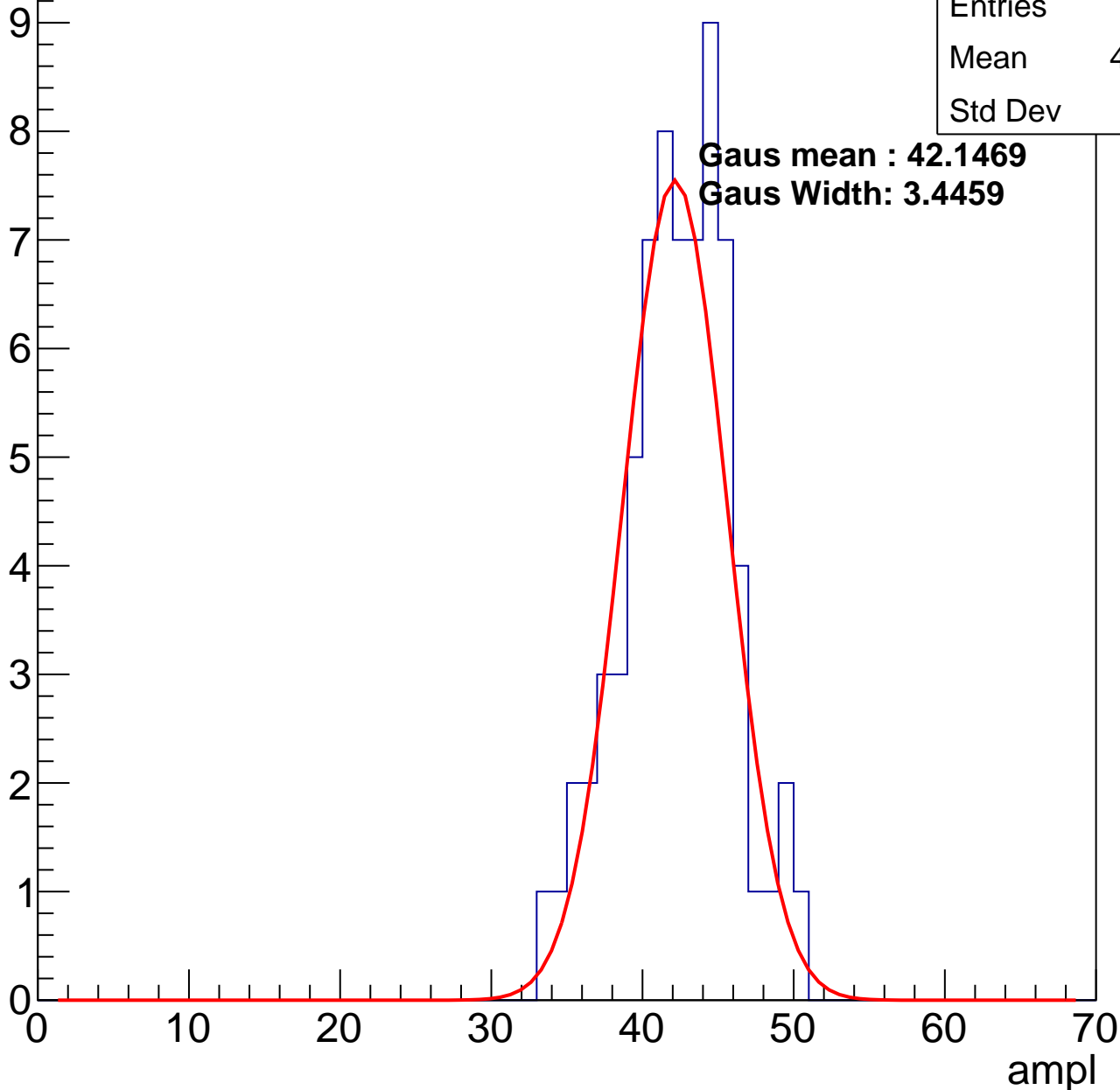
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.83
Std Dev	3.58

**Gaus mean : 42.1469**

**Gaus Width: 3.4459**

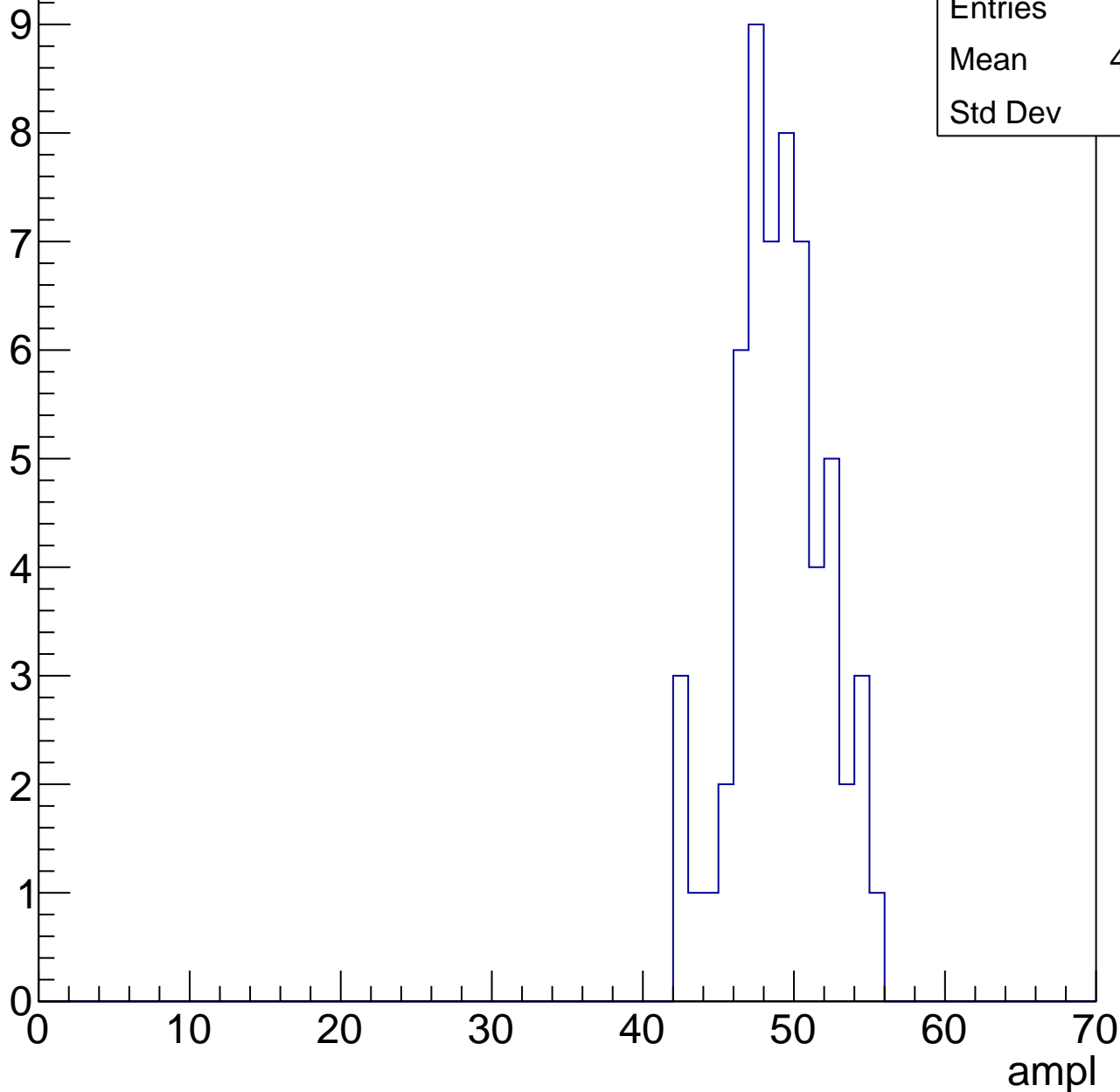


# B0L102S, U17-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	48.59
Std Dev	3.02

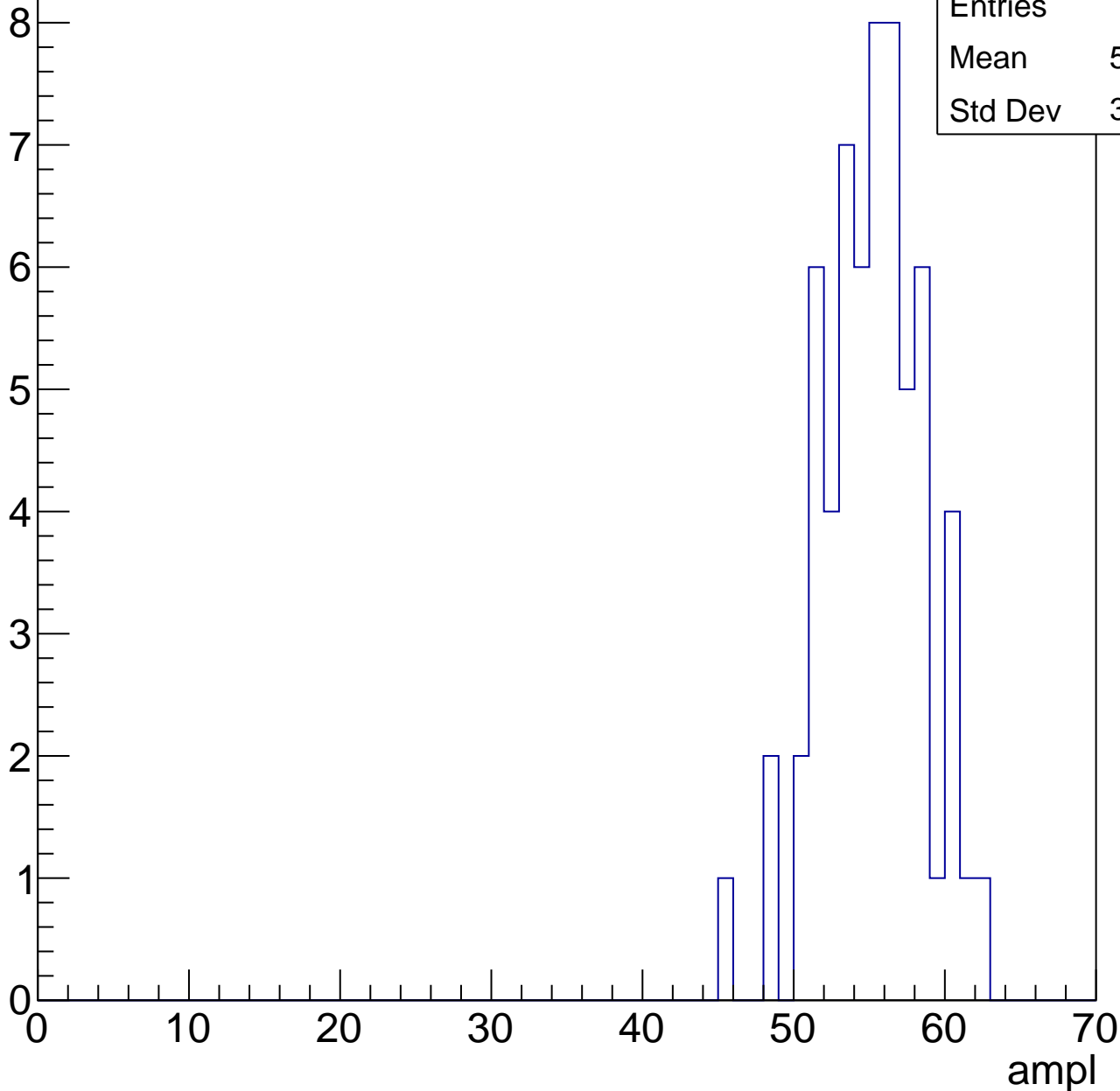


# B0L102S, U17-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

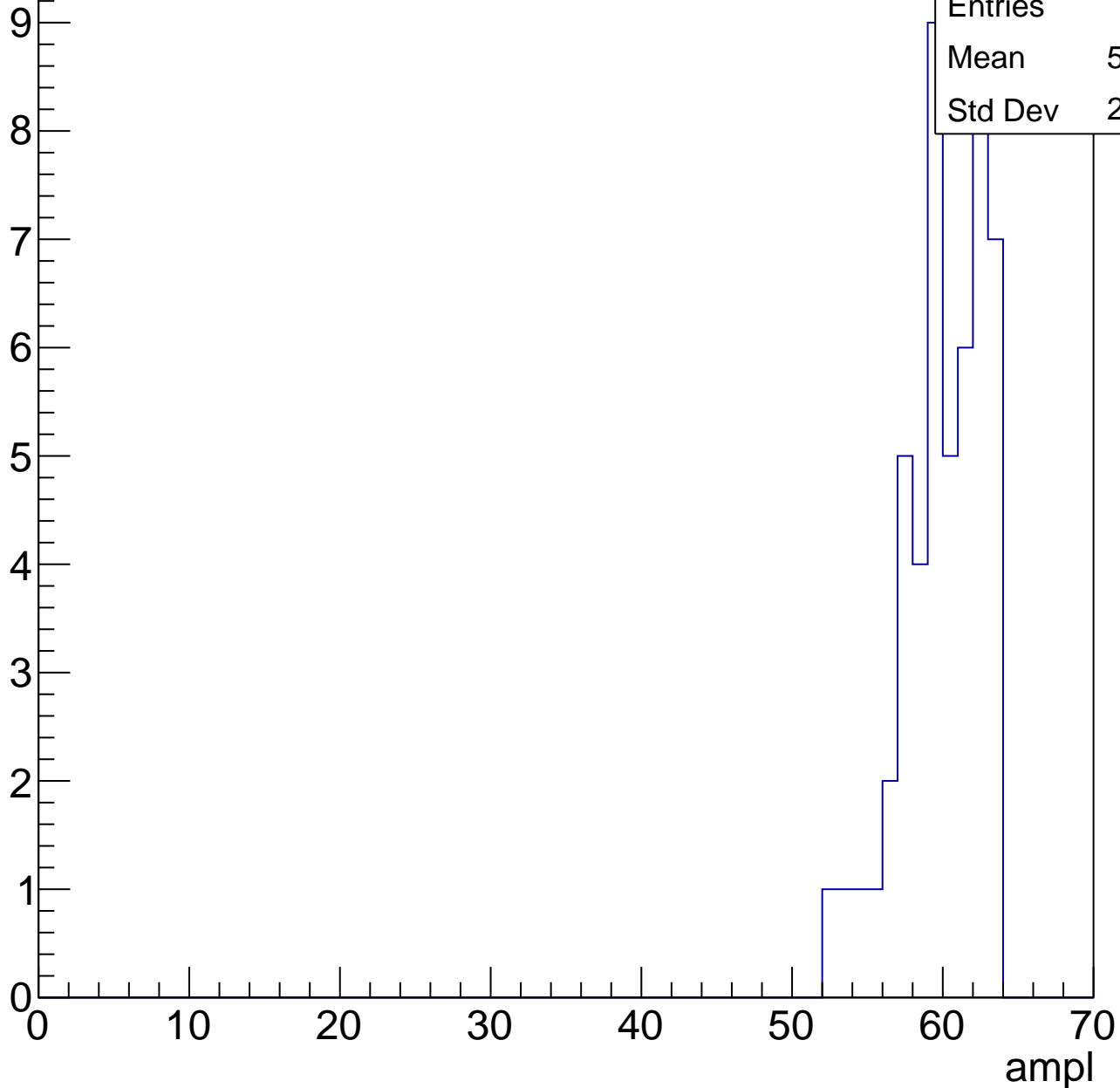
Entries	62
Mean	54.73
Std Dev	3.342



# B0L102S, U17-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

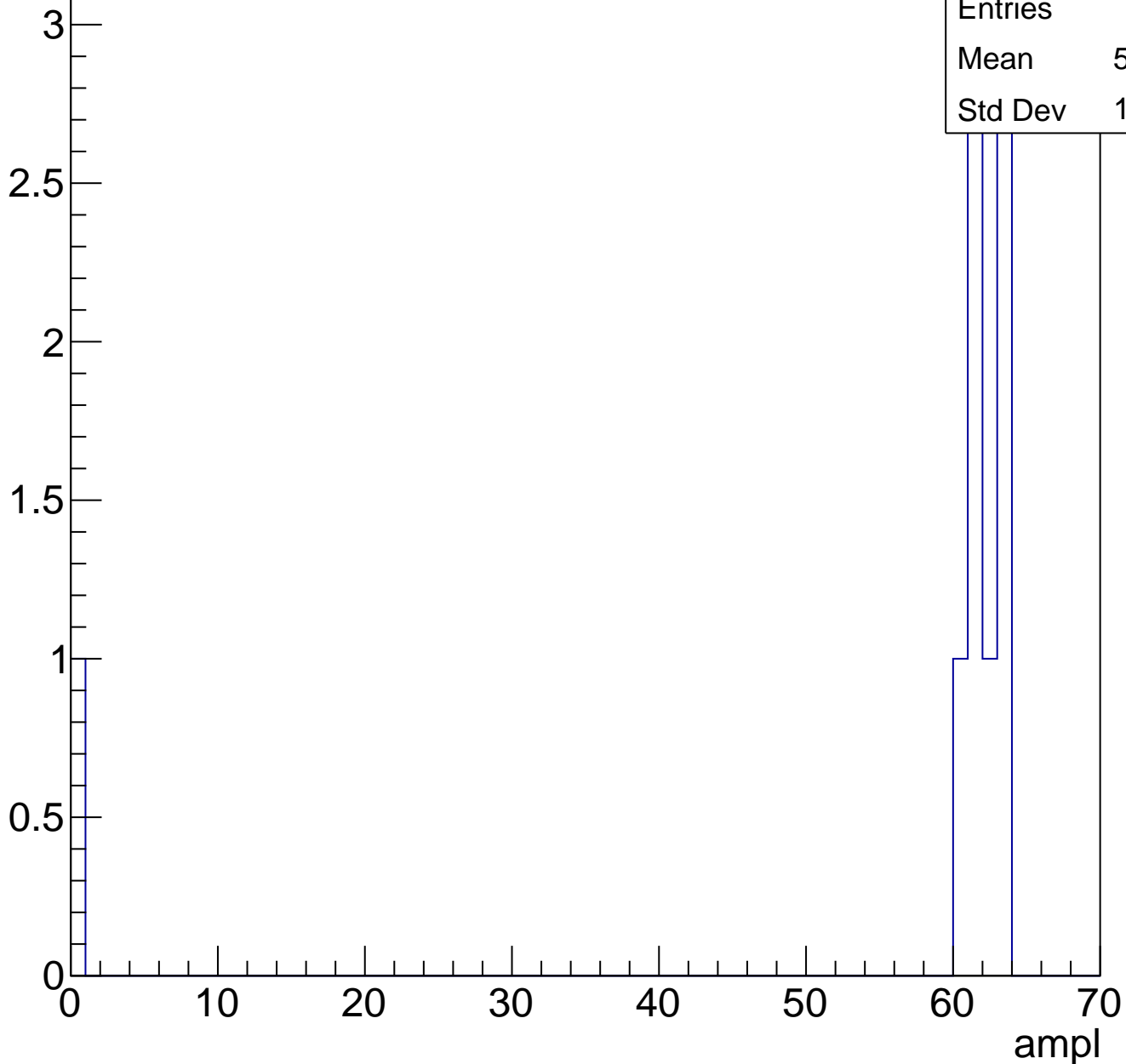


Entries	50
Mean	59.54
Std Dev	2.707

# B0L102S, U17-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch69, adc0

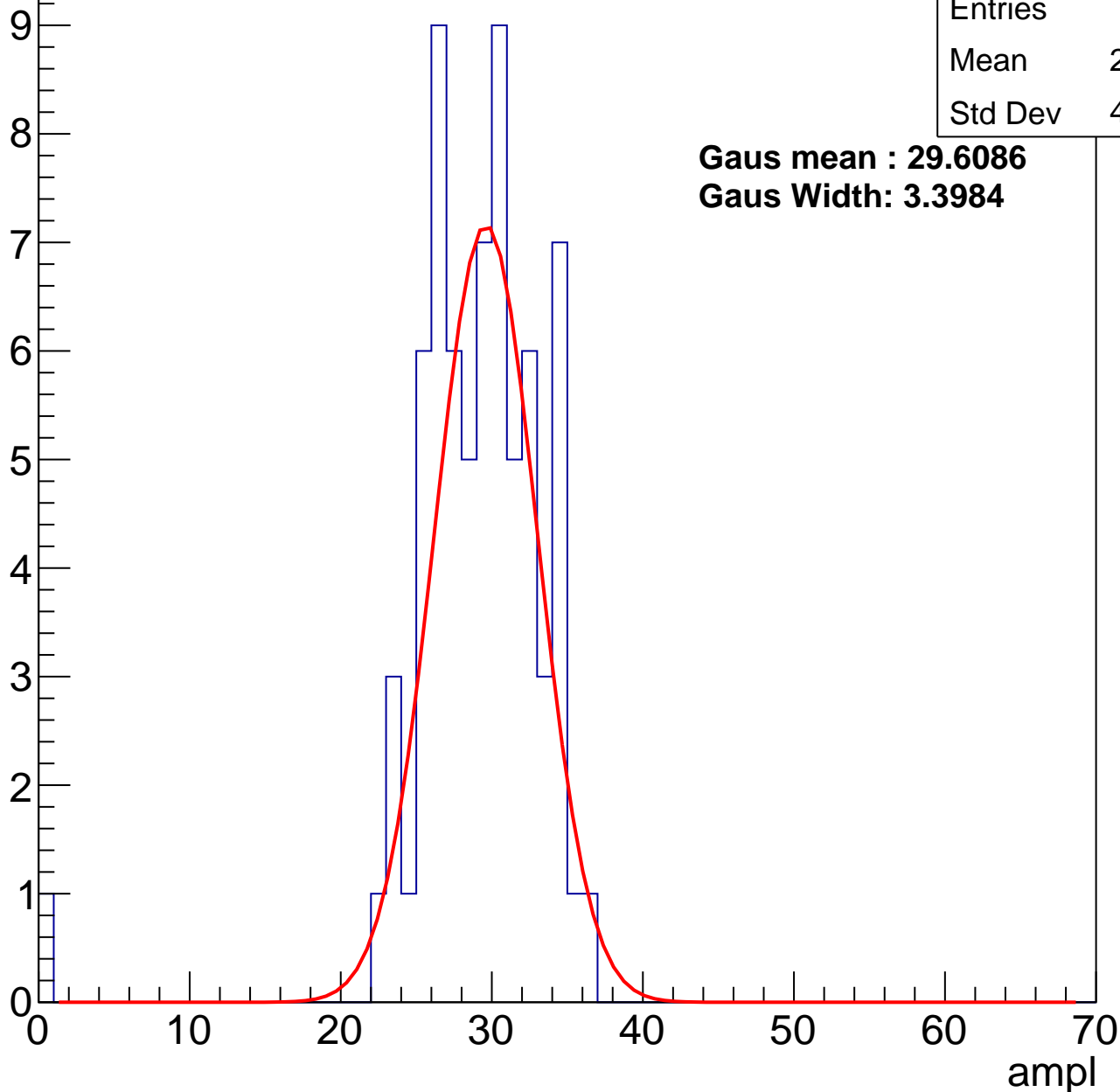
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	28.58
Std Dev	4.758

**Gaus mean : 29.6086**

**Gaus Width: 3.3984**



# B0L102S, U17-ch69, adc1

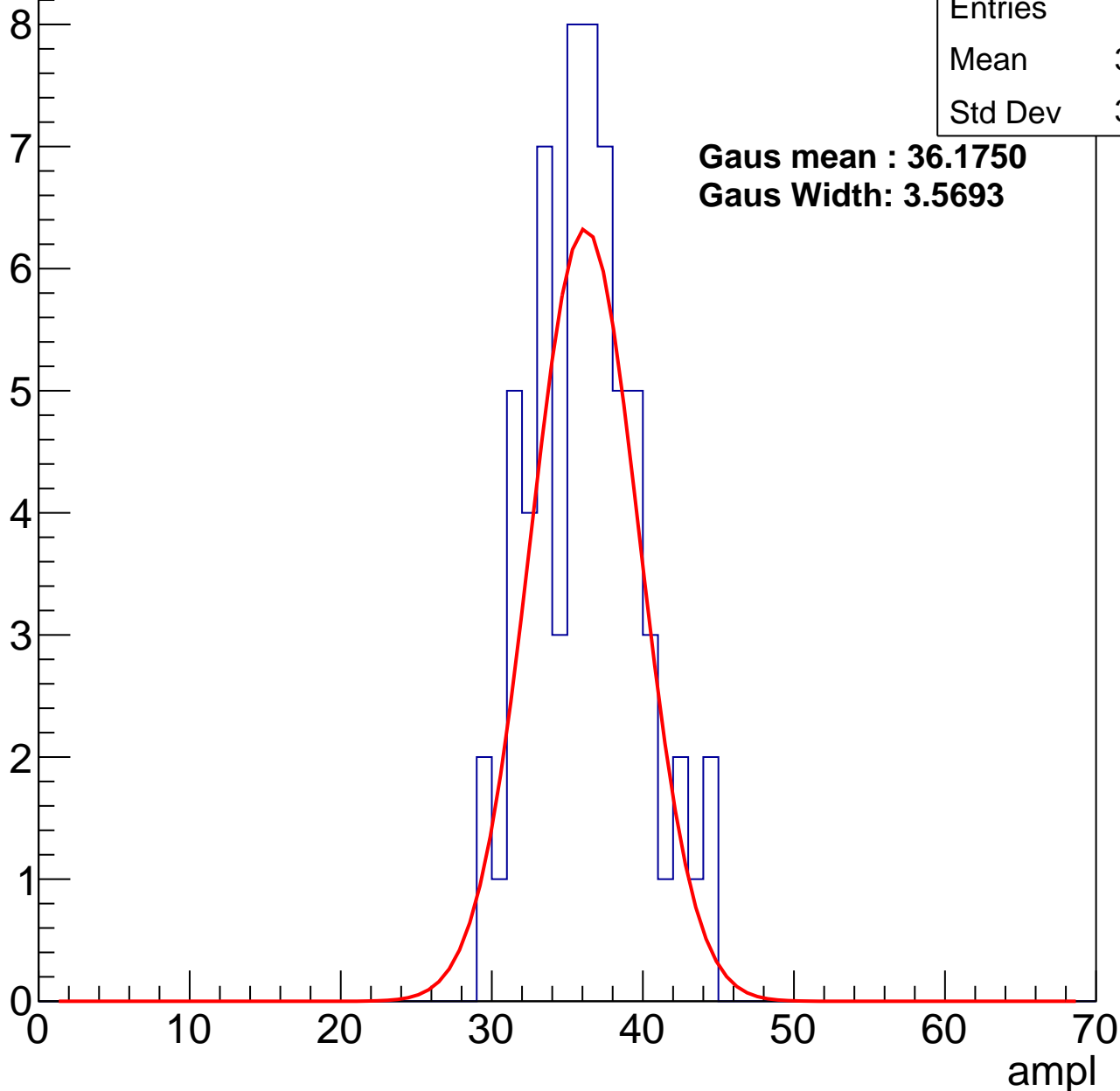
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.81
Std Dev	3.531

**Gaus mean : 36.1750**

**Gaus Width: 3.5693**



# B0L102S, U17-ch69, adc2

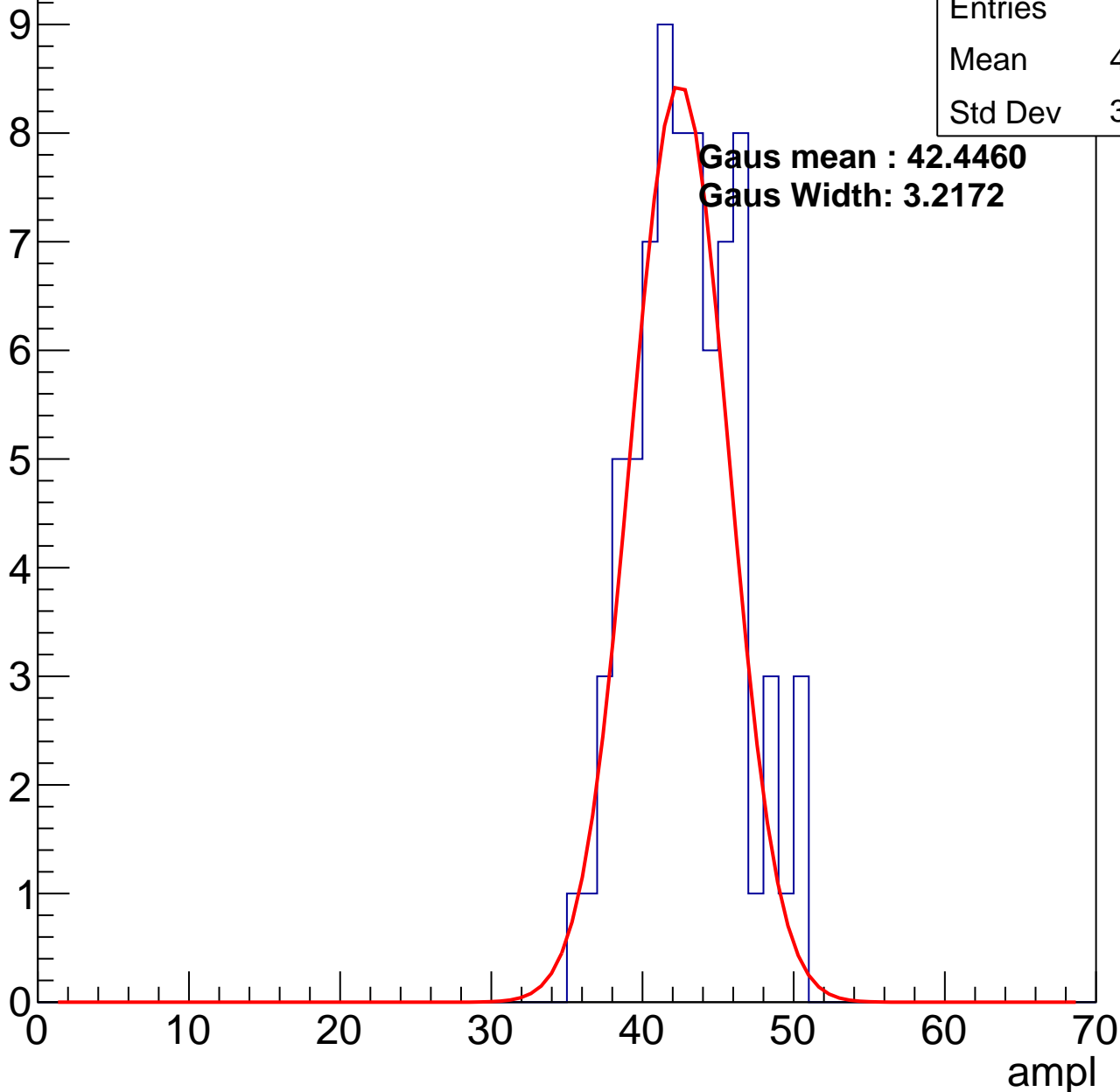
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	42.54
Std Dev	3.435

**Gaus mean : 42.4460**

**Gaus Width: 3.2172**

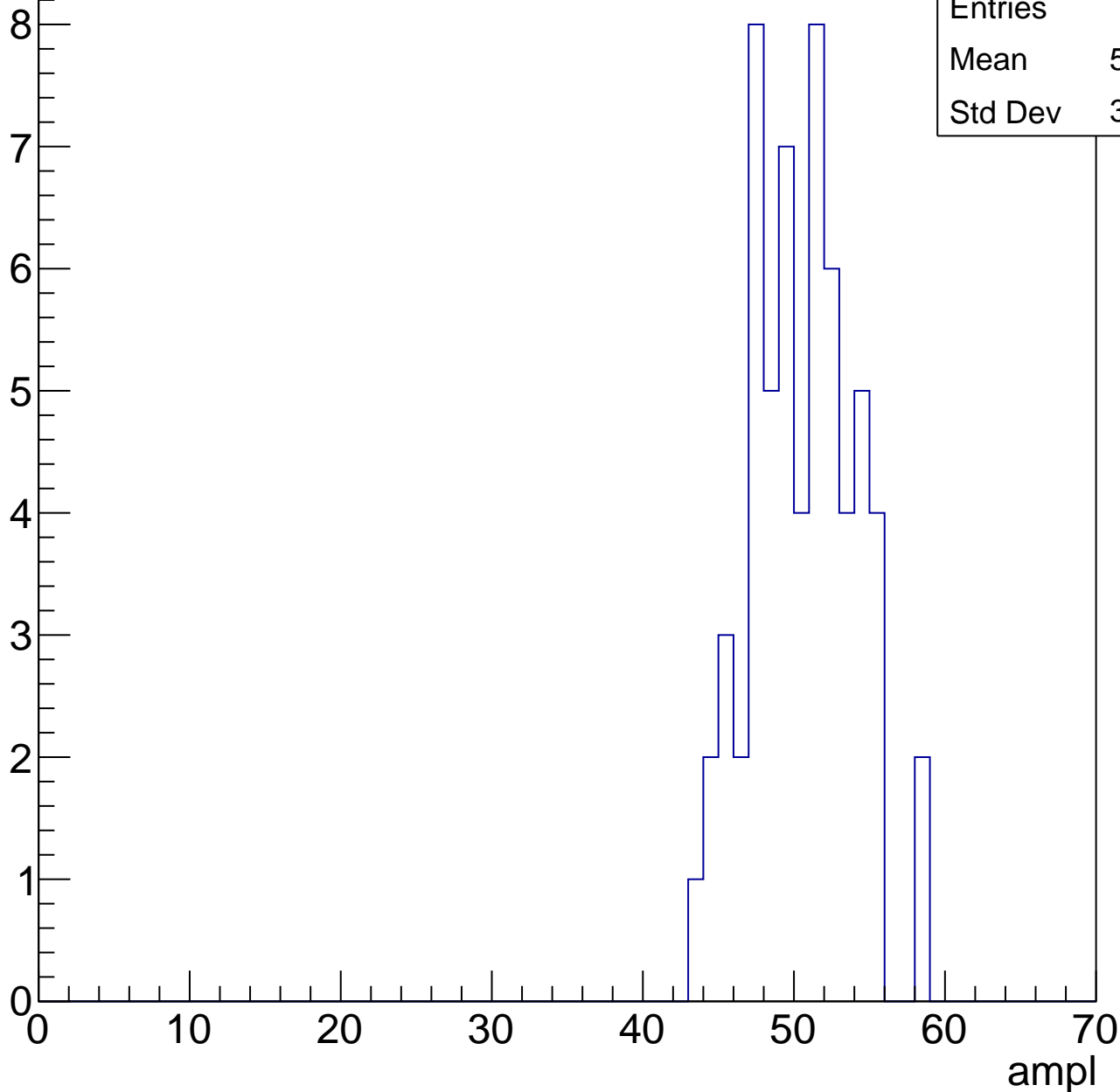


# B0L102S, U17-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	50.08
Std Dev	3.394

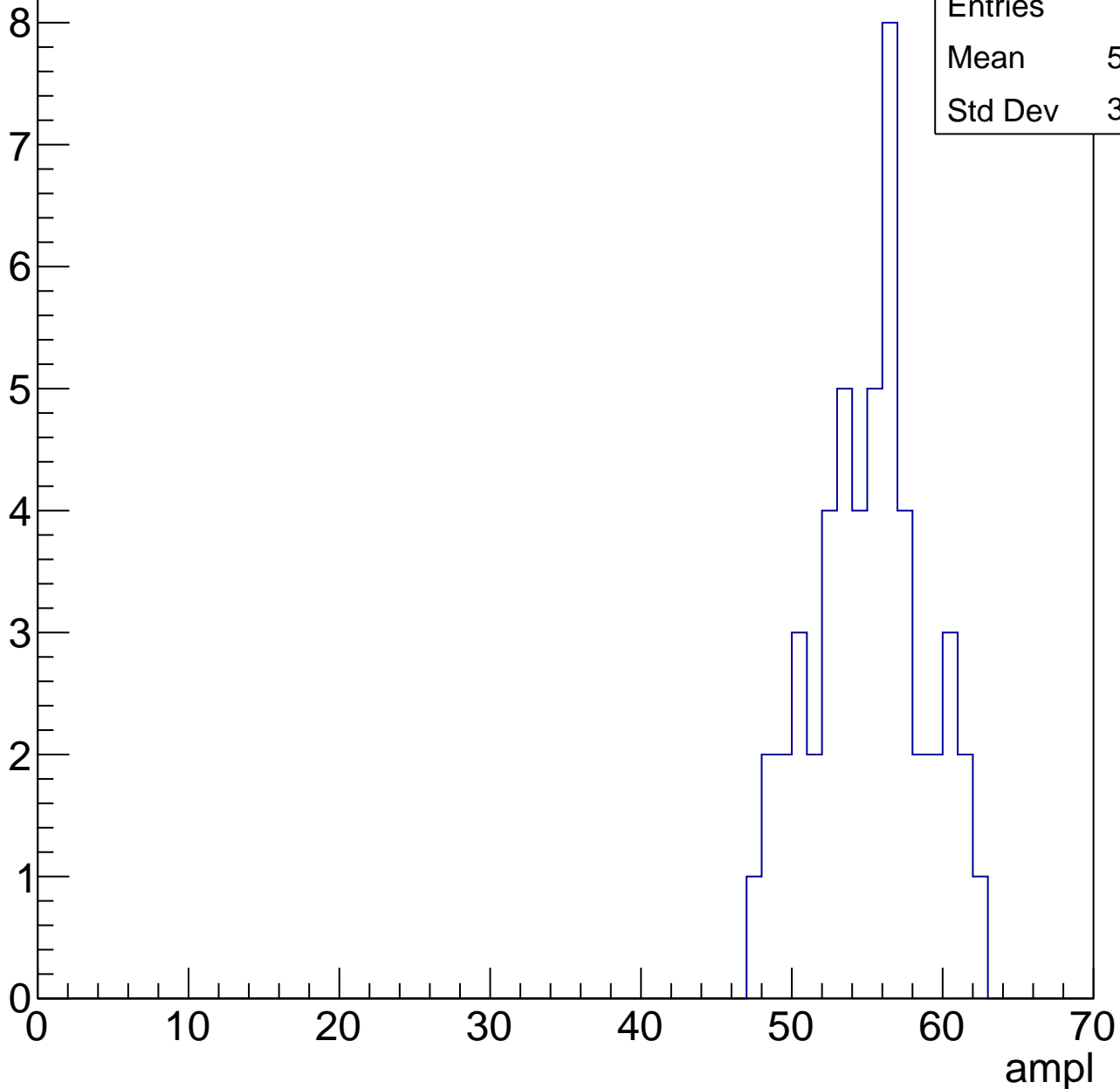


# B0L102S, U17-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

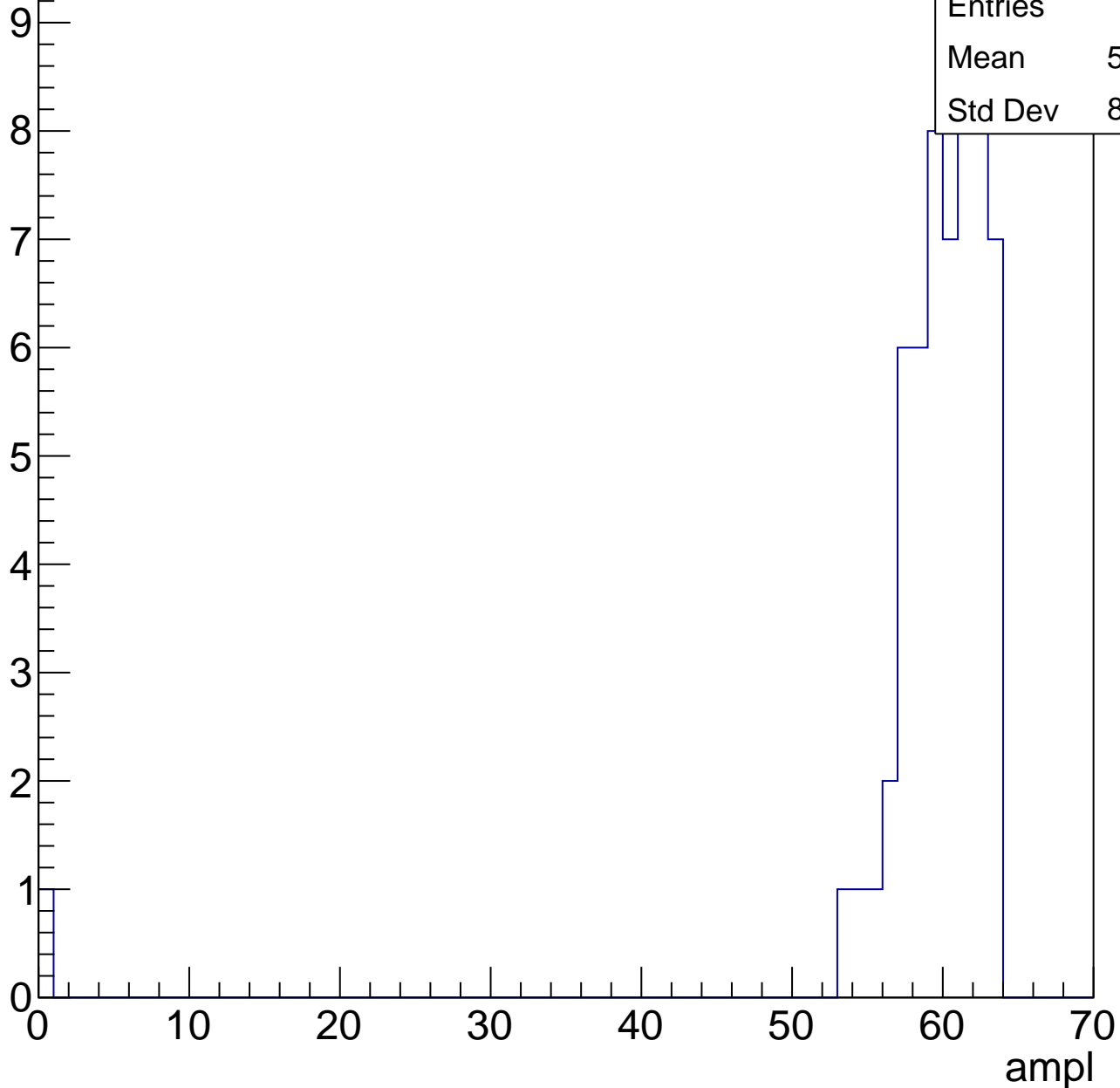
Entries	50
Mean	54.62
Std Dev	3.644



# B0L102S, U17-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch70, adc0

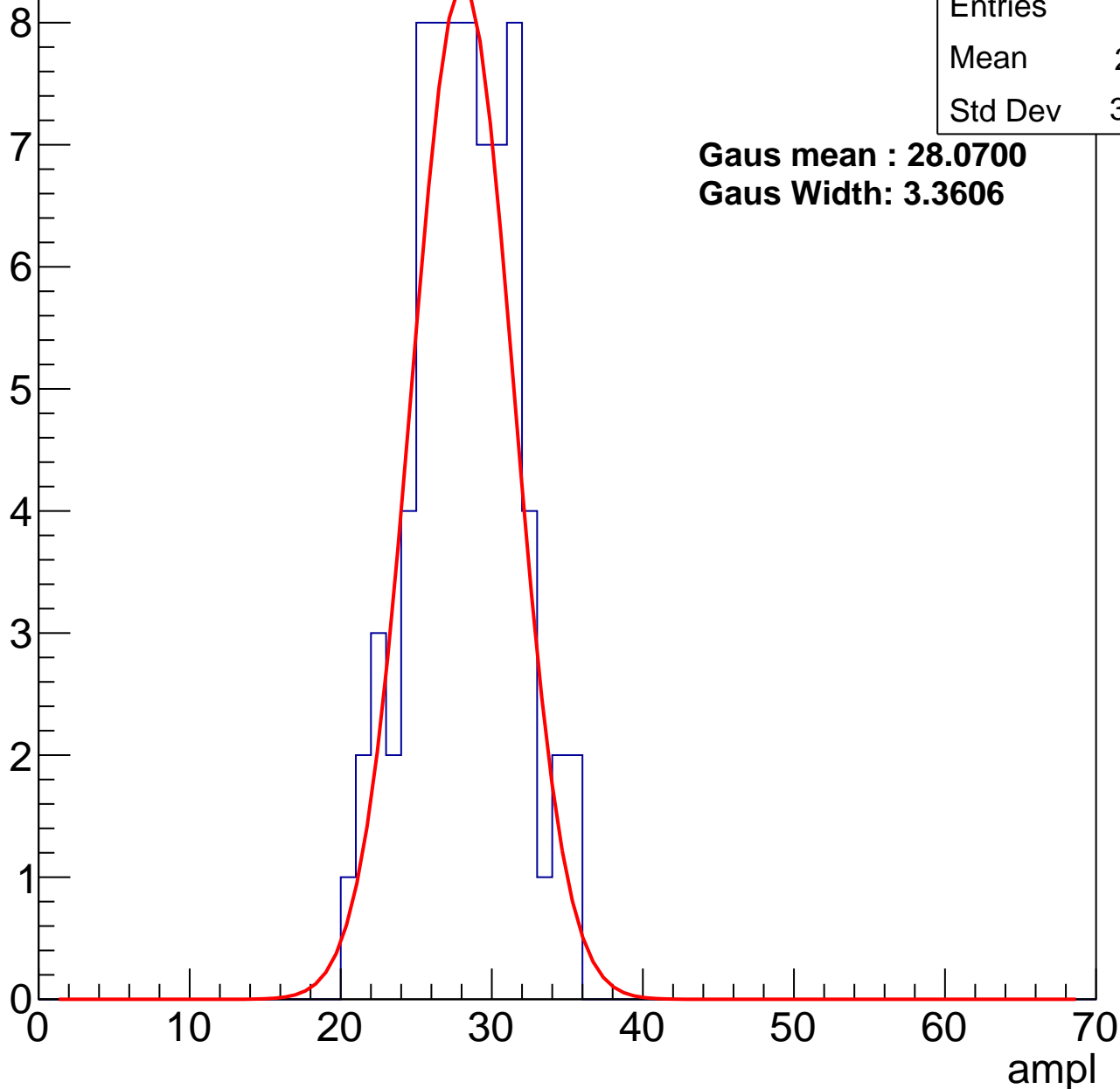
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	27.71
Std Dev	3.377

**Gaus mean : 28.0700**

**Gaus Width: 3.3606**



# B0L102S, U17-ch70, adc1

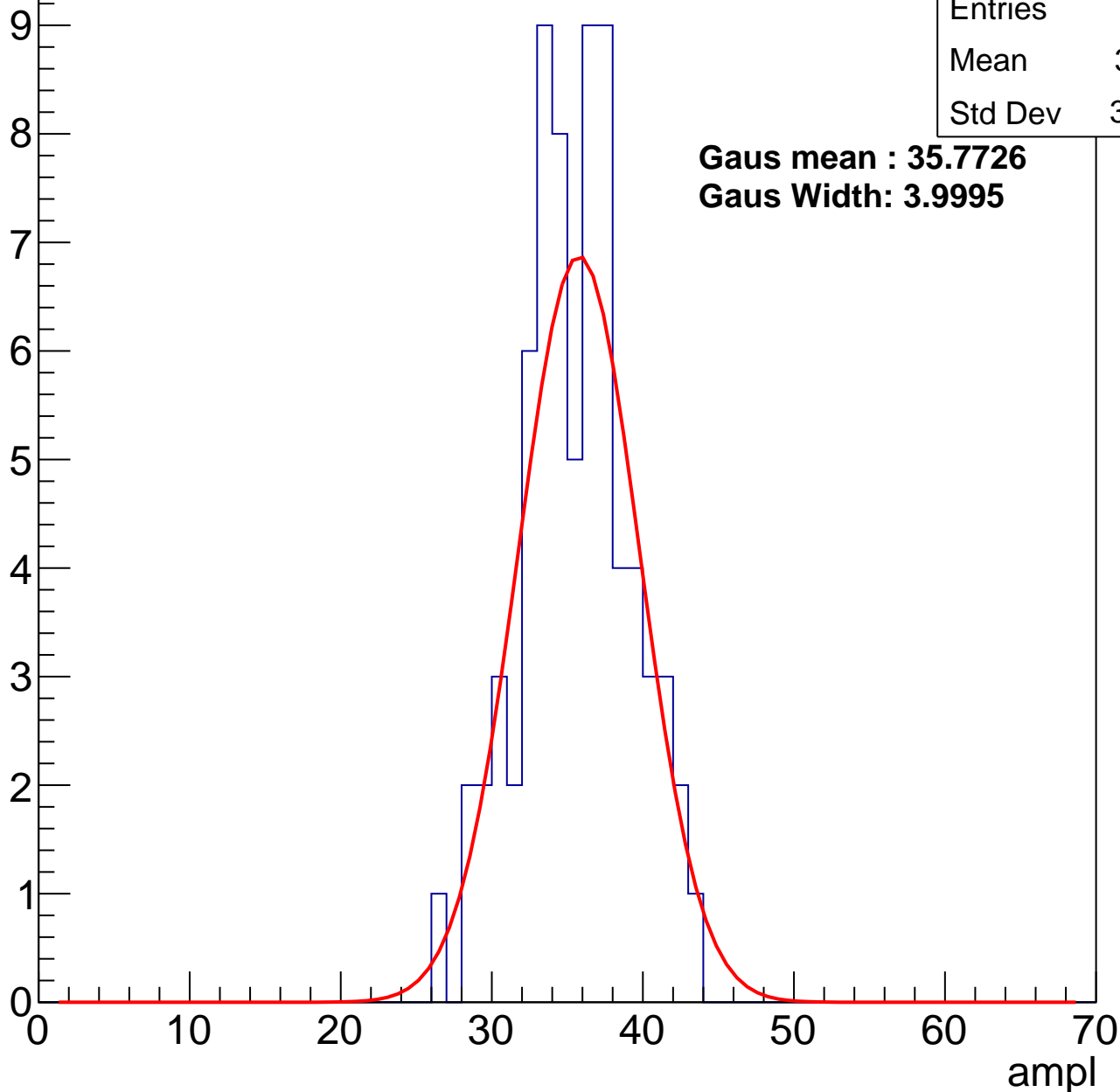
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.11
Std Dev	3.598

**Gaus mean : 35.7726**

**Gaus Width: 3.9995**

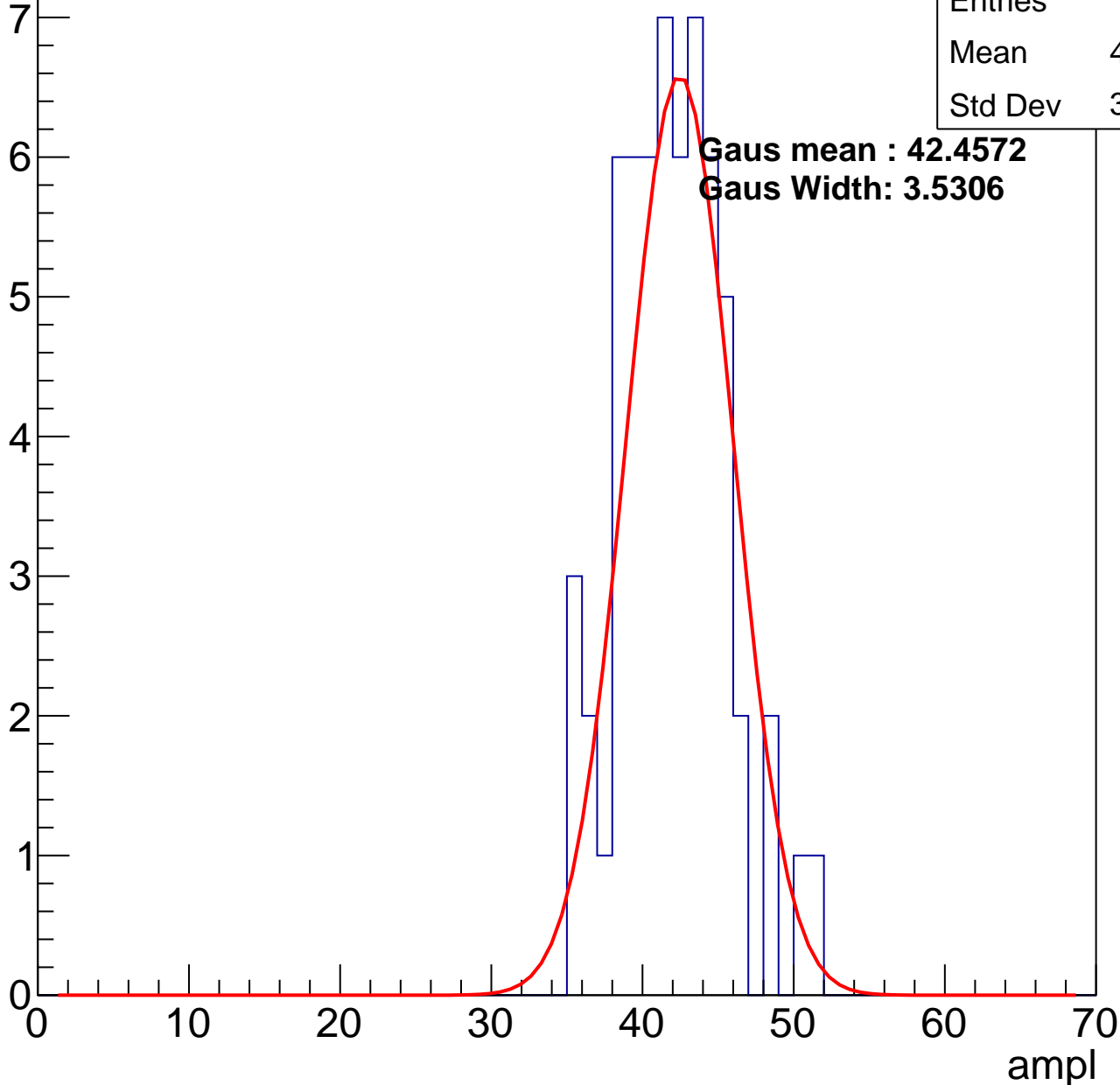


# B0L102S, U17-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	41.54
Std Dev	3.476



# B0L102S, U17-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

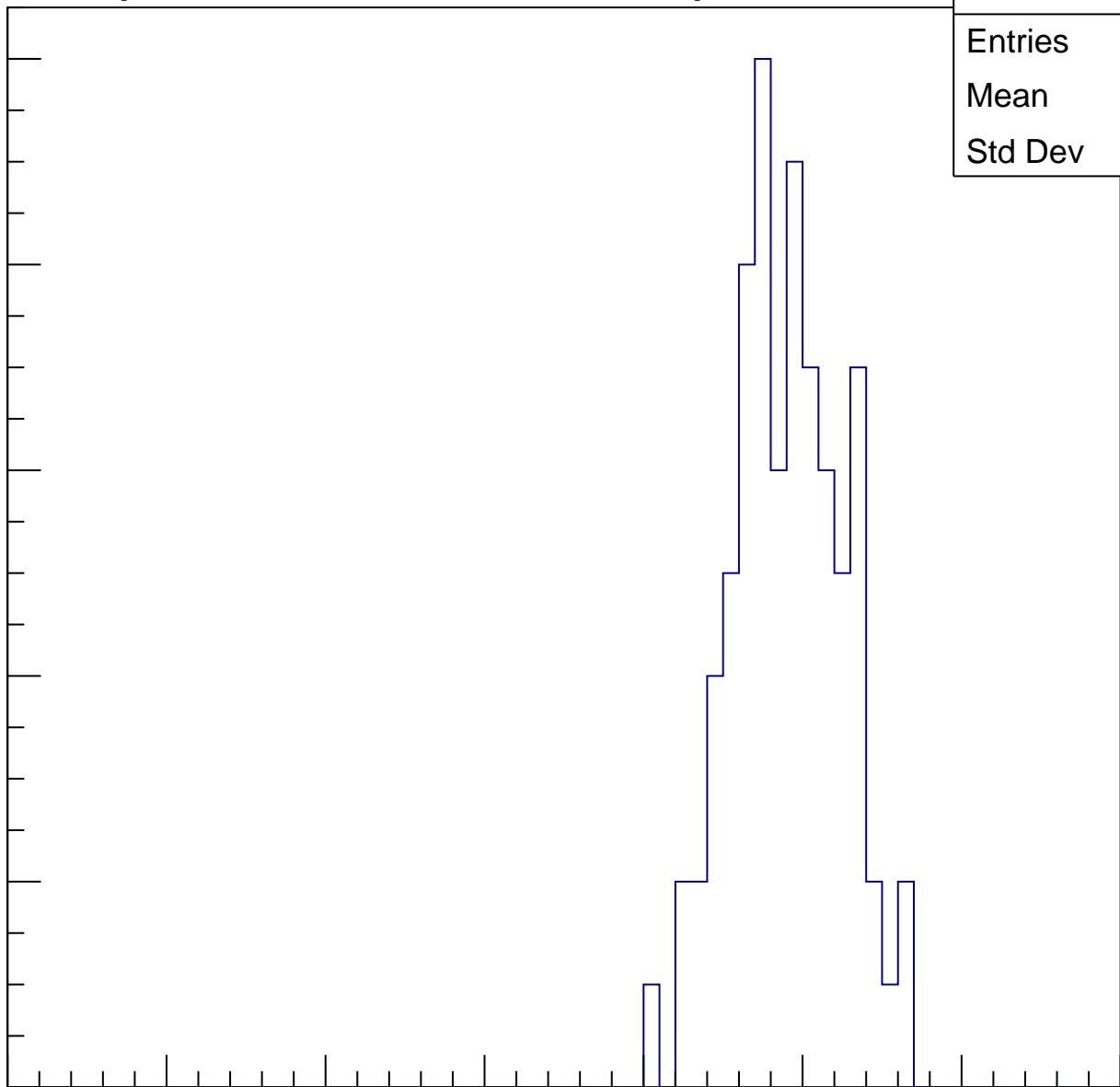
Entries	77
Mean	48.57
Std Dev	3.432

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

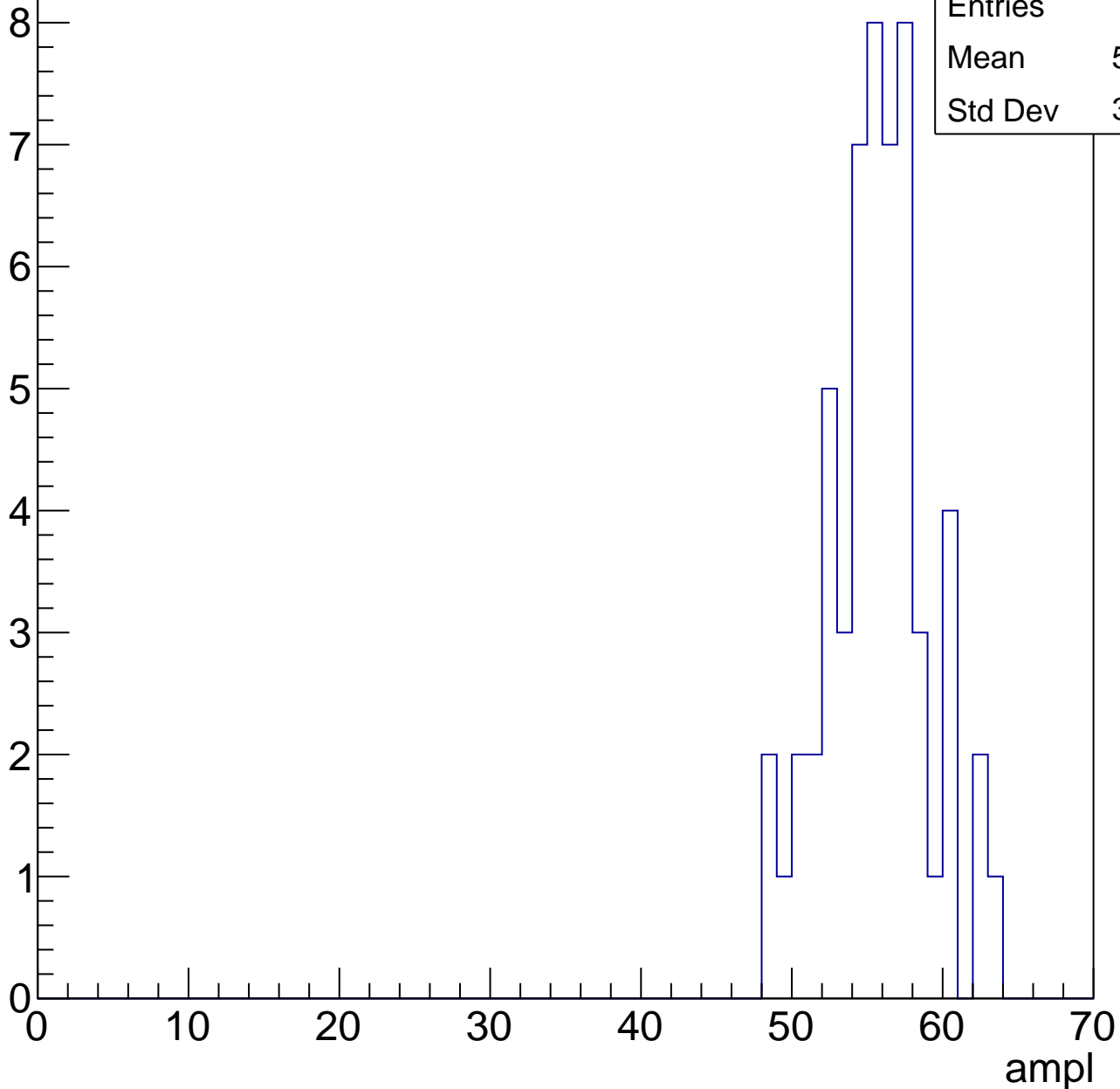


# B0L102S, U17-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	55.21
Std Dev	3.331

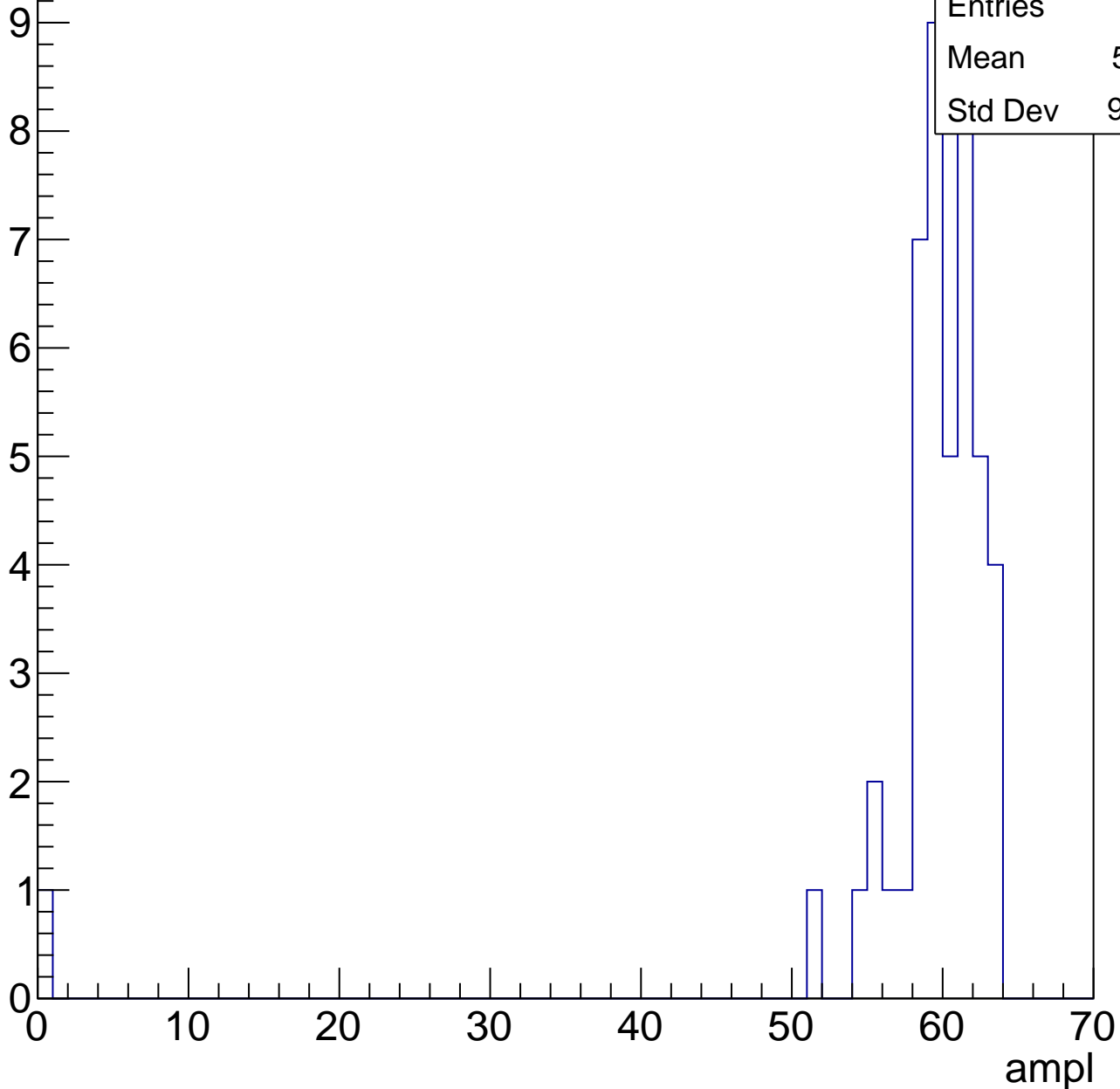


# B0L102S, U17-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

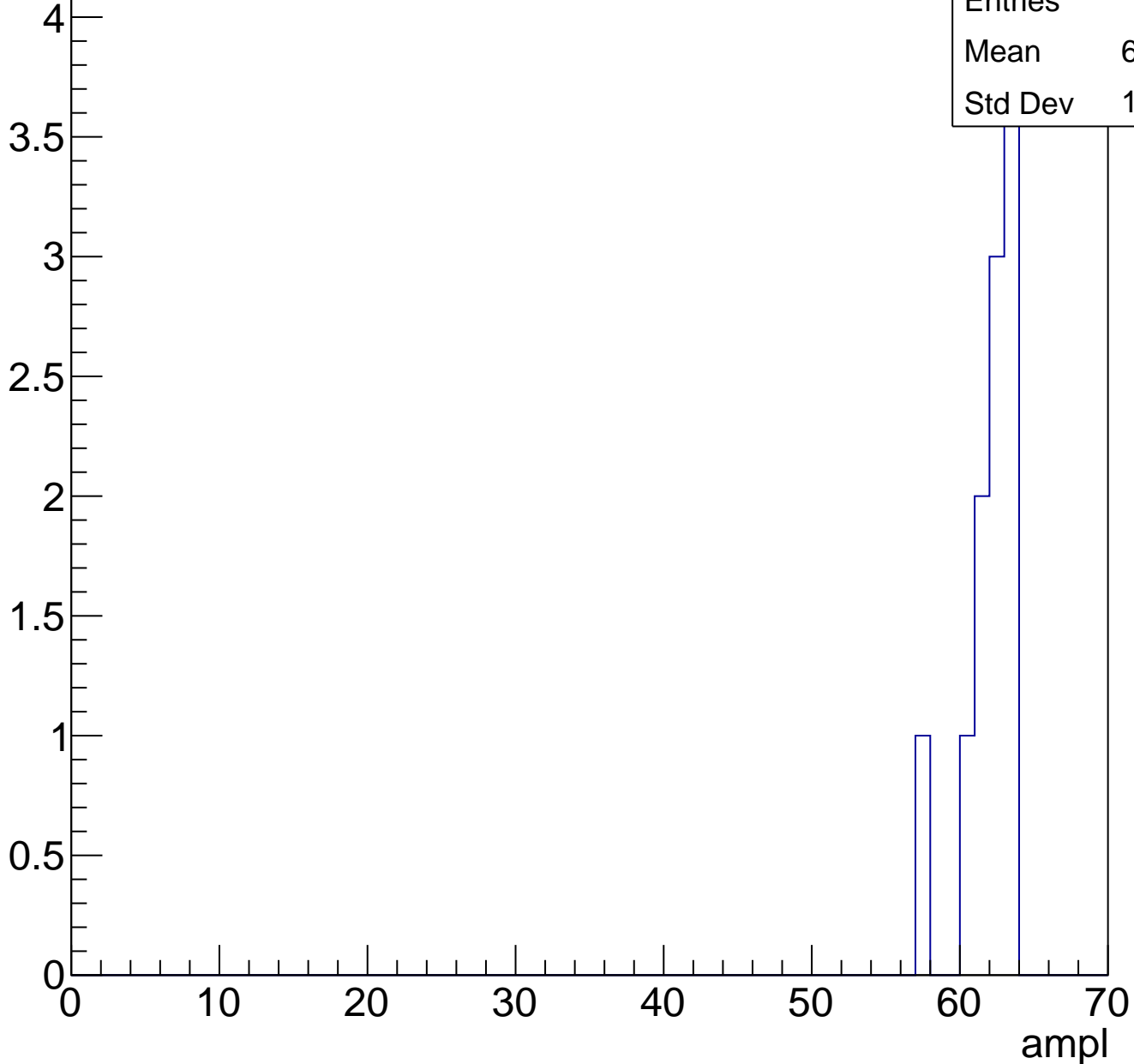
Entries	45
Mean	58.11
Std Dev	9.107



# B0L102S, U17-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch71, adc0

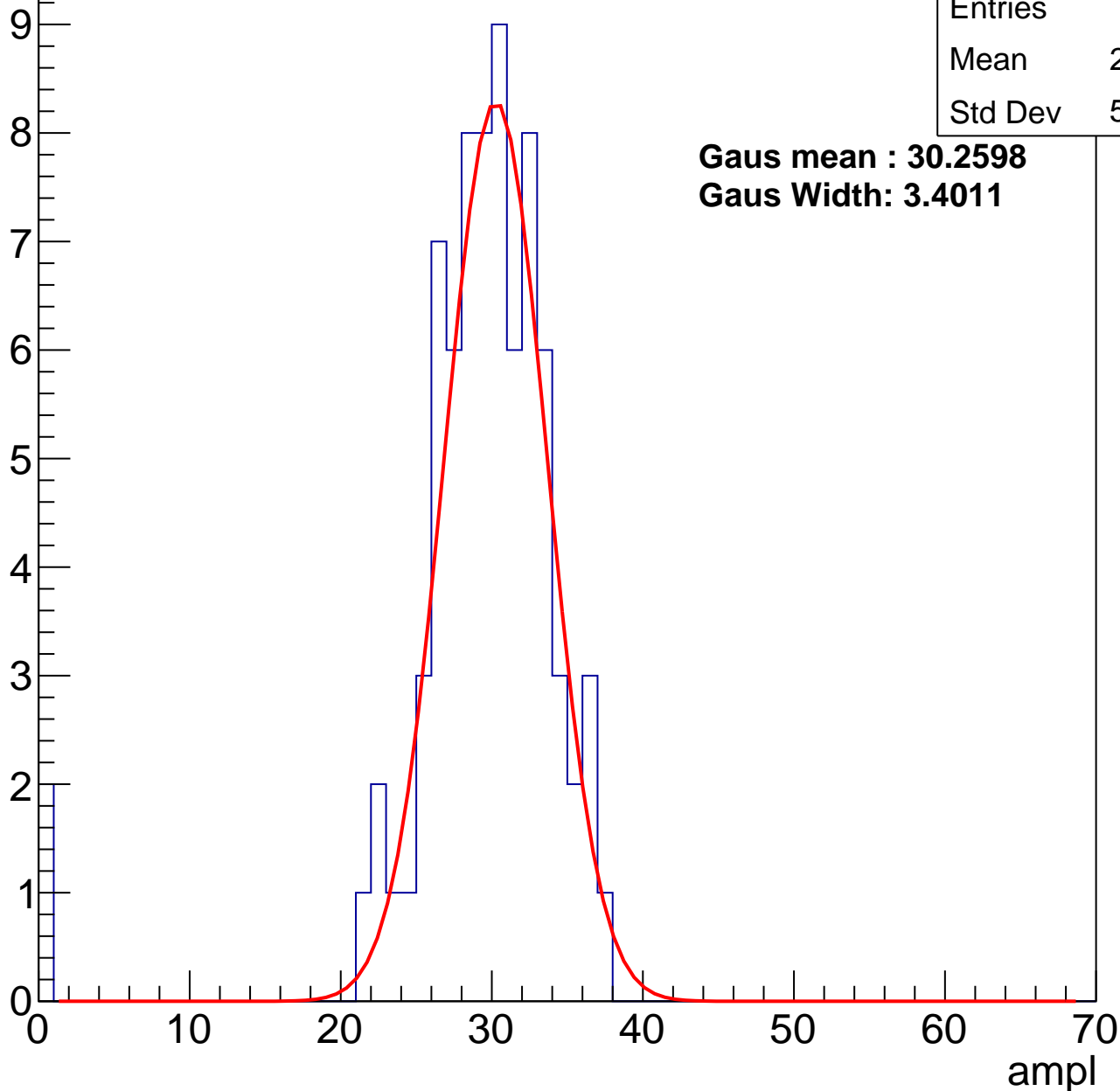
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.75
Std Dev	5.813

**Gaus mean : 30.2598**

**Gaus Width: 3.4011**



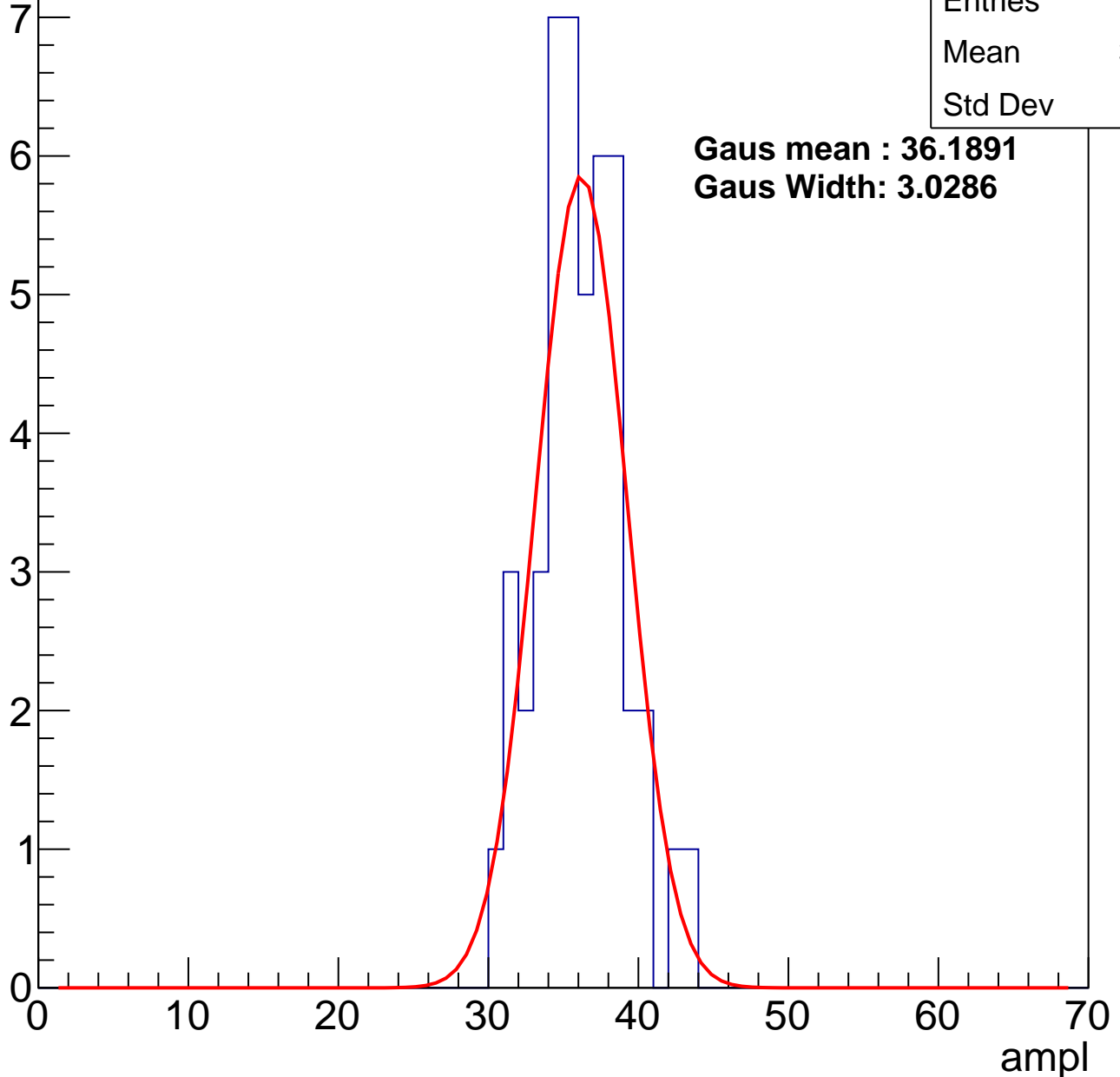
# B0L102S, U17-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	35.7
Std Dev	2.82

**Gaus mean : 36.1891**  
**Gaus Width: 3.0286**



# B0L102S, U17-ch71, adc2

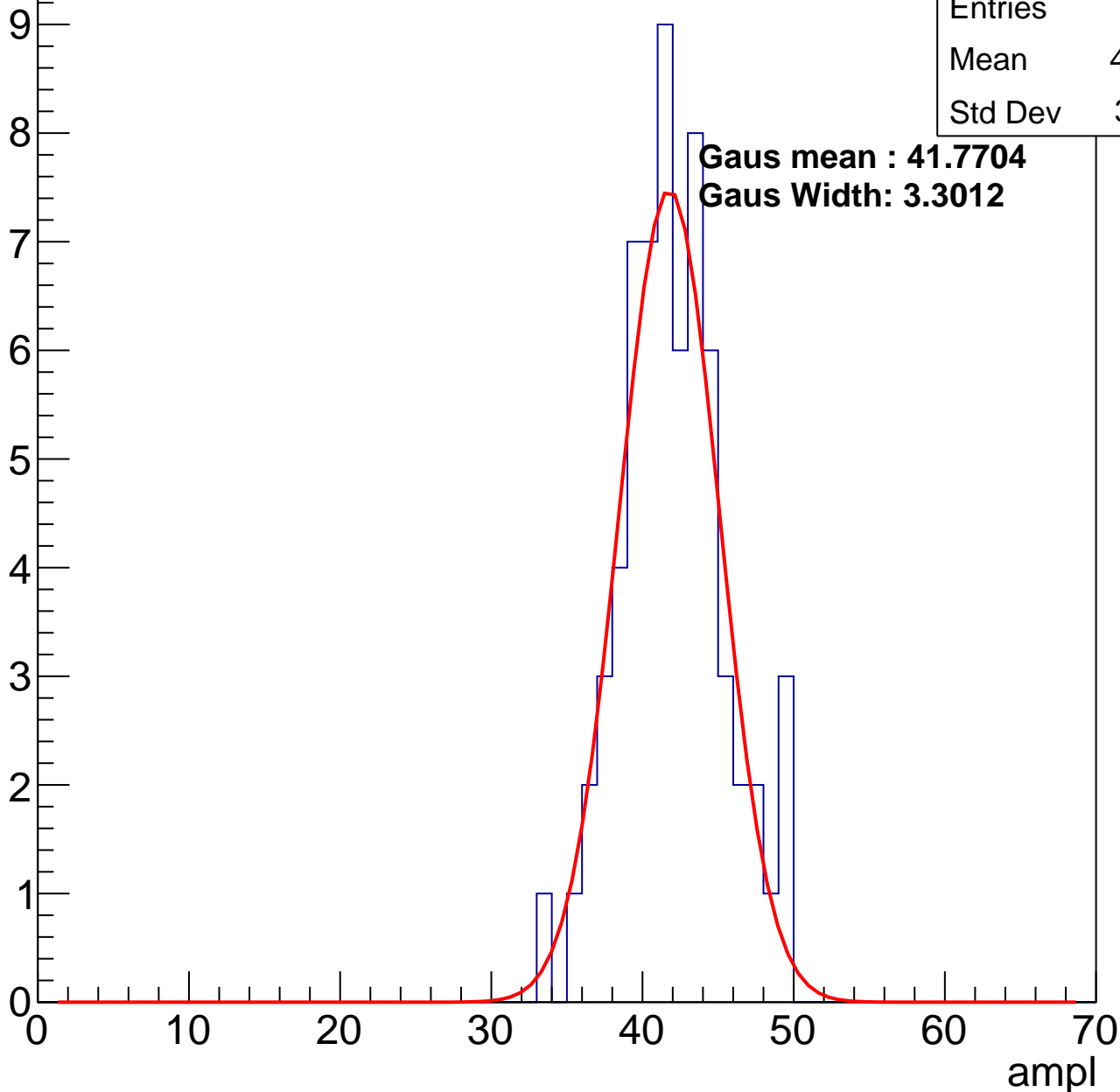
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.55
Std Dev	3.401

**Gaus mean : 41.7704**

**Gaus Width: 3.3012**

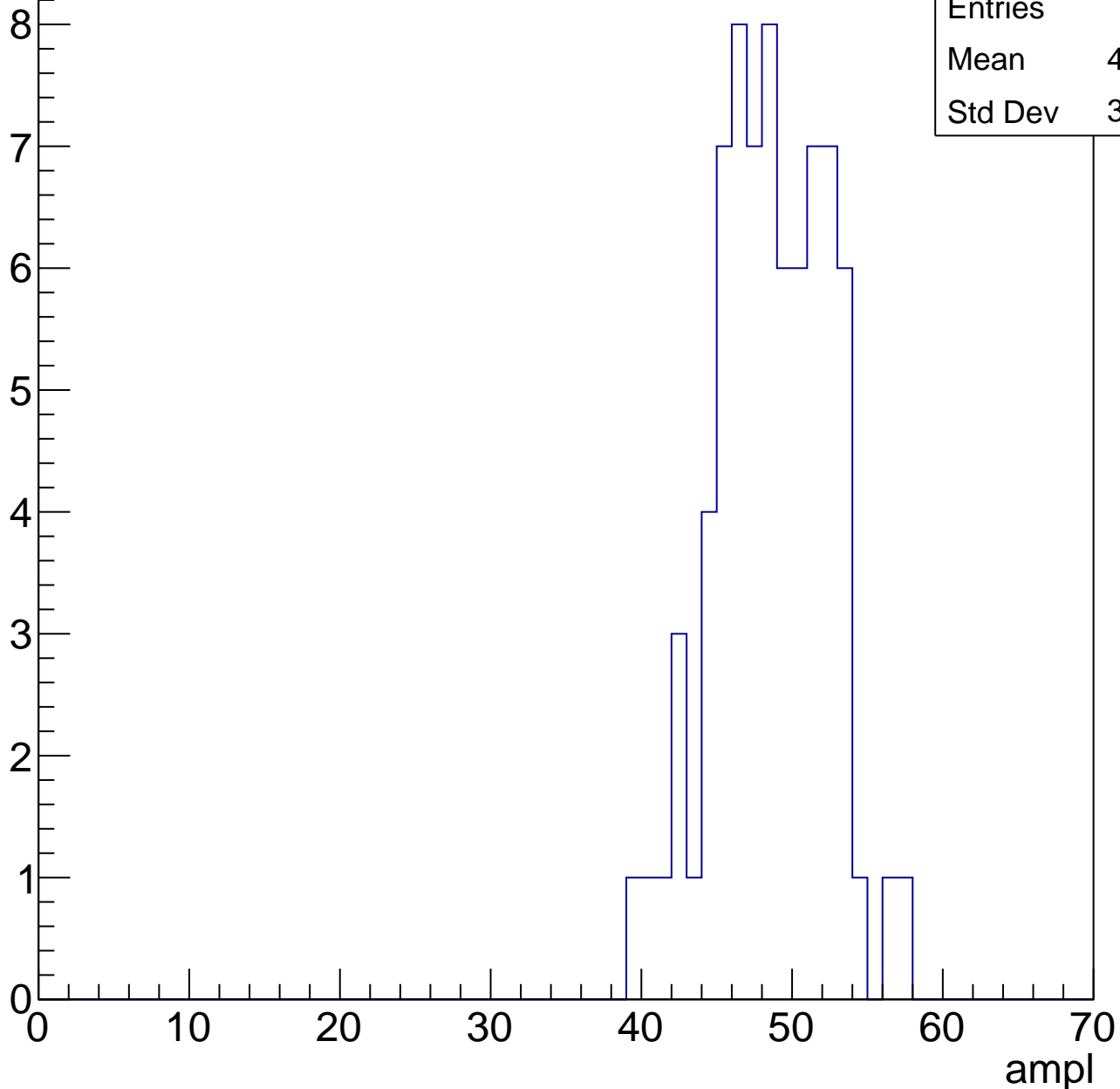


# B0L102S, U17-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	48.17
Std Dev	3.676

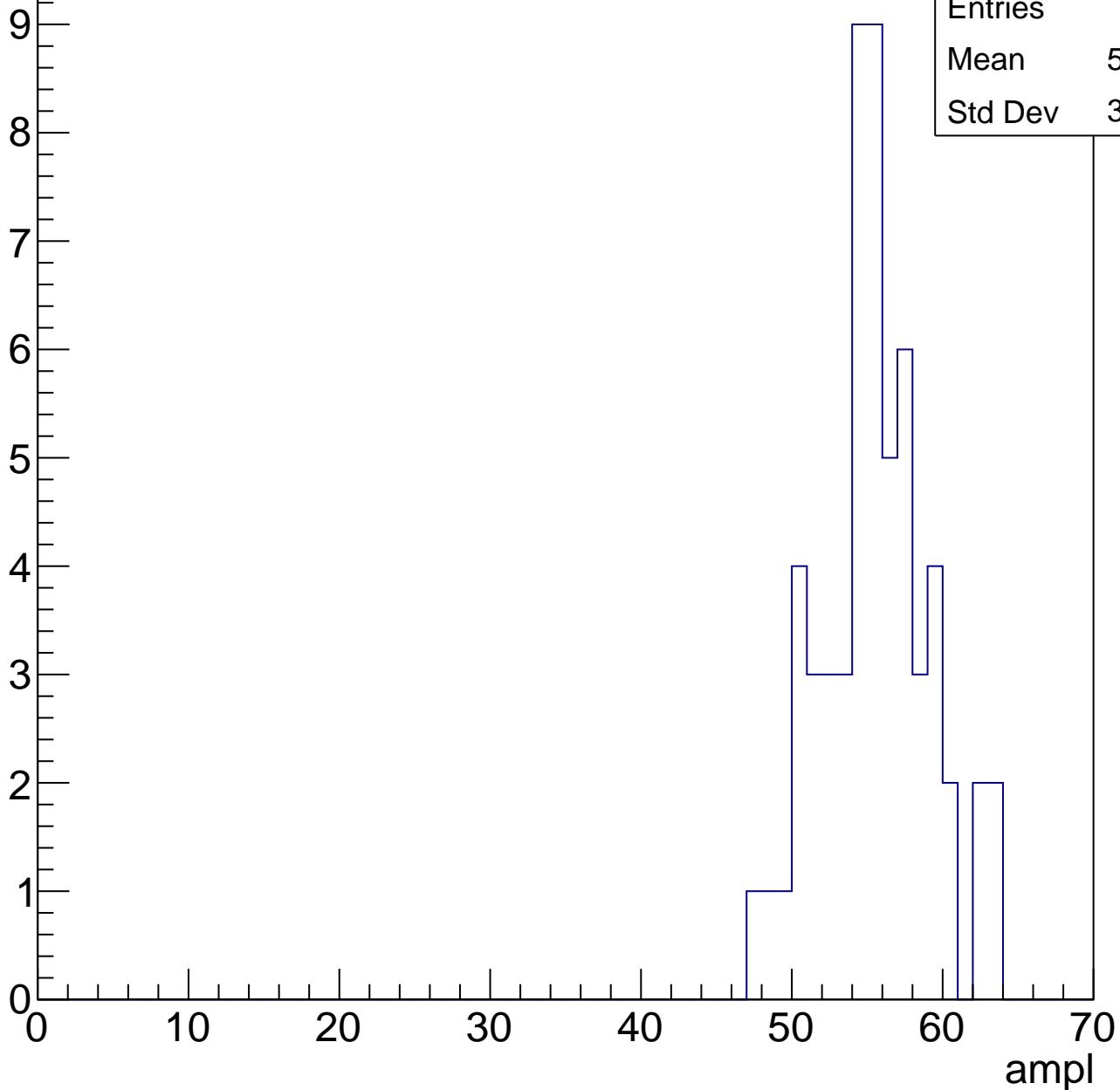


# B0L102S, U17-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	55.09
Std Dev	3.573

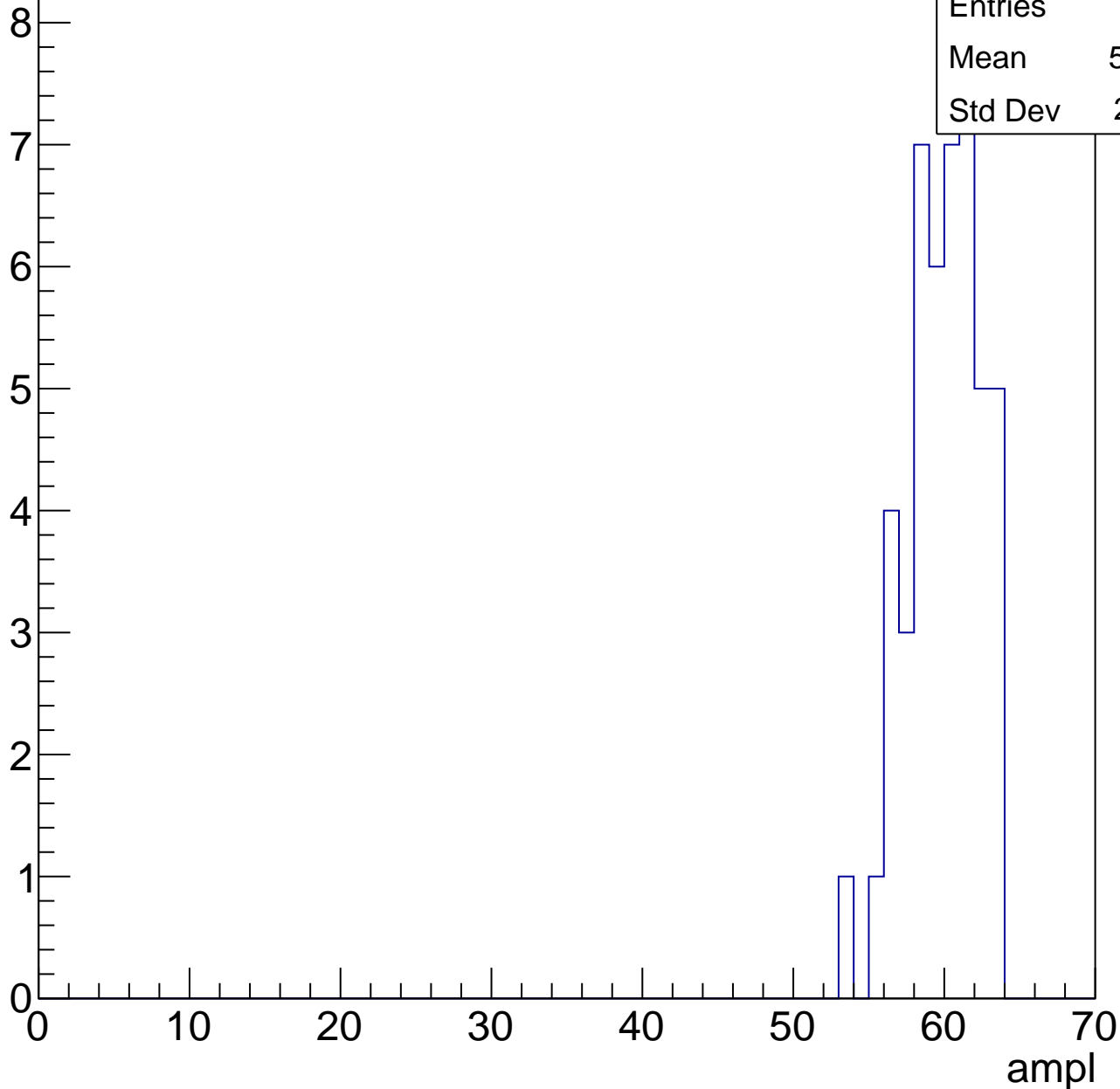


# B0L102S, U17-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

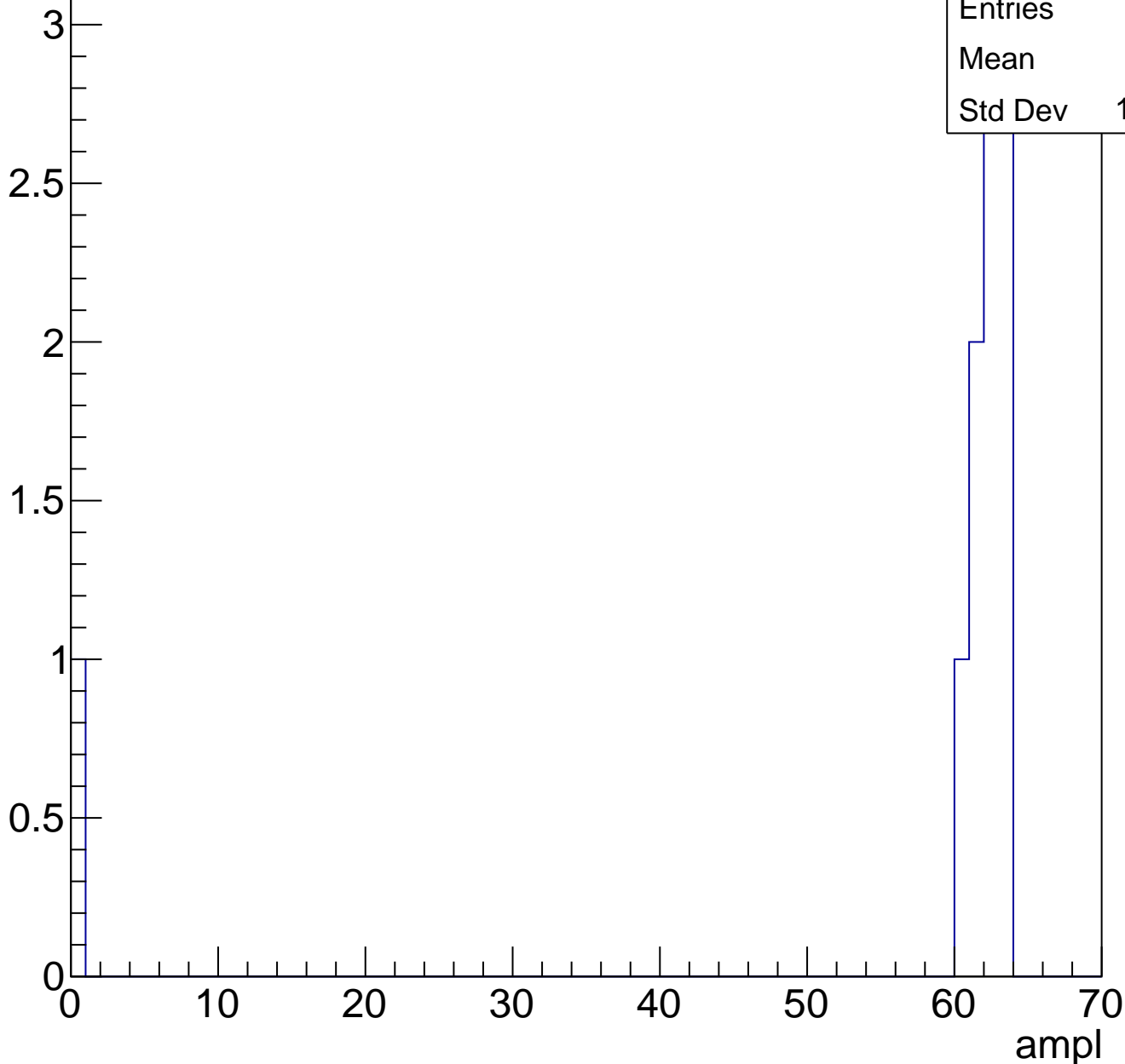
Entries	47
Mean	59.49
Std Dev	2.351



# B0L102S, U17-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



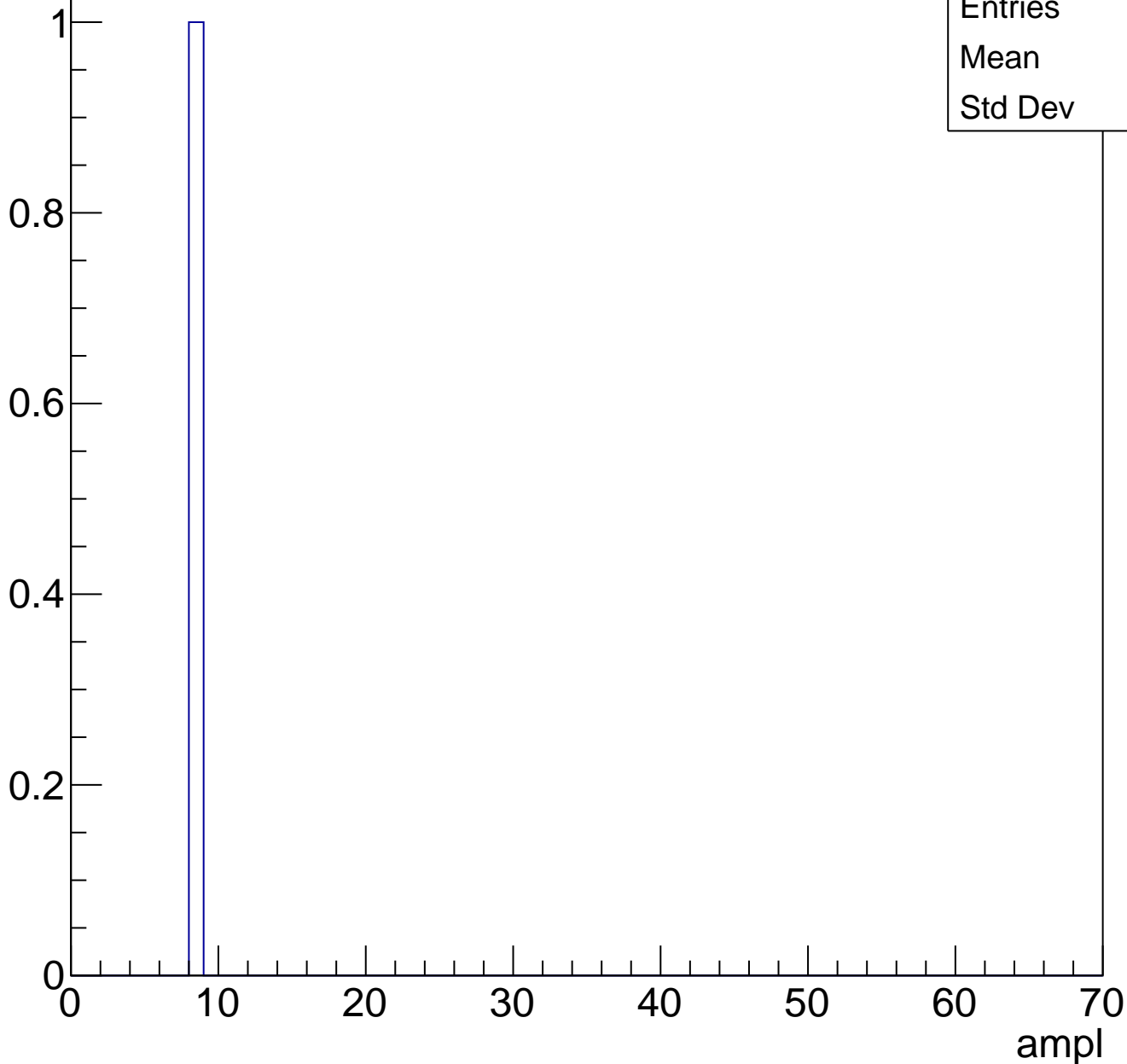
Entries	10
Mean	55.7
Std Dev	18.59



# B0L102S, U17-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch72, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	78
Mean	27.62
Std Dev	3.458

**Gaus mean : 27.8134**

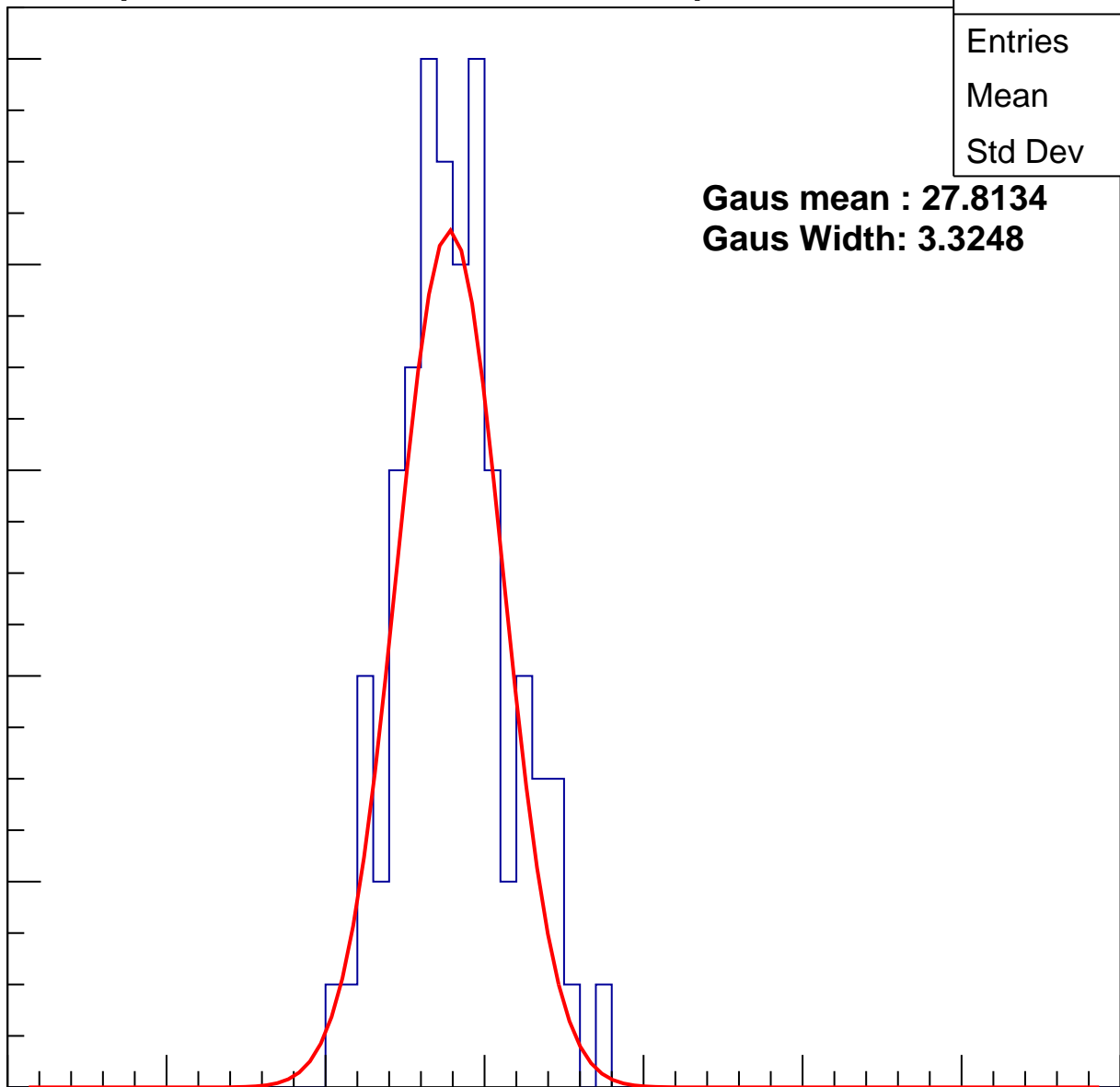
**Gaus Width: 3.3248**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch72, adc1

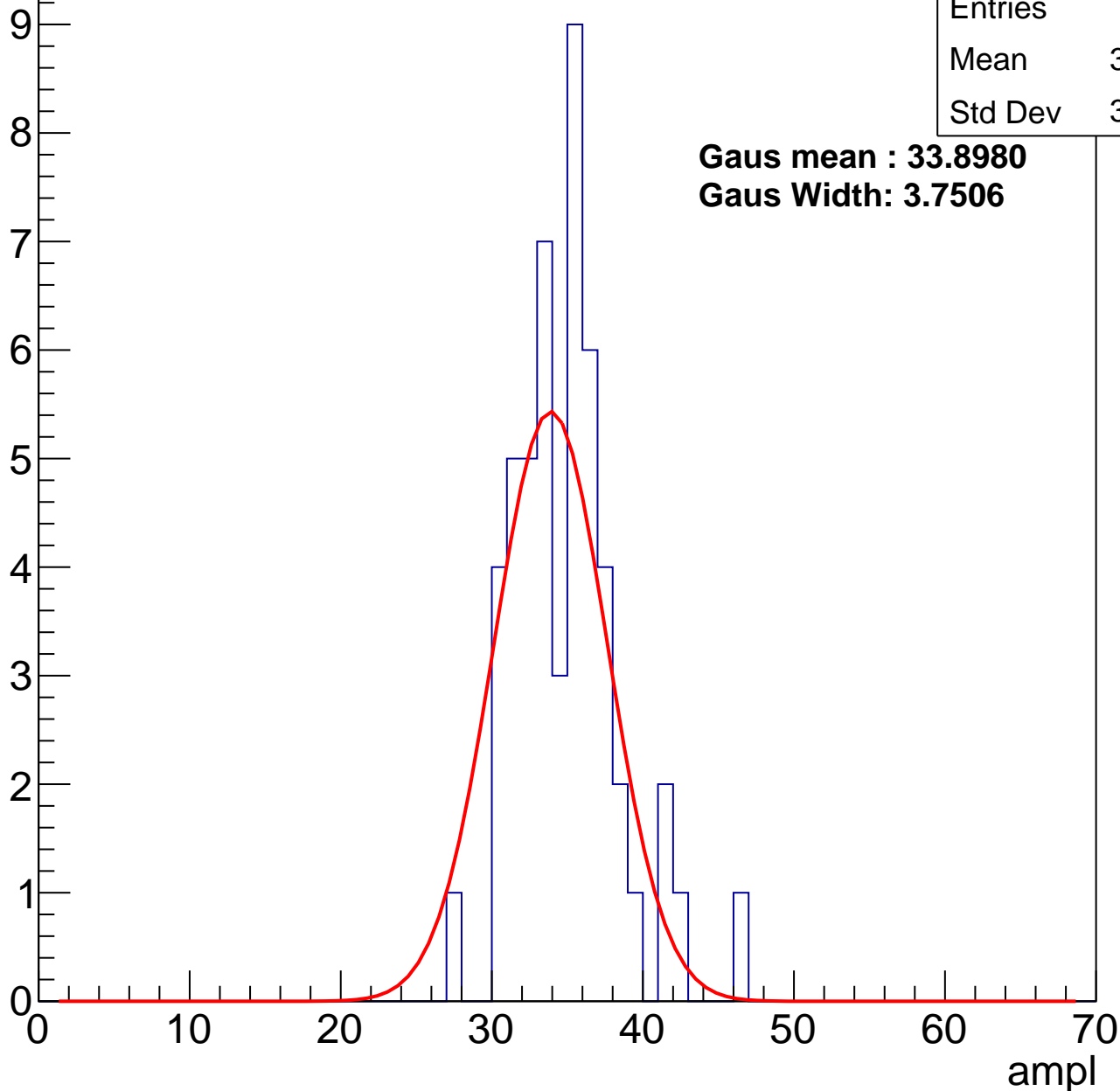
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	34.49
Std Dev	3.444

**Gaus mean : 33.8980**

**Gaus Width: 3.7506**



# B0L102S, U17-ch72, adc2

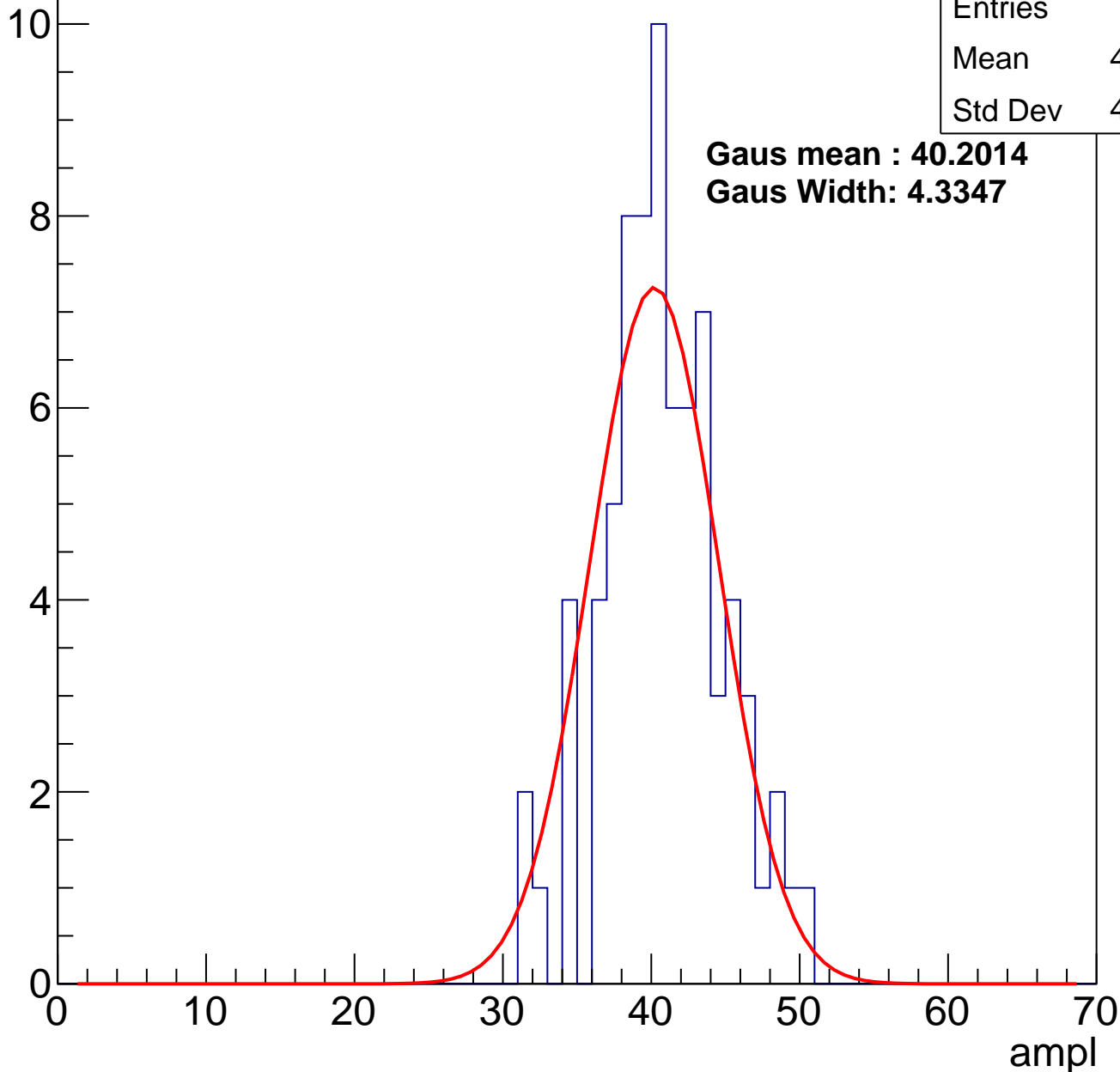
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	76
Mean	40.34
Std Dev	4.002

**Gaus mean : 40.2014**

**Gaus Width: 4.3347**

Entry

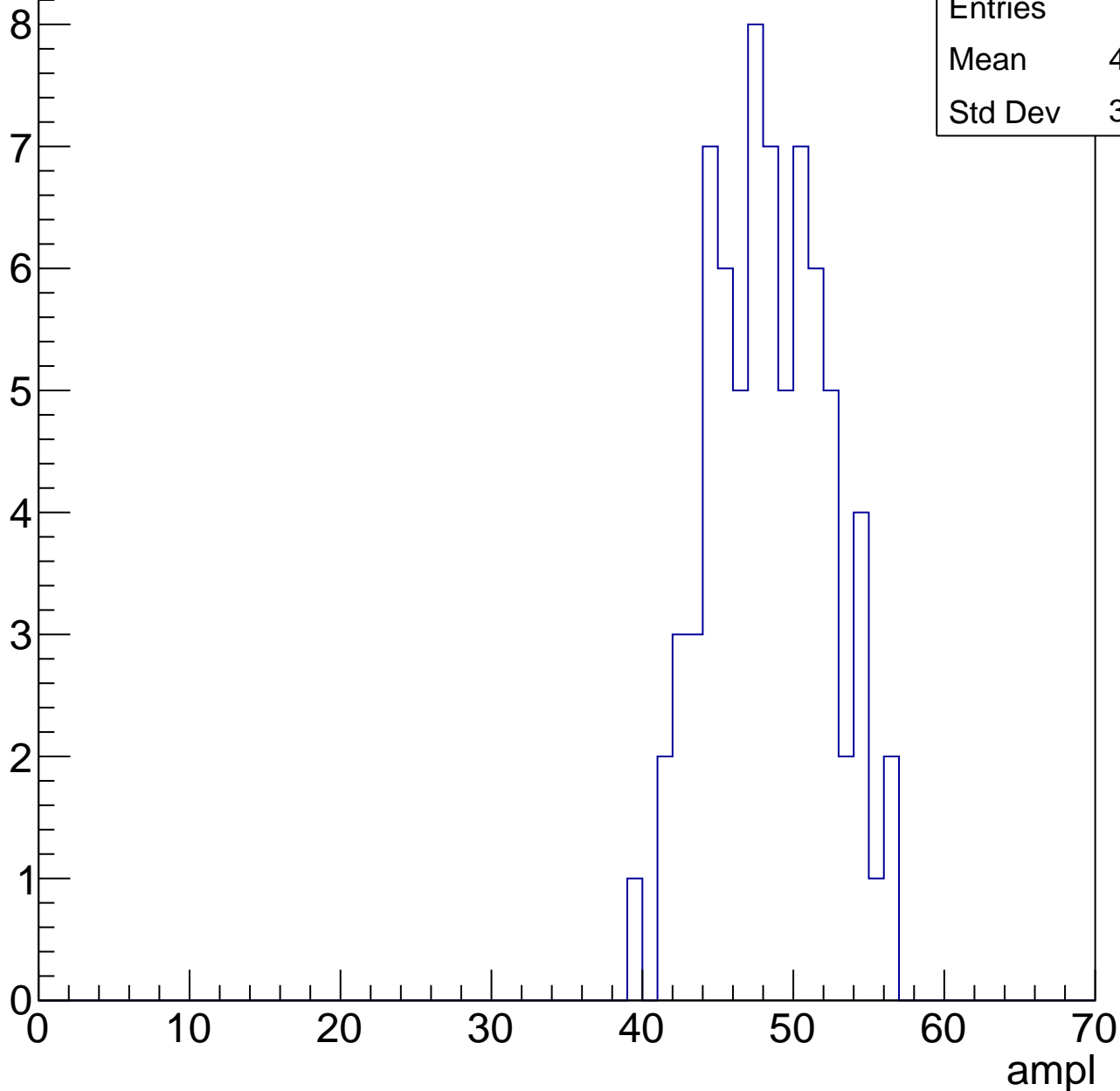


# B0L102S, U17-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	47.92
Std Dev	3.844

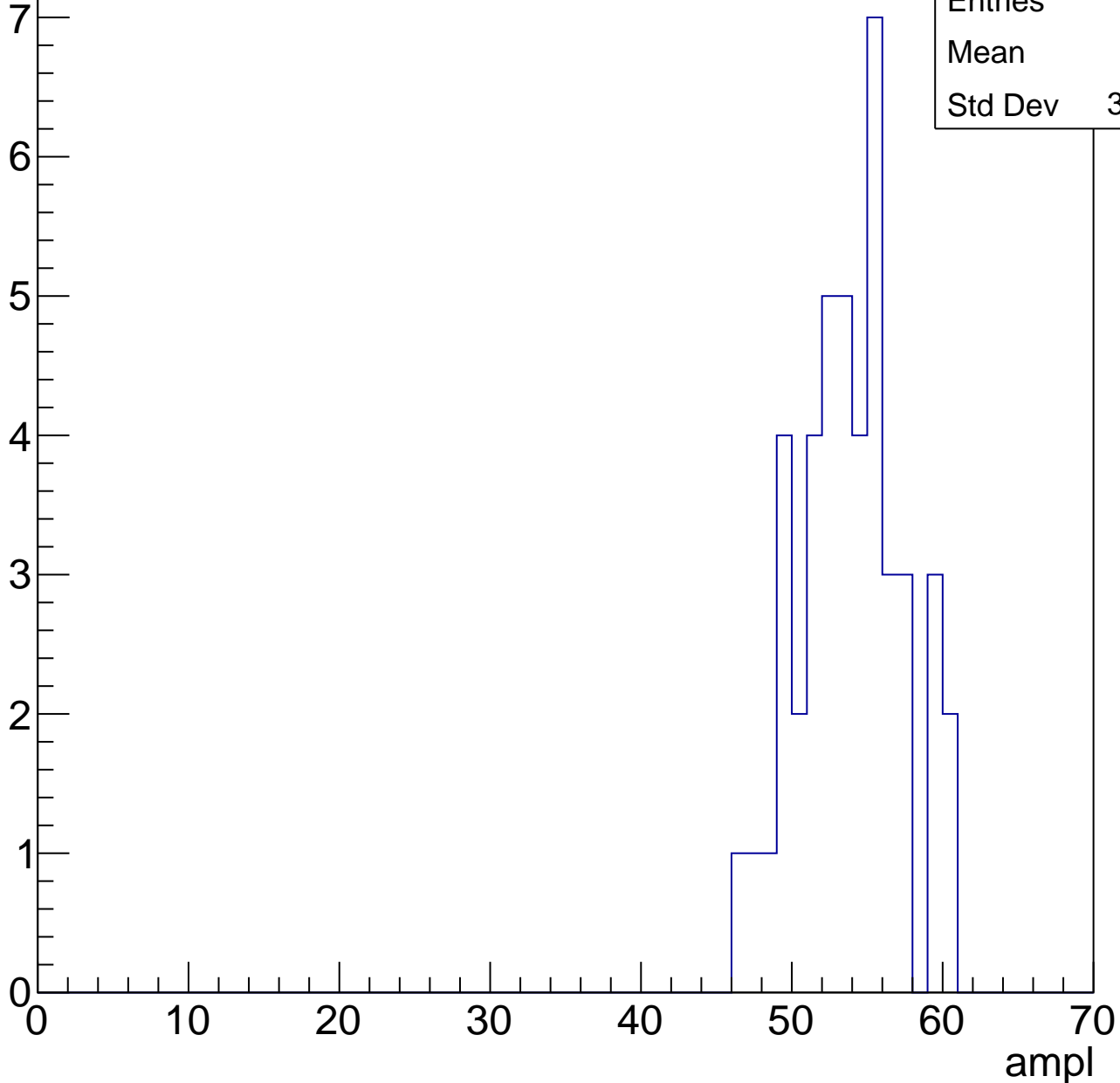


# B0L102S, U17-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	53.4
Std Dev	3.395

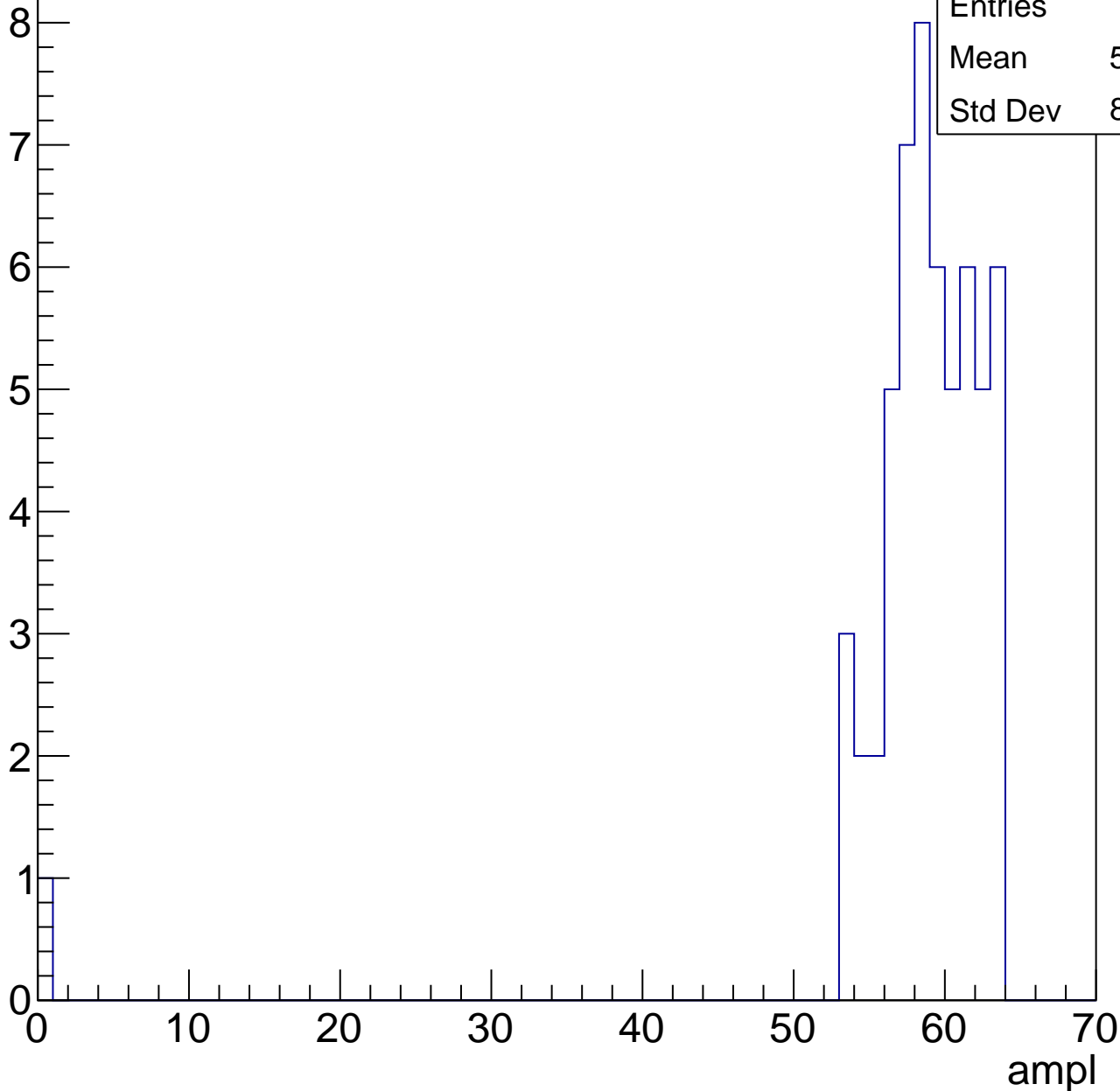


# B0L102S, U17-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	57.64
Std Dev	8.258

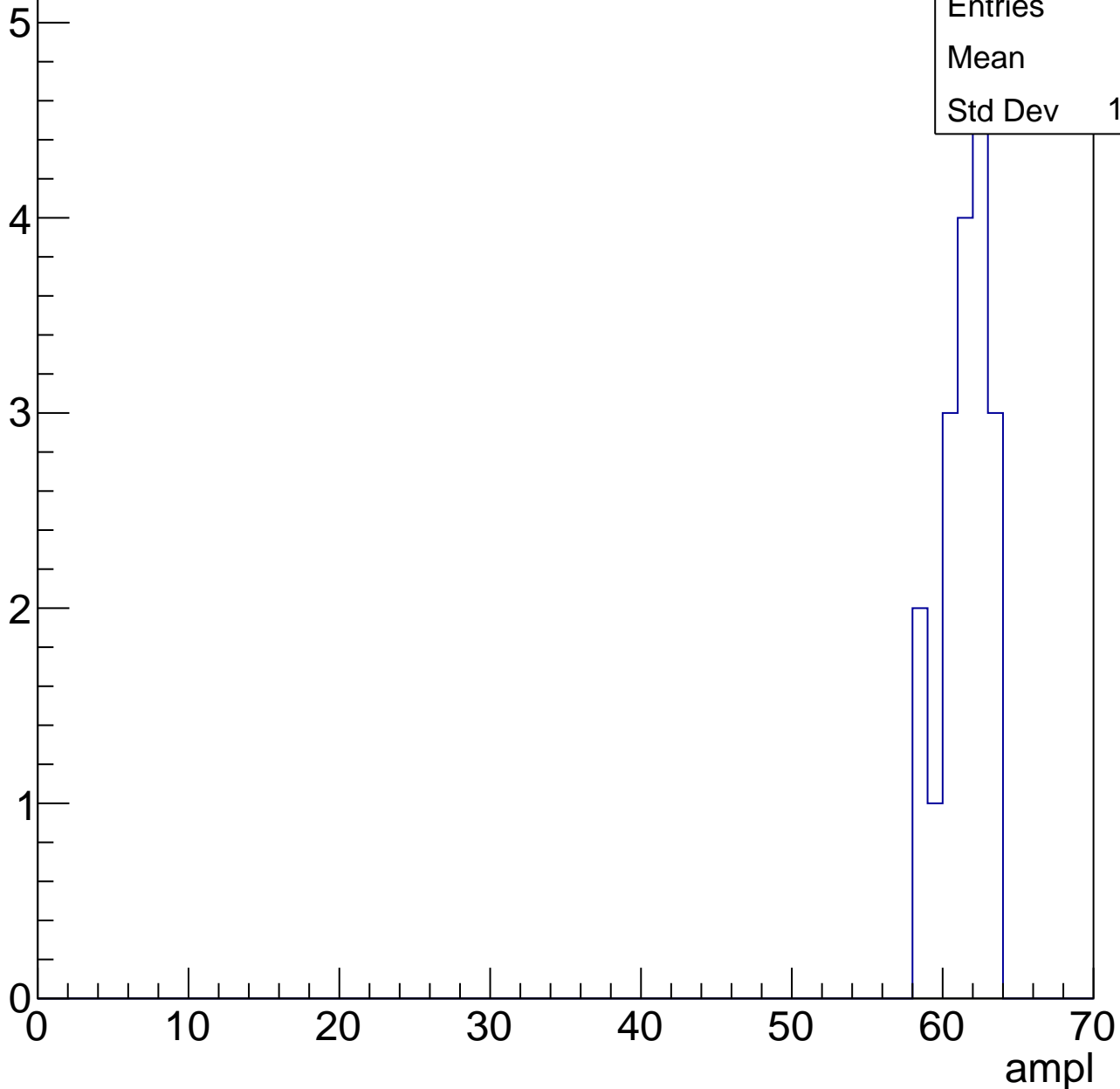


# B0L102S, U17-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	18
Mean	61
Std Dev	1.528

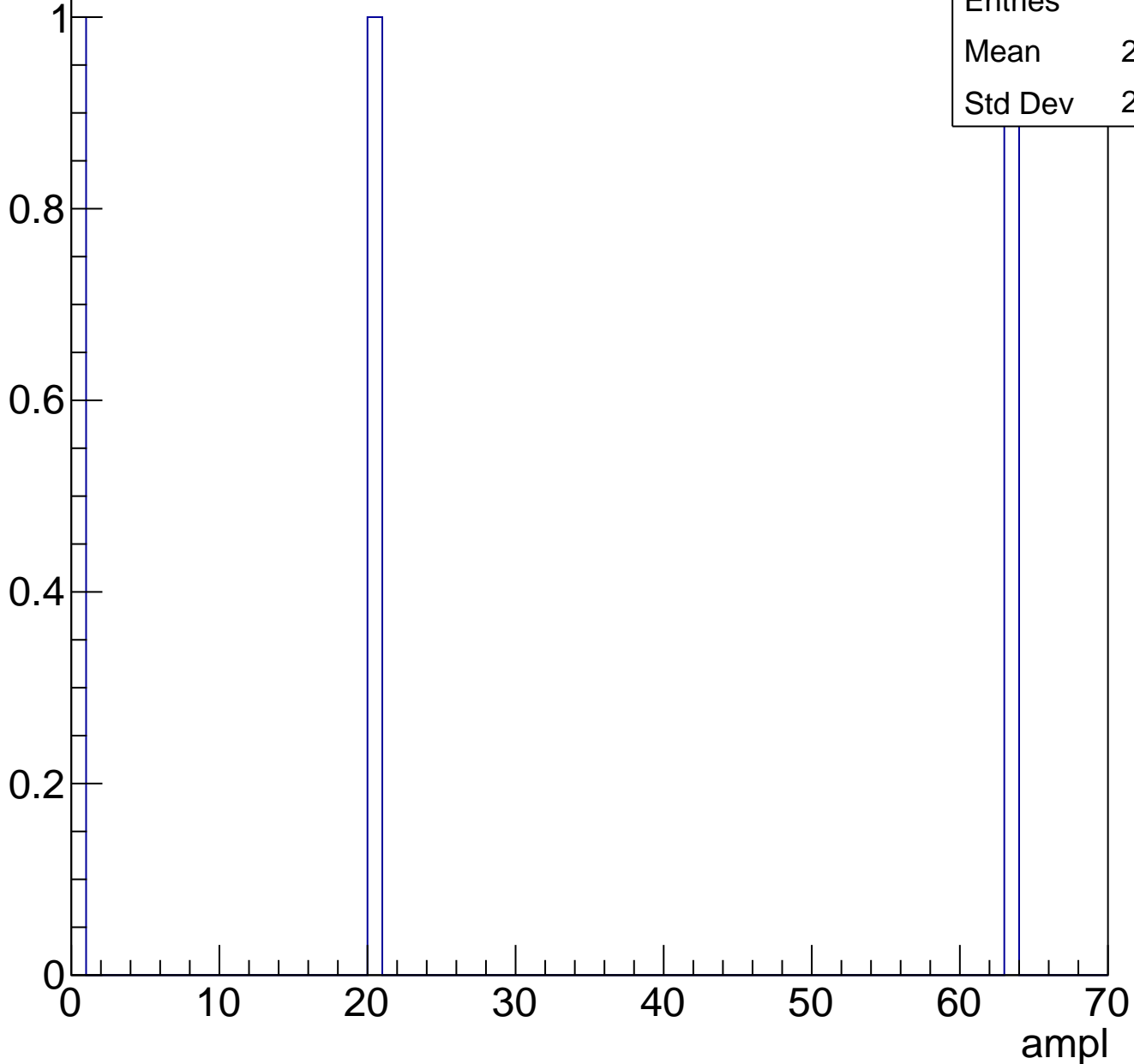




# B0L102S, U17-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	27.67
Std Dev	26.28

# B0L102S, U17-ch73, adc0

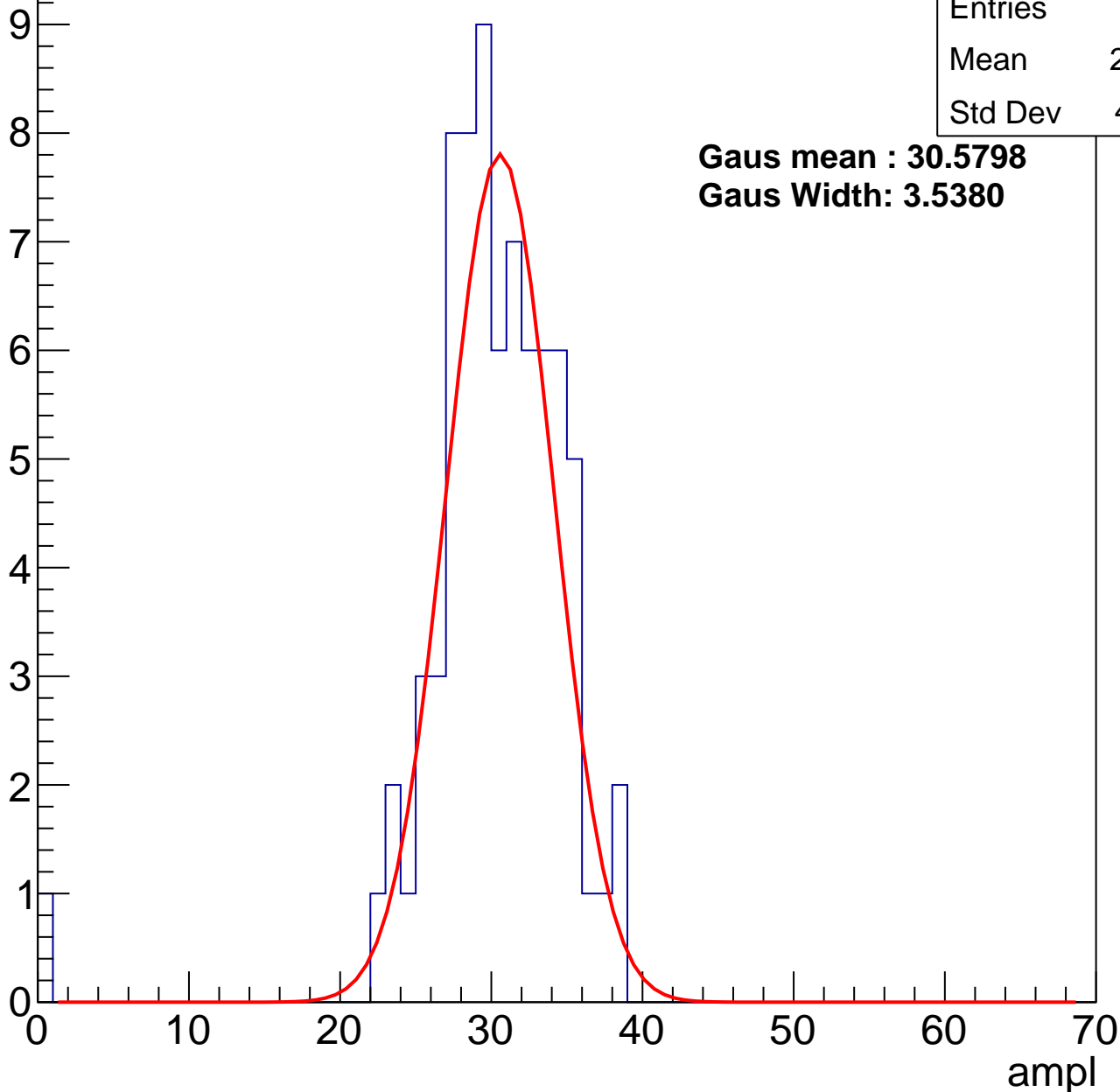
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	29.75
Std Dev	4.931

**Gaus mean : 30.5798**

**Gaus Width: 3.5380**



# B0L102S, U17-ch73, adc1

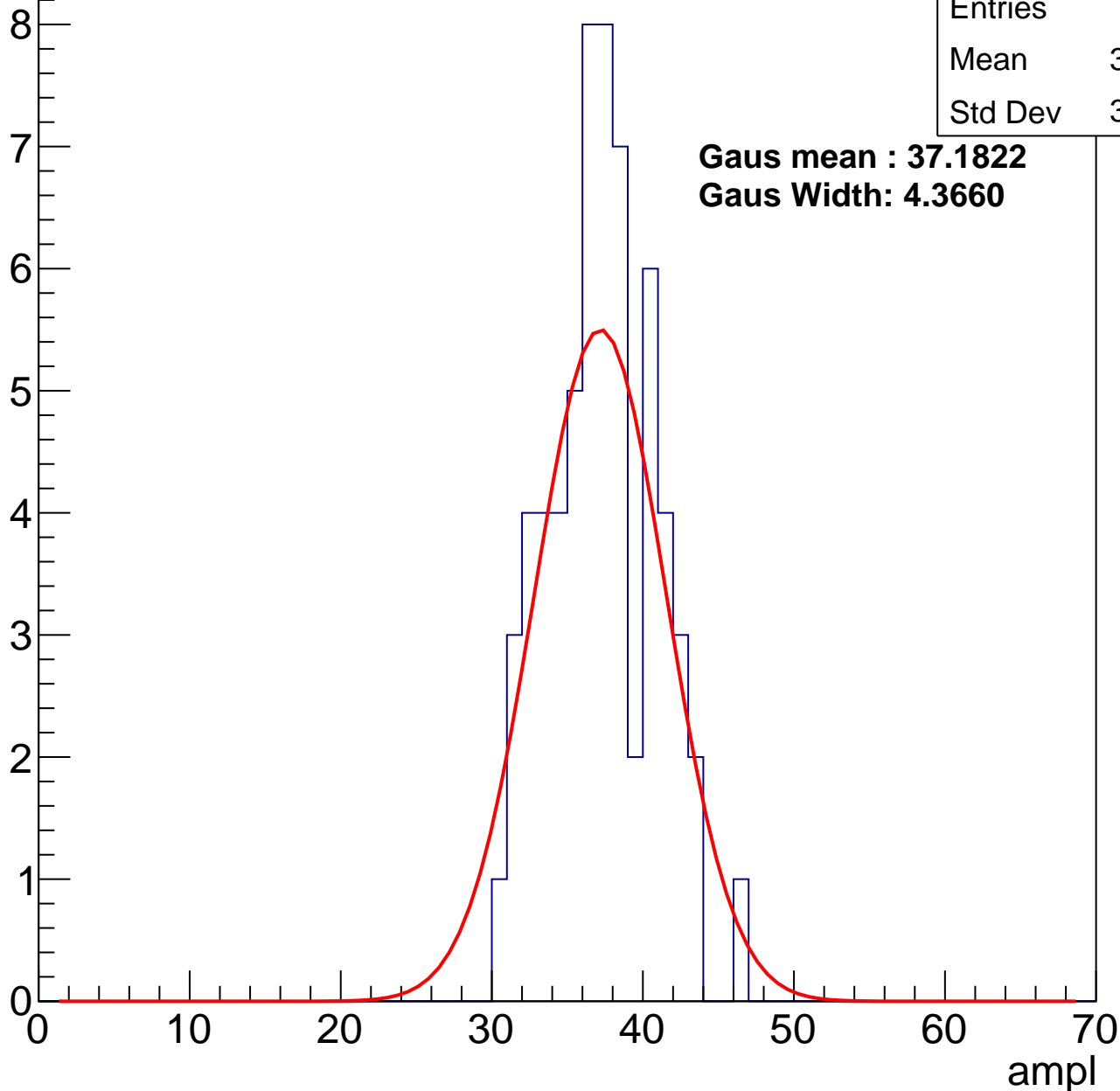
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	36.84
Std Dev	3.465

**Gaus mean : 37.1822**

**Gaus Width: 4.3660**



# B0L102S, U17-ch73, adc2

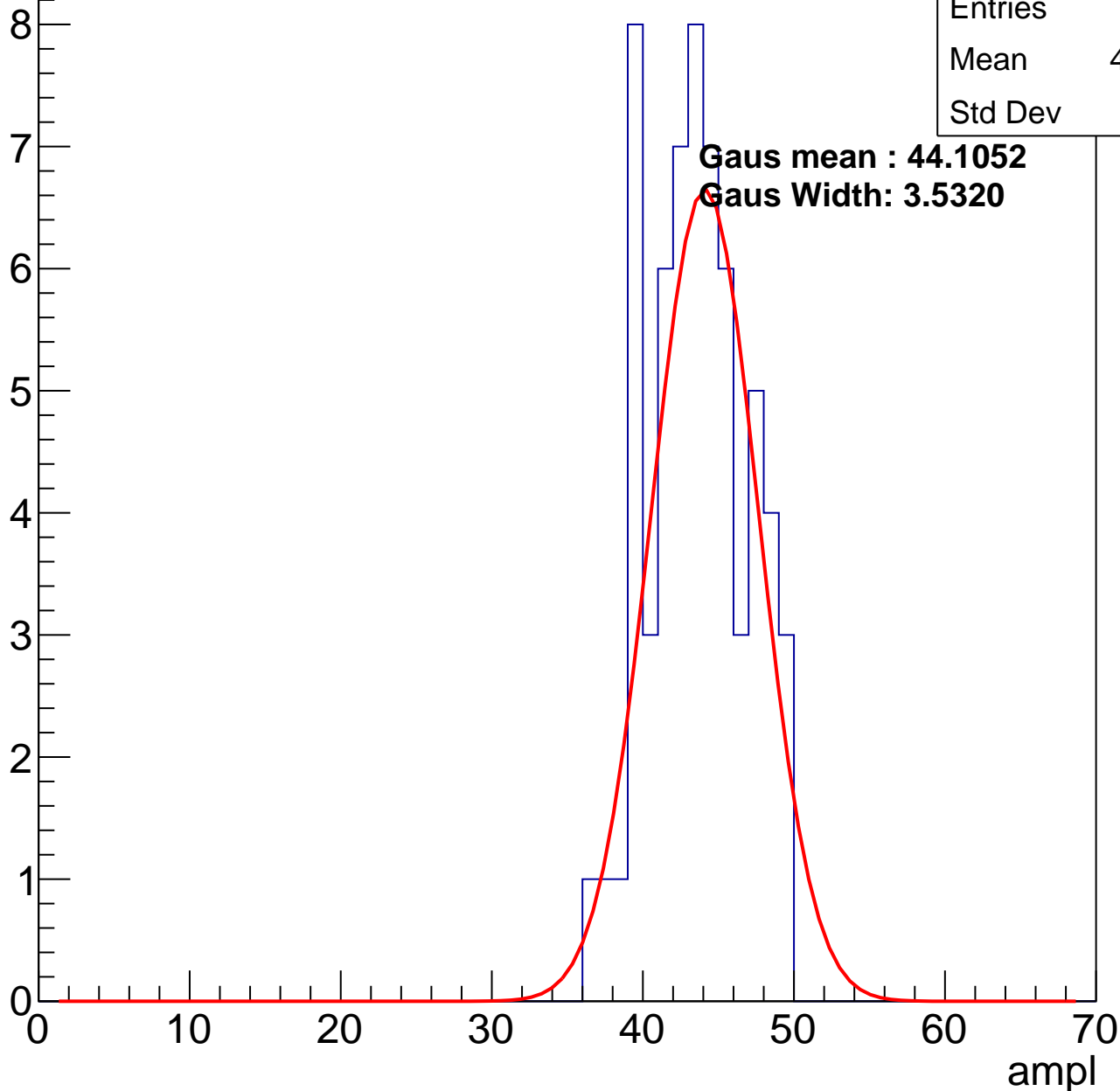
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.13
Std Dev	3.19

**Gaus mean : 44.1052**

**Gaus Width: 3.5320**



# B0L102S, U17-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

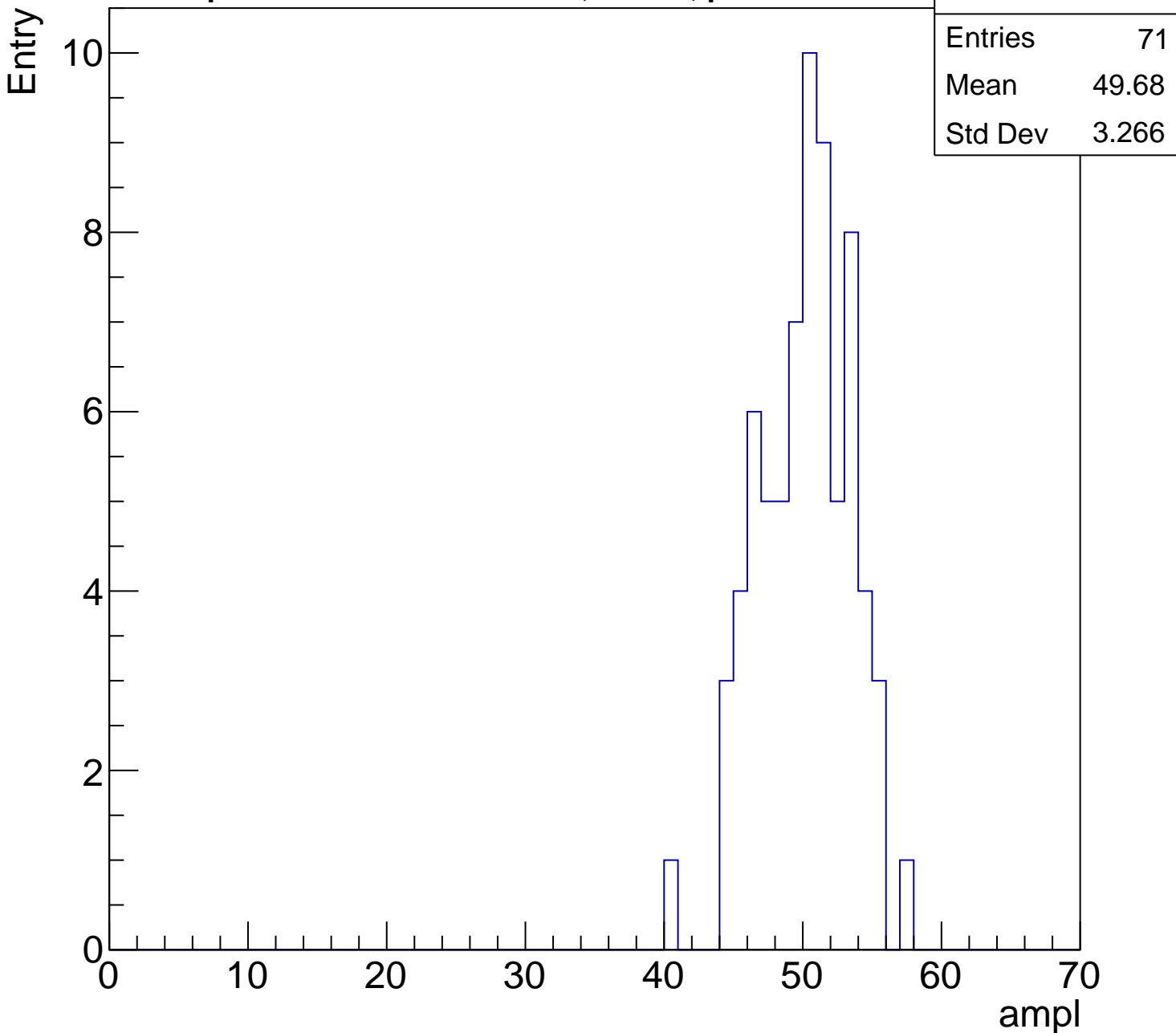
Entries	71
Mean	49.68
Std Dev	3.266

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

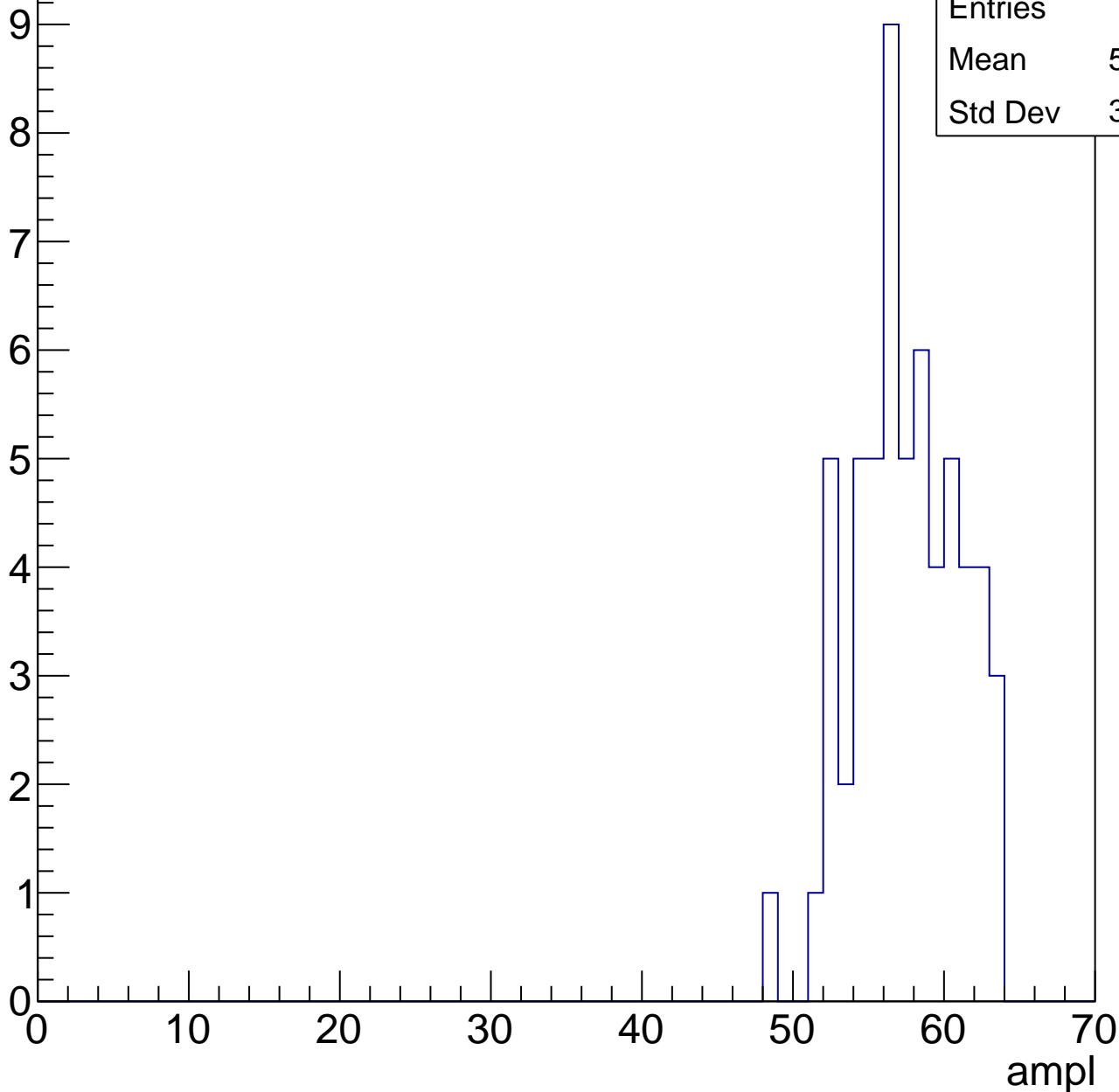


# B0L102S, U17-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

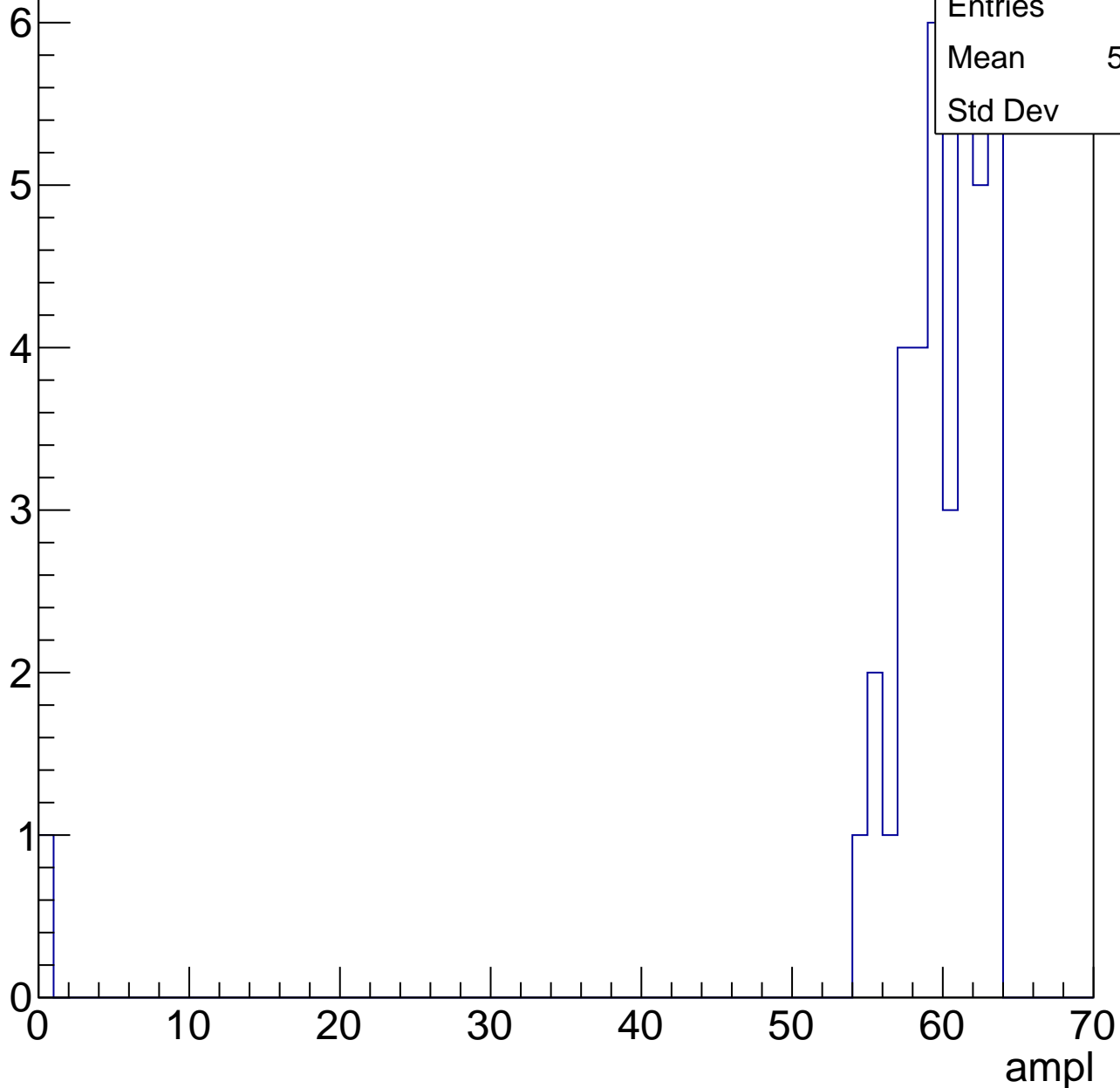
Entries	59
Mean	57.02
Std Dev	3.412



# B0L102S, U17-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

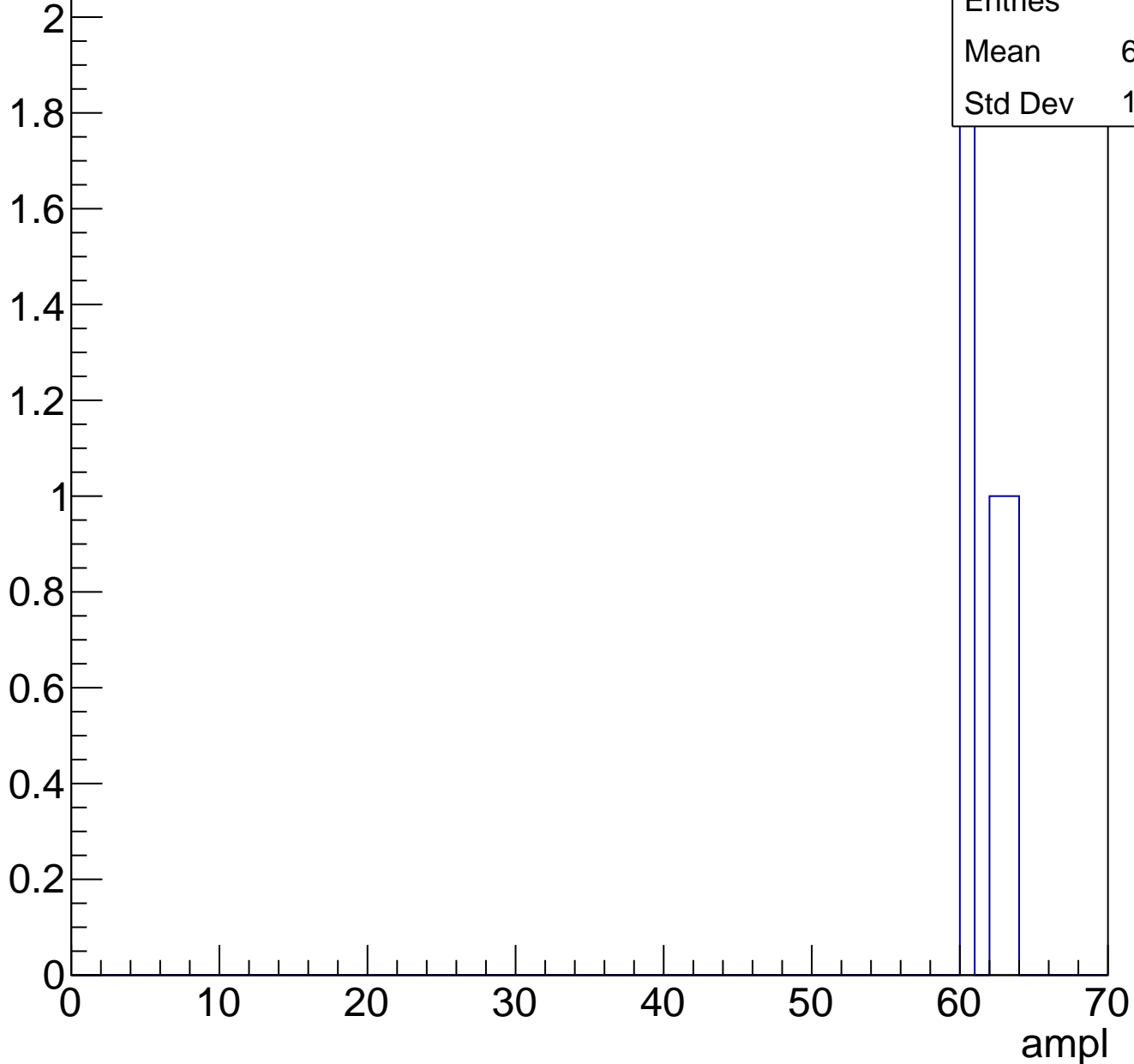
Entry



# B0L102S, U17-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L102S, U17-ch74, adc0

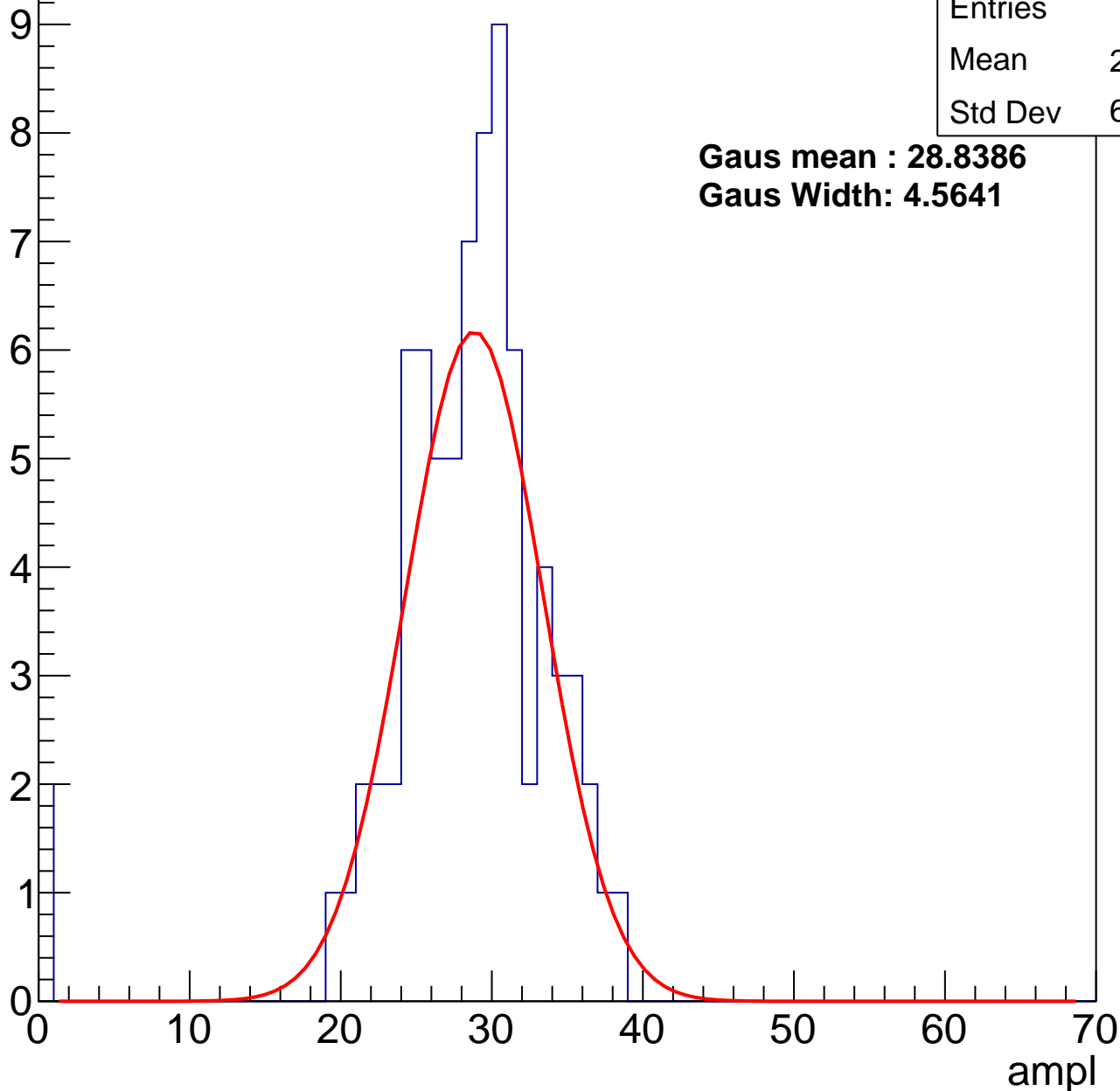
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	27.74
Std Dev	6.094

**Gaus mean : 28.8386**

**Gaus Width: 4.5641**



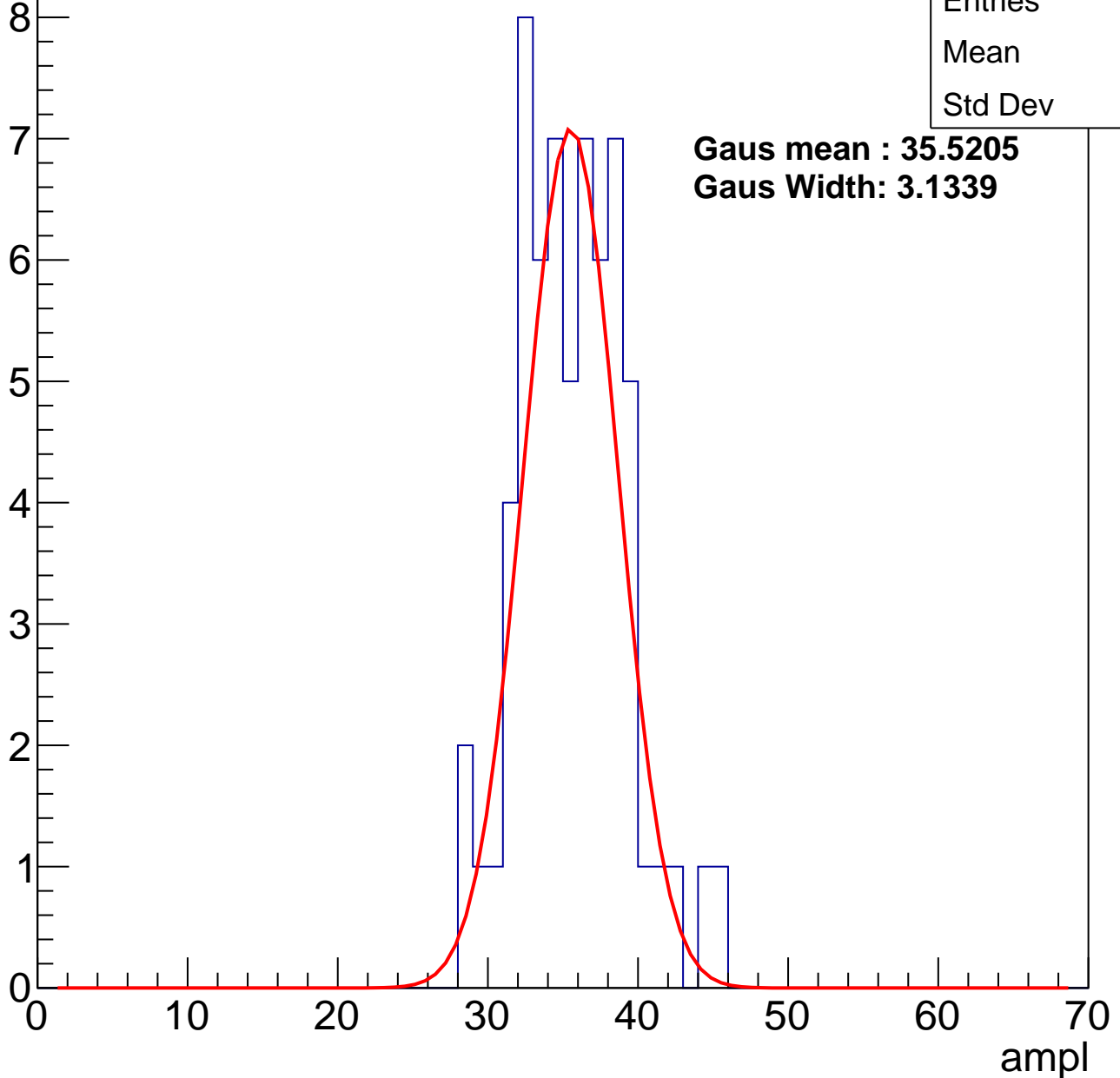
# B0L102S, U17-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.2
Std Dev	3.51

**Gaus mean : 35.5205**  
**Gaus Width: 3.1339**



# B0L102S, U17-ch74, adc2

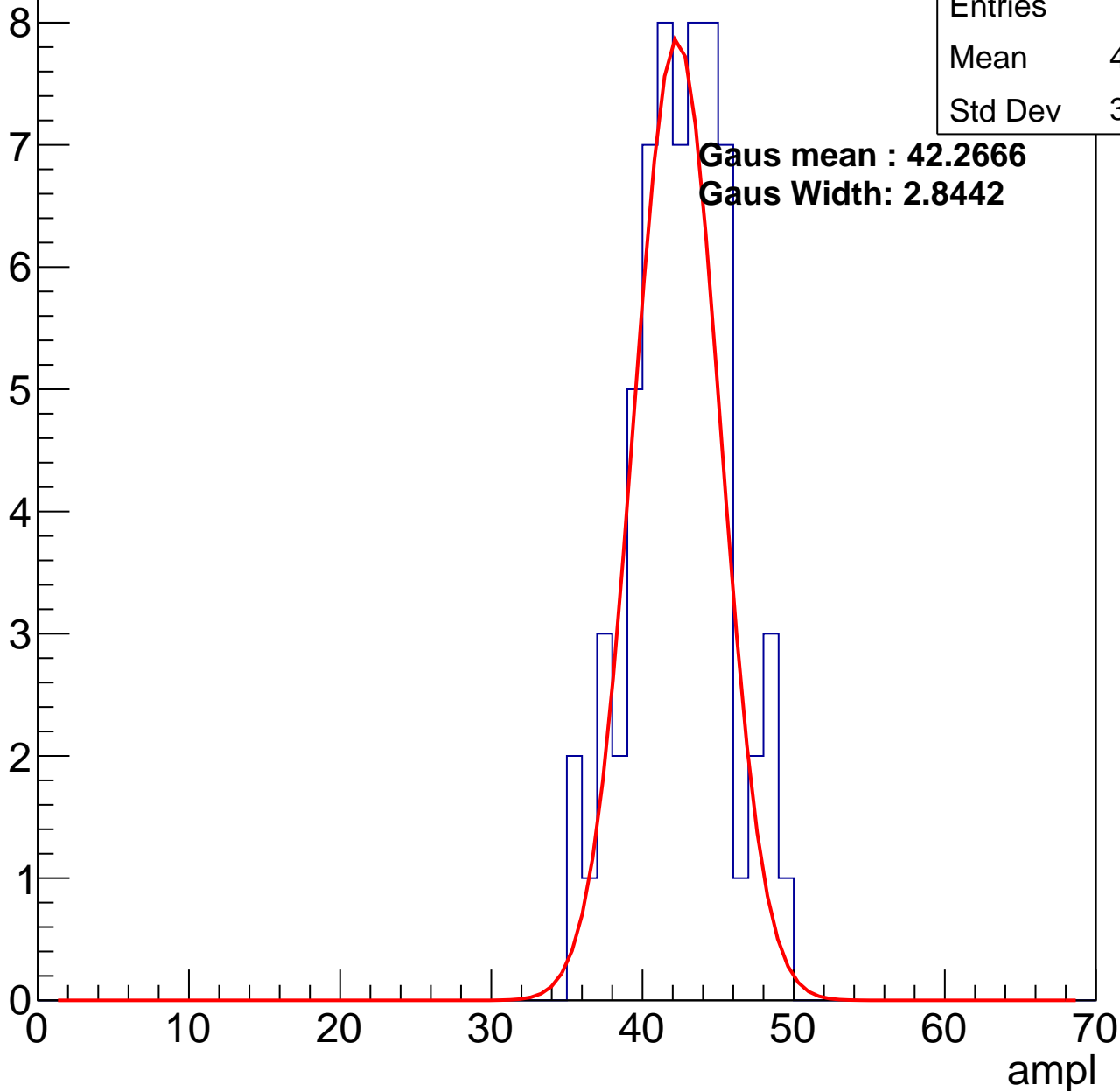
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.06
Std Dev	3.157

**Gaus mean : 42.2666**

**Gaus Width: 2.8442**

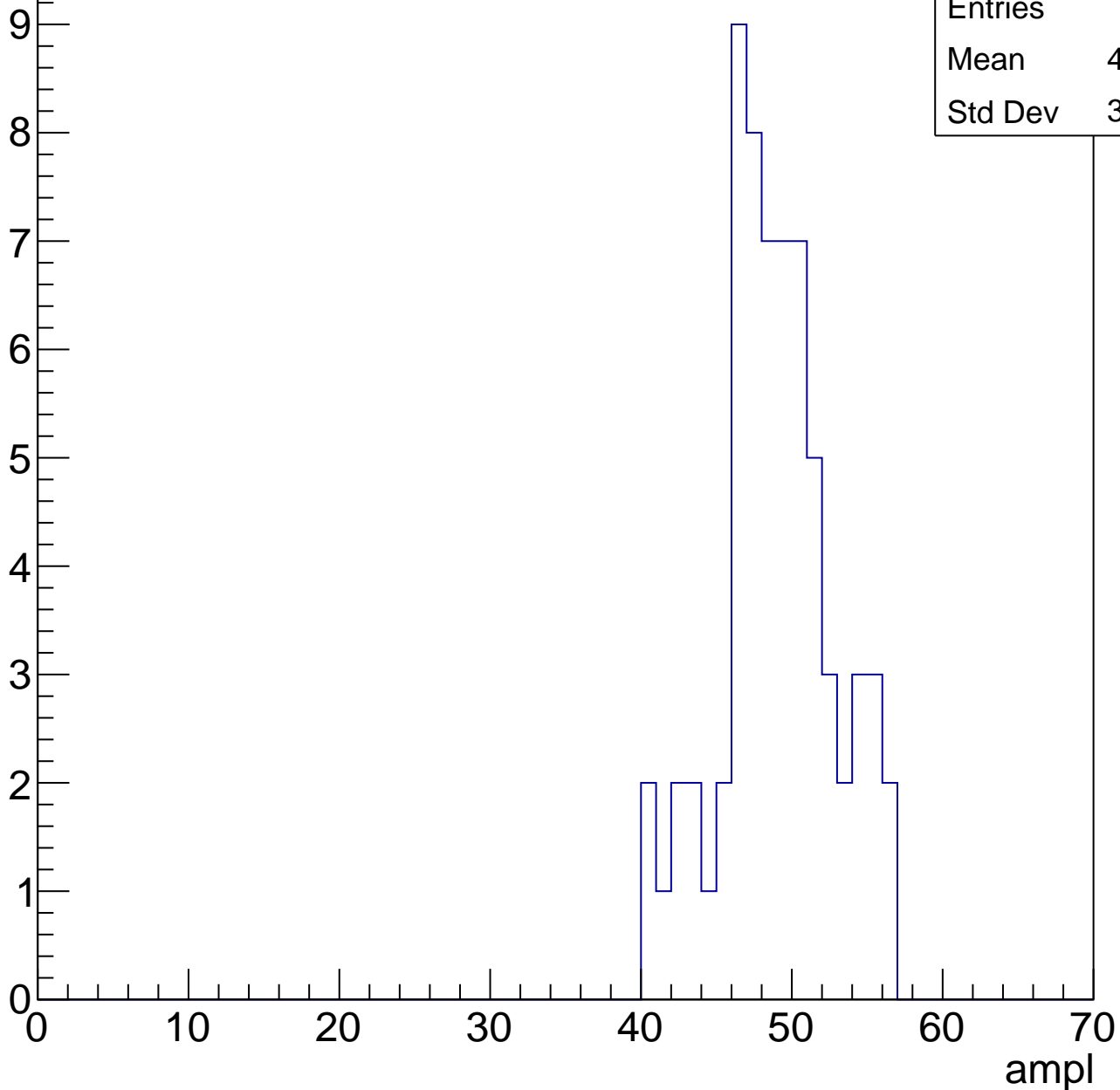


# B0L102S, U17-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.48
Std Dev	3.739

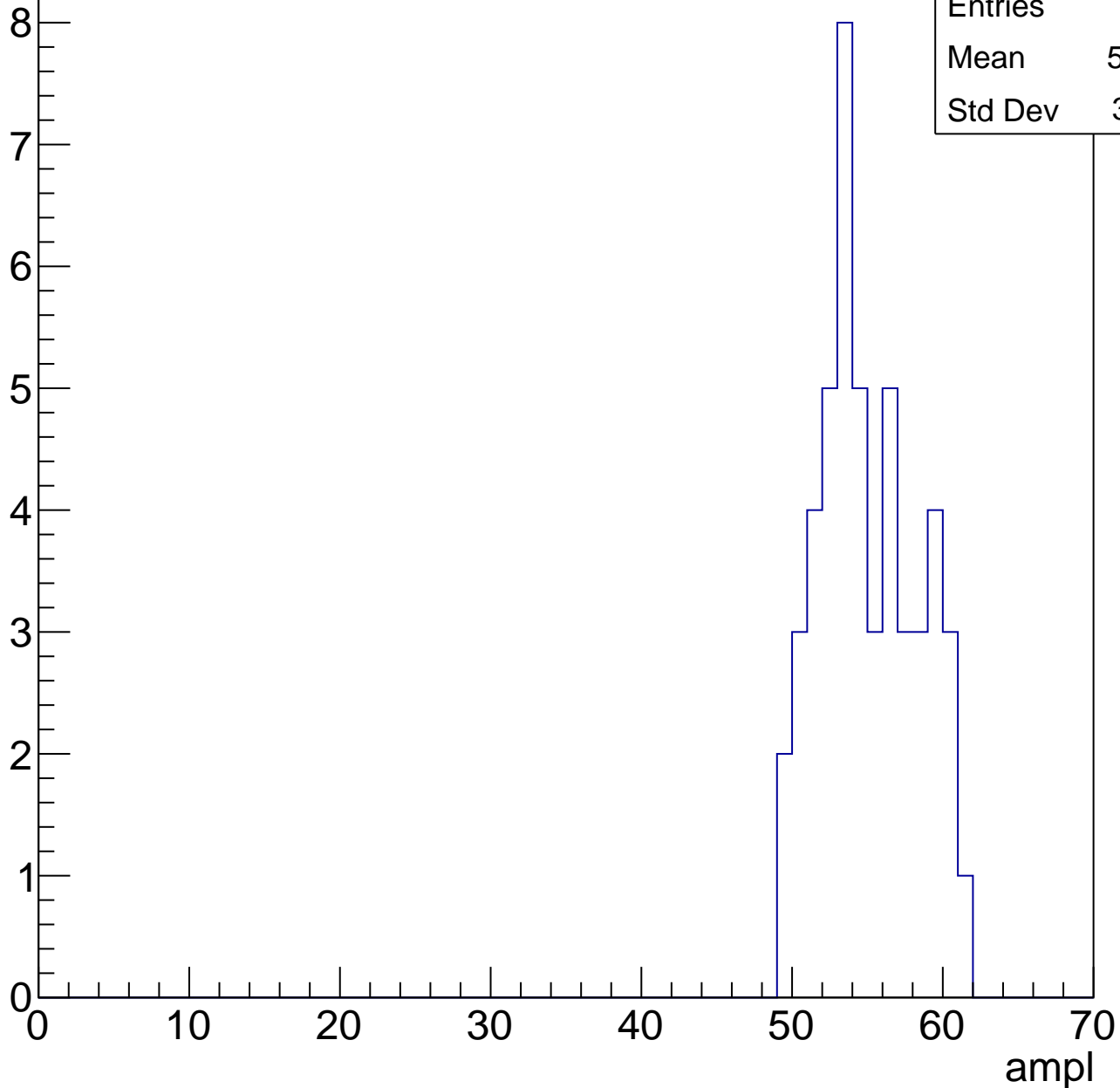


# B0L102S, U17-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	54.55
Std Dev	3.201

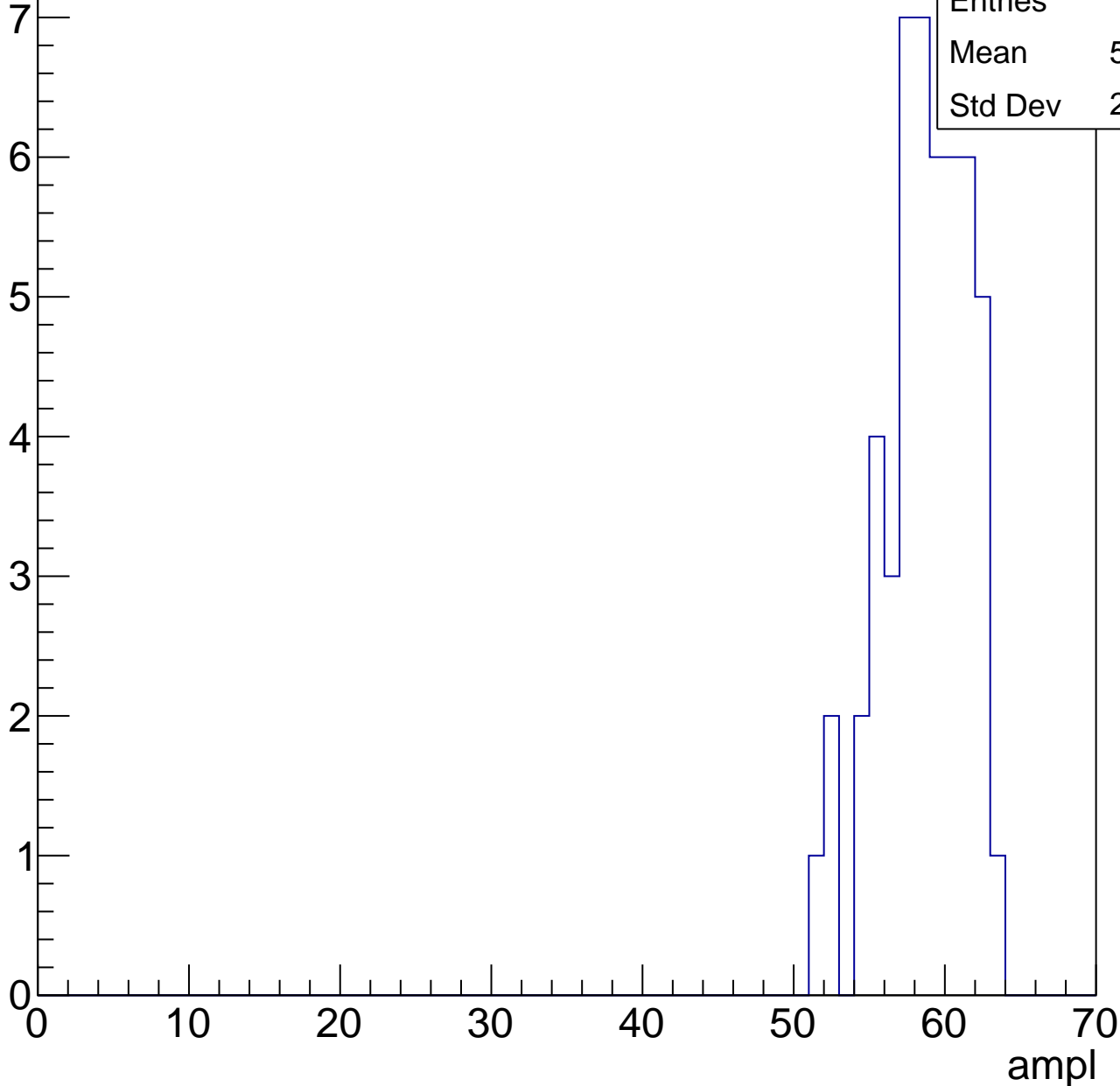


# B0L102S, U17-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.18
Std Dev	2.812

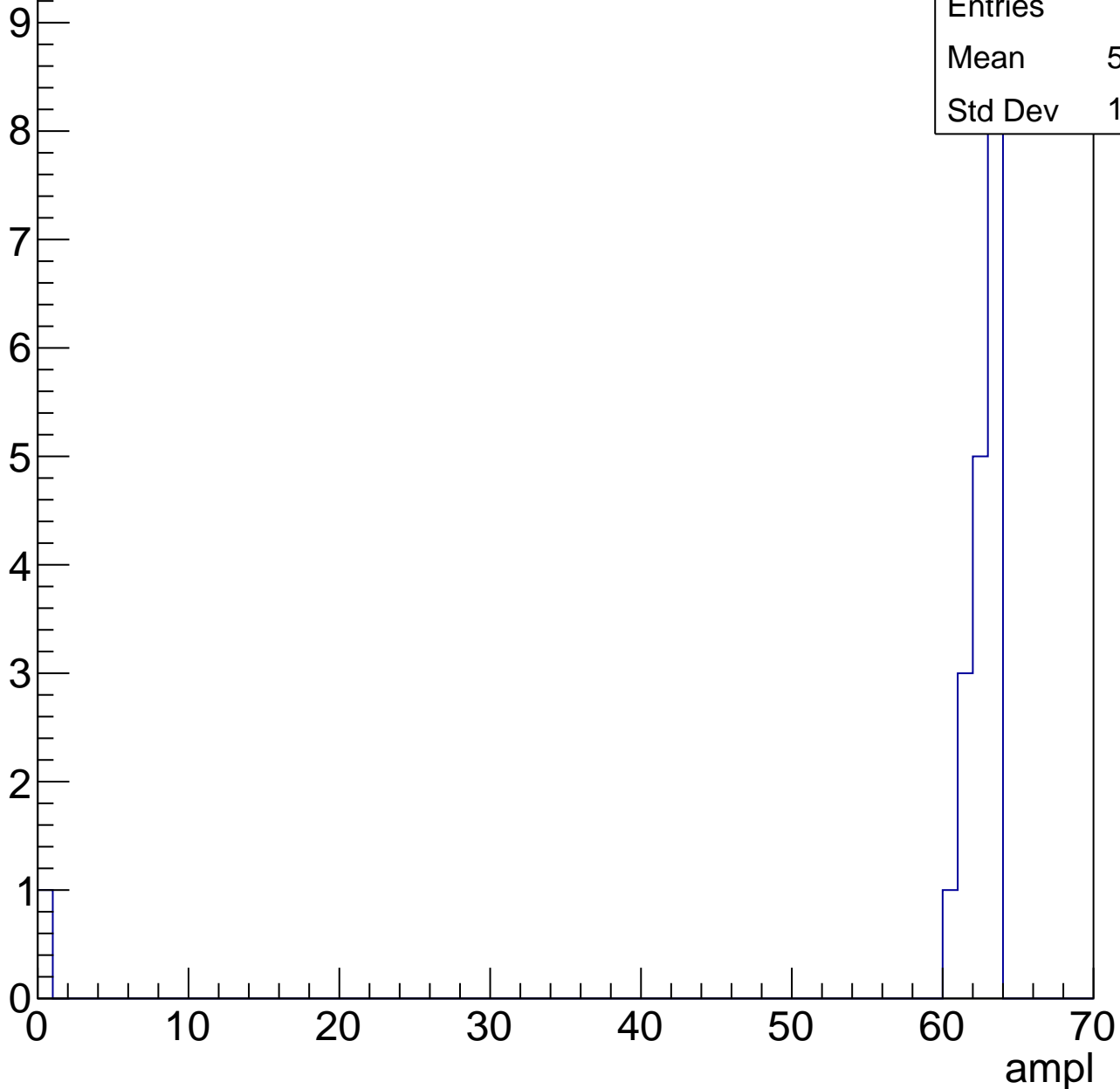


# B0L102S, U17-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	19
Mean	58.95
Std Dev	13.92

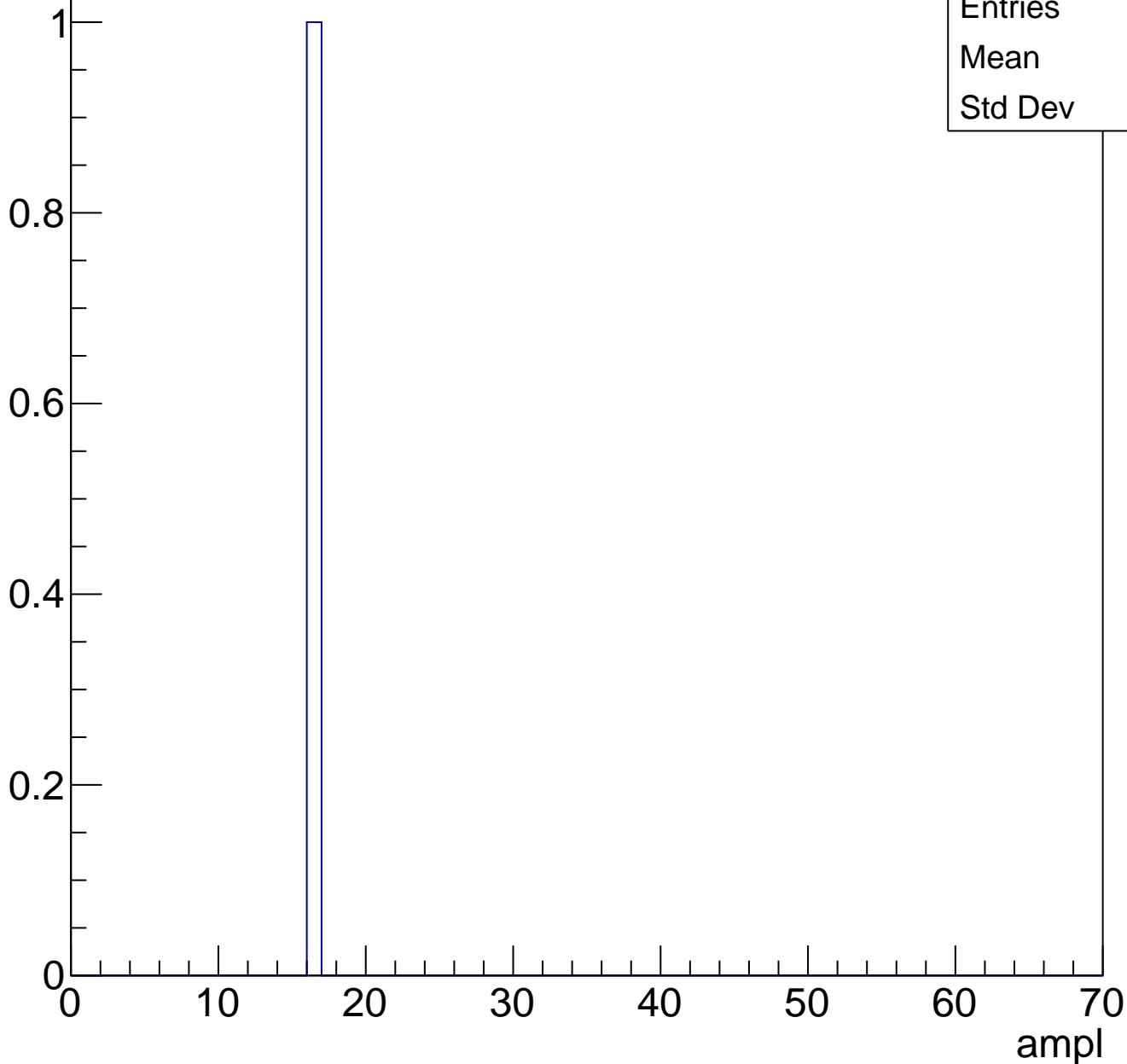




# B0L102S, U17-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch75, adc0

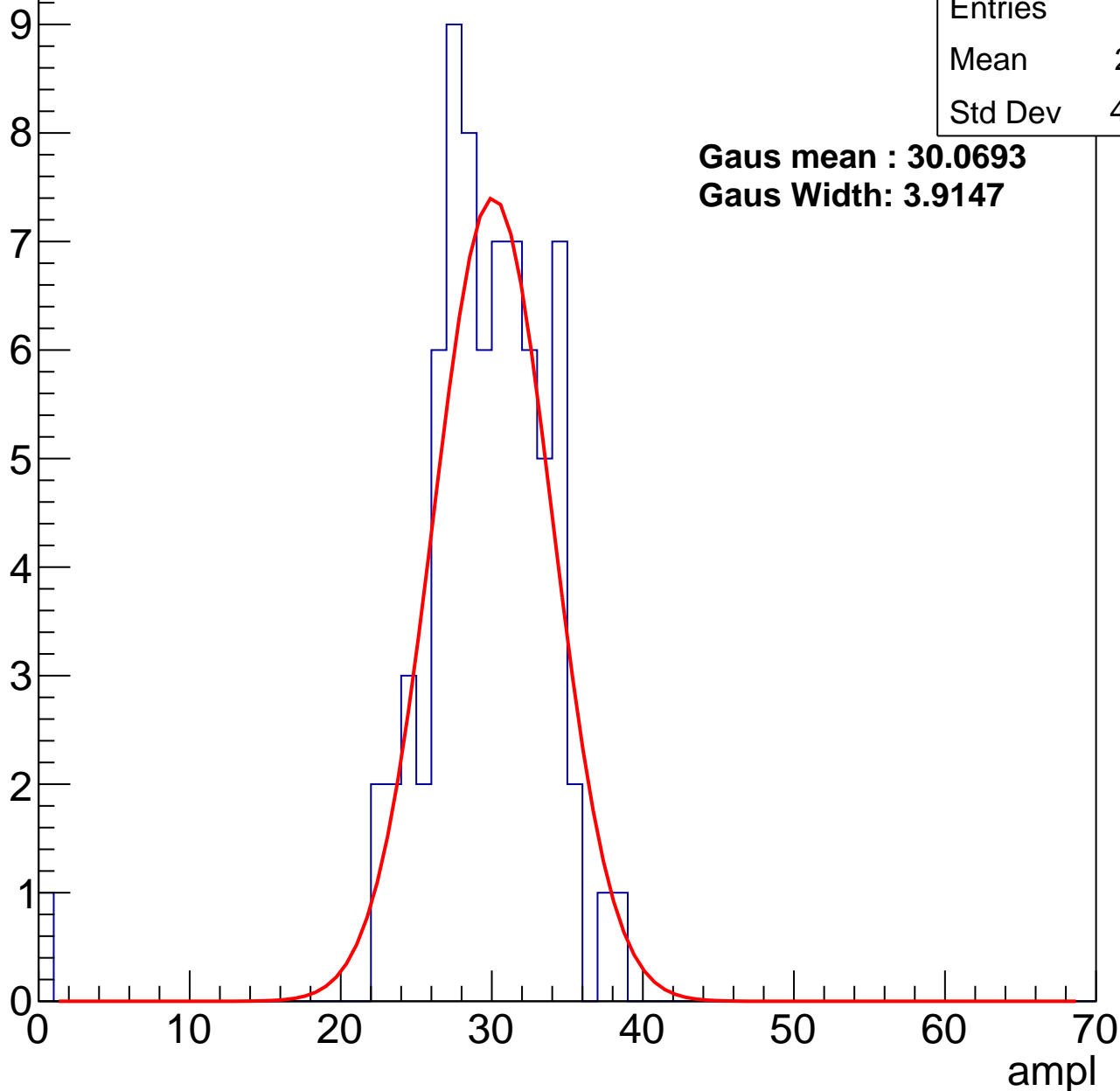
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	29.01
Std Dev	4.868

**Gaus mean : 30.0693**

**Gaus Width: 3.9147**



# B0L102S, U17-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	65
Mean	36
Std Dev	3.774

**Gaus mean : 36.4526**

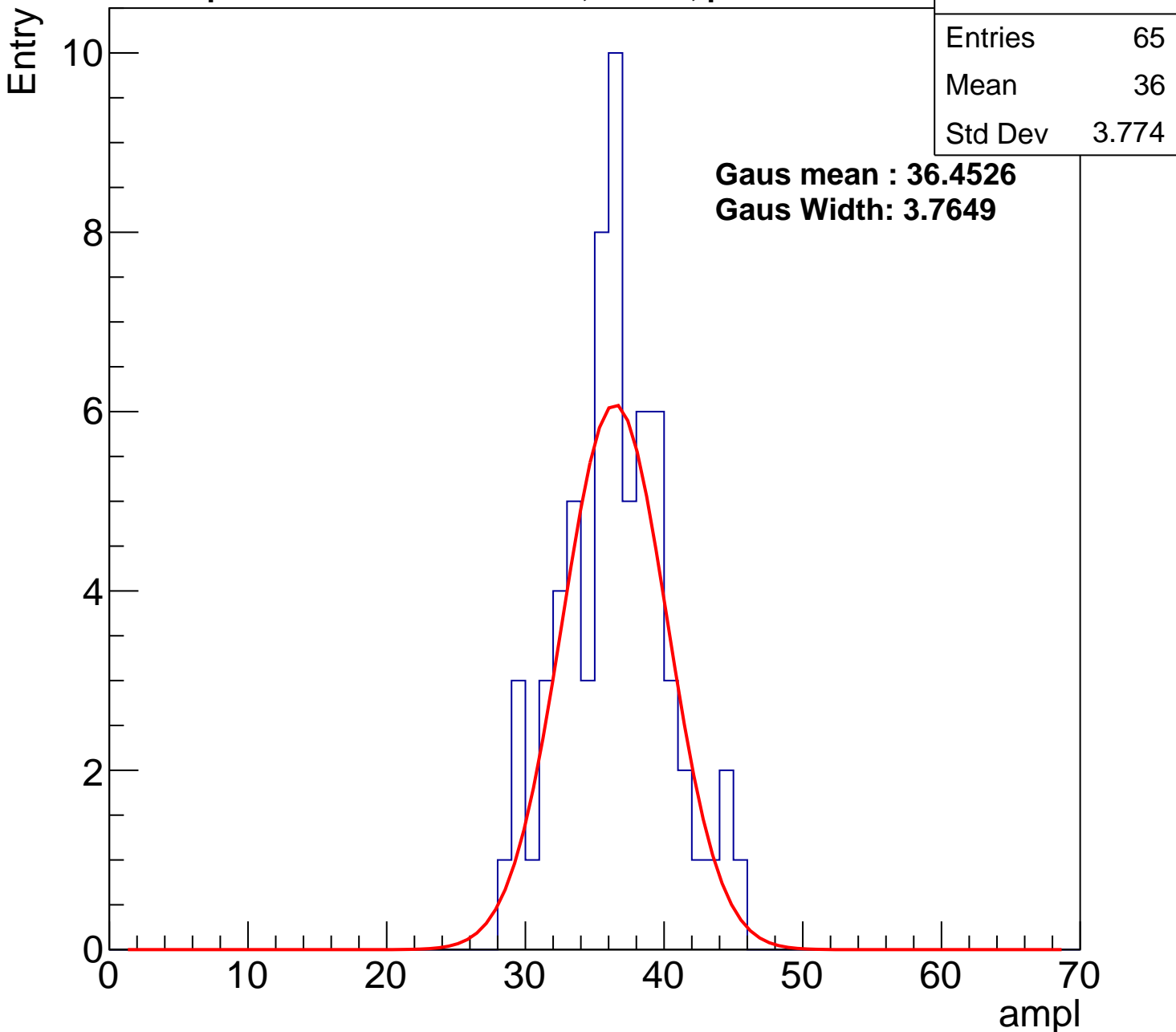
**Gaus Width: 3.7649**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch75, adc2

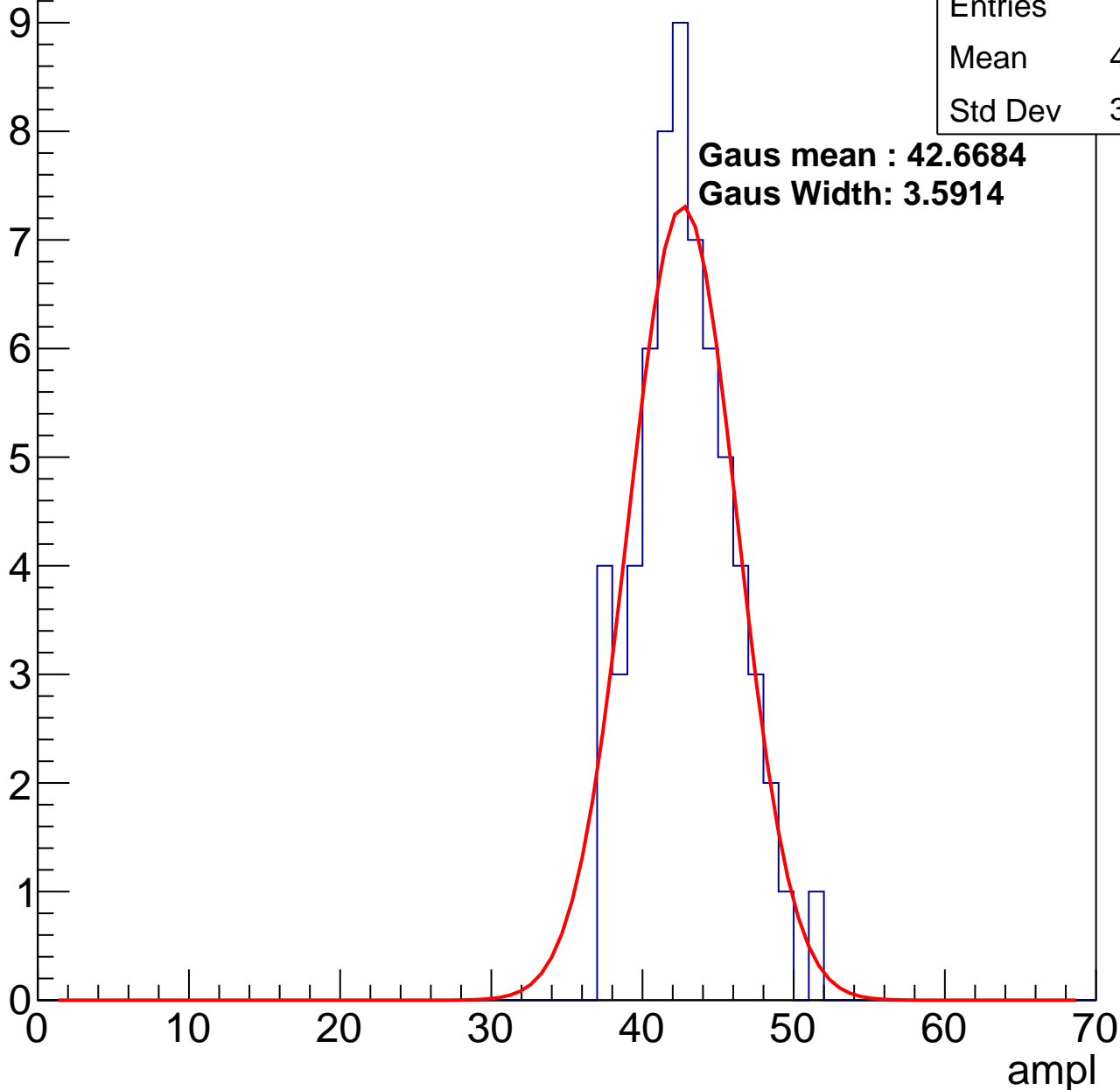
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	42.46
Std Dev	3.136

**Gaus mean : 42.6684**

**Gaus Width: 3.5914**

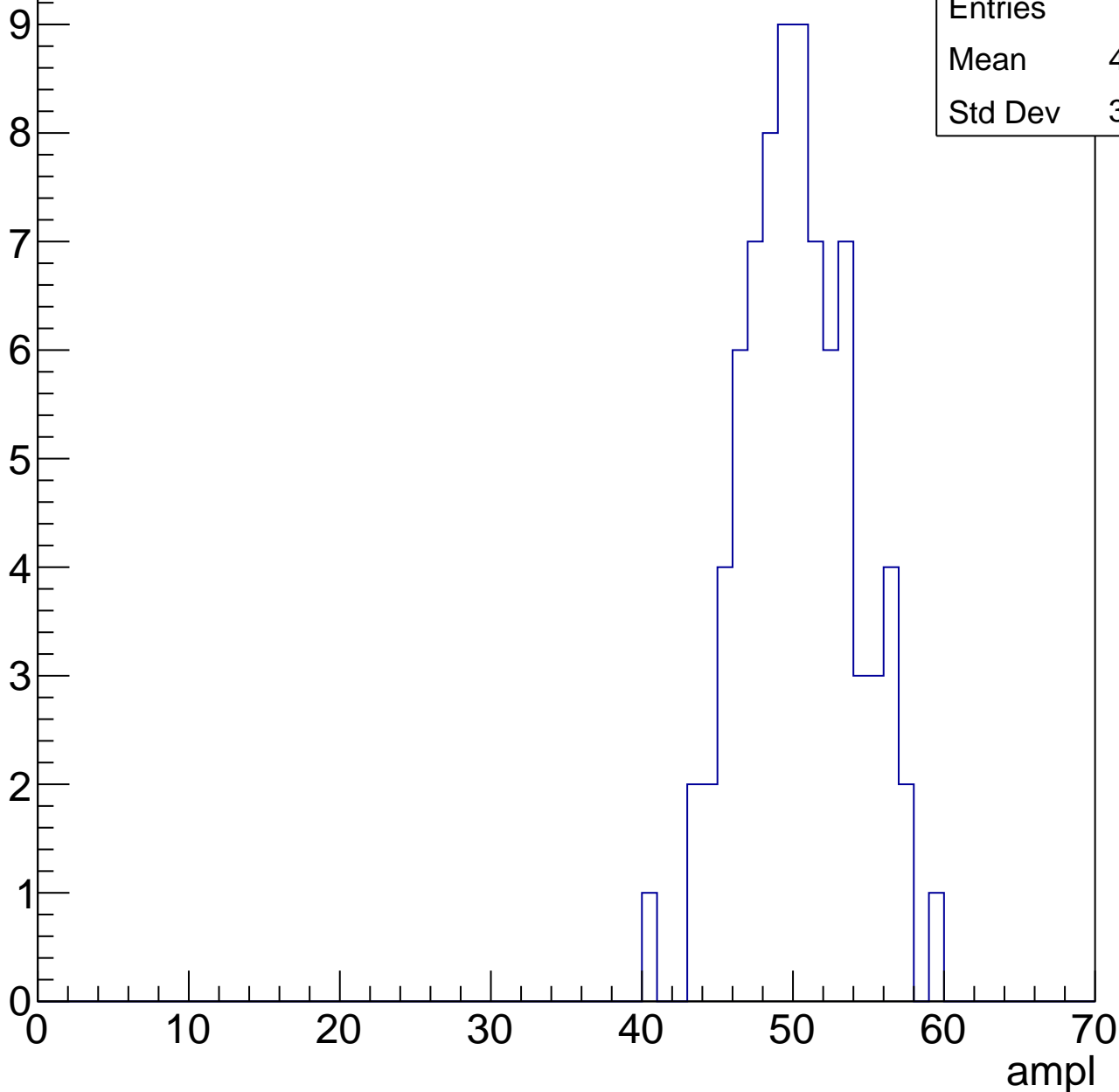


# B0L102S, U17-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	49.85
Std Dev	3.699

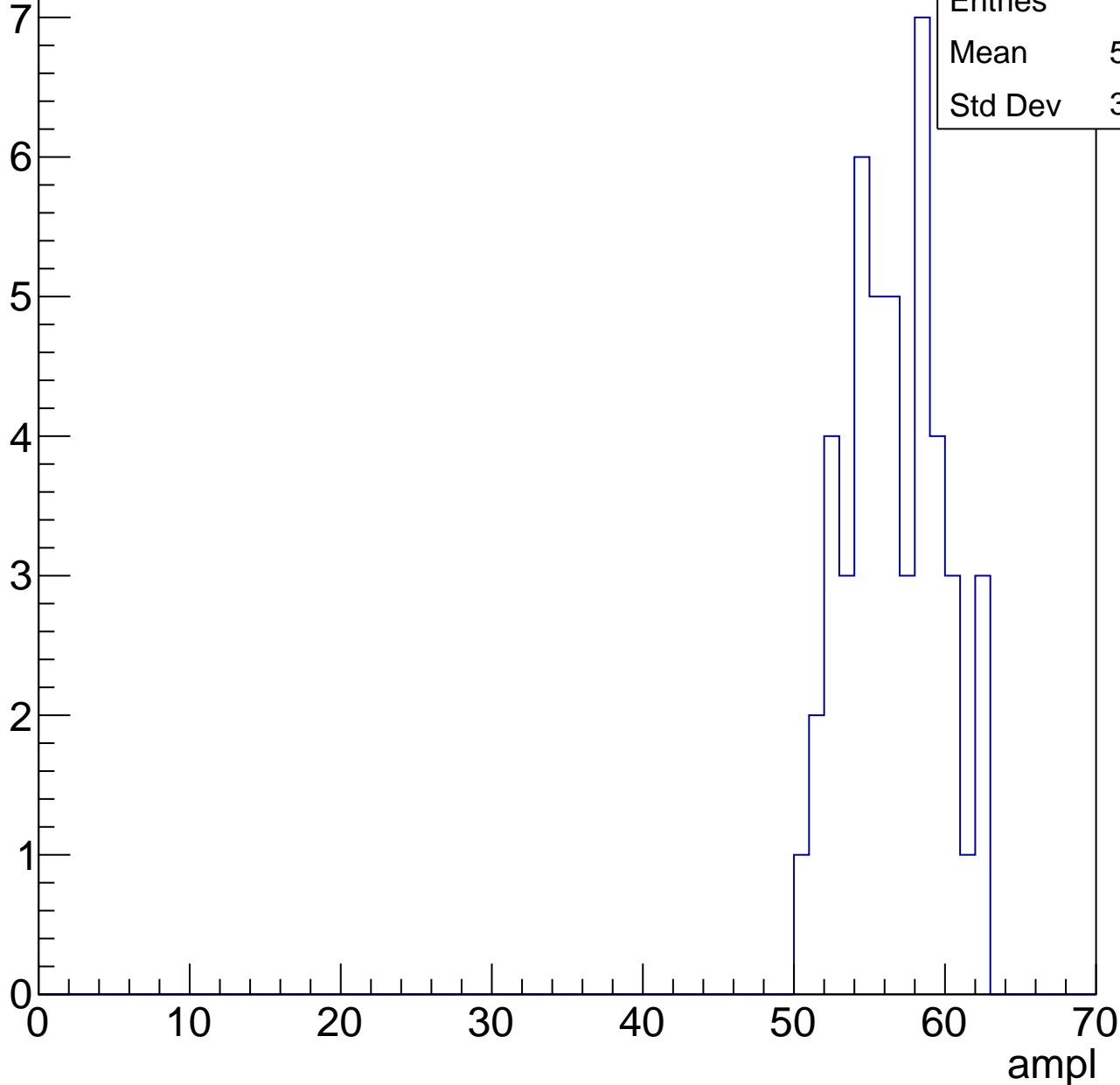


# B0L102S, U17-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	56.13
Std Dev	3.105

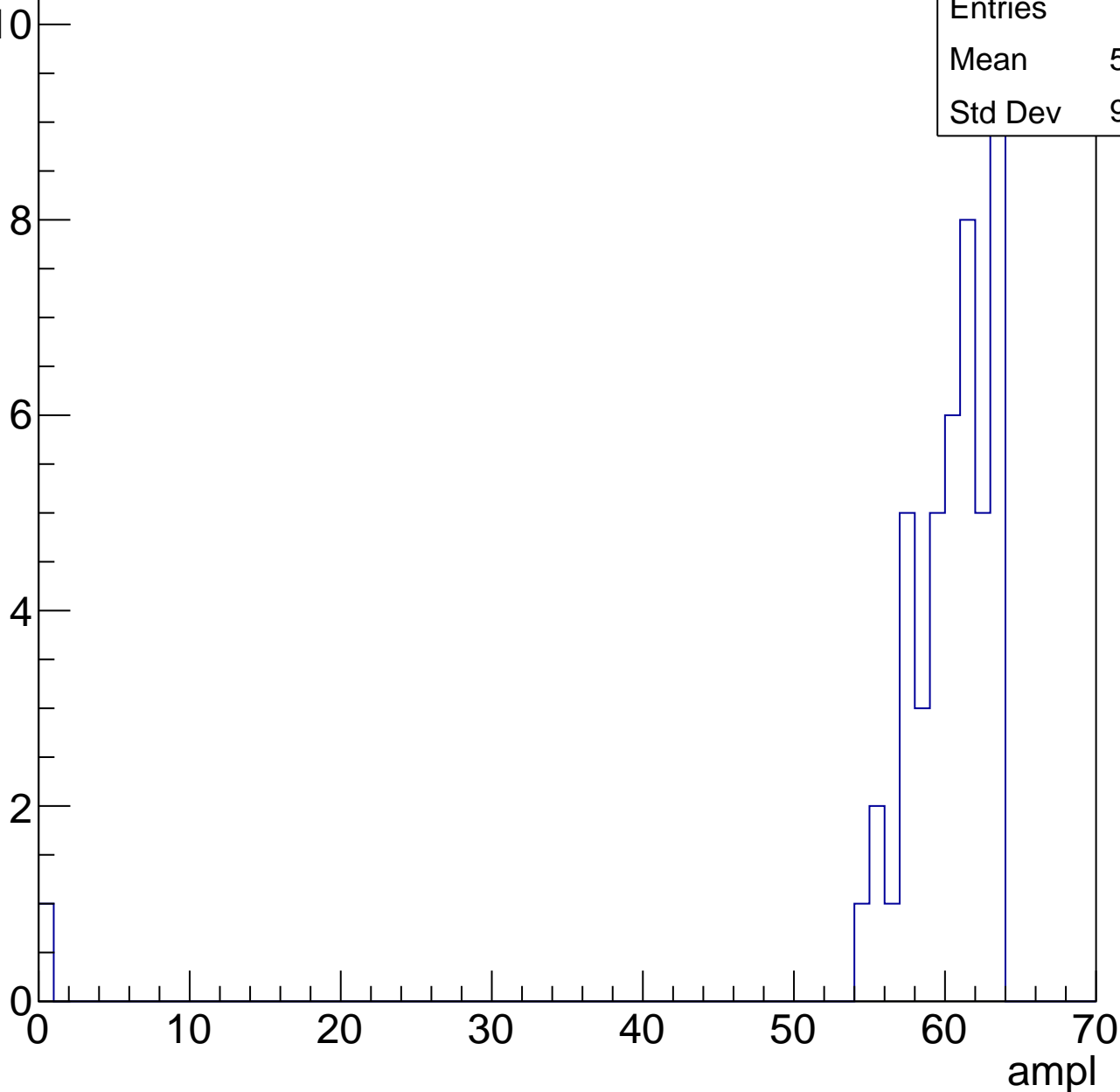


# B0L102S, U17-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	58.77
Std Dev	9.004



# B0L102S, U17-ch75, adc6

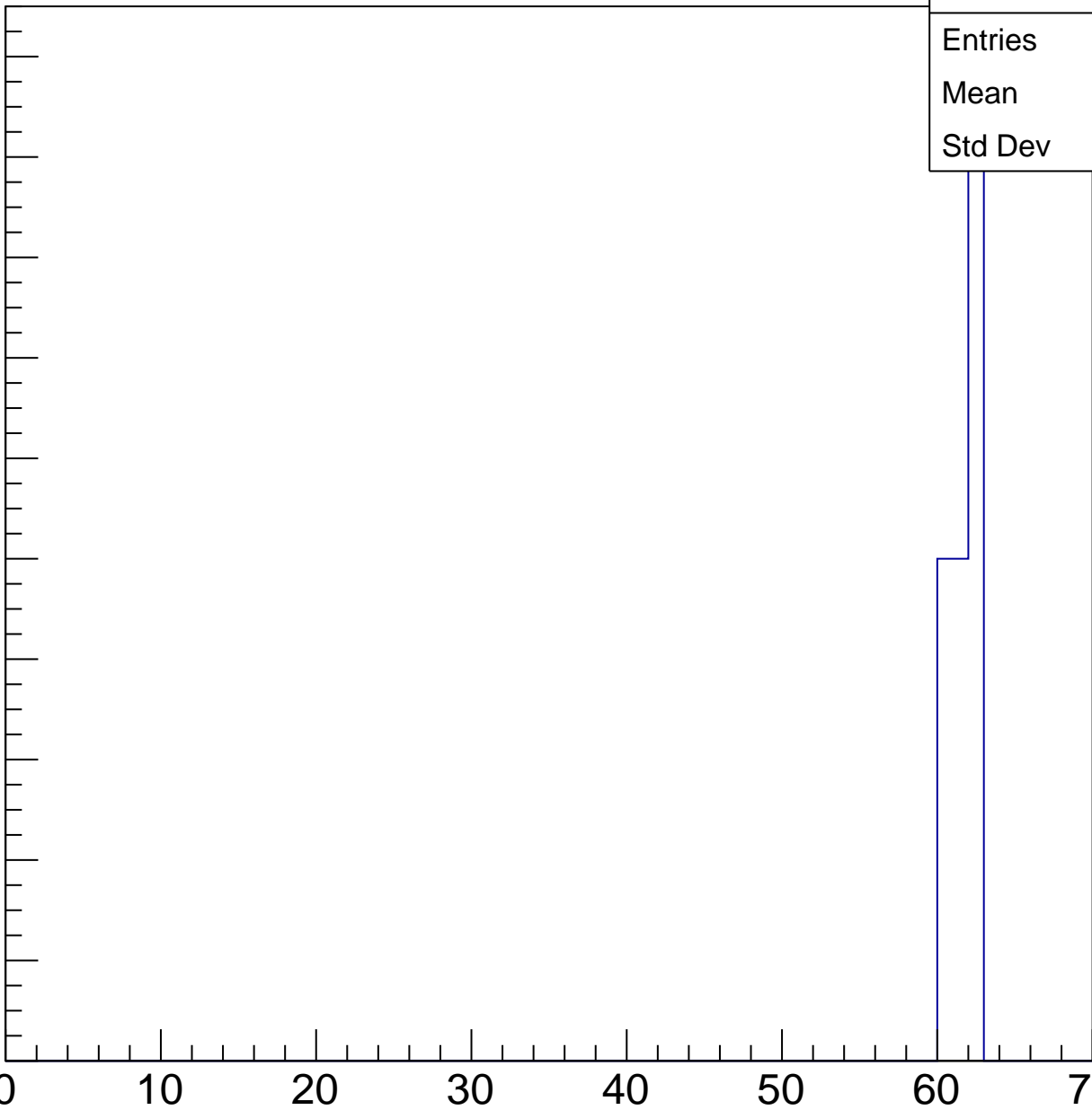
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.25
Std Dev	0.8292

ampl





# B0L102S, U17-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch76, adc0

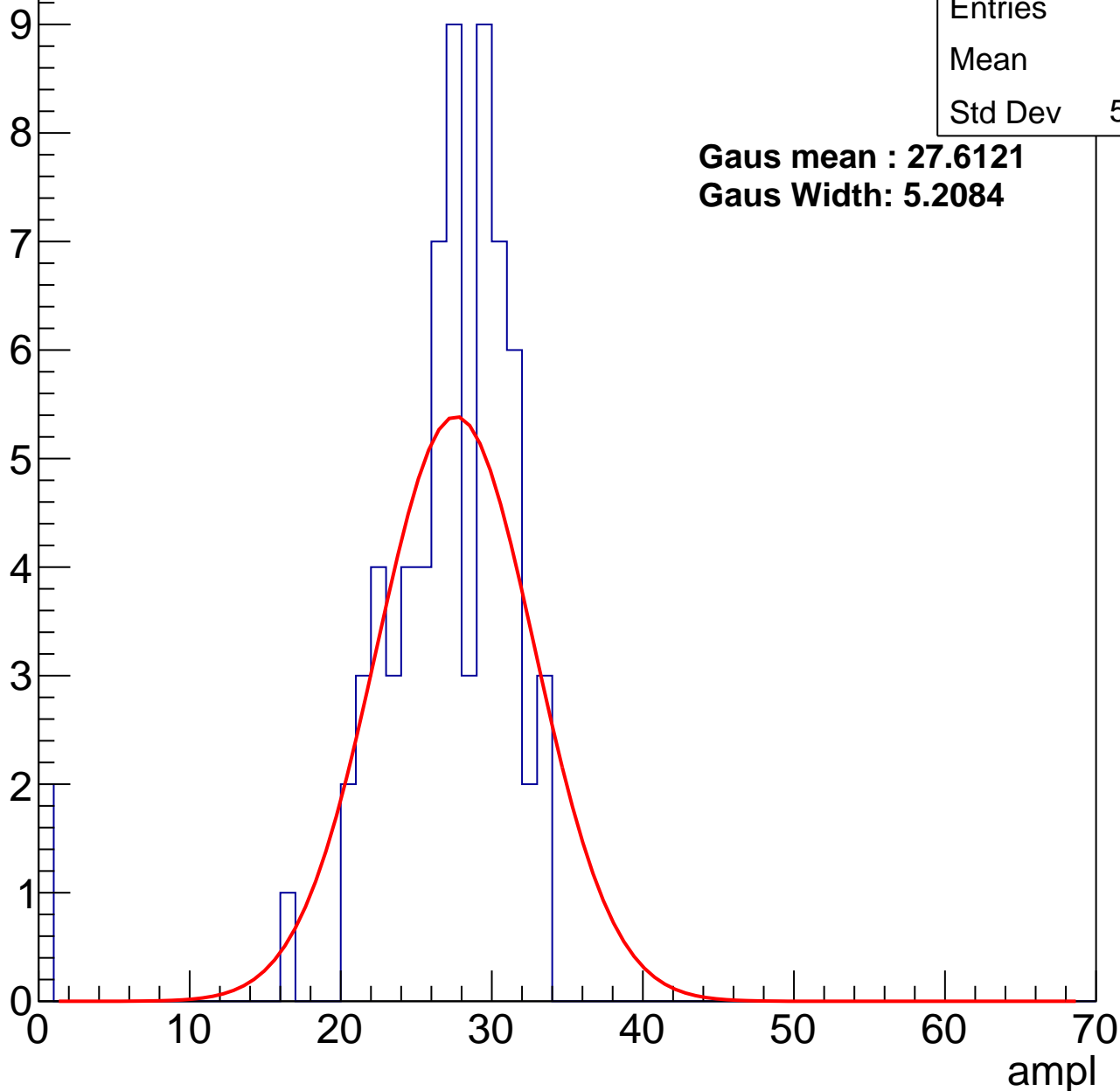
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	26.1
Std Dev	5.764

**Gaus mean : 27.6121**

**Gaus Width: 5.2084**



# B0L102S, U17-ch76, adc1

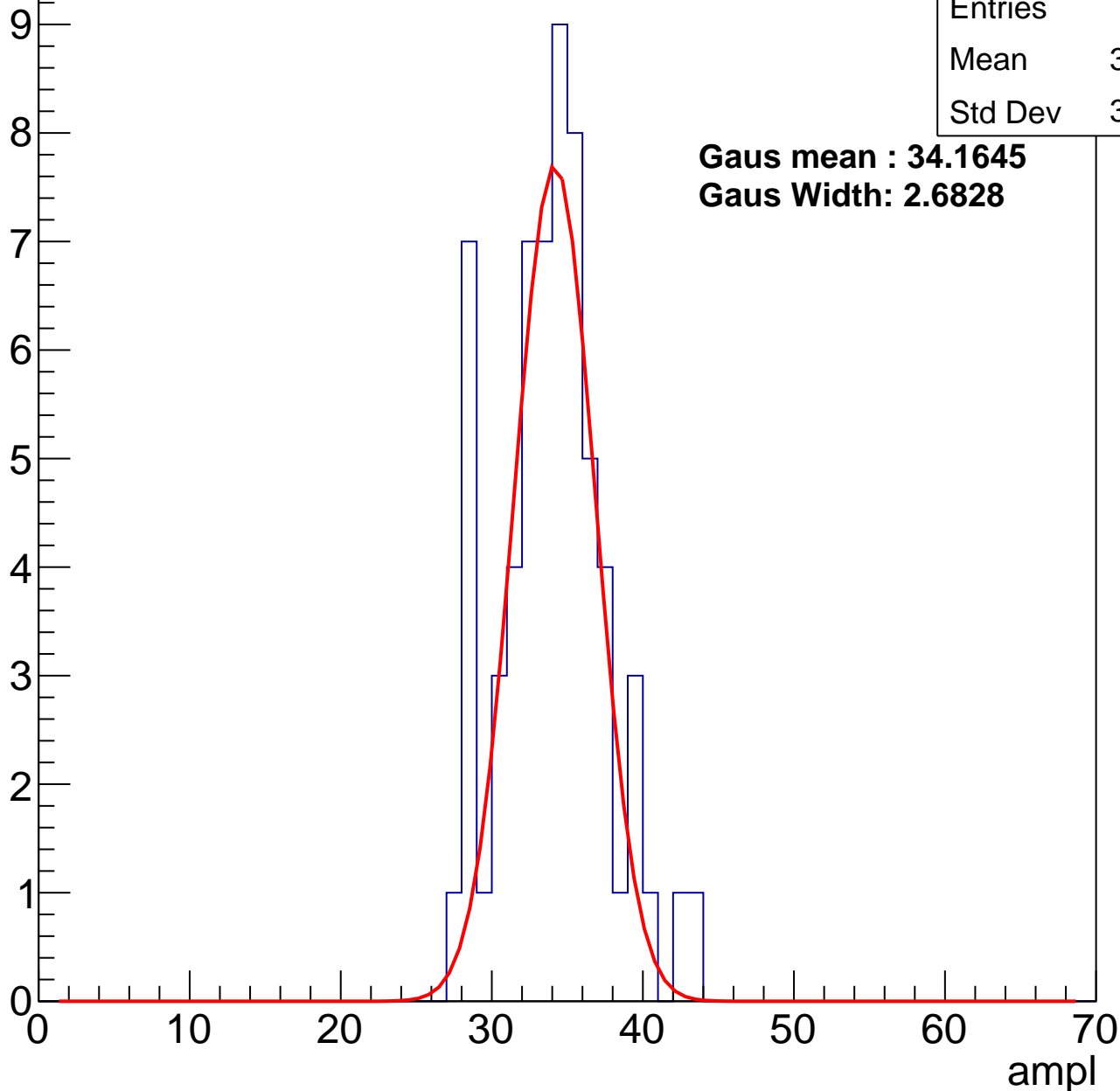
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	33.57
Std Dev	3.495

**Gaus mean : 34.1645**

**Gaus Width: 2.6828**



# B0L102S, U17-ch76, adc2

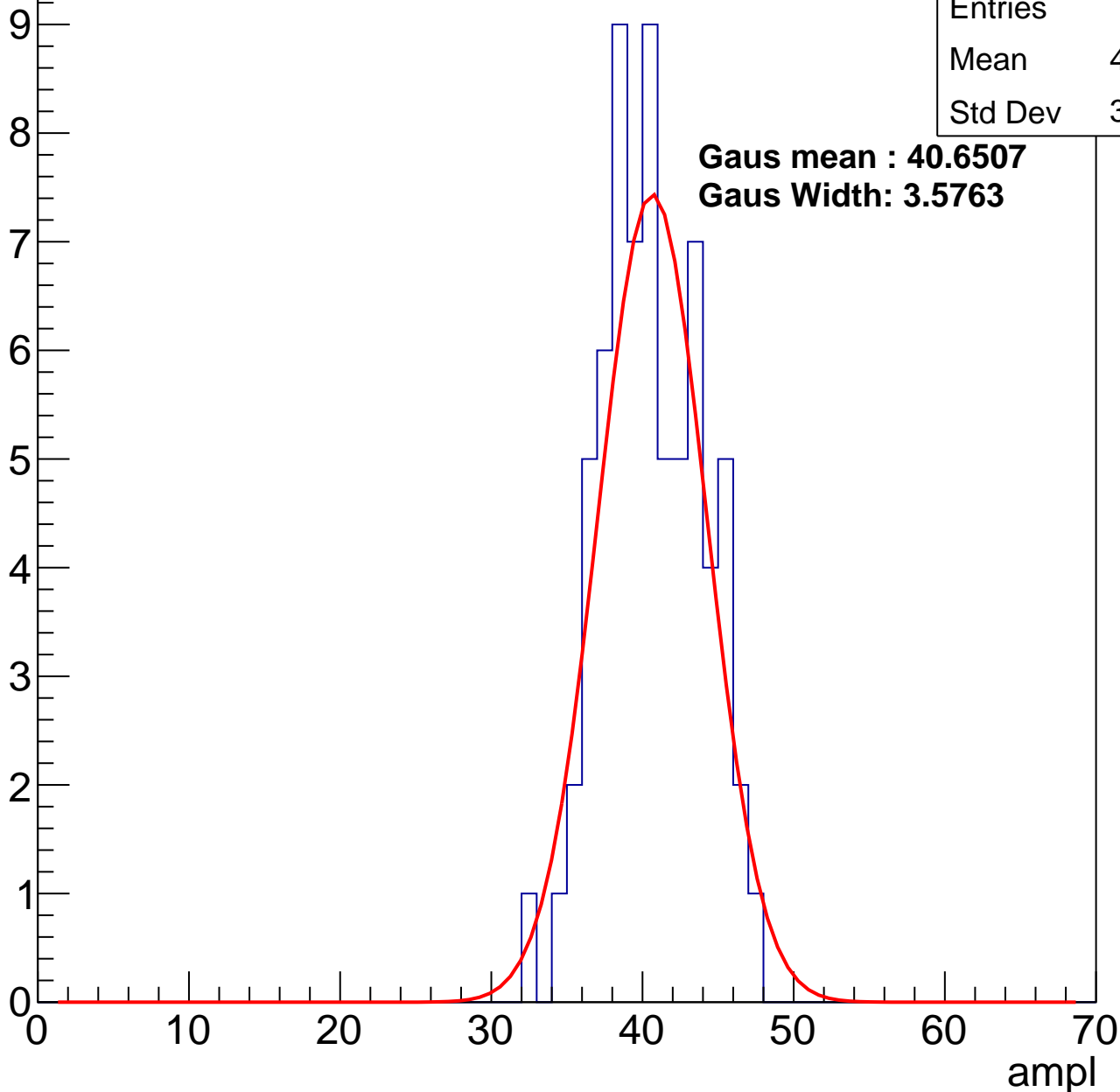
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	40.13
Std Dev	3.239

**Gaus mean : 40.6507**

**Gaus Width: 3.5763**



# B0L102S, U17-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

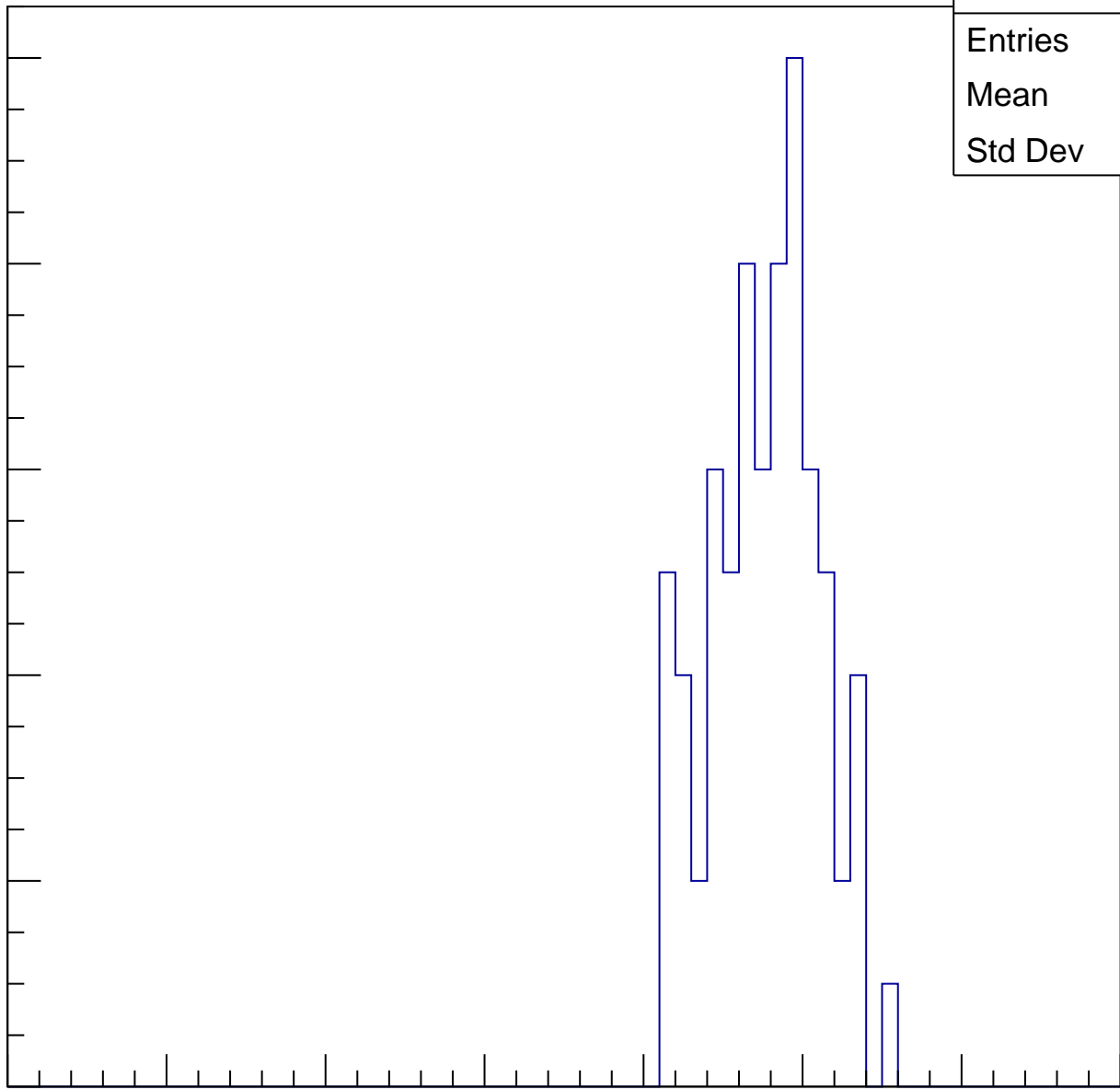
Entries	72
Mean	47.19
Std Dev	3.398

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

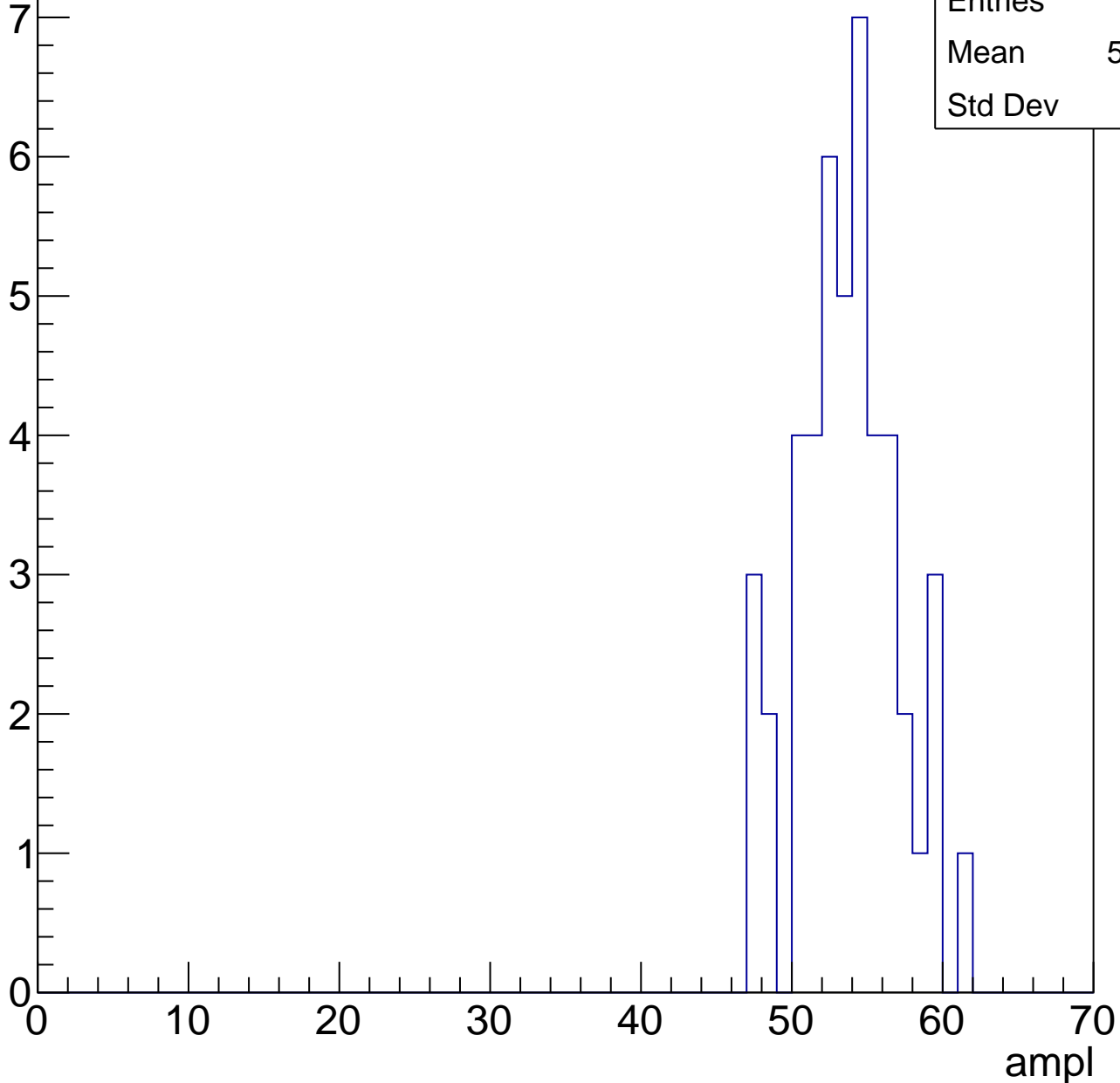


# B0L102S, U17-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	53.26
Std Dev	3.3

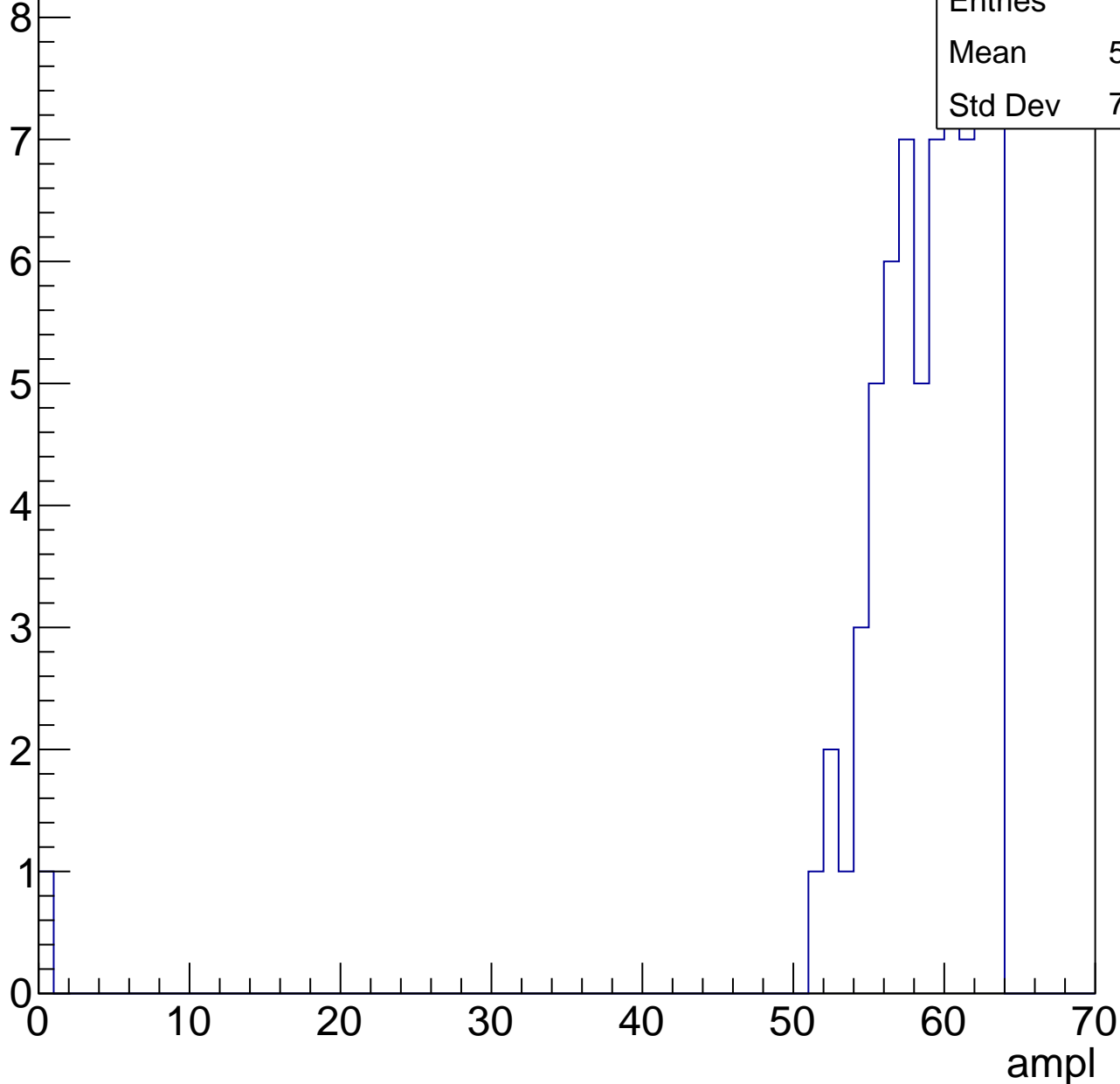


# B0L102S, U17-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	57.83
Std Dev	7.673



# B0L102S, U17-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

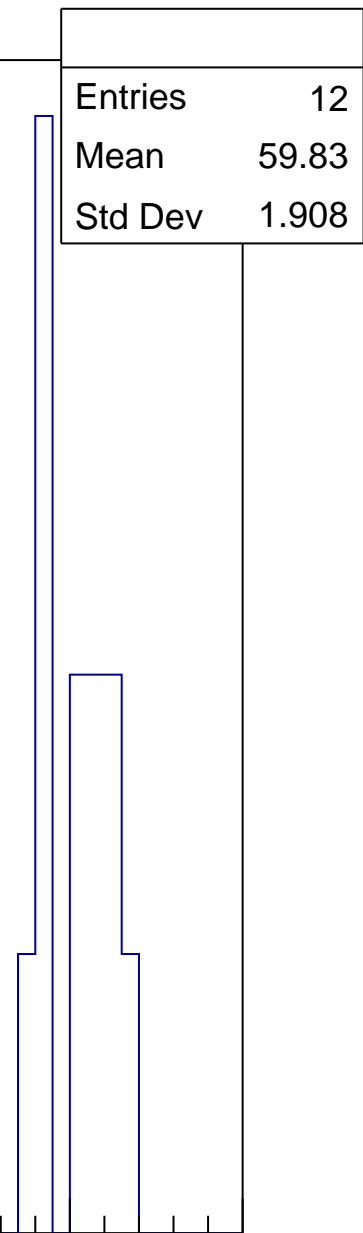
Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	12
Mean	59.83
Std Dev	1.908

ampl

0 10 20 30 40 50 60 70

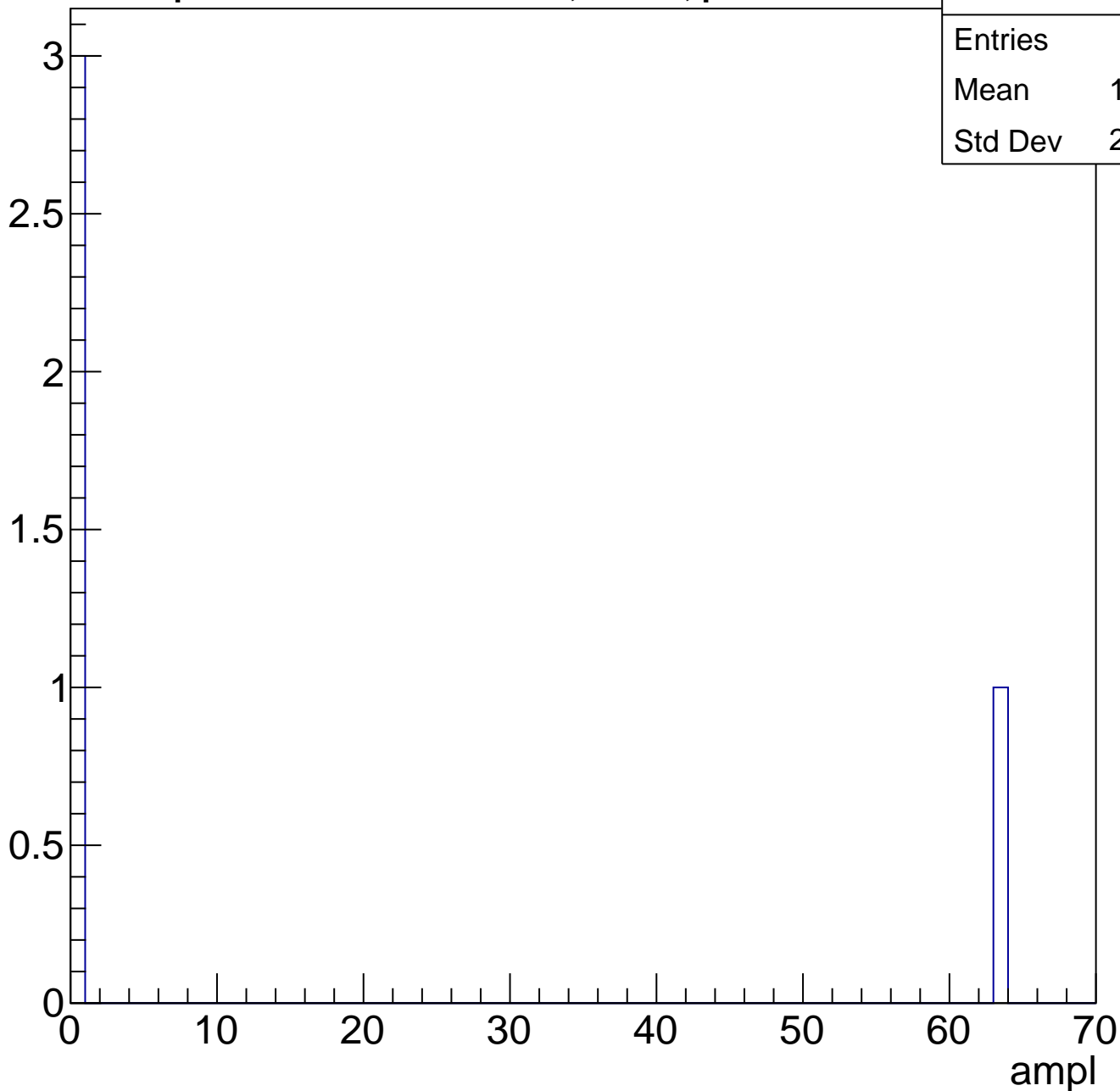




# B0L102S, U17-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	4
Mean	15.75
Std Dev	27.28

# B0L102S, U17-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	78
Mean	29.14
Std Dev	5.909

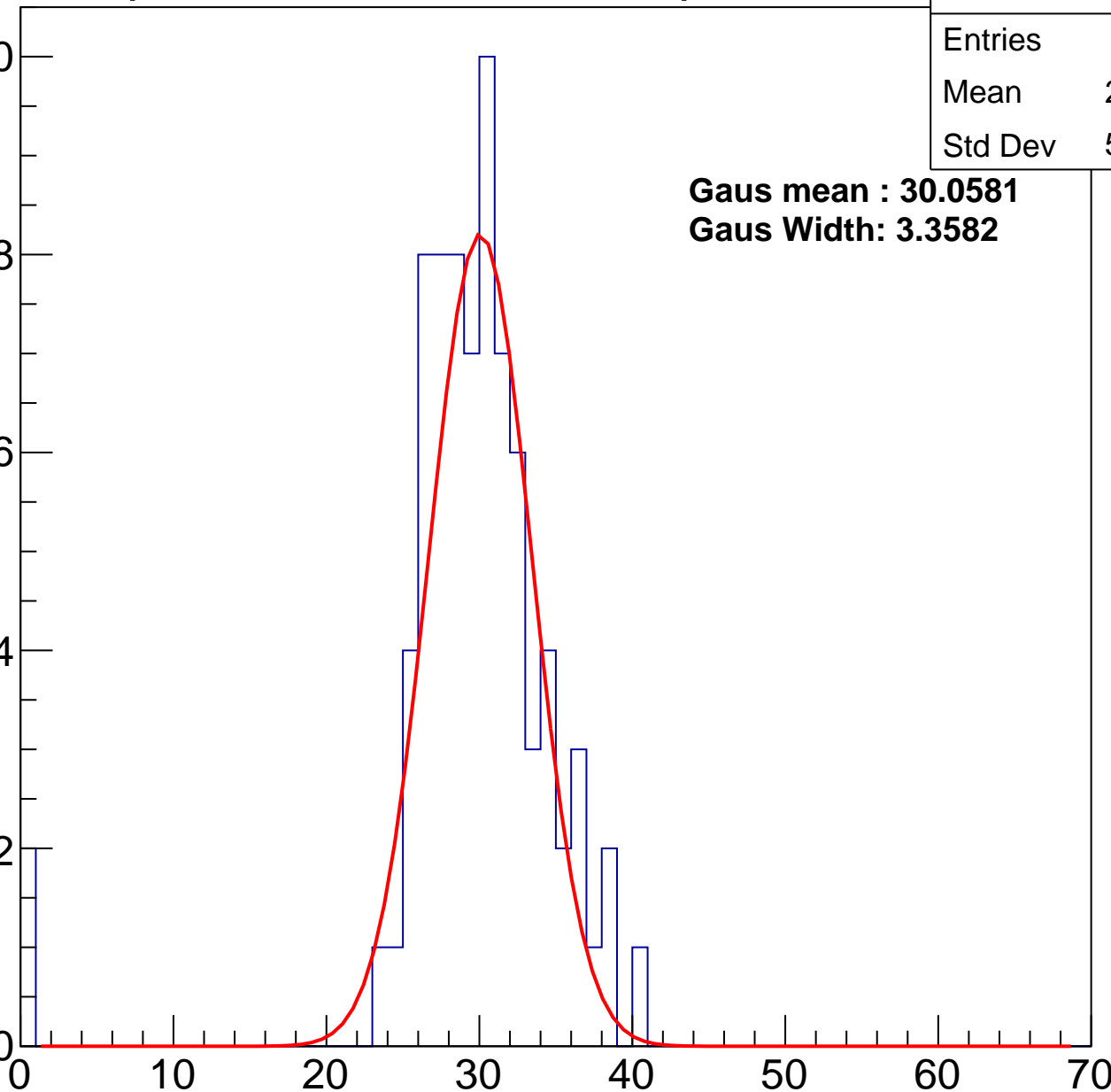
**Gaus mean : 30.0581**

**Gaus Width: 3.3582**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L102S, U17-ch77, adc1

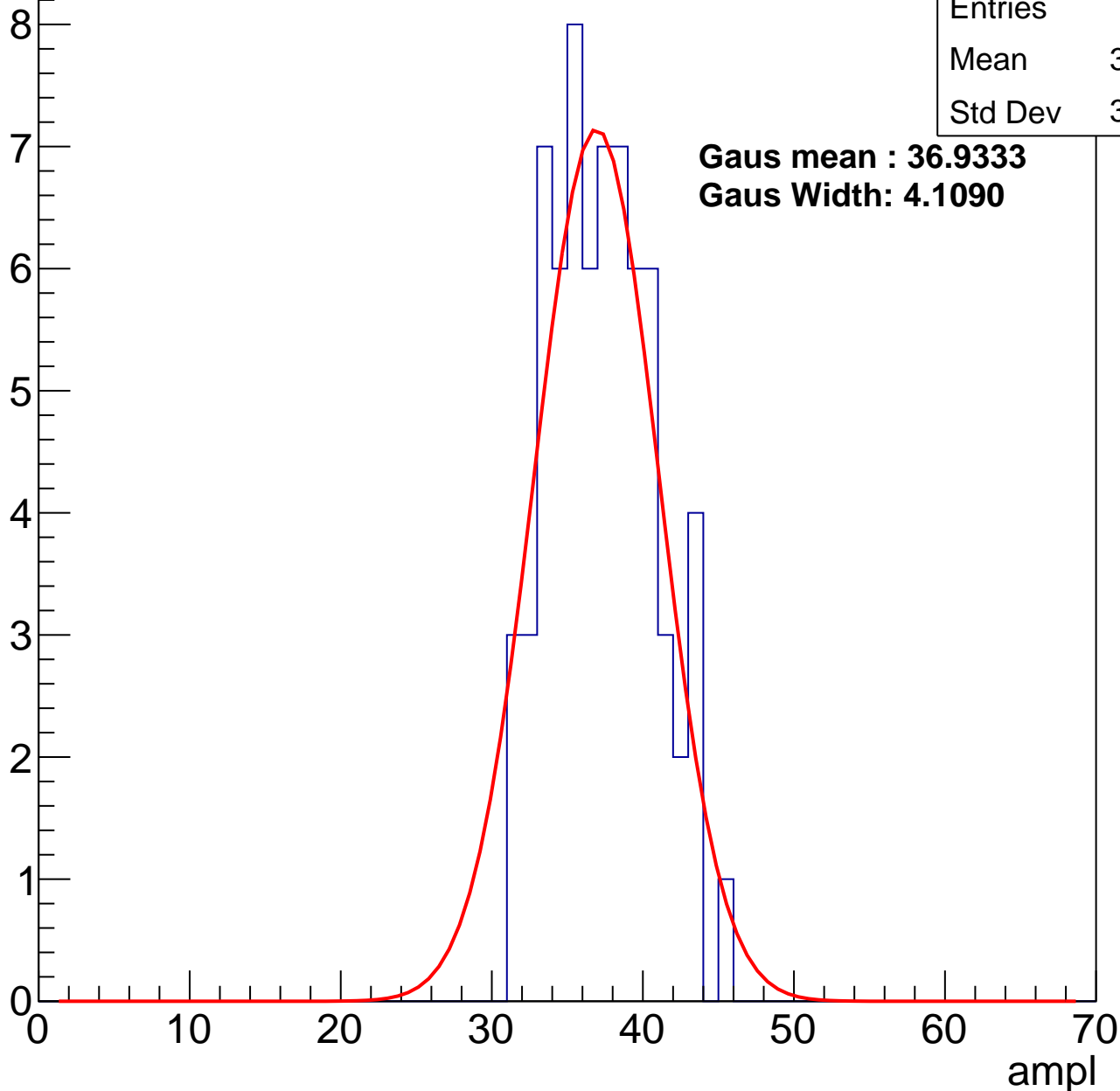
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.86
Std Dev	3.355

**Gaus mean : 36.9333**

**Gaus Width: 4.1090**



# B0L102S, U17-ch77, adc2

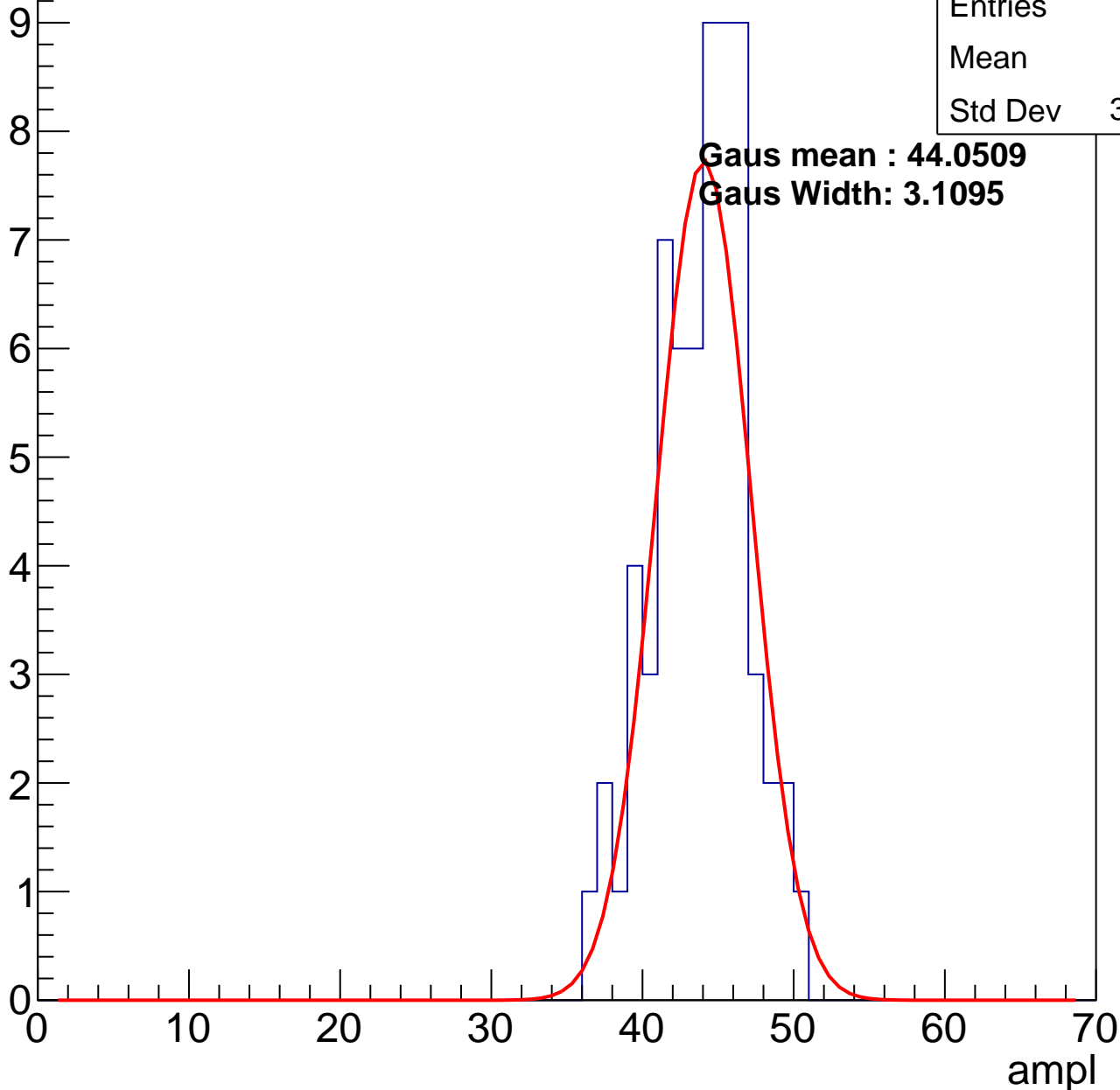
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	43.4
Std Dev	3.052

**Gaus mean : 44.0509**

**Gaus Width: 3.1095**

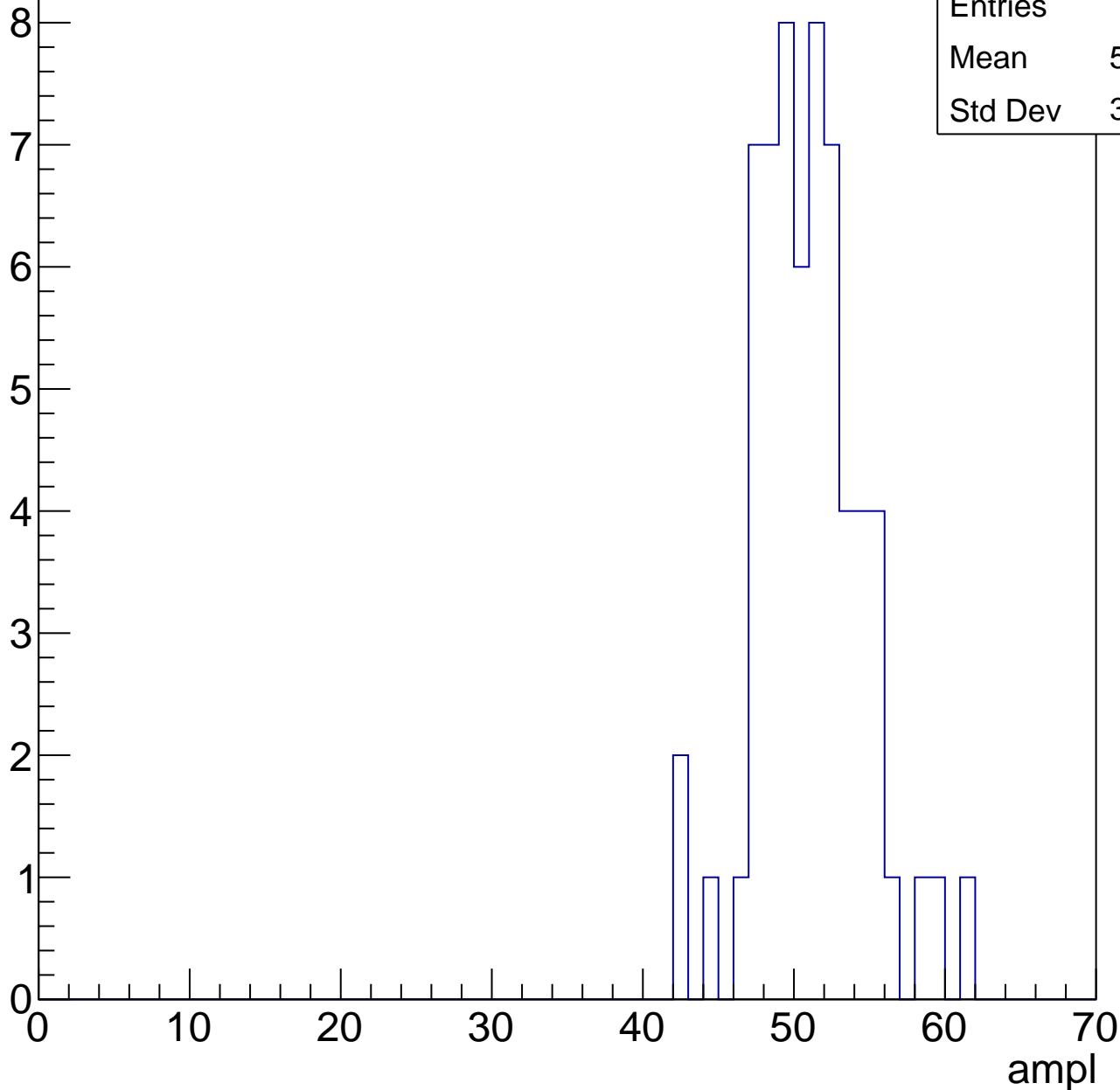


# B0L102S, U17-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	50.56
Std Dev	3.558

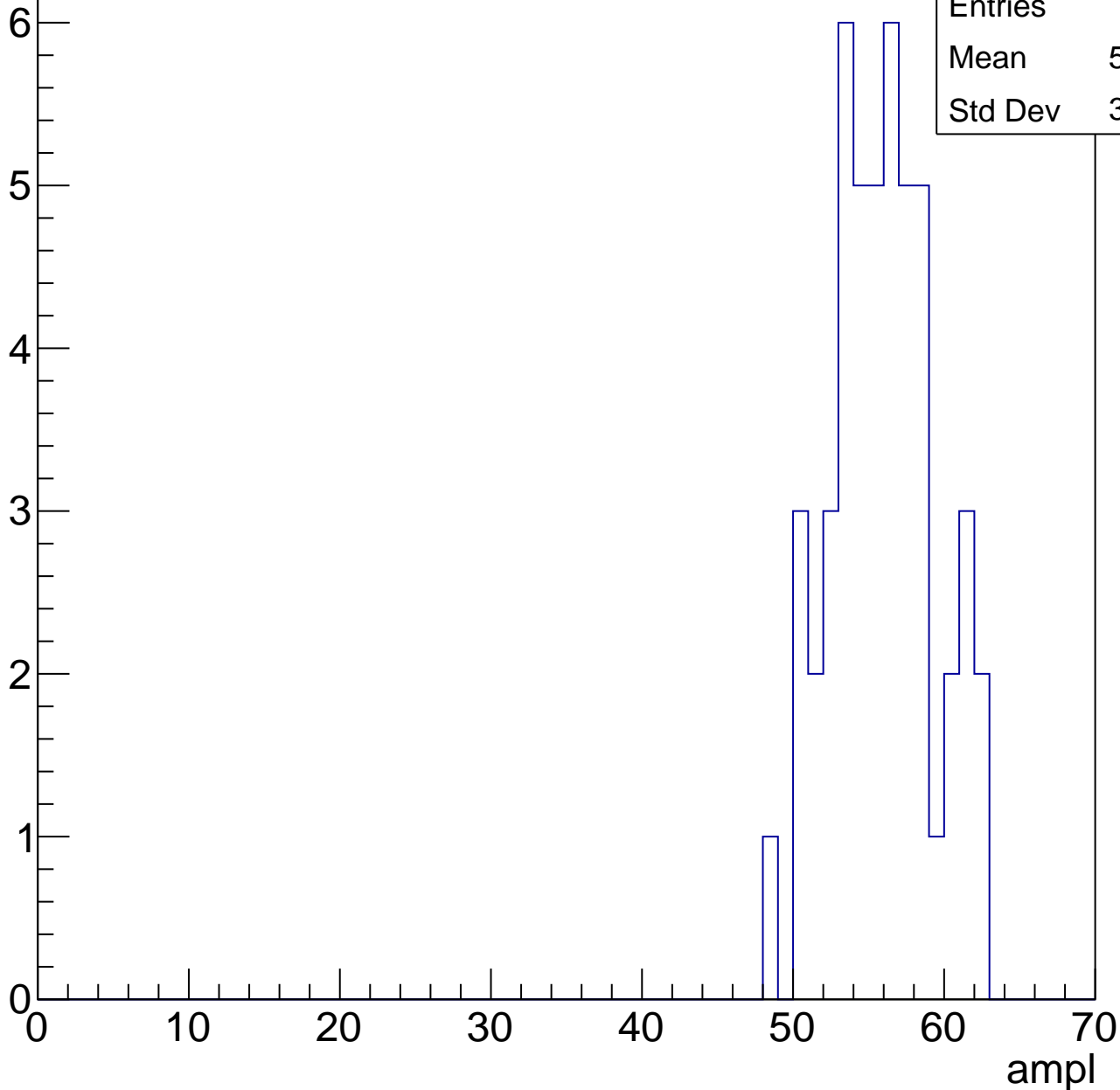


# B0L102S, U17-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

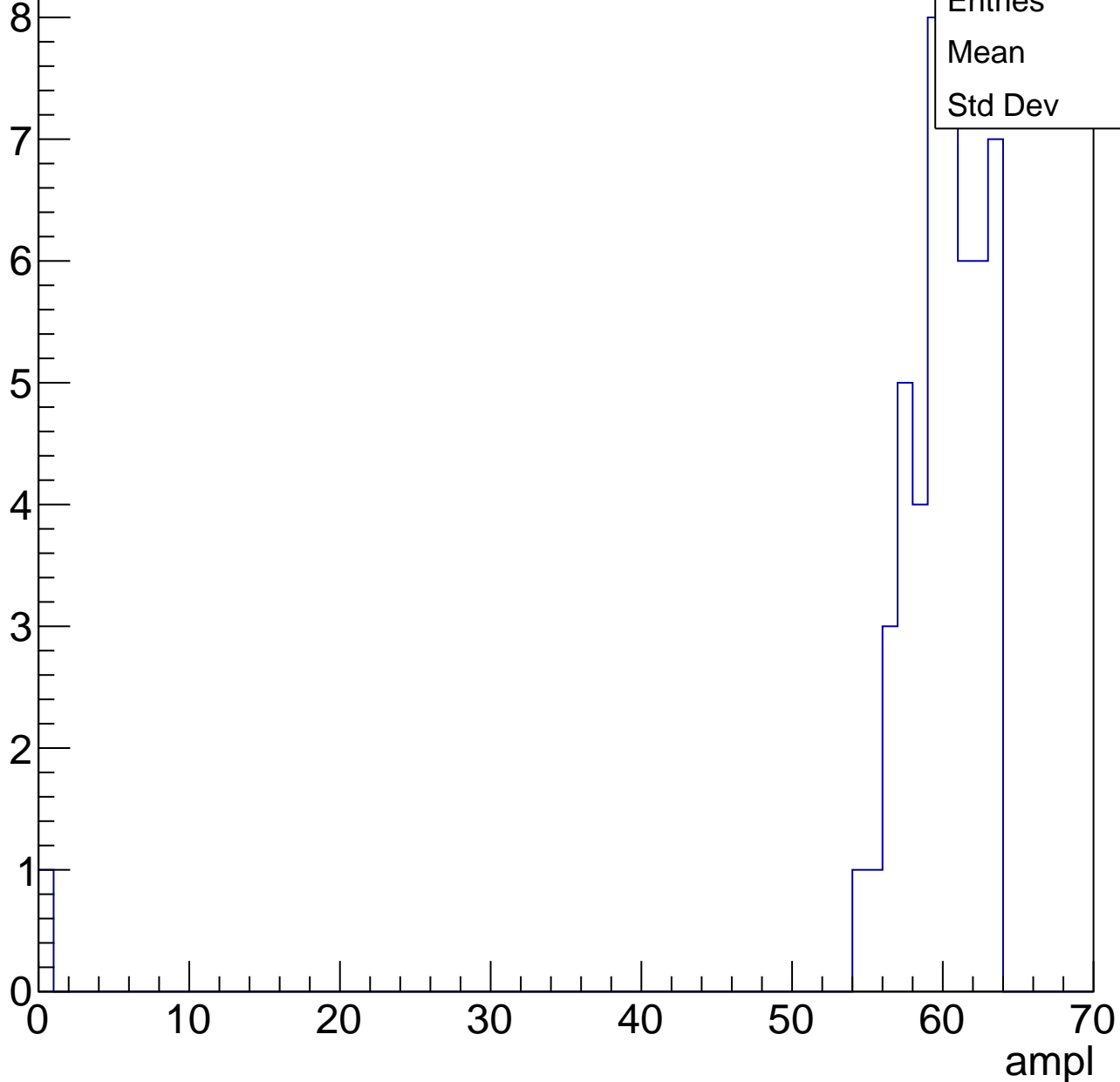
Entries	49
Mean	55.43
Std Dev	3.338



# B0L102S, U17-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	50
Mean	58.5
Std Dev	8.67

# B0L102S, U17-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch78, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	94
Mean	27.13
Std Dev	4.147

**Gaus mean : 27.9561**

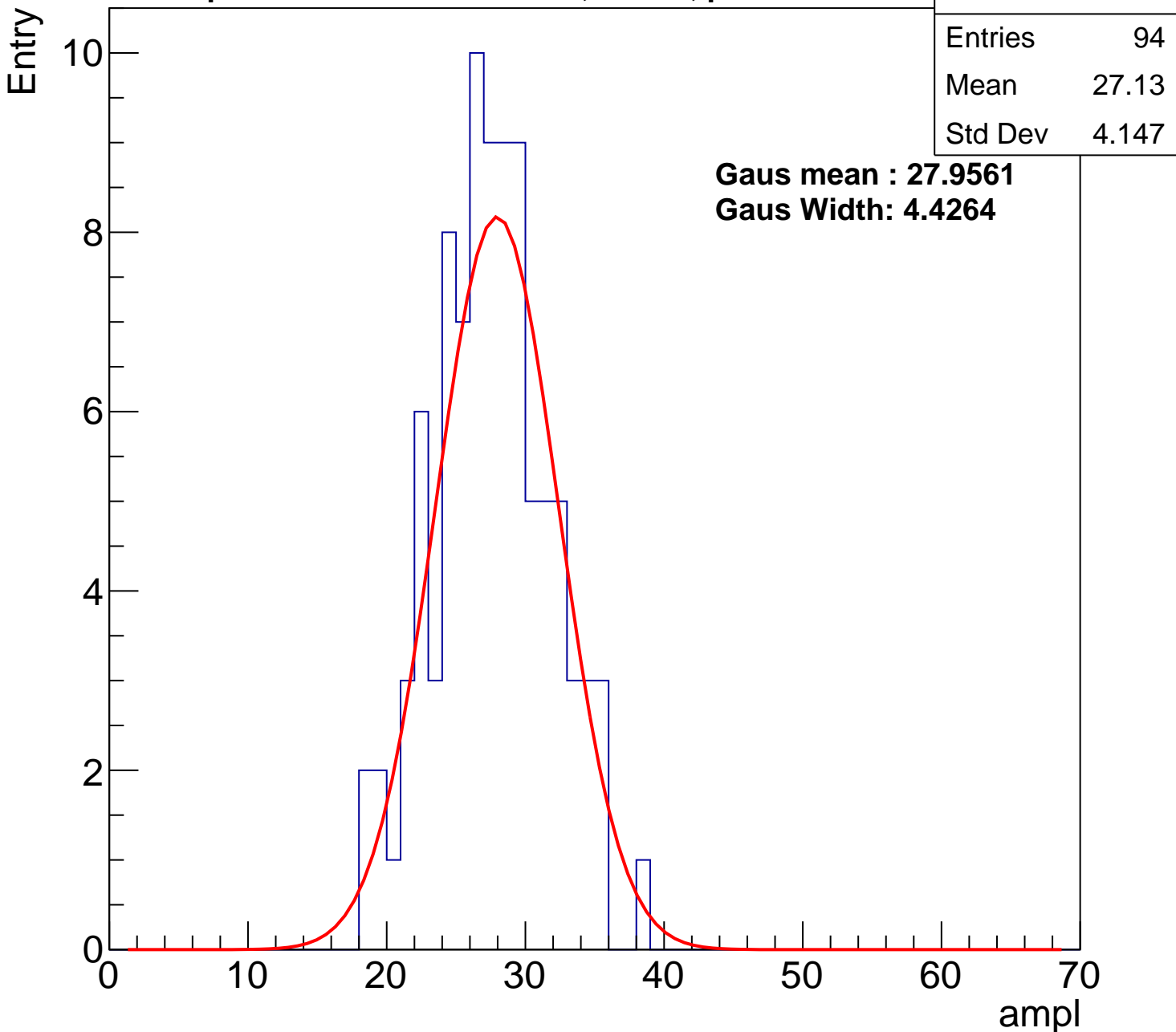
**Gaus Width: 4.4264**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch78, adc1

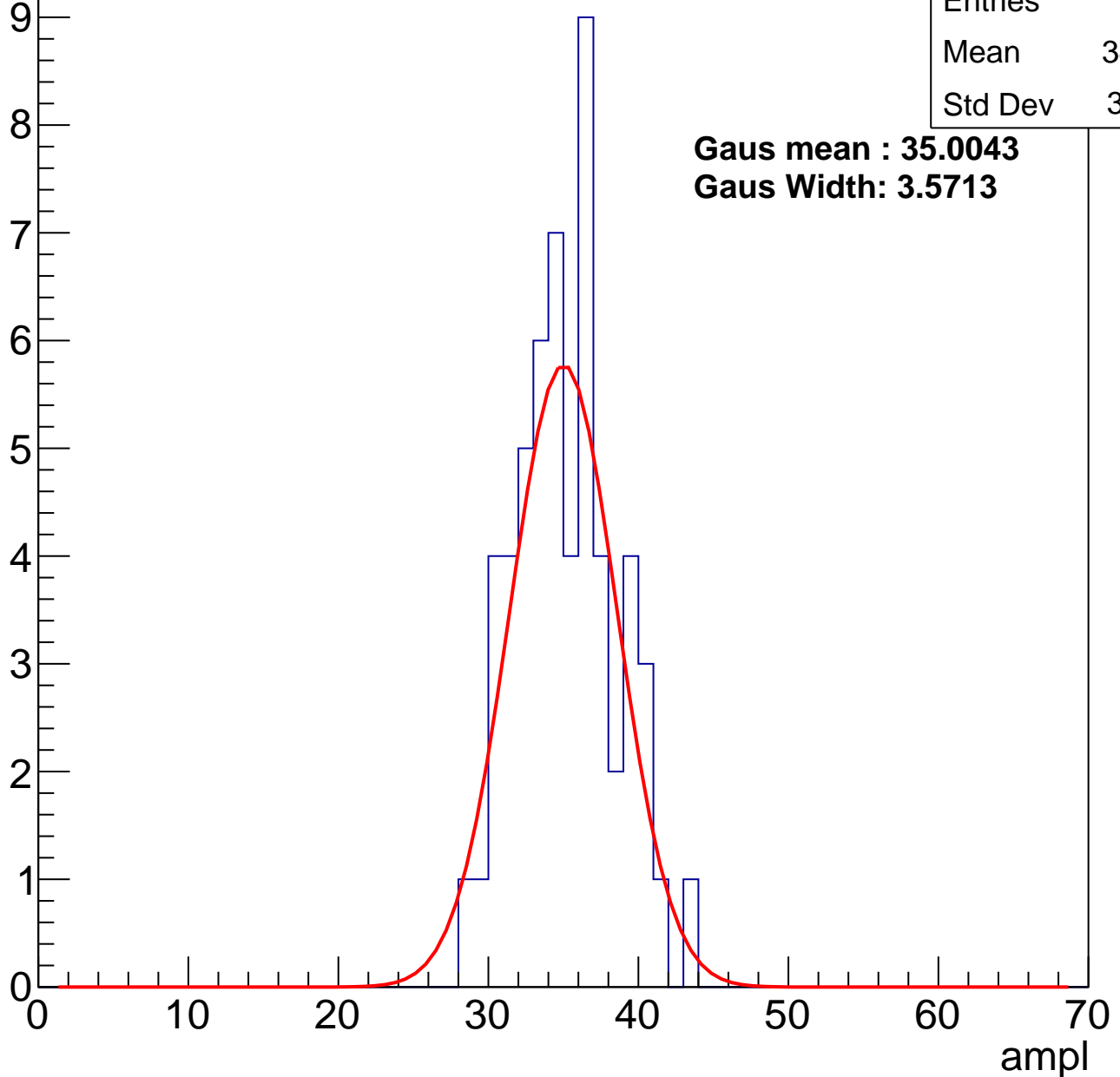
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	34.73
Std Dev	3.281

**Gaus mean : 35.0043**

**Gaus Width: 3.5713**



# B0L102S, U17-ch78, adc2

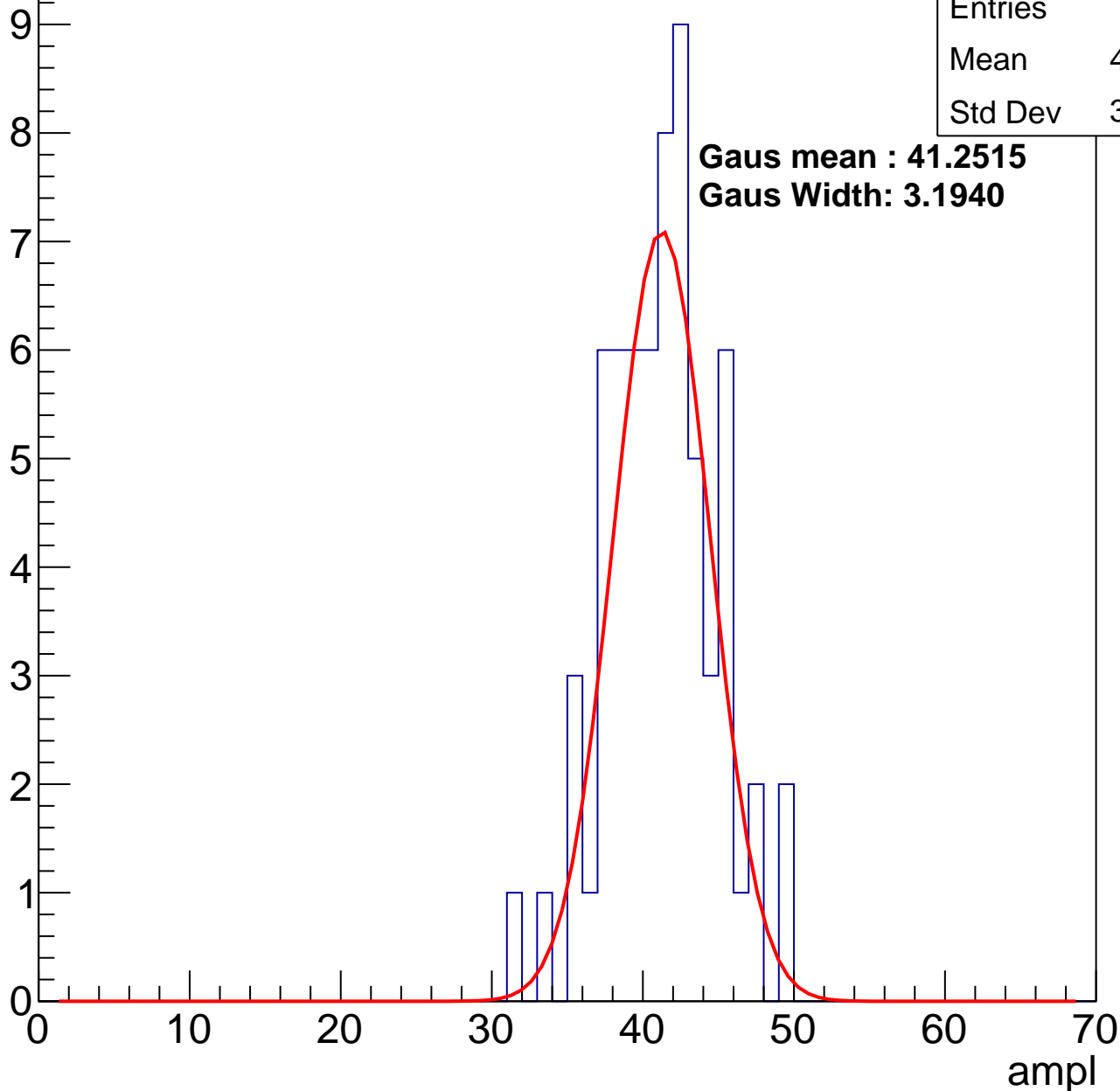
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	40.76
Std Dev	3.589

**Gaus mean : 41.2515**

**Gaus Width: 3.1940**

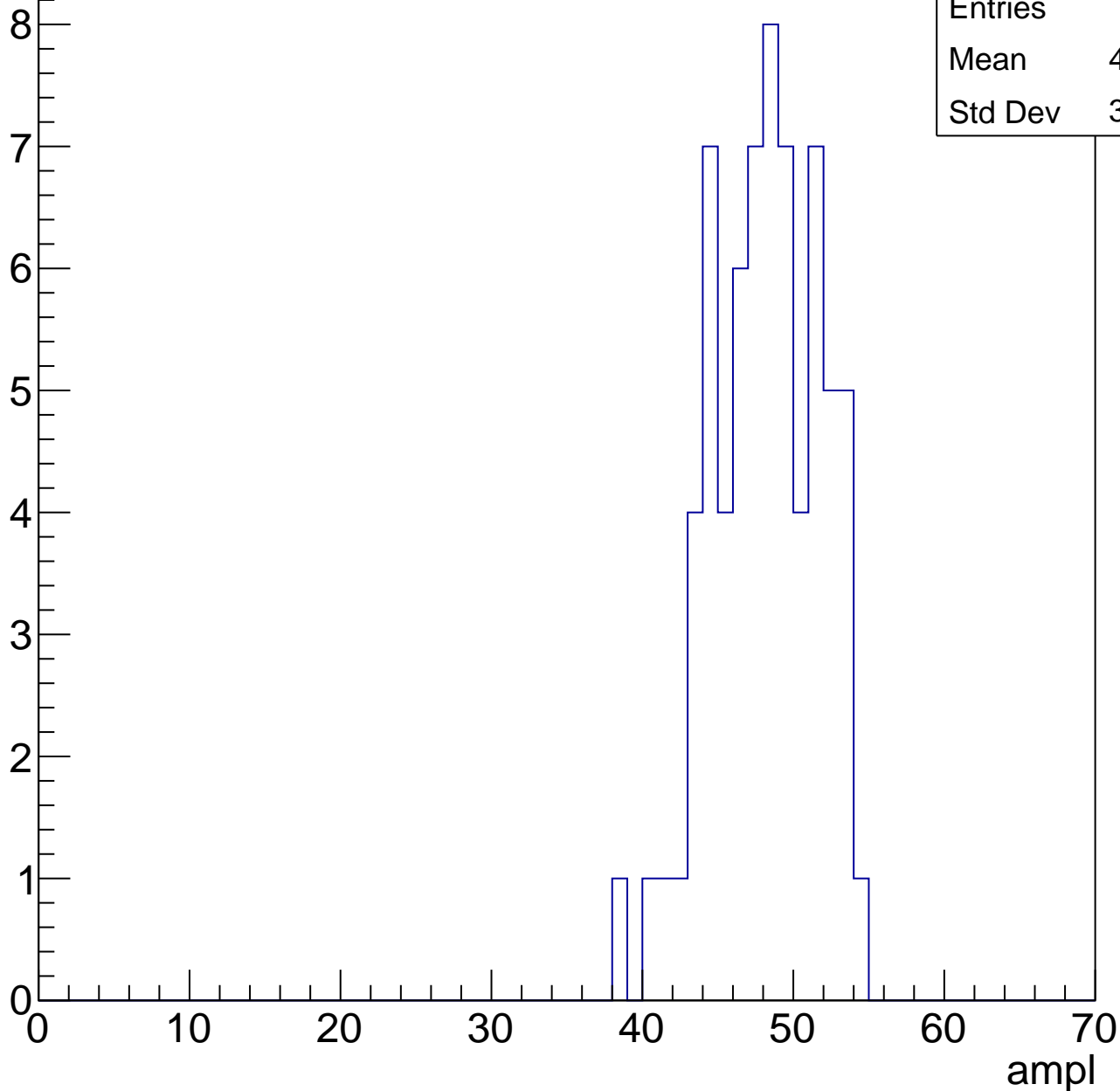


# B0L102S, U17-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	47.67
Std Dev	3.504

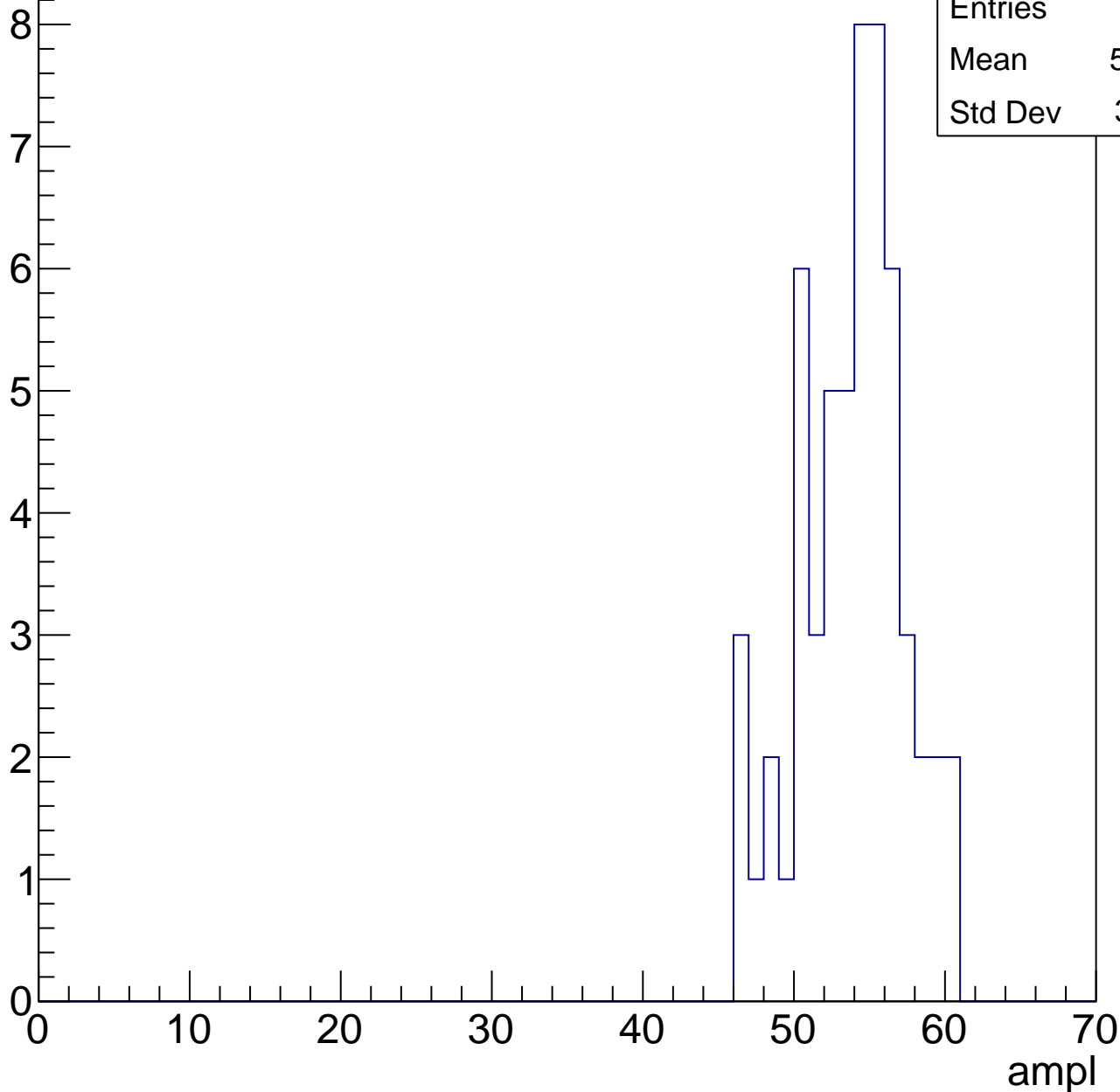


# B0L102S, U17-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	53.35
Std Dev	3.441



# B0L102S, U17-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

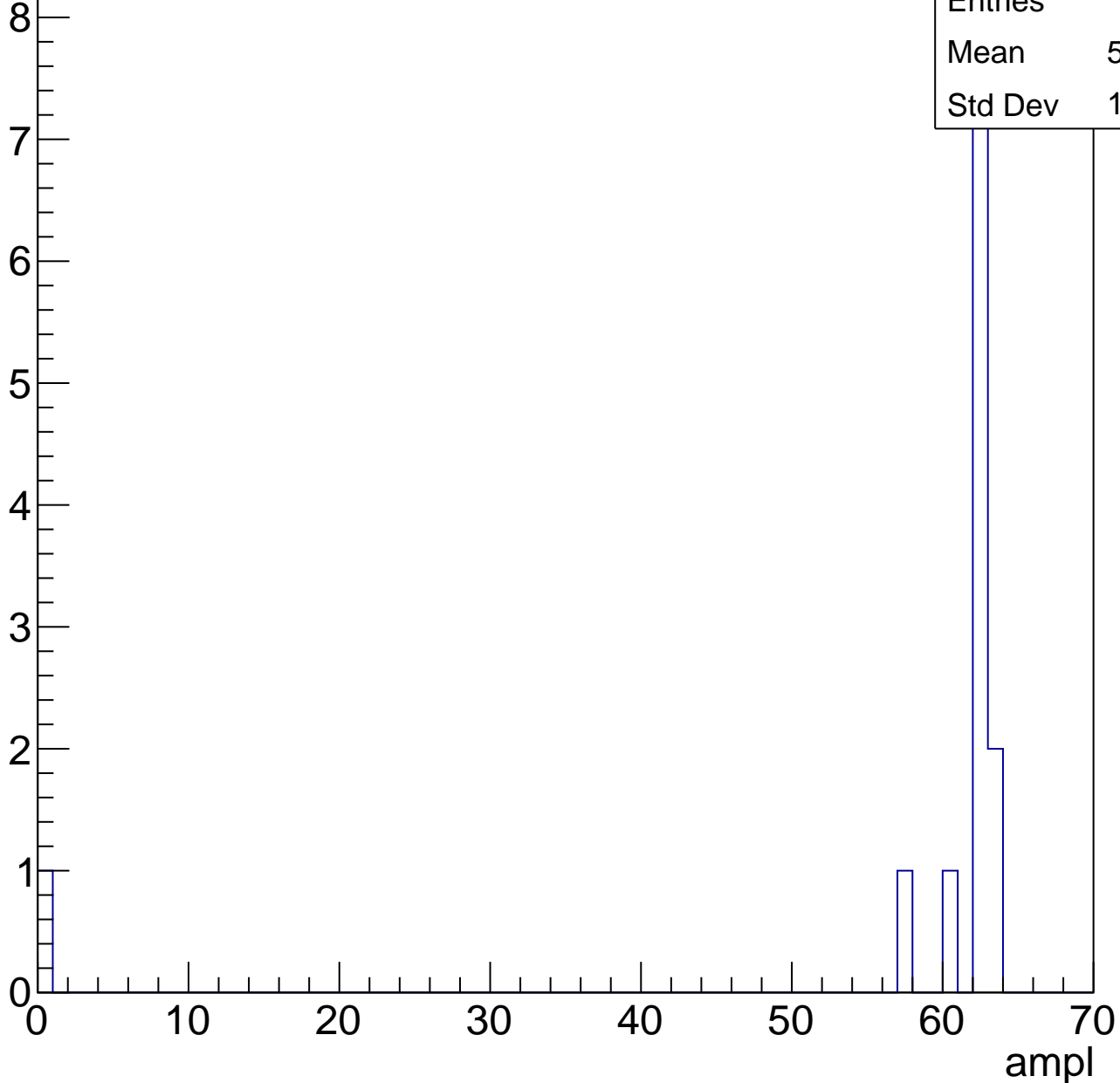
Entries	55
Mean	59.29
Std Dev	2.325

# B0L102S, U17-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	56.85
Std Dev	16.48

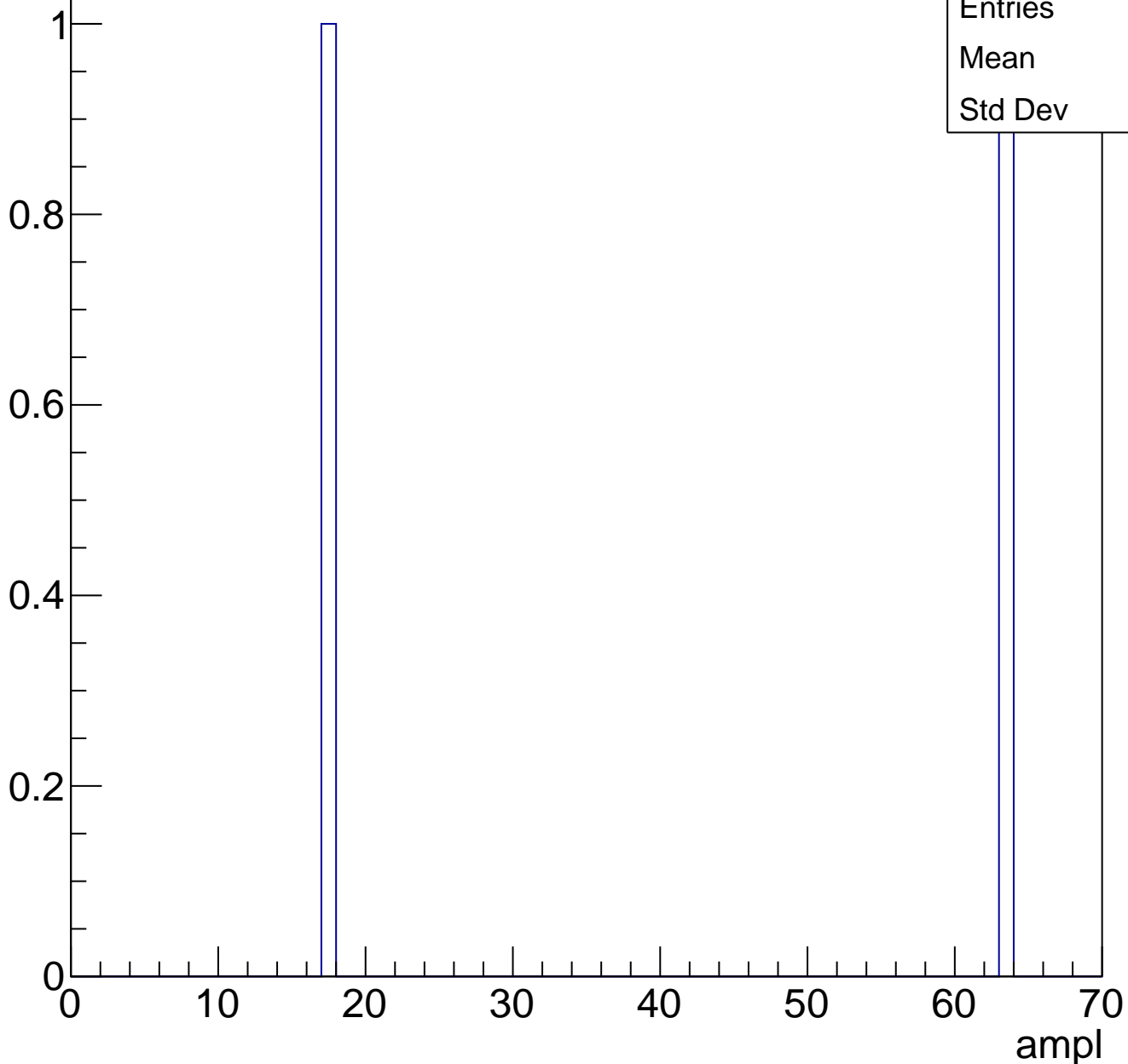




# B0L102S, U17-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	40
Std Dev	23

# B0L102S, U17-ch79, adc0

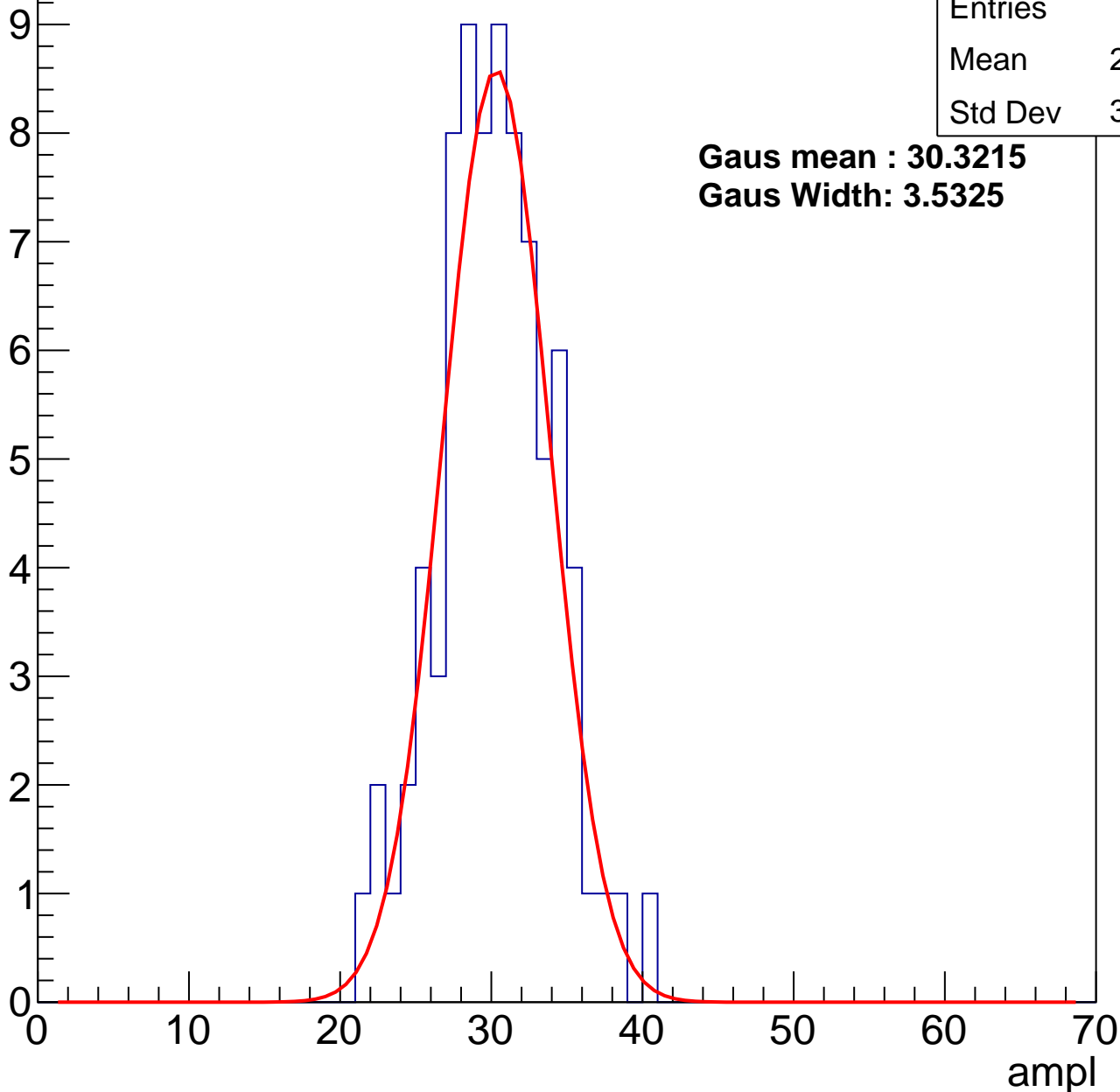
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.83
Std Dev	3.714

**Gaus mean : 30.3215**

**Gaus Width: 3.5325**



# B0L102S, U17-ch79, adc1

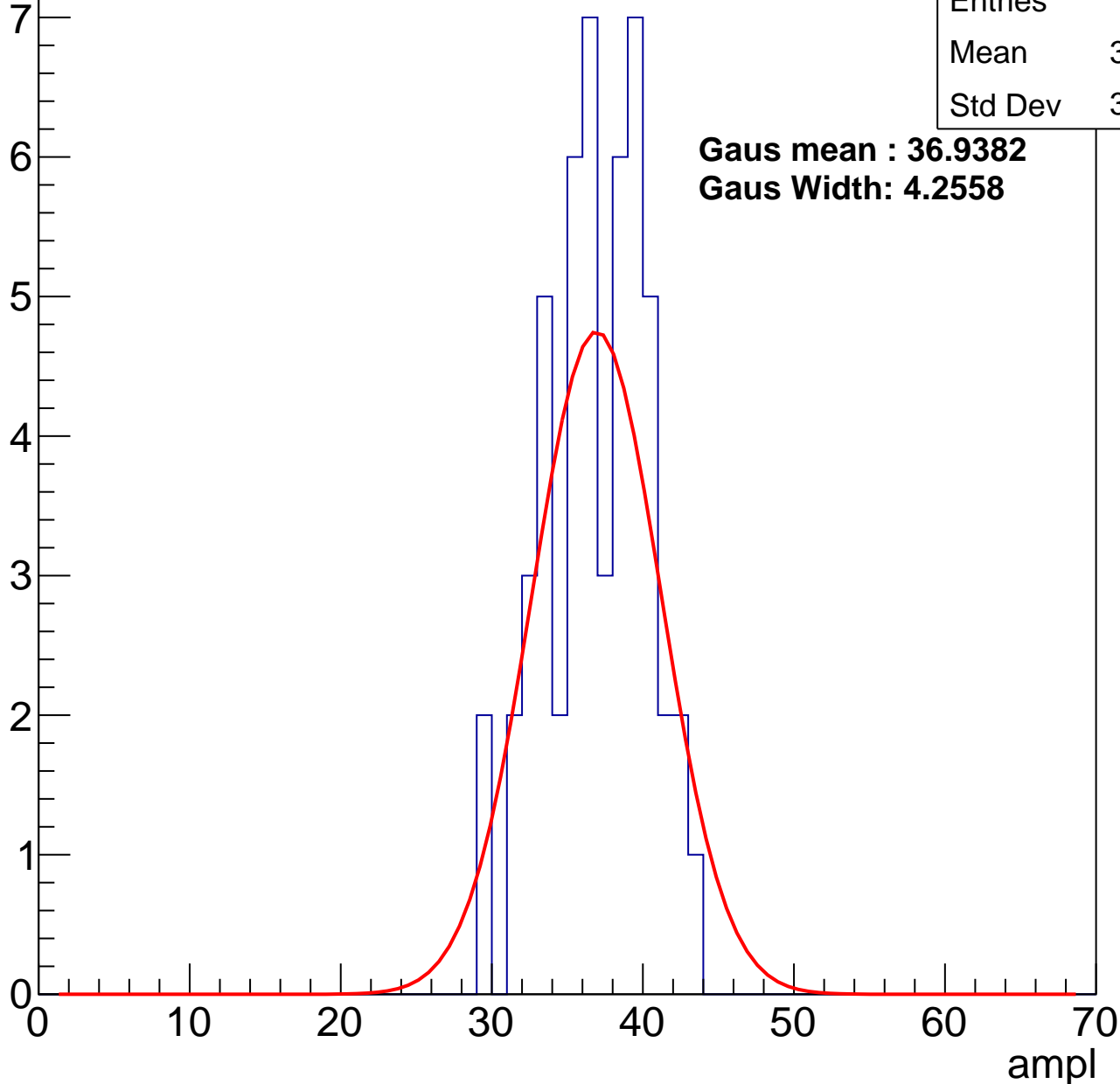
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	36.45
Std Dev	3.323

**Gaus mean : 36.9382**

**Gaus Width: 4.2558**



# B0L102S, U17-ch79, adc2

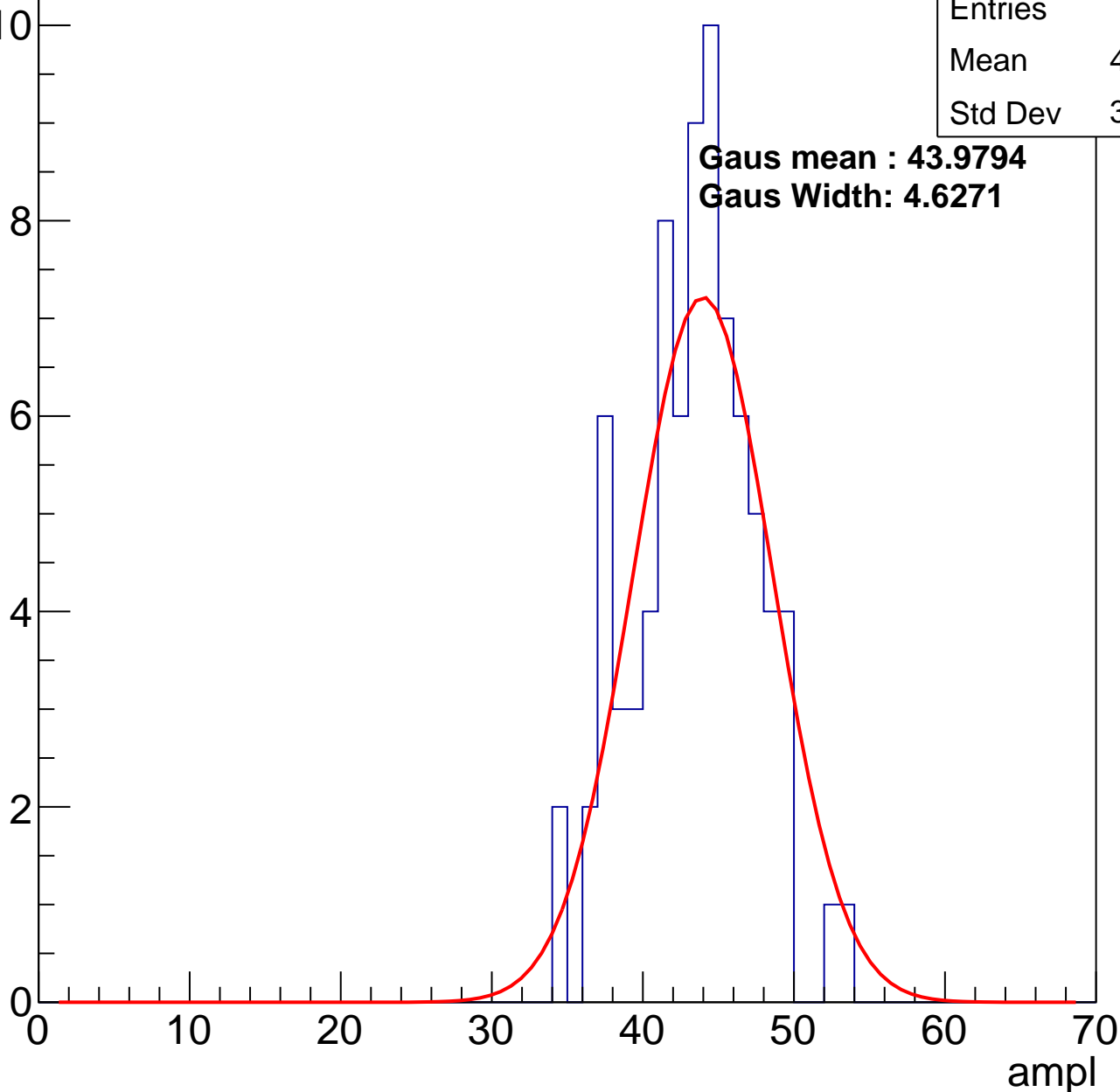
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	81
Mean	42.95
Std Dev	3.959

**Gaus mean : 43.9794**

**Gaus Width: 4.6271**

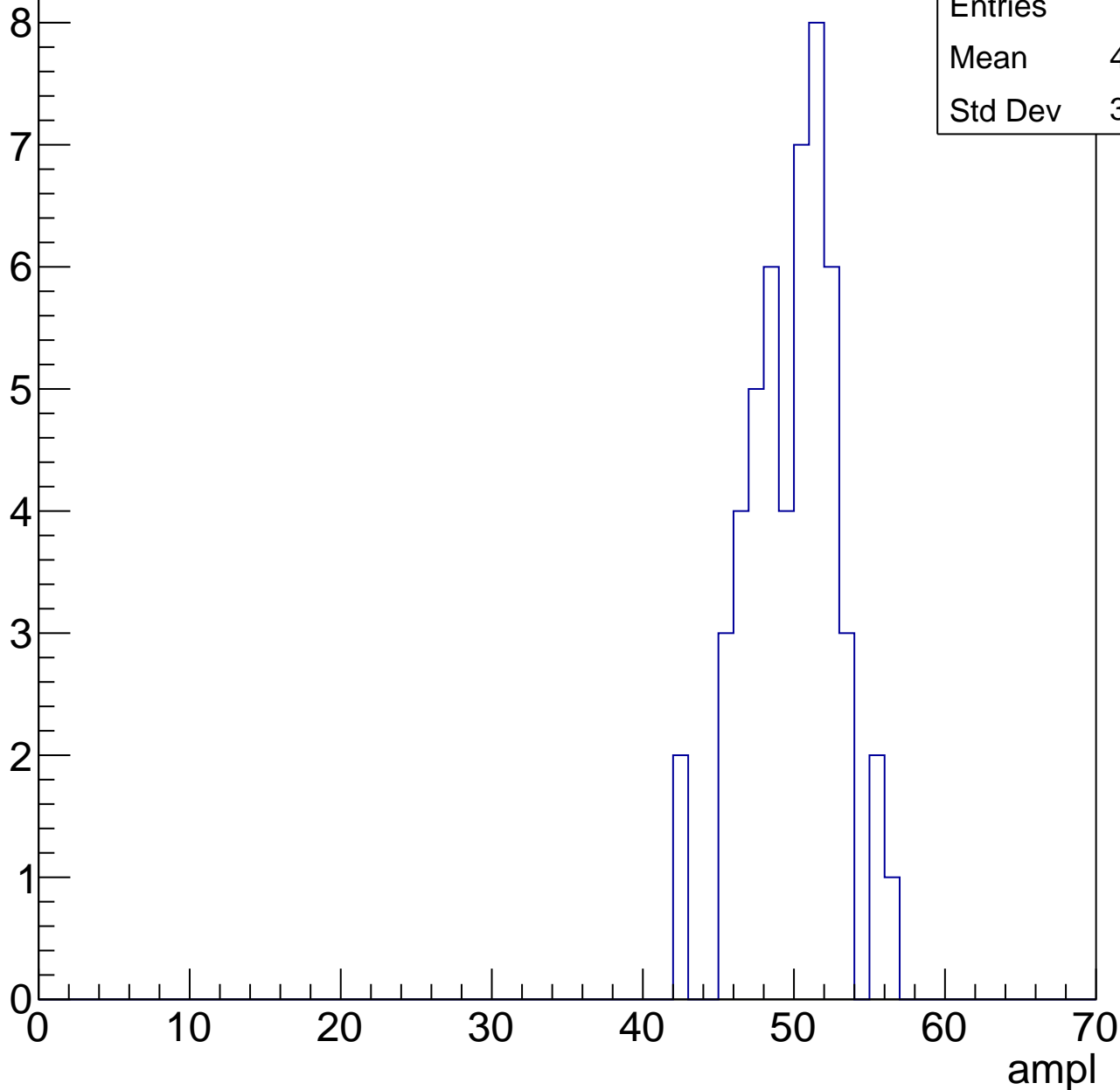


# B0L102S, U17-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	49.35
Std Dev	3.015



# B0L102S, U17-ch79, adc4

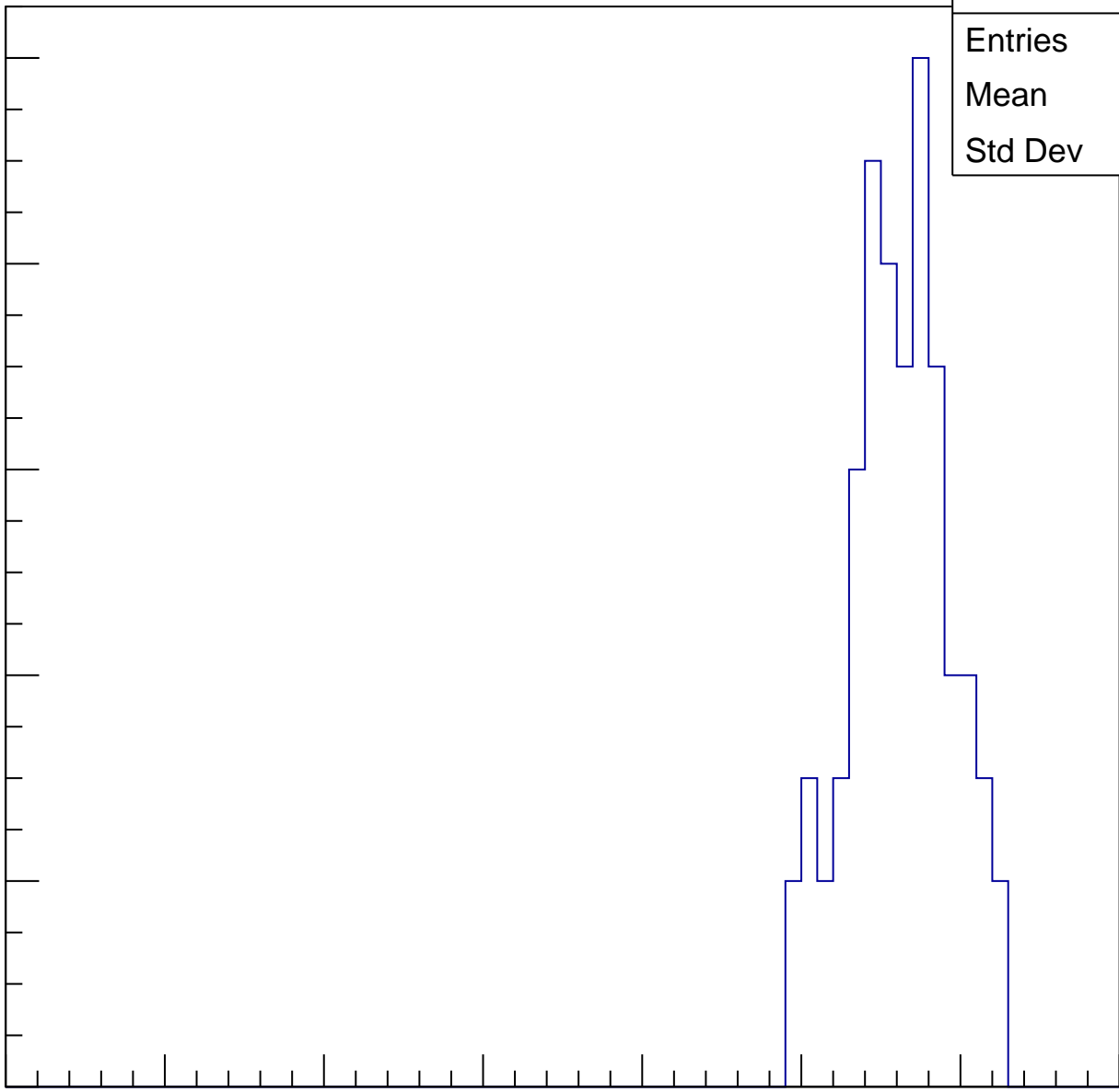
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	70
Mean	55.73
Std Dev	3.121

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl

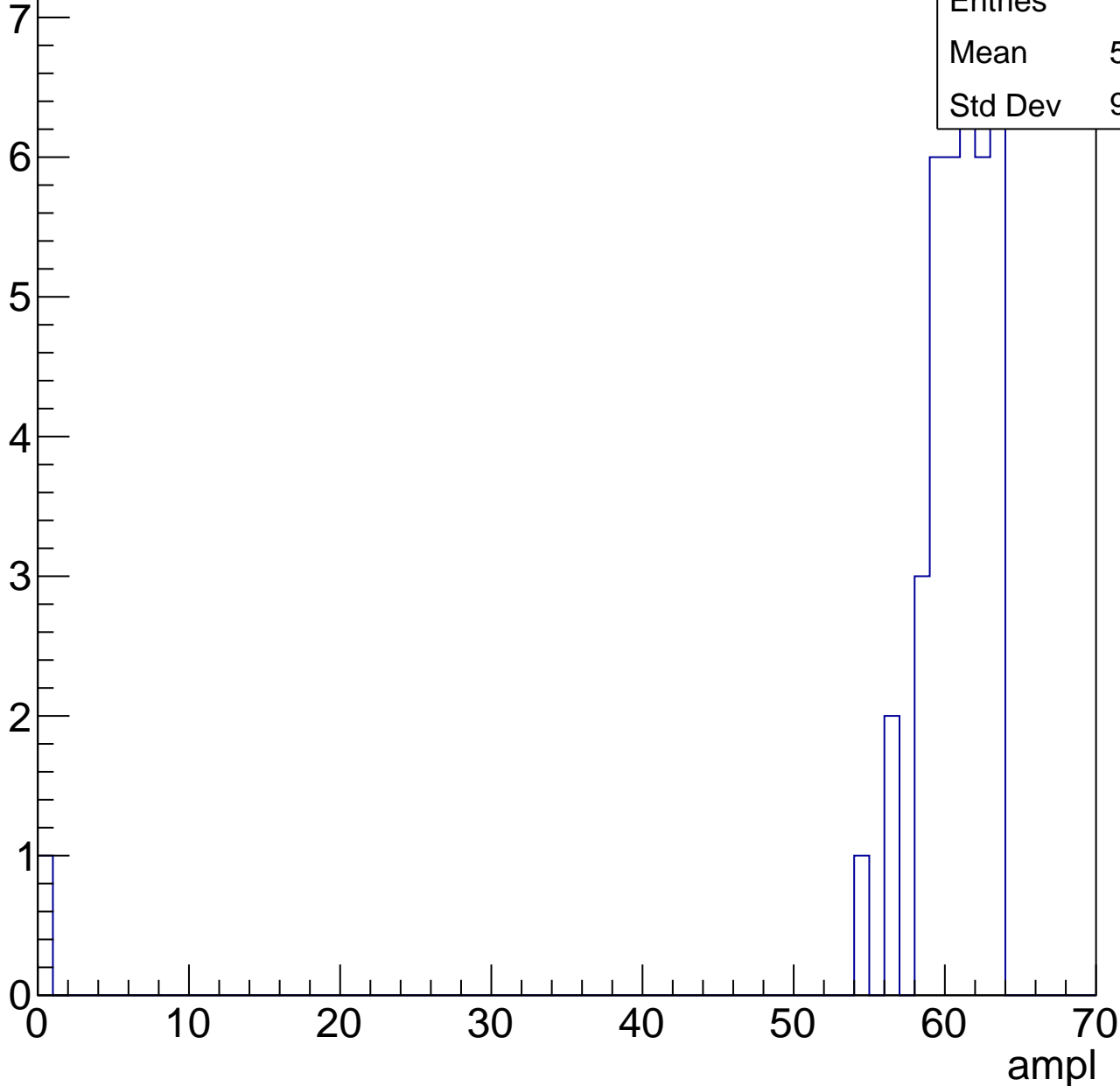


# B0L102S, U17-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.82
Std Dev	9.774



# B0L102S, U17-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L102S, U17-ch80, adc0

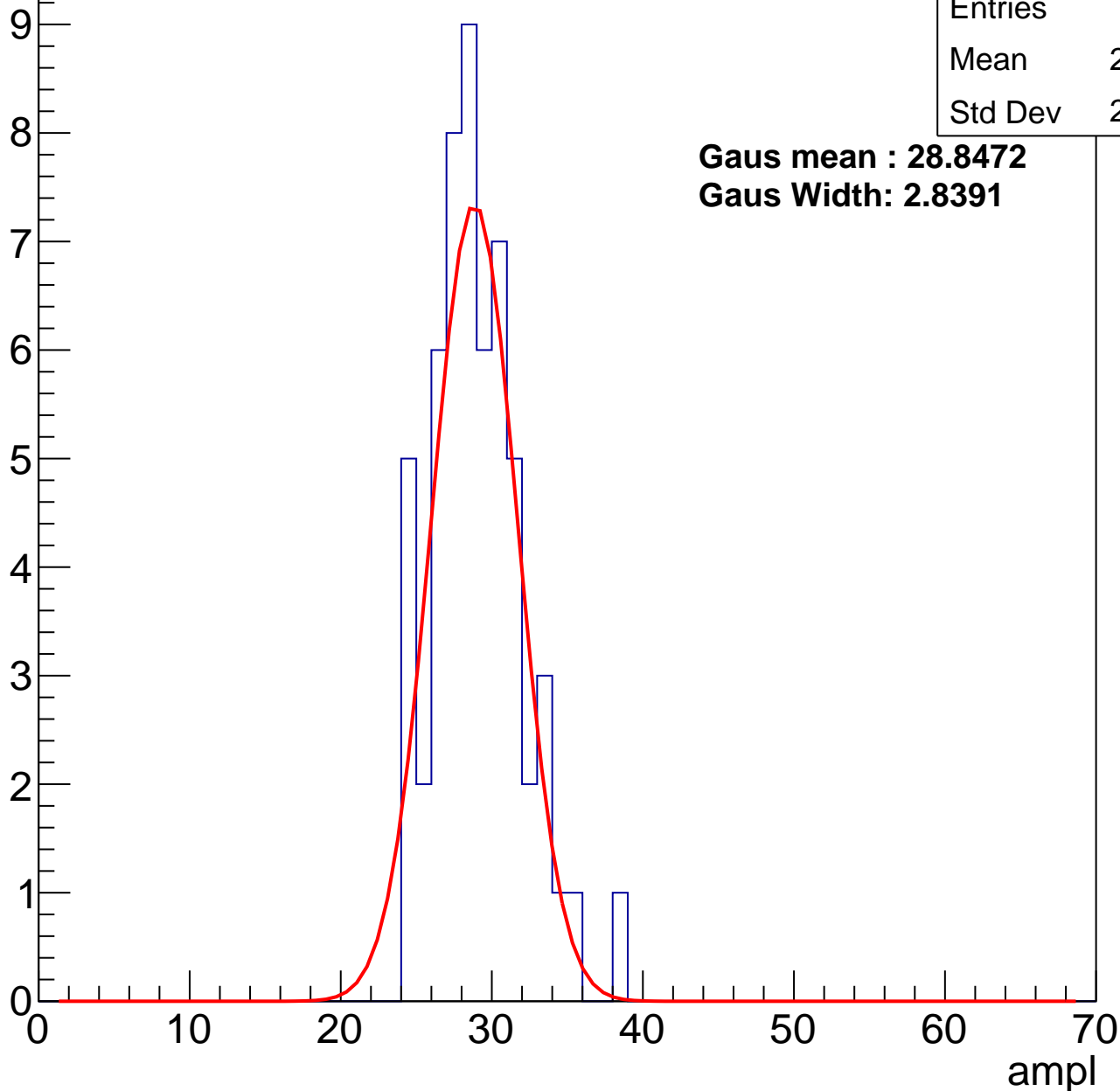
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	28.62
Std Dev	2.925

**Gaus mean : 28.8472**

**Gaus Width: 2.8391**



# B0L102S, U17-ch80, adc1

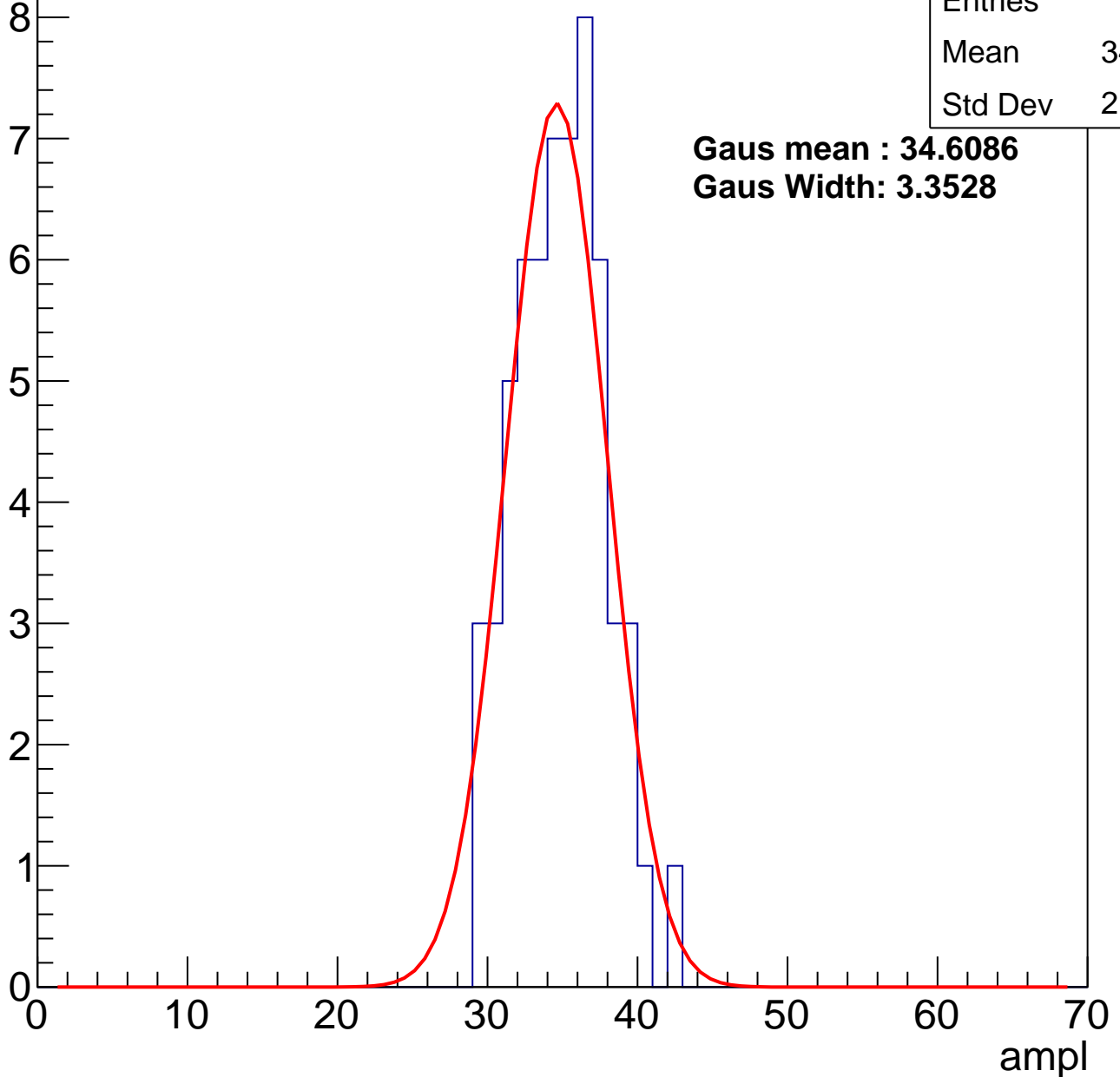
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	34.37
Std Dev	2.928

**Gaus mean : 34.6086**

**Gaus Width: 3.3528**



# B0L102S, U17-ch80, adc2

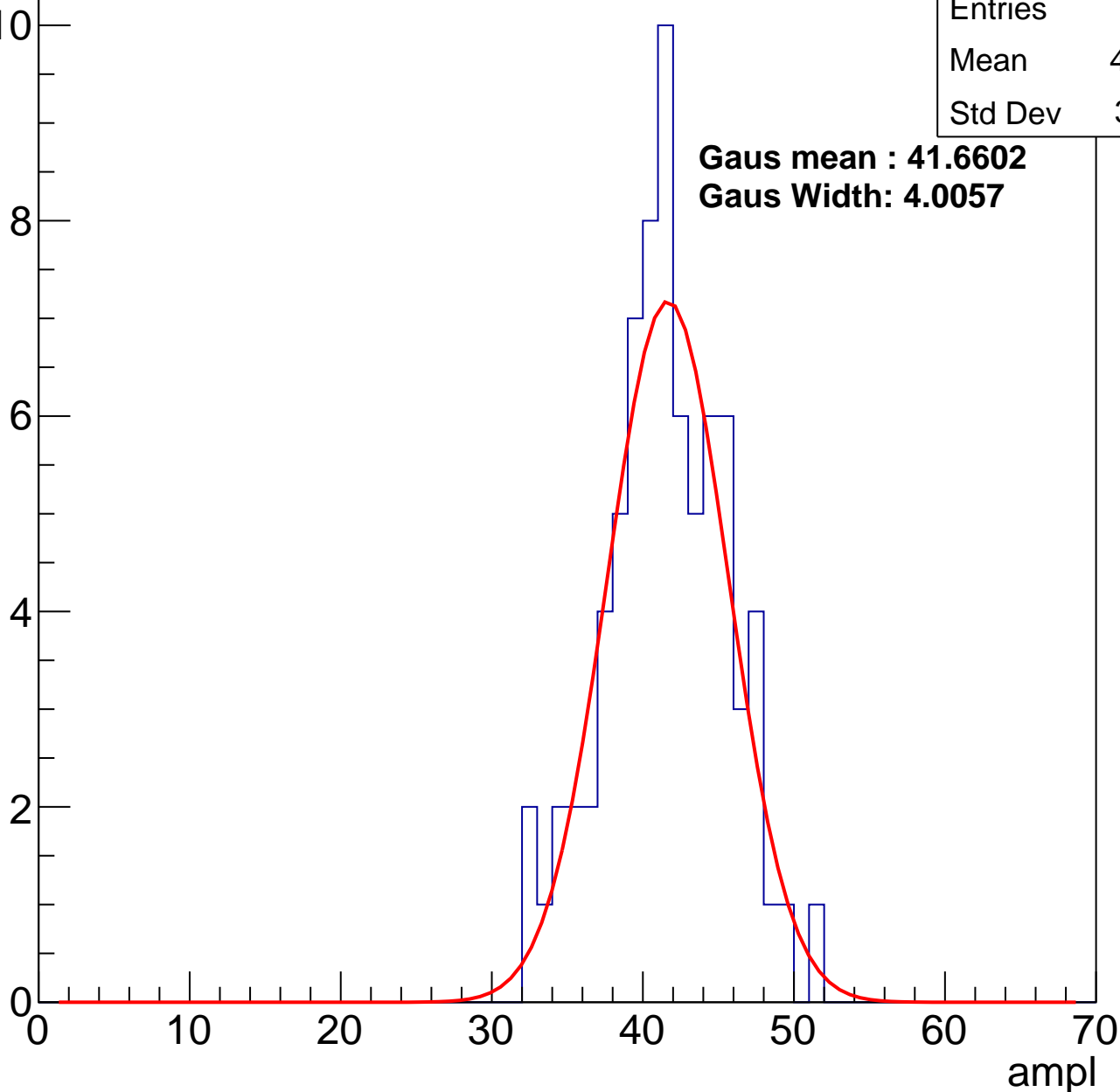
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	41.09
Std Dev	3.971

**Gaus mean : 41.6602**

**Gaus Width: 4.0057**

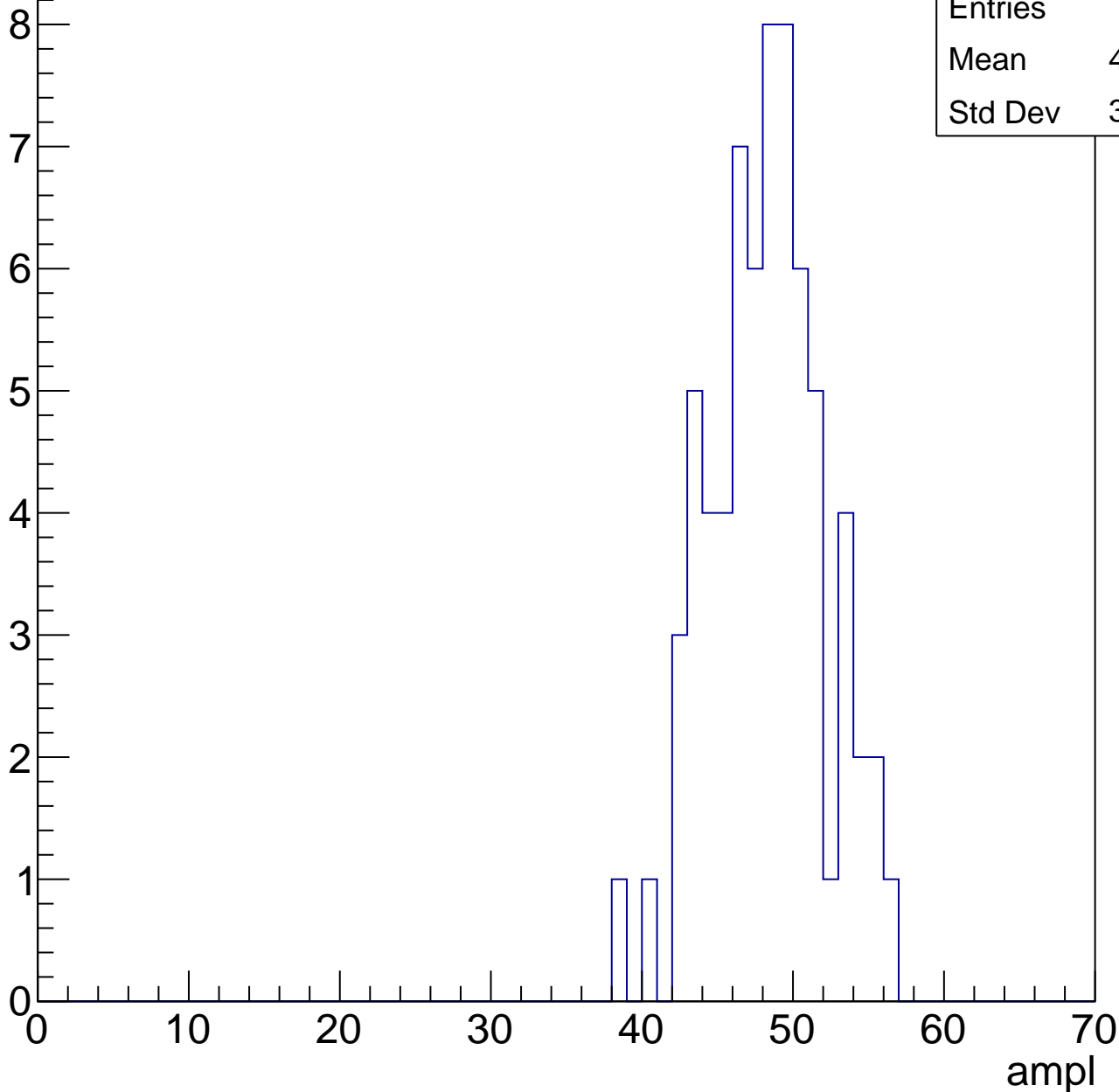


# B0L102S, U17-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	47.76
Std Dev	3.746

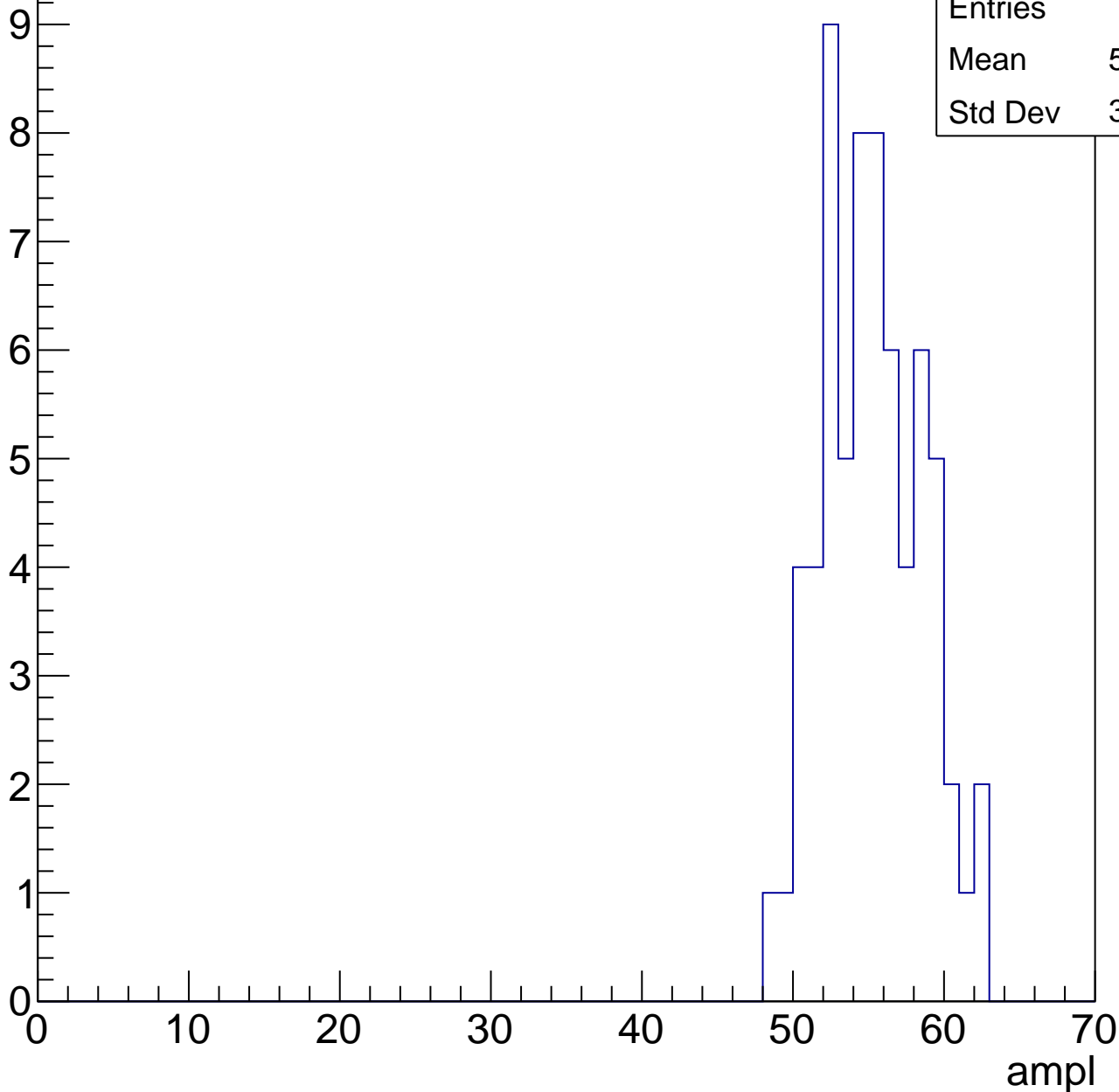


# B0L102S, U17-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	54.82
Std Dev	3.247

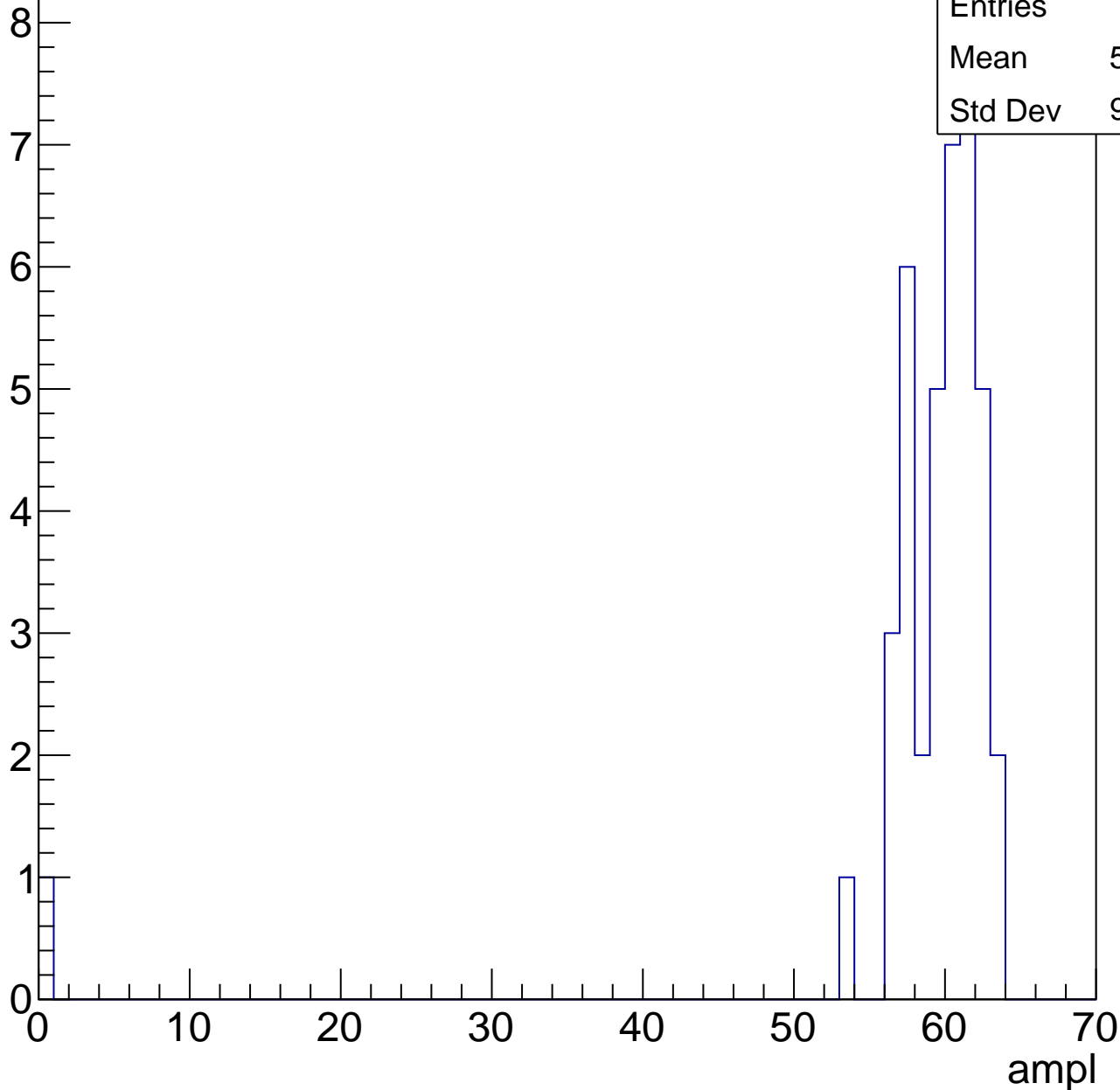


# B0L102S, U17-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	40
Mean	57.95
Std Dev	9.542

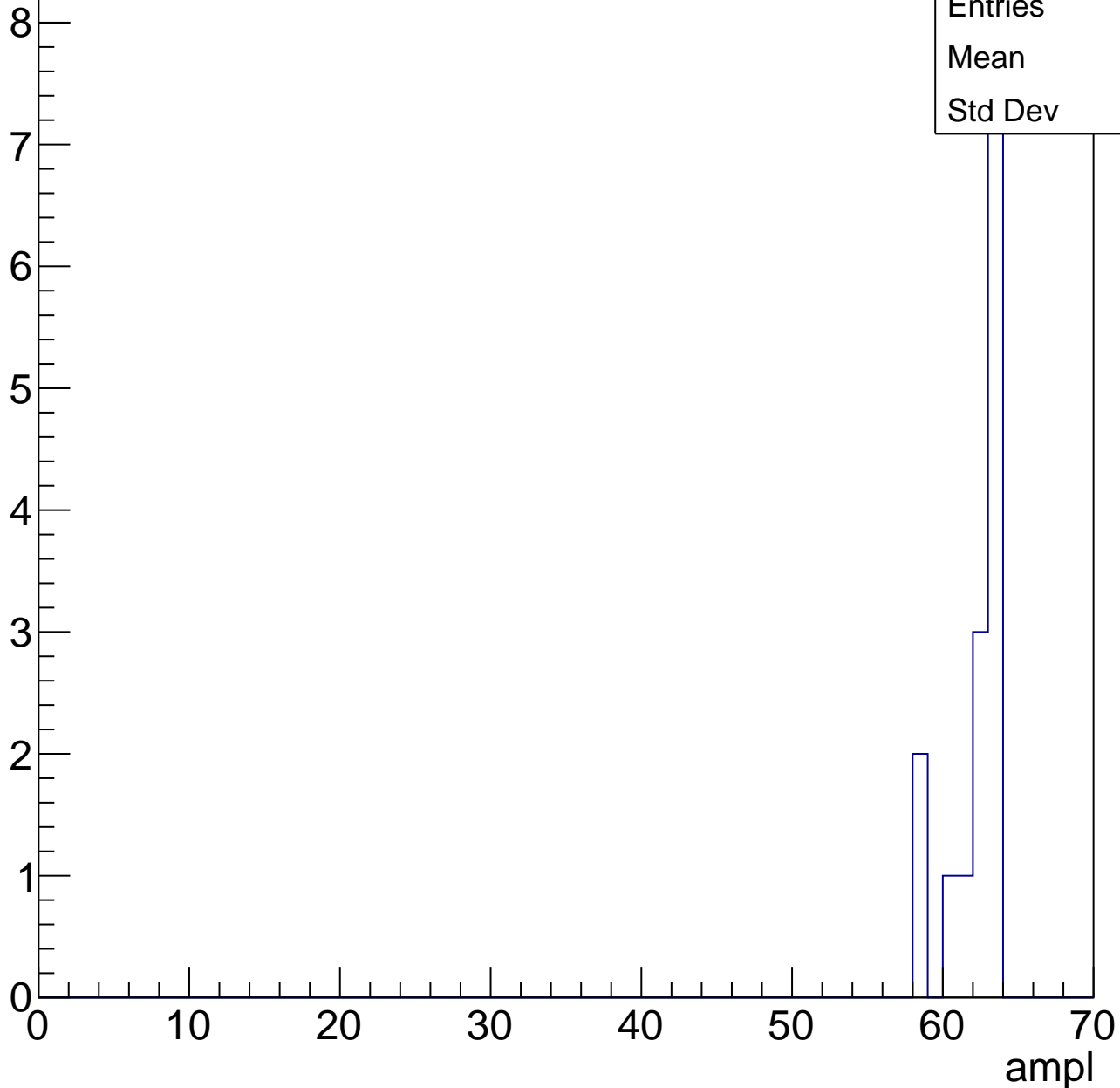


# B0L102S, U17-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.8
Std Dev	1.72





# B0L102S, U17-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L102S, U17-ch81, adc0

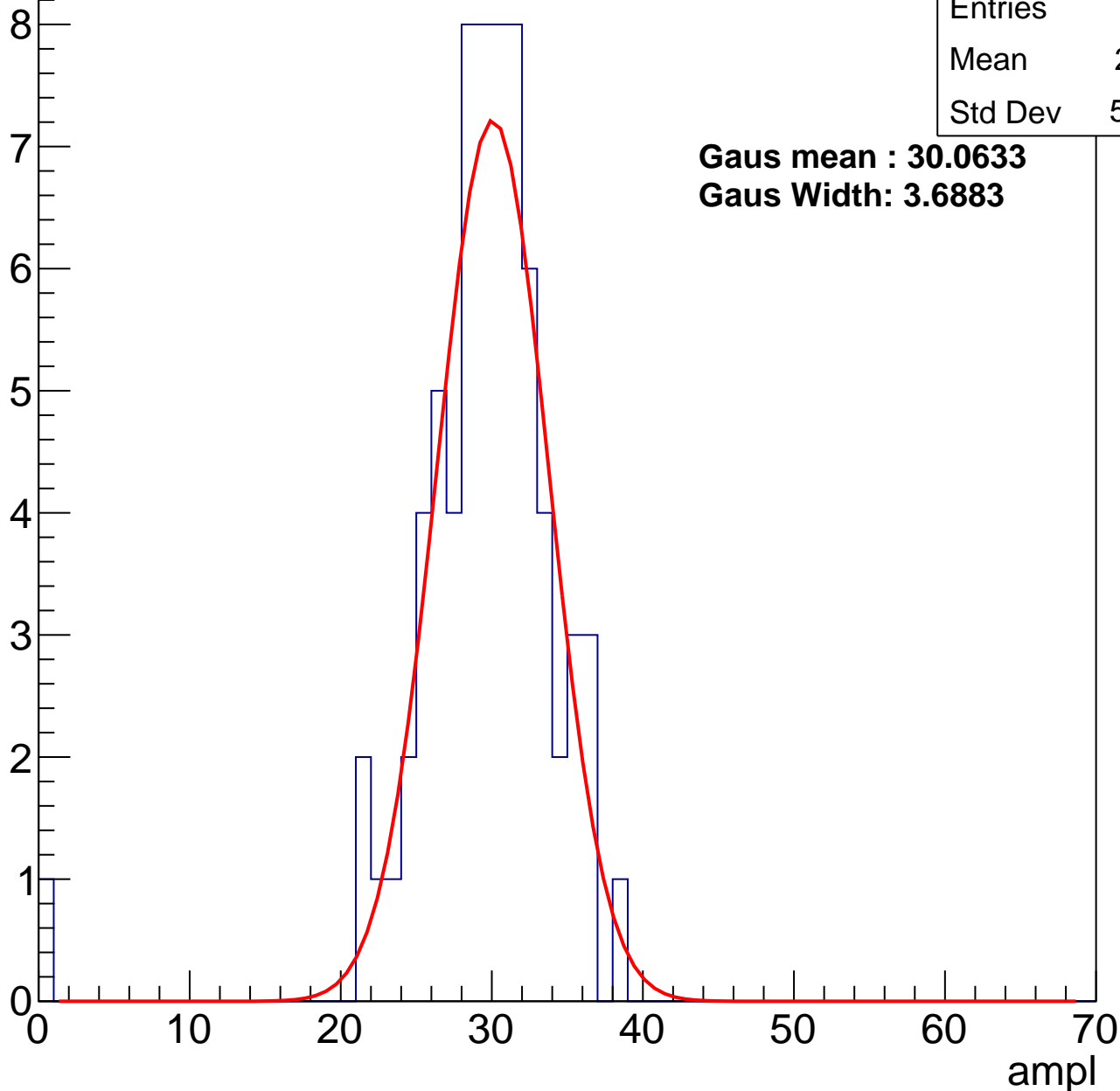
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	29.01
Std Dev	5.008

**Gaus mean : 30.0633**

**Gaus Width: 3.6883**



# B0L102S, U17-ch81, adc1

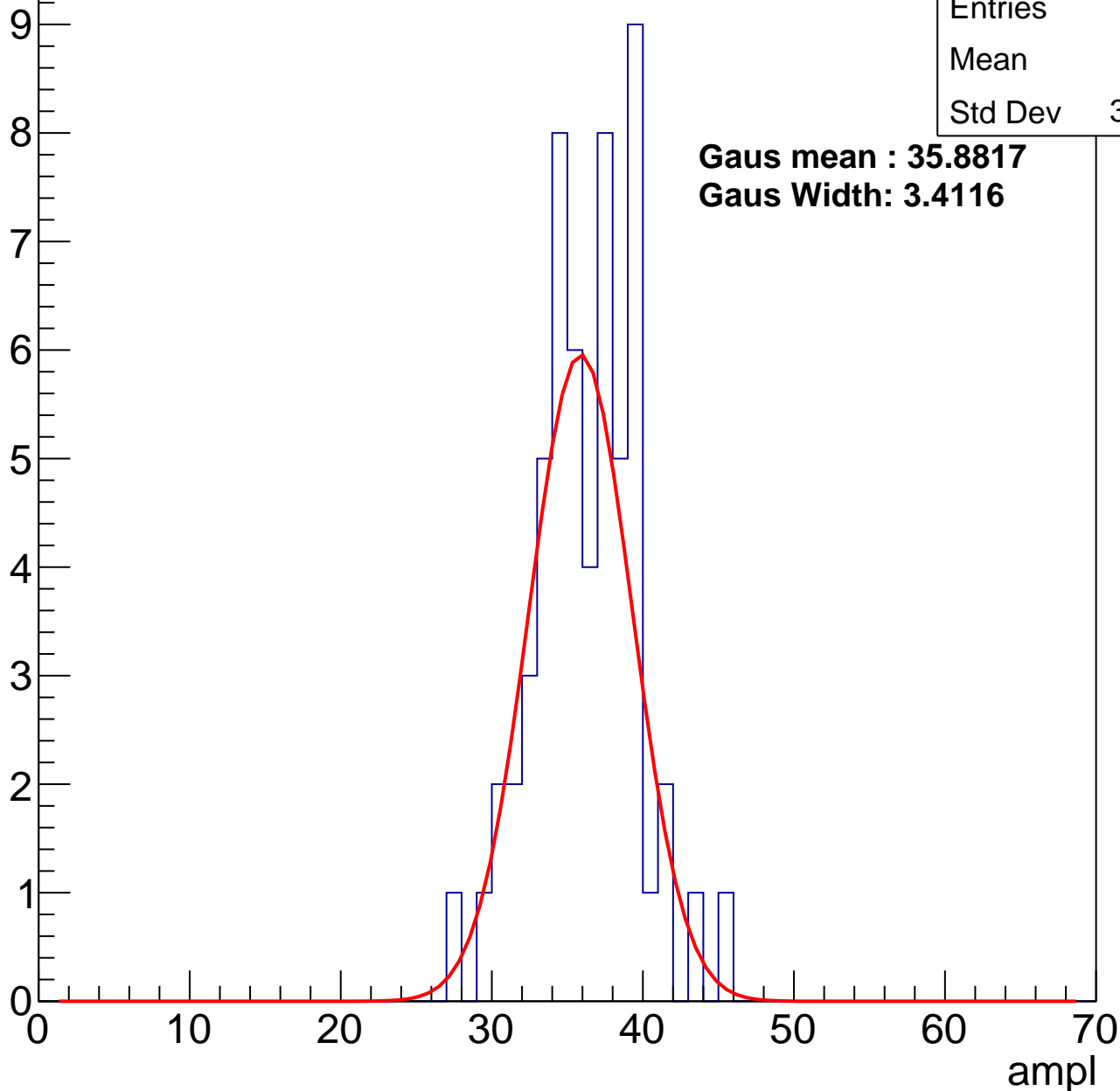
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	35.8
Std Dev	3.409

**Gaus mean : 35.8817**

**Gaus Width: 3.4116**



# B0L102S, U17-ch81, adc2

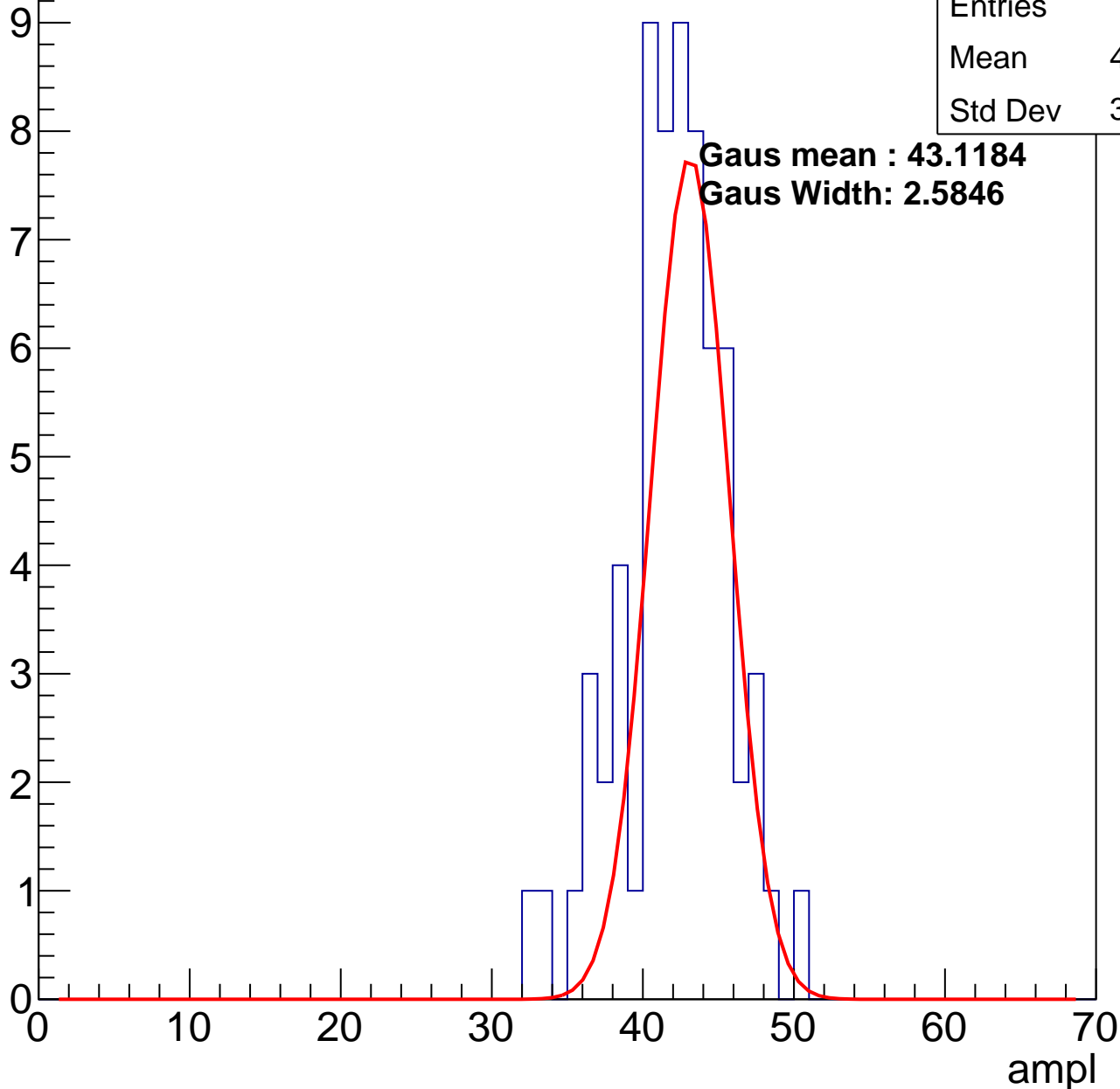
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.64
Std Dev	3.458

**Gaus mean : 43.1184**

**Gaus Width: 2.5846**

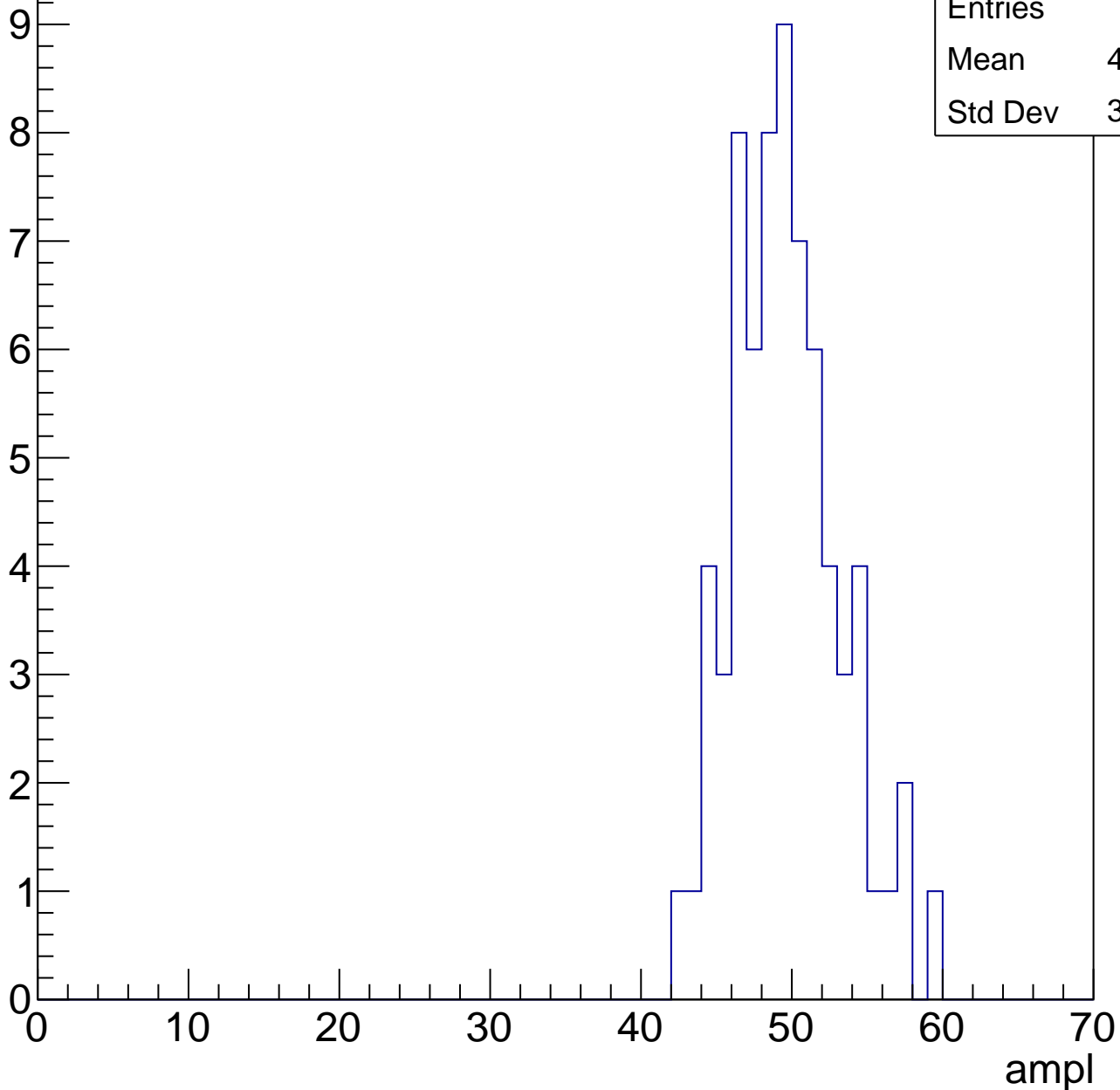


# B0L102S, U17-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

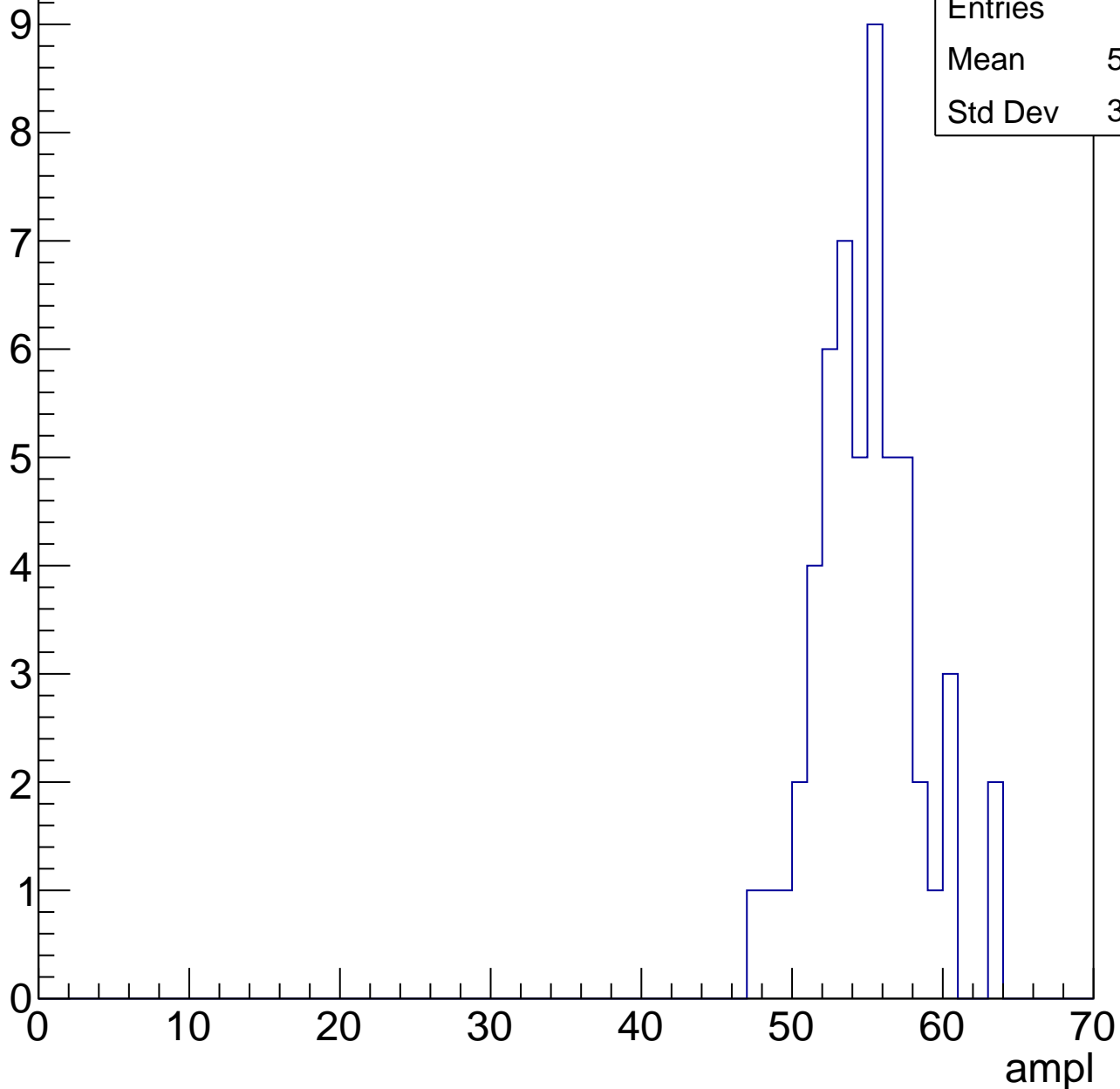
Entries	69
Mean	49.19
Std Dev	3.536



# B0L102S, U17-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



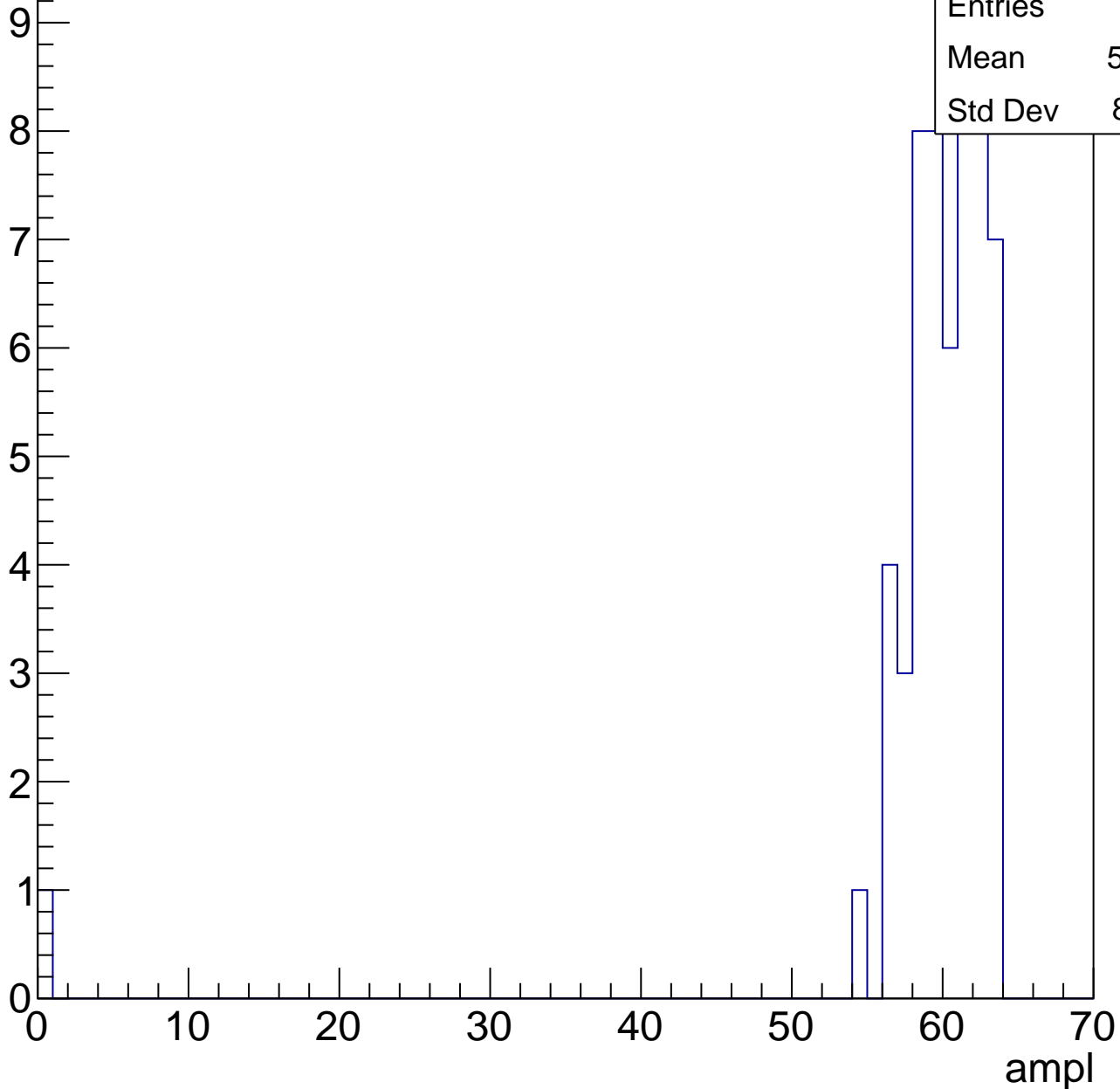
Entries	54
Mean	54.48
Std Dev	3.326

# B0L102S, U17-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

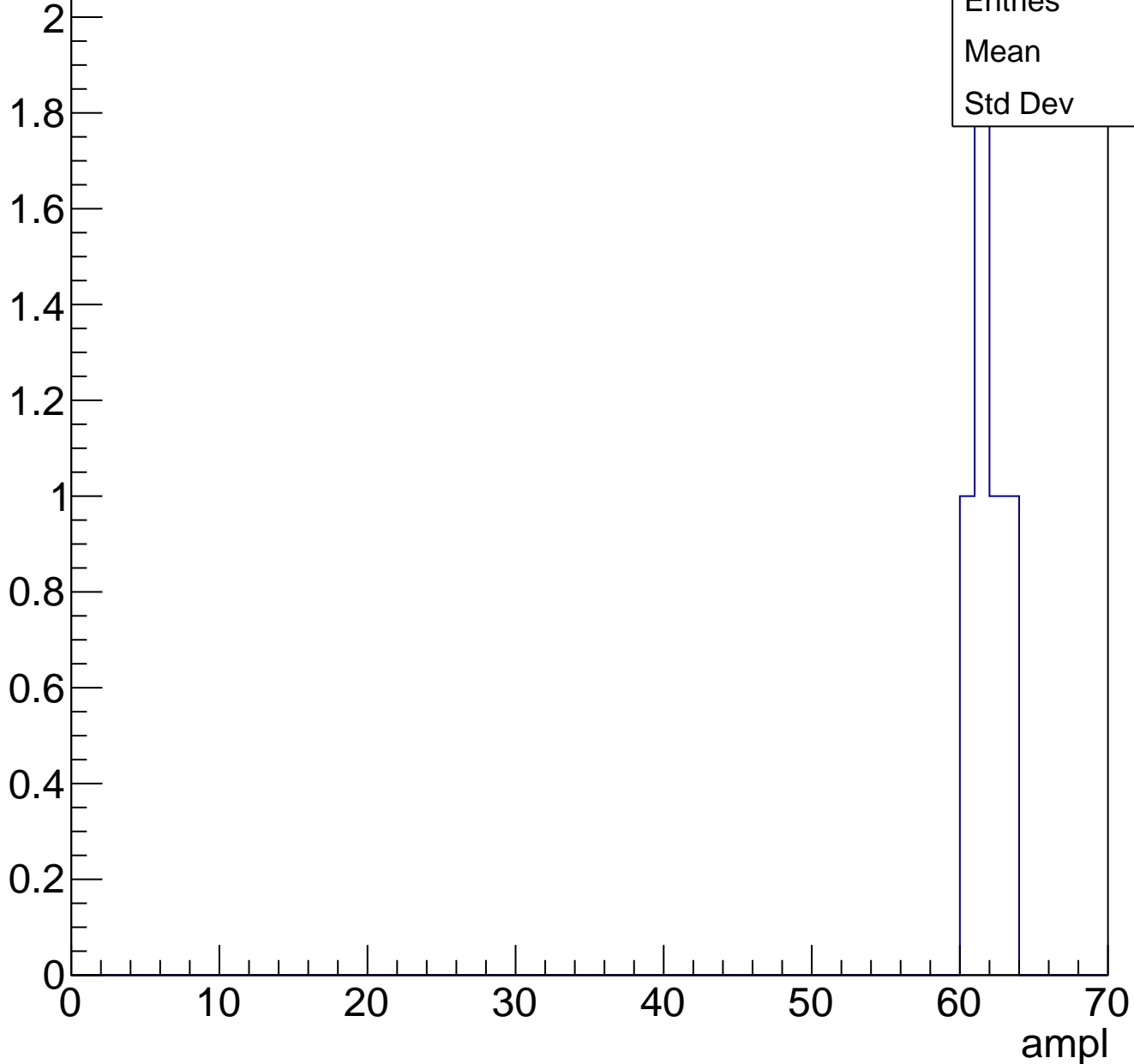
Entries	55
Mean	58.76
Std Dev	8.301



# B0L102S, U17-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

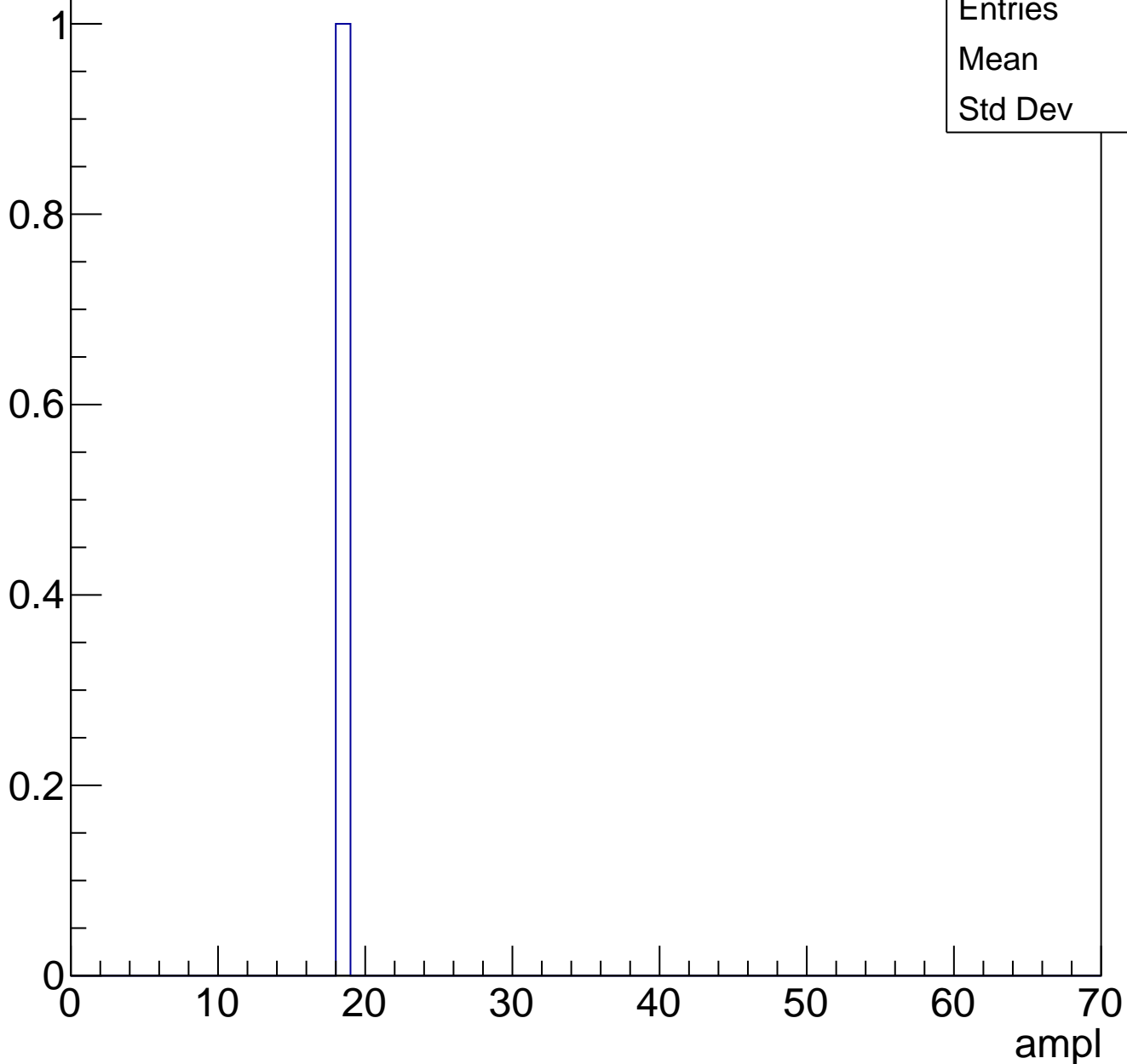




# B0L102S, U17-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch82, adc0

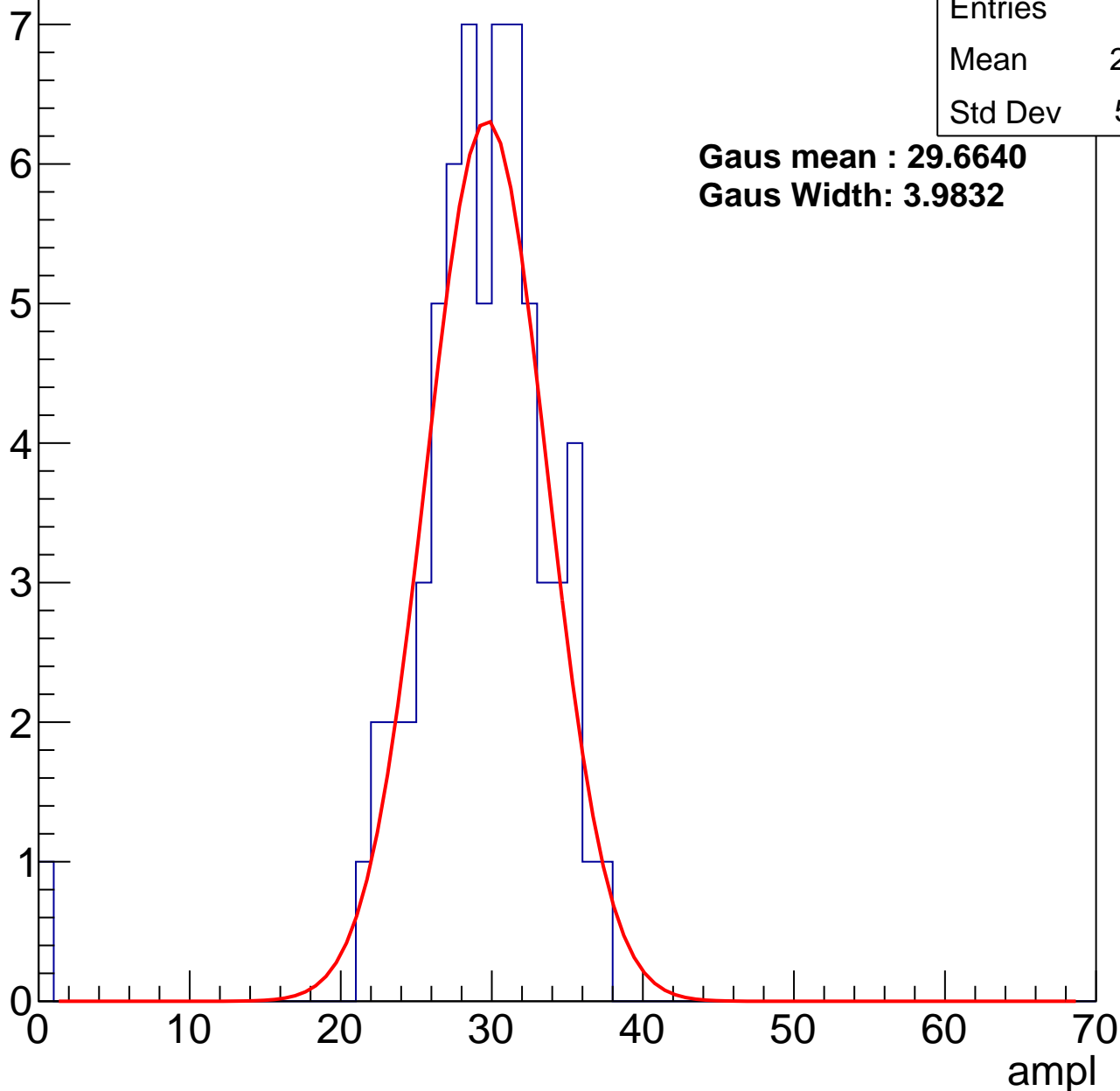
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	28.74
Std Dev	5.121

**Gaus mean : 29.6640**

**Gaus Width: 3.9832**



# B0L102S, U17-ch82, adc1

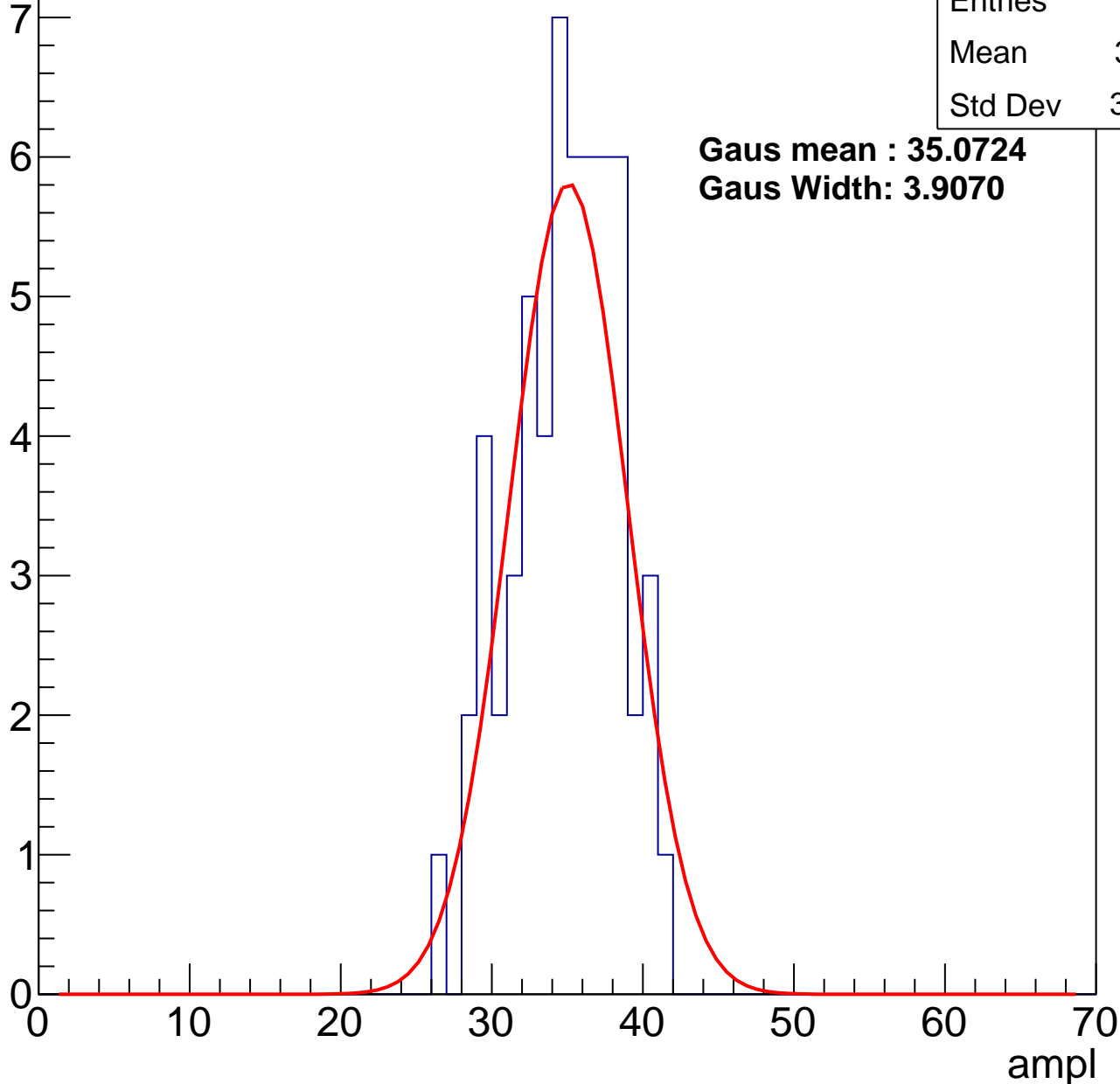
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	34.41
Std Dev	3.464

**Gaus mean : 35.0724**

**Gaus Width: 3.9070**



# B0L102S, U17-ch82, adc2

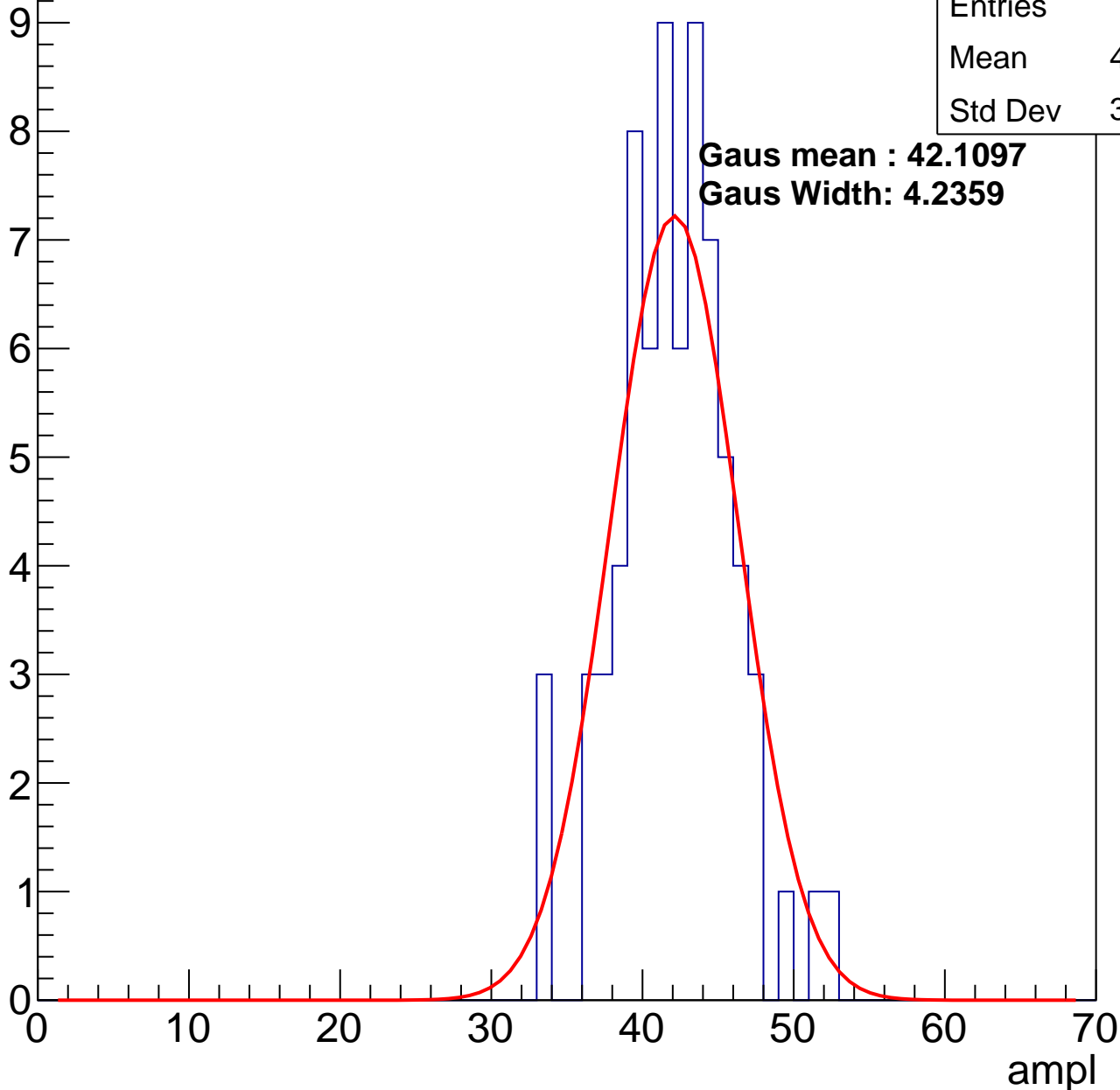
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	41.64
Std Dev	3.769

**Gaus mean : 42.1097**

**Gaus Width: 4.2359**

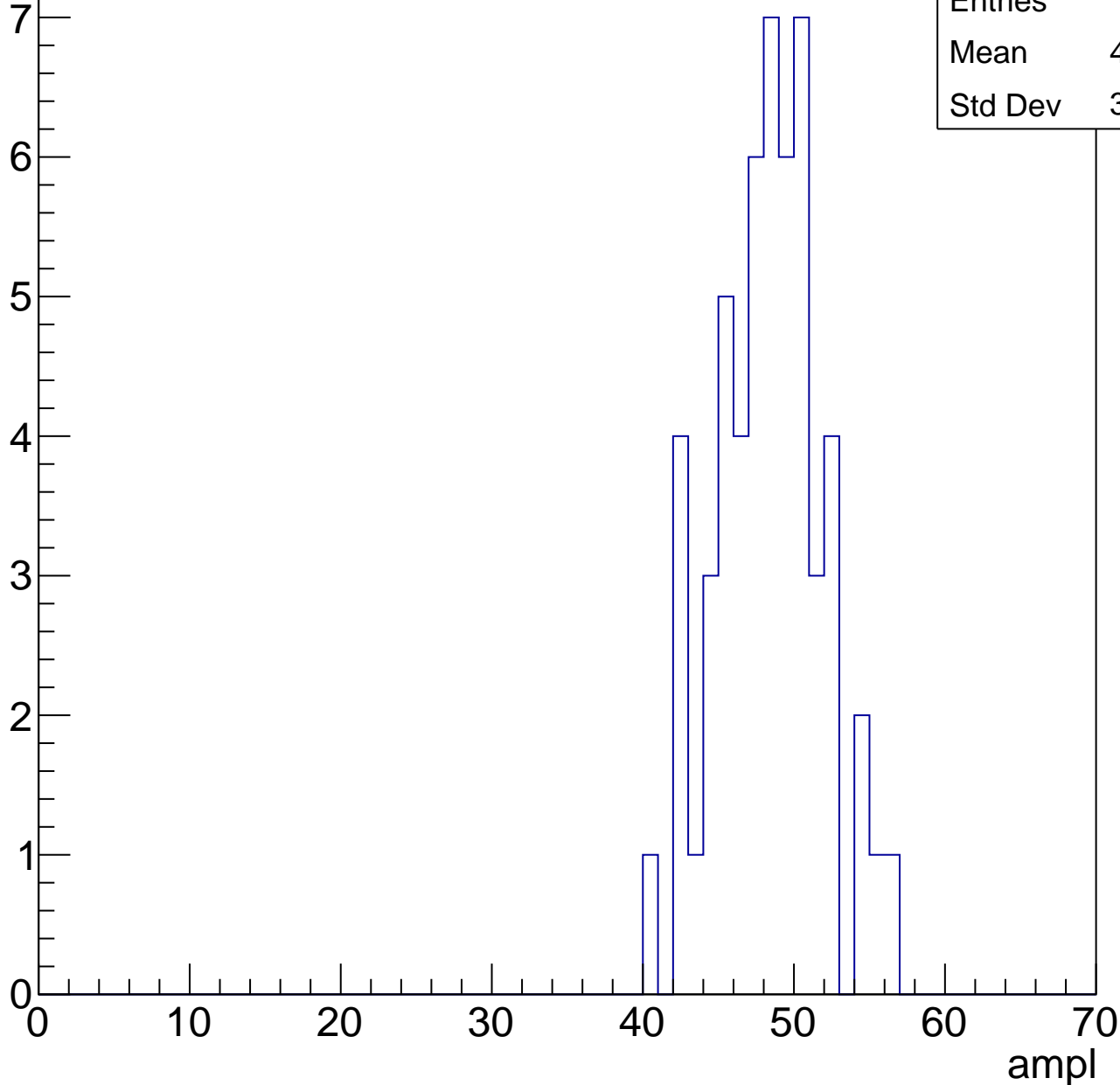


# B0L102S, U17-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	47.89
Std Dev	3.457



# B0L102S, U17-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	63
Mean	53.38
Std Dev	3.169

Entry

10

8

6

4

2

0

0

10

20

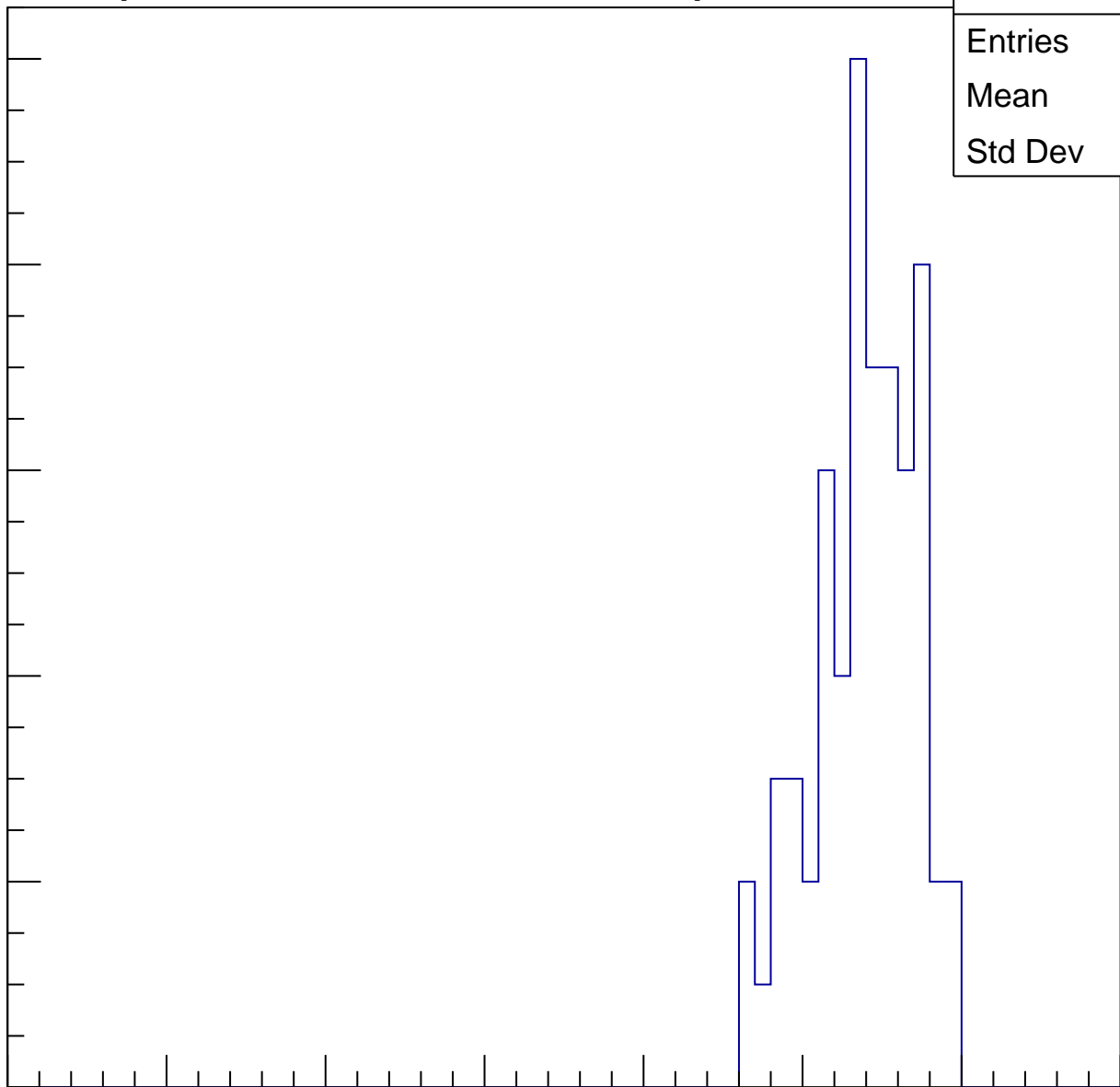
30

40

50

60

ampl

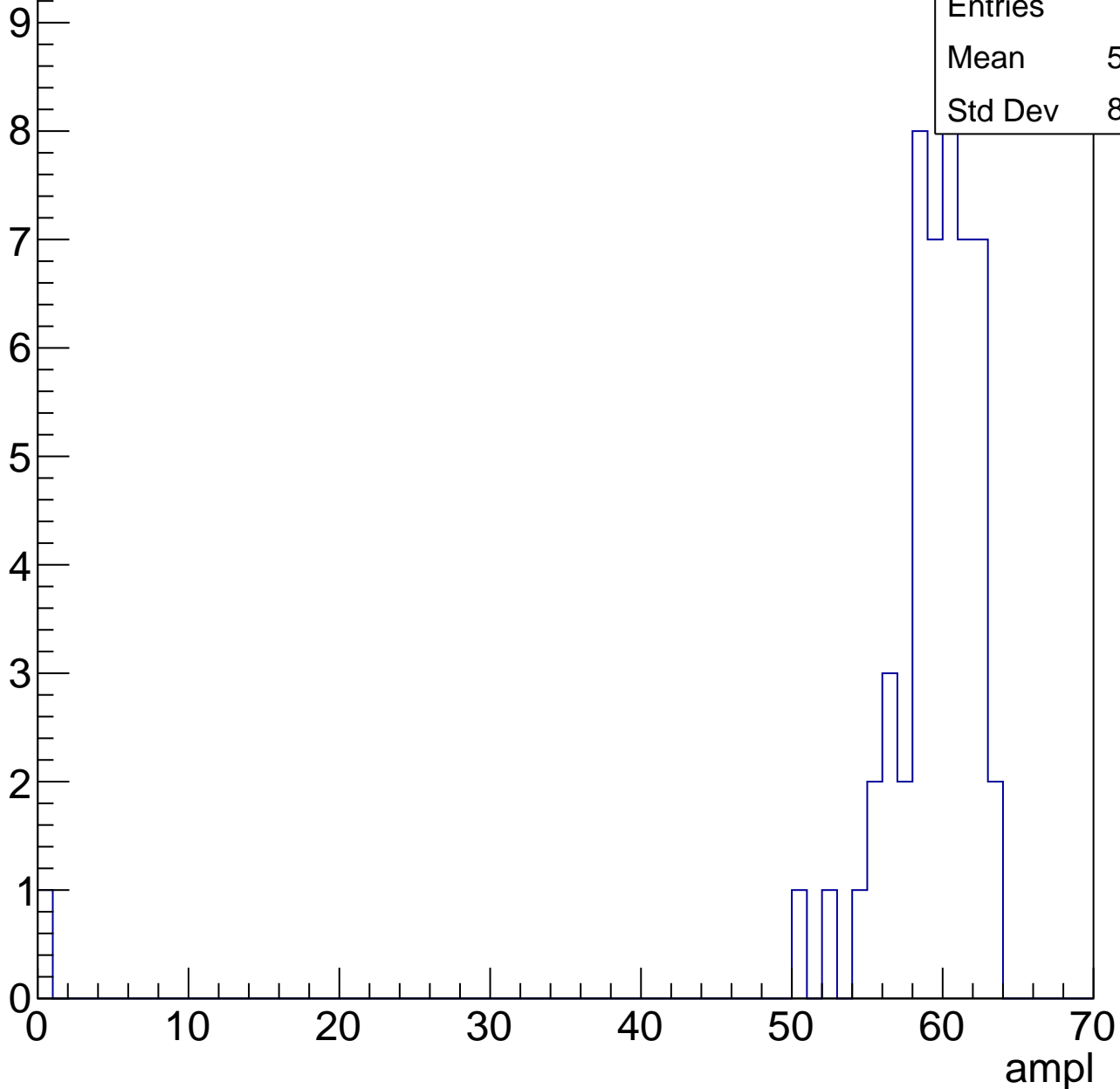


# B0L102S, U17-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	57.88
Std Dev	8.608

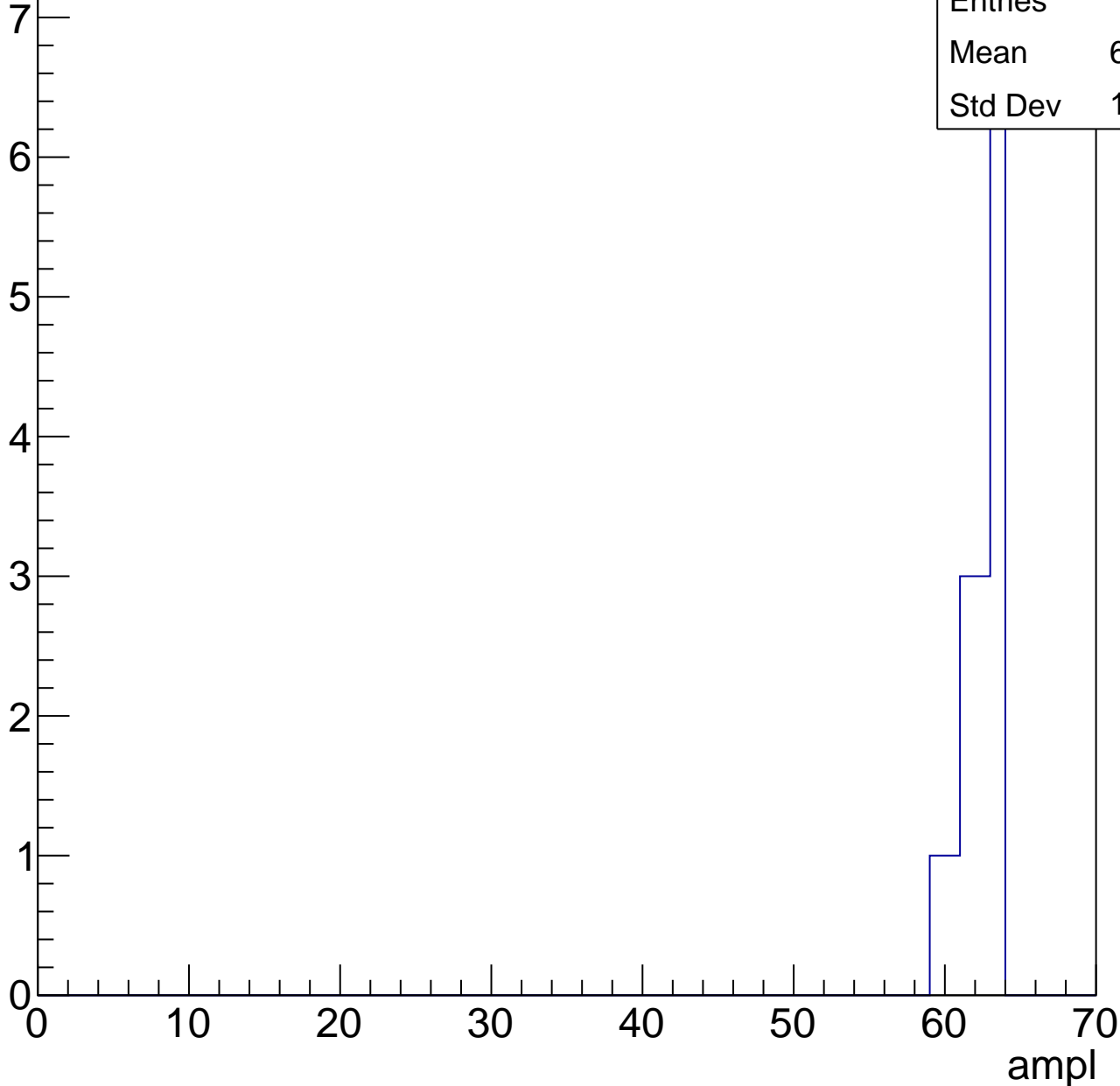


# B0L102S, U17-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.93
Std Dev	1.236

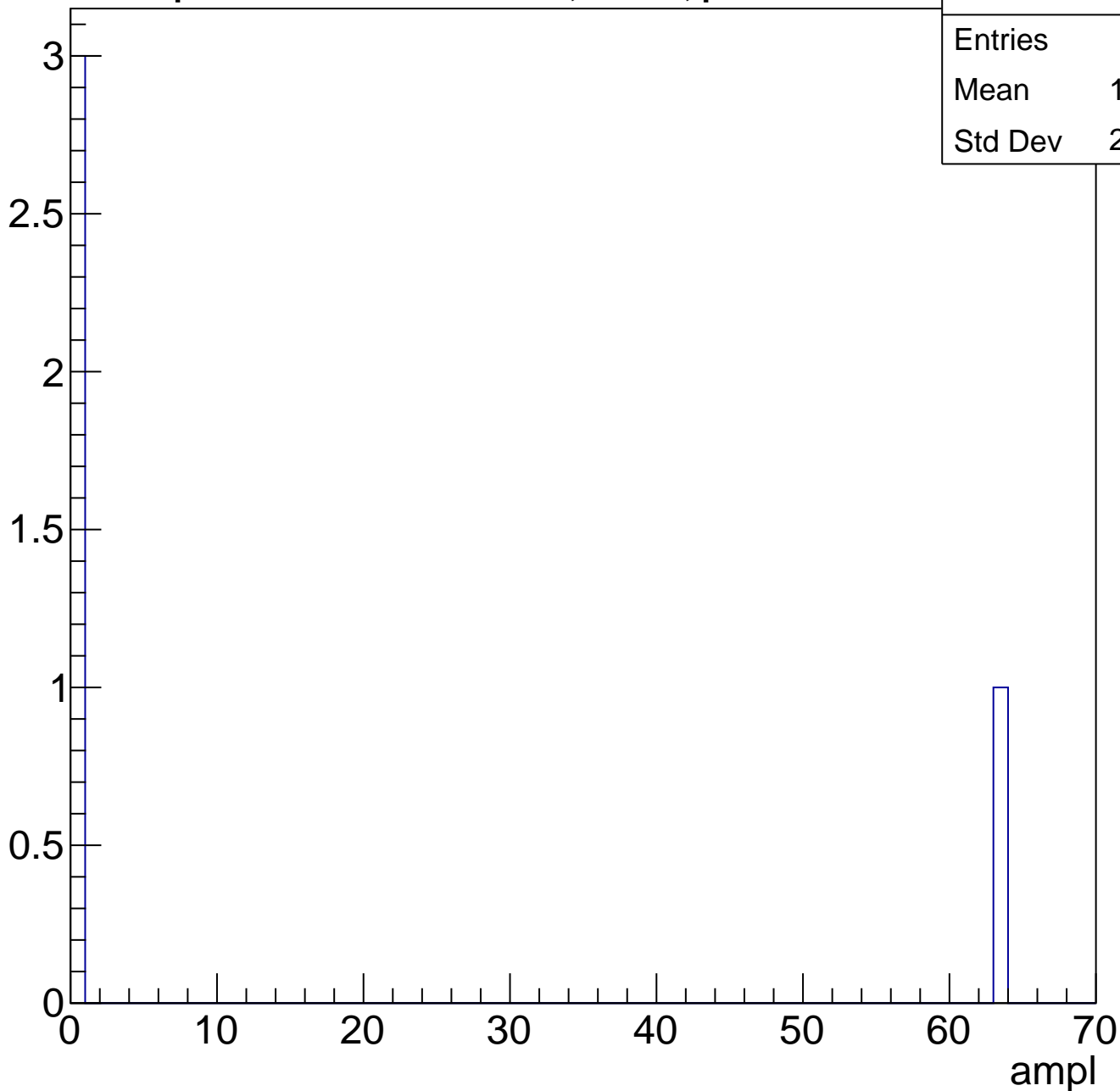




# B0L102S, U17-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch83, adc0

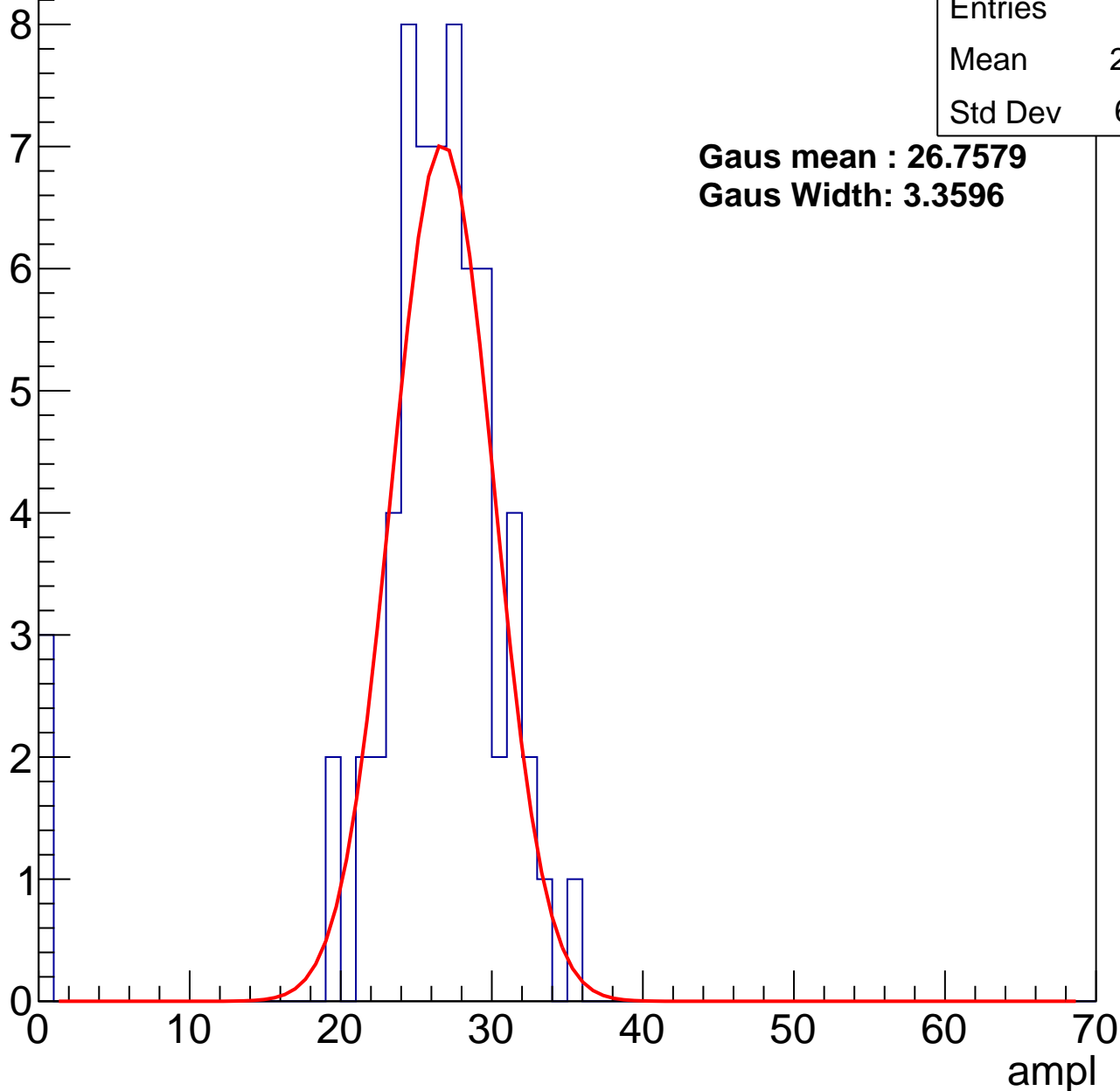
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	25.22
Std Dev	6.401

**Gaus mean : 26.7579**

**Gaus Width: 3.3596**



# B0L102S, U17-ch83, adc1

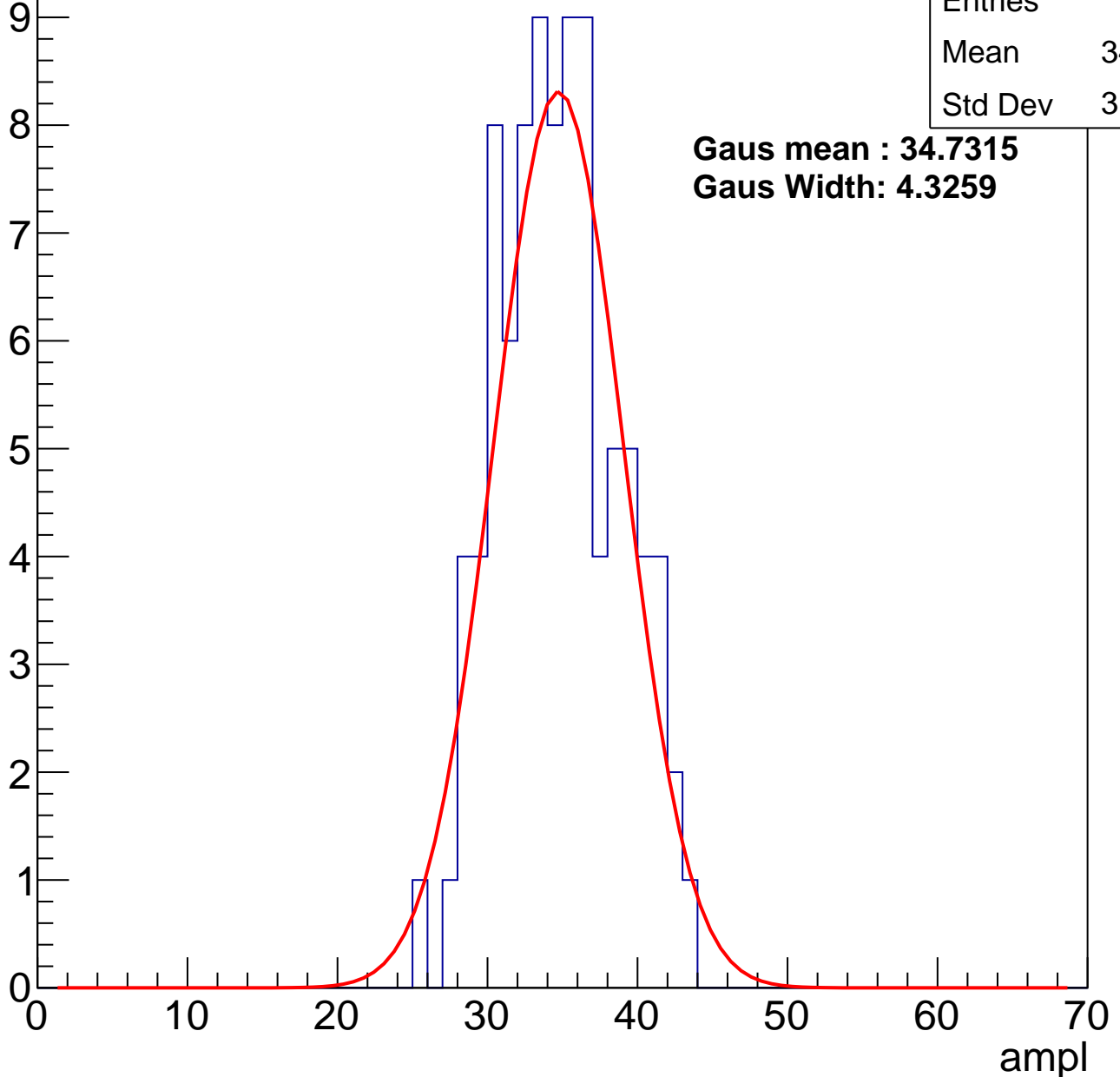
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	92
Mean	34.28
Std Dev	3.935

**Gaus mean : 34.7315**

**Gaus Width: 4.3259**



# B0L102S, U17-ch83, adc2

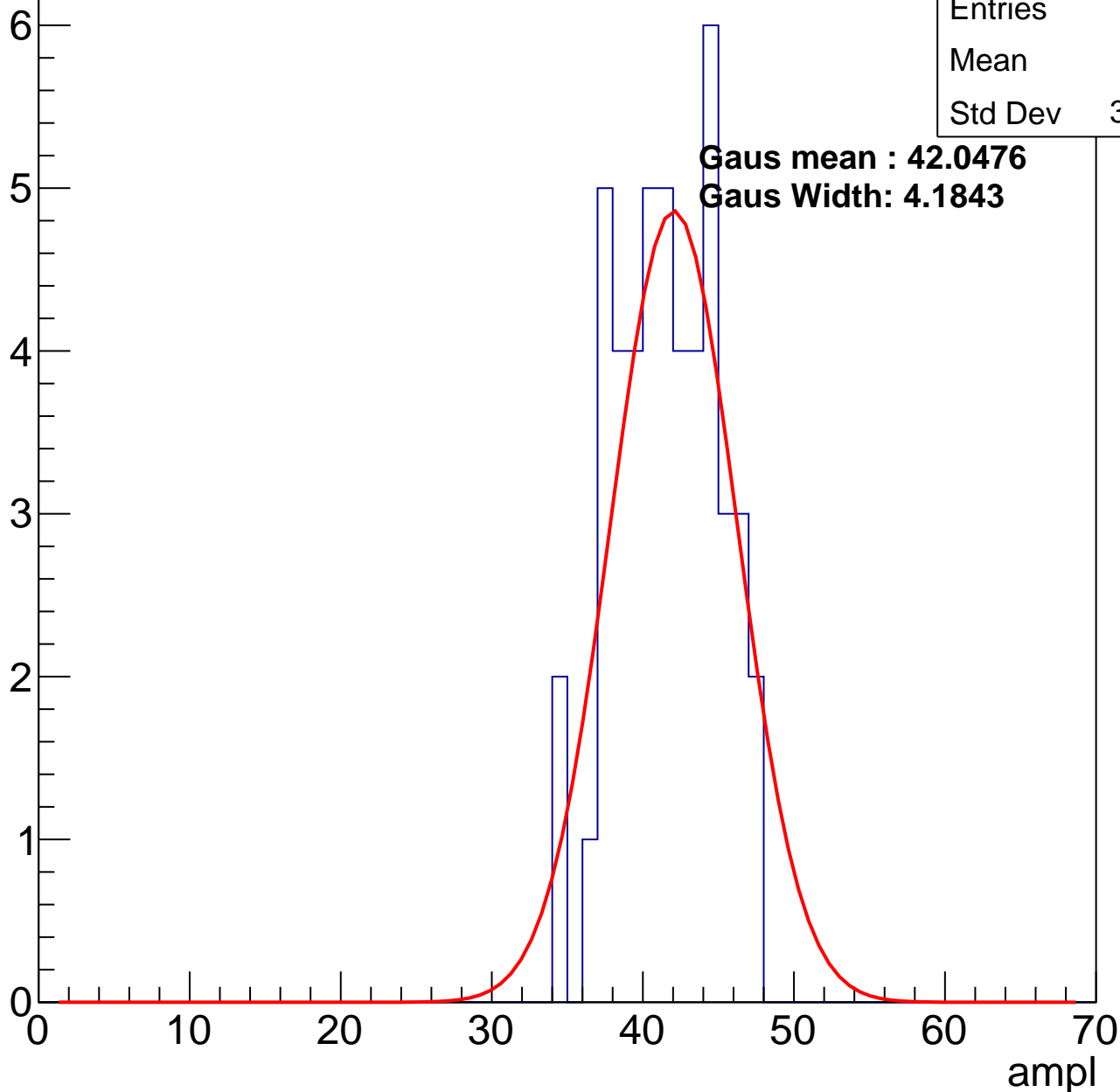
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	41.1
Std Dev	3.318

**Gaus mean : 42.0476**

**Gaus Width: 4.1843**

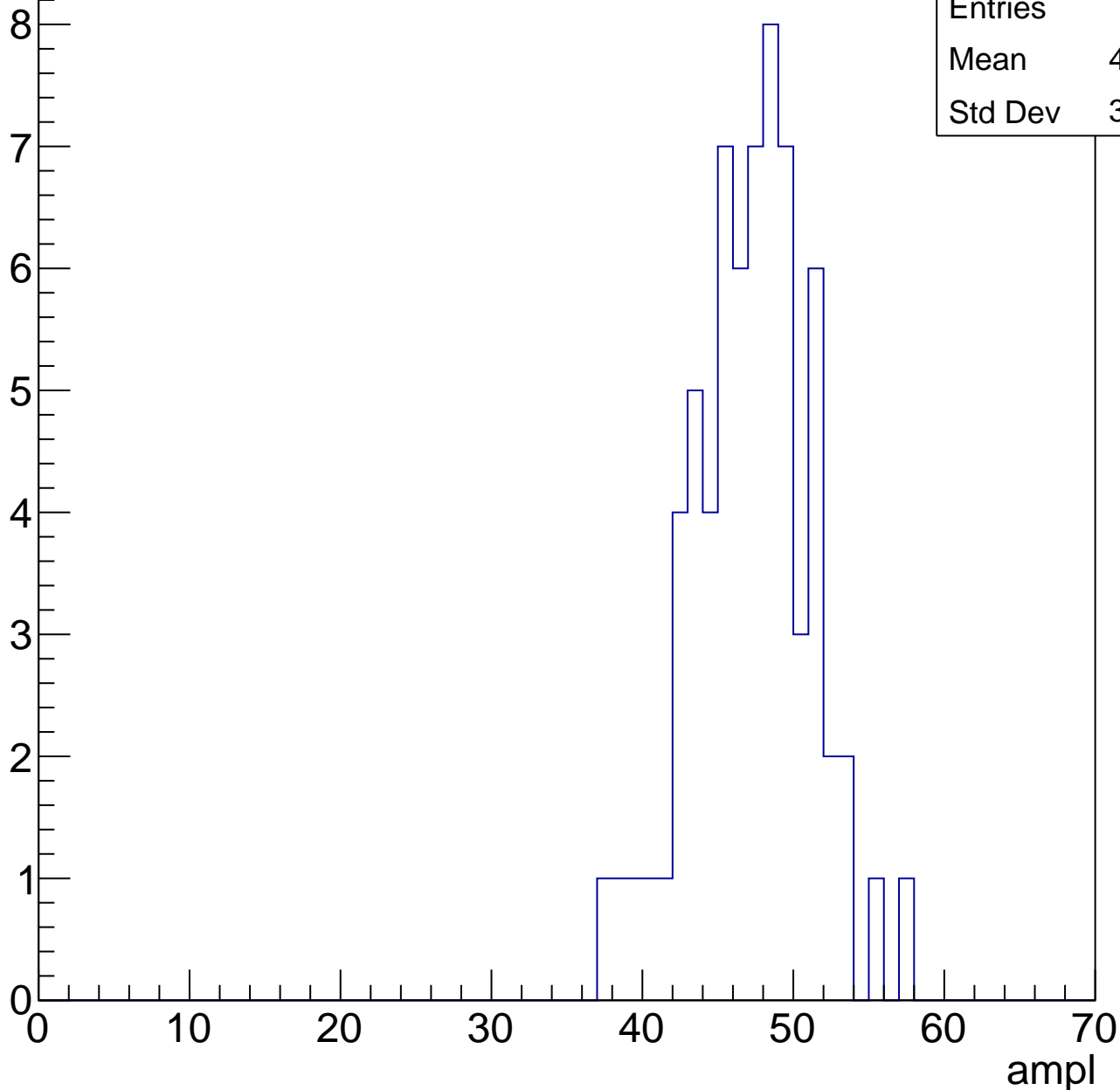


# B0L102S, U17-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	46.75
Std Dev	3.874

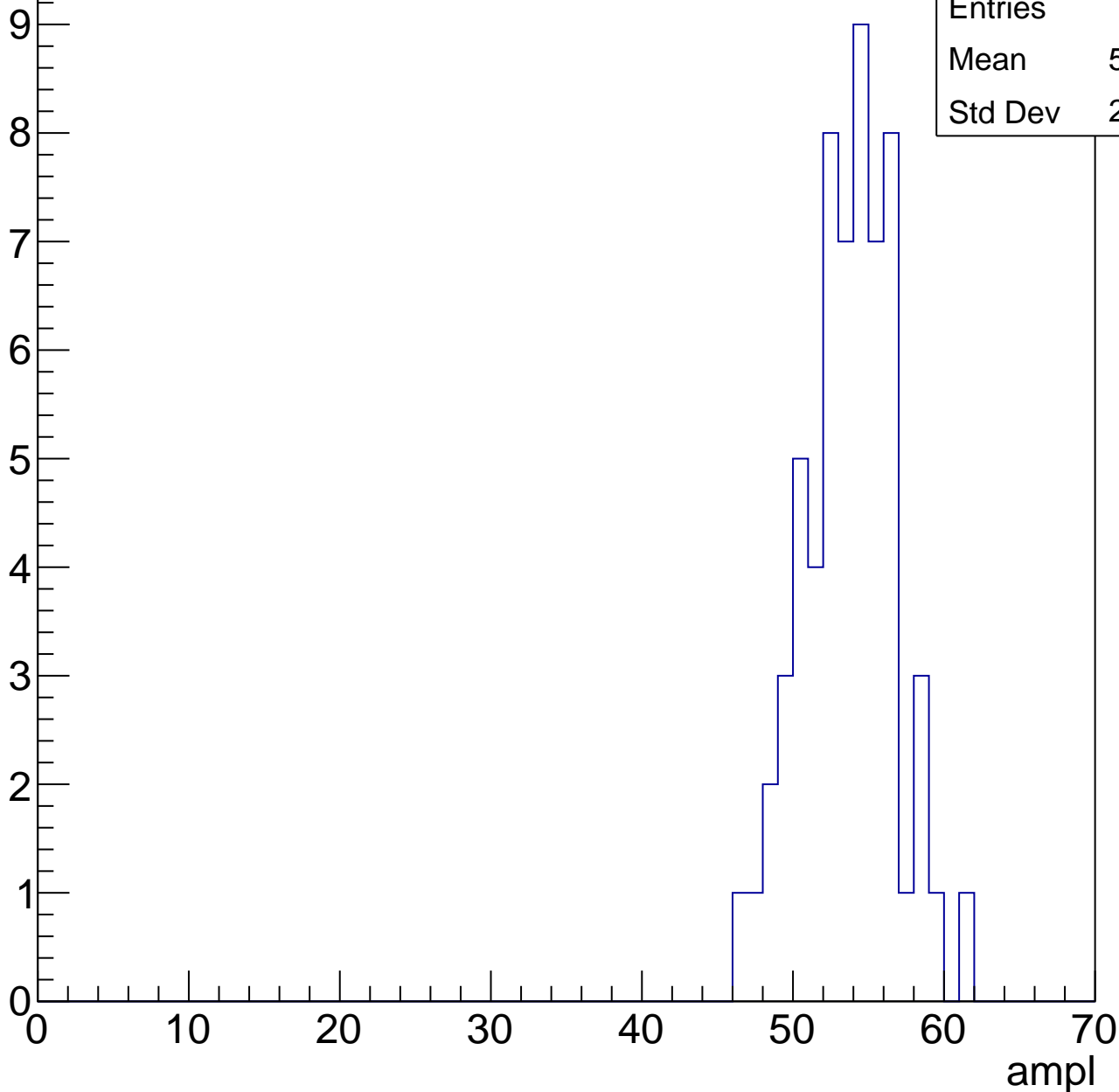


# B0L102S, U17-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	53.23
Std Dev	2.999

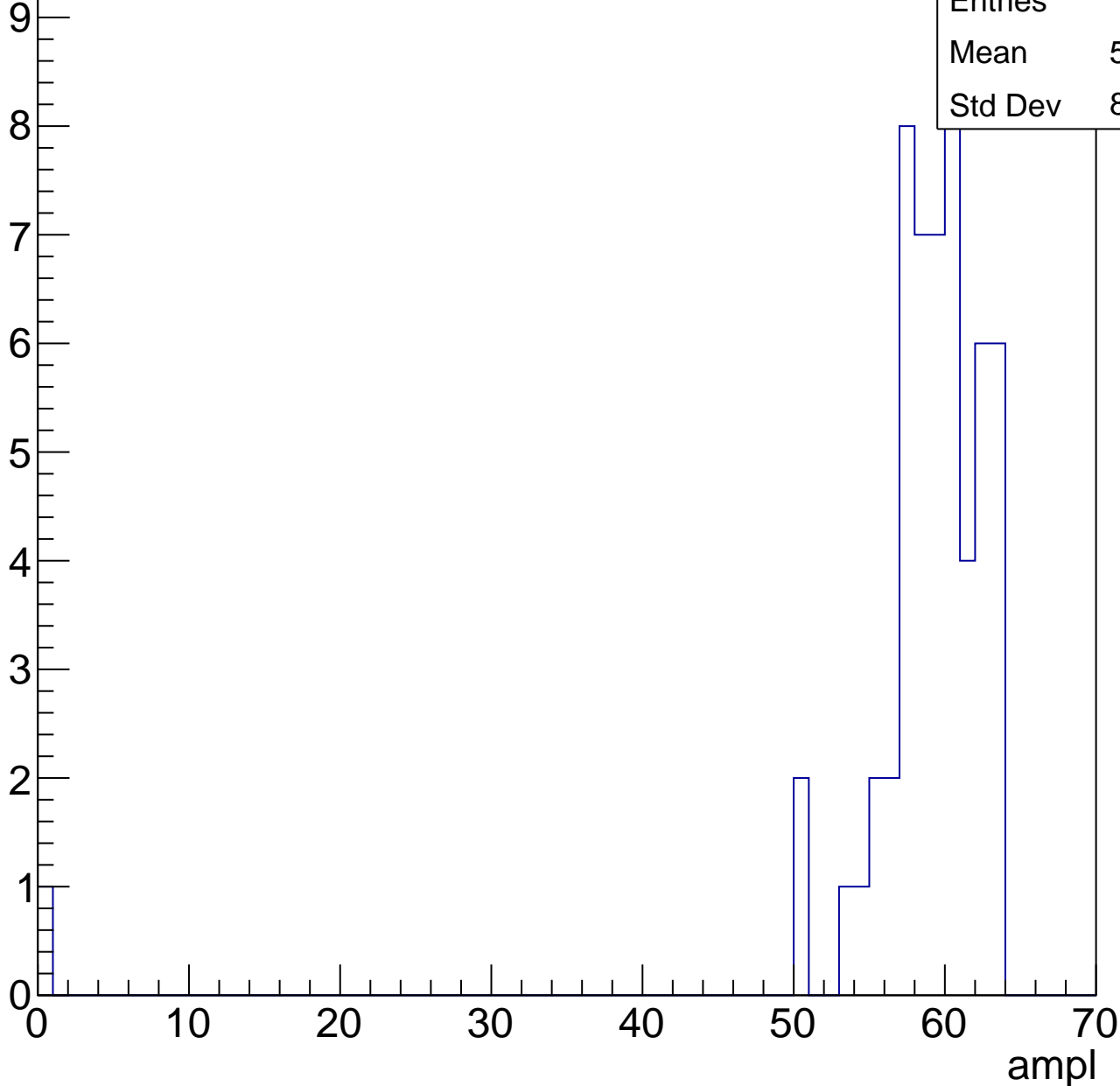


# B0L102S, U17-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	57.82
Std Dev	8.335

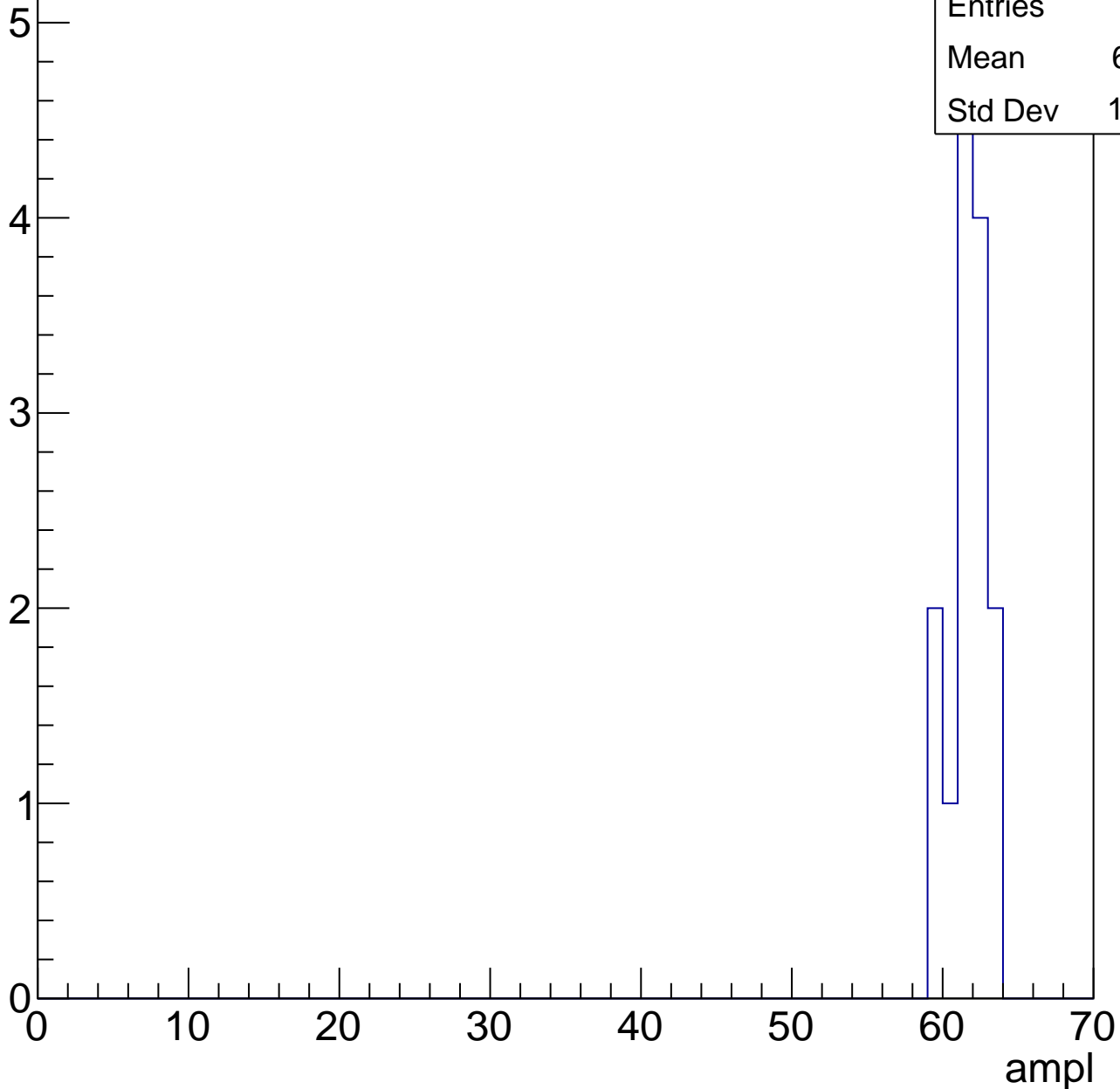


# B0L102S, U17-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.21
Std Dev	1.206





# B0L102S, U17-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch84, adc0

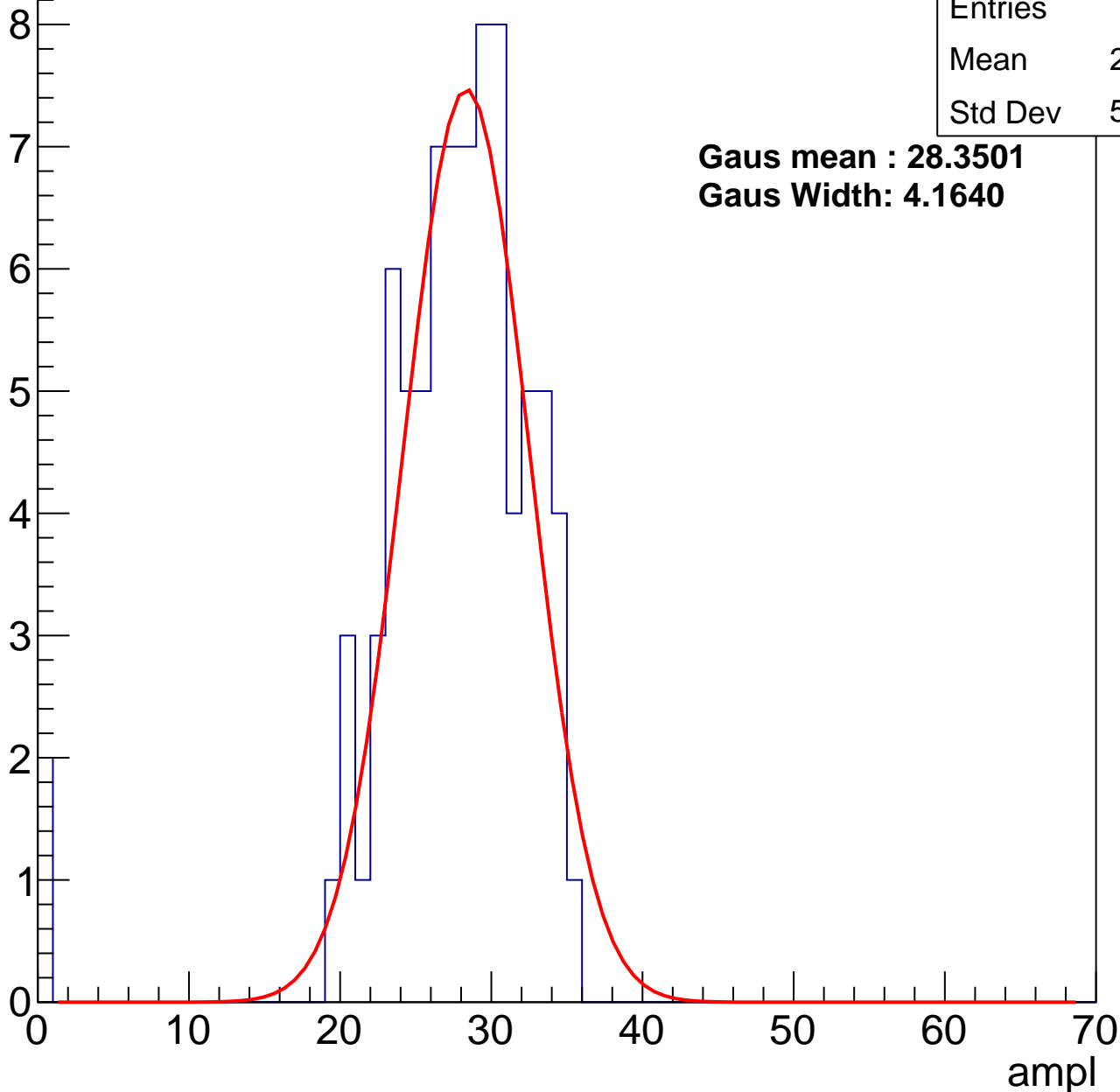
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	82
Mean	26.93
Std Dev	5.725

**Gaus mean : 28.3501**

**Gaus Width: 4.1640**



# B0L102S, U17-ch84, adc1

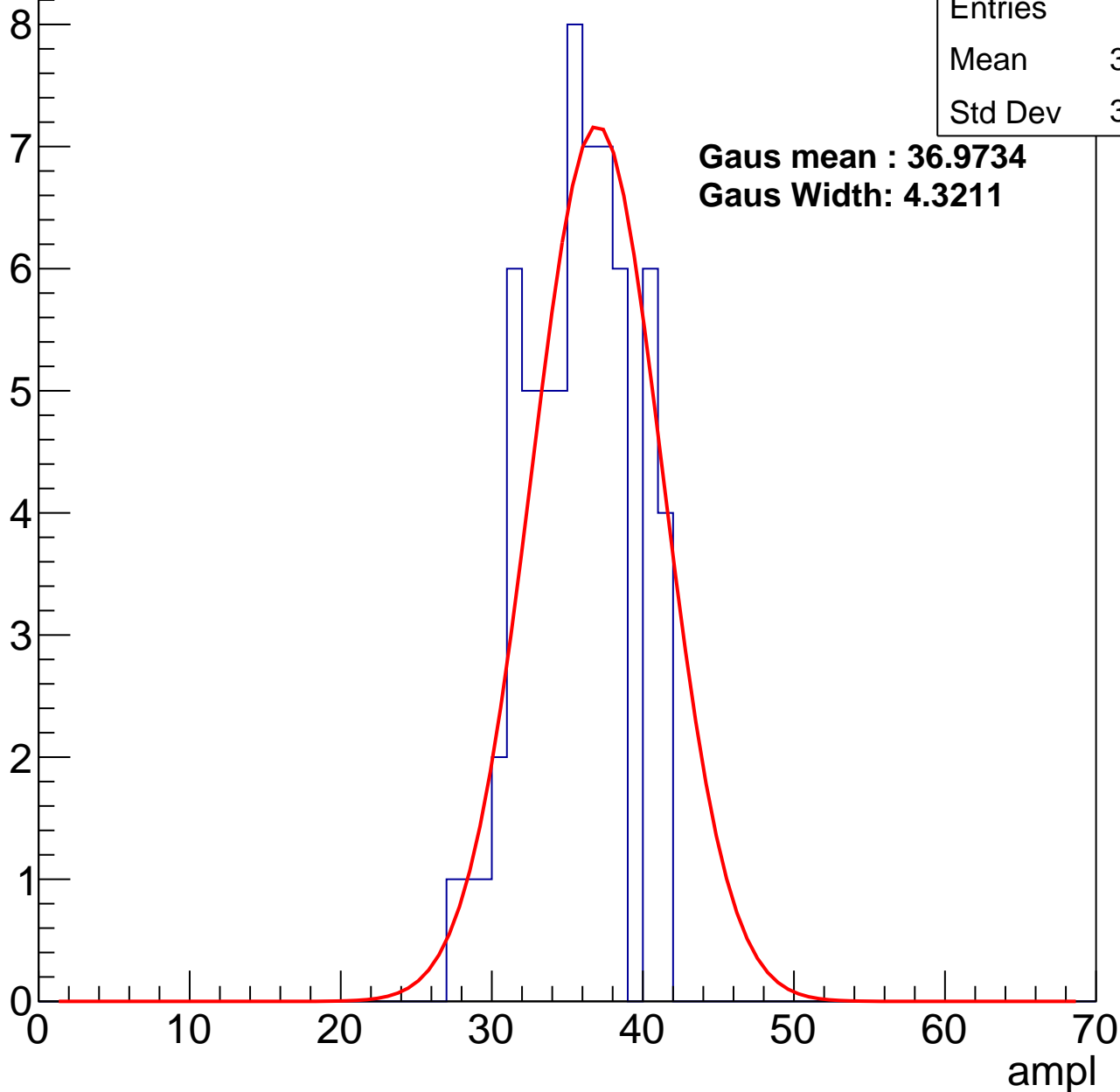
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.12
Std Dev	3.416

**Gaus mean : 36.9734**

**Gaus Width: 4.3211**



# B0L102S, U17-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	82
Mean	42.49
Std Dev	3.829

**Gaus mean : 43.0733**

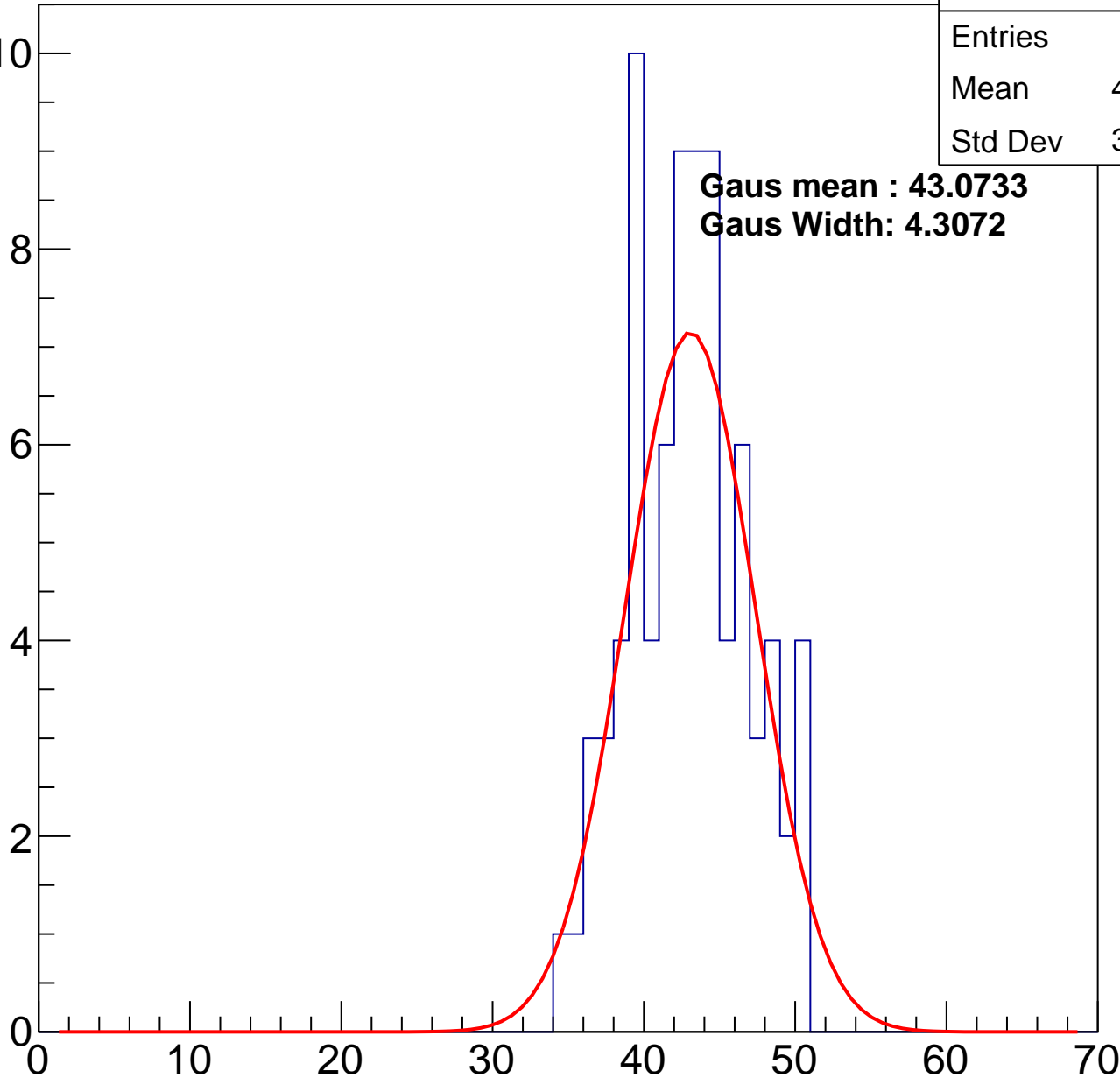
**Gaus Width: 4.3072**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

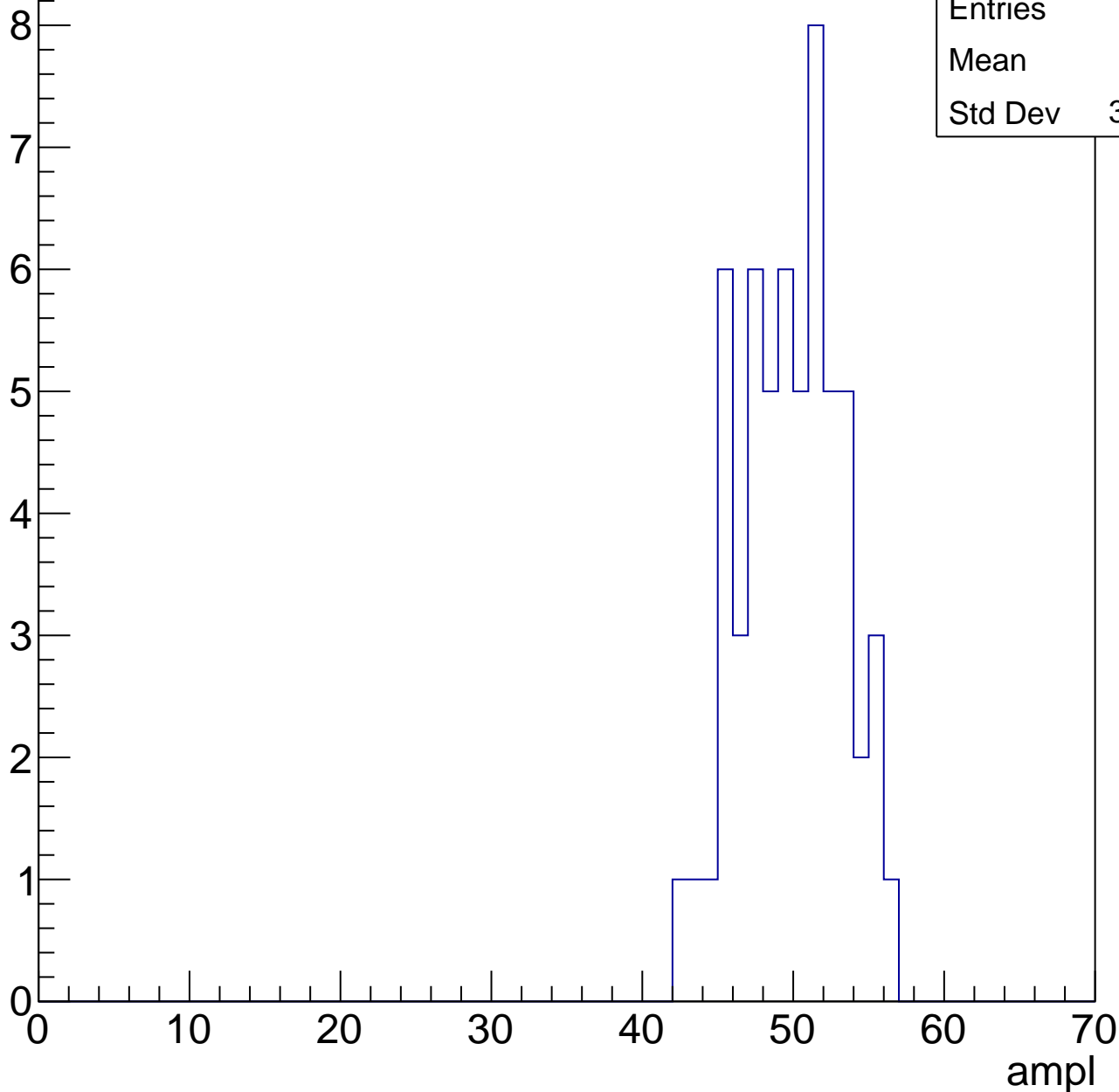


# B0L102S, U17-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	49.4
Std Dev	3.274

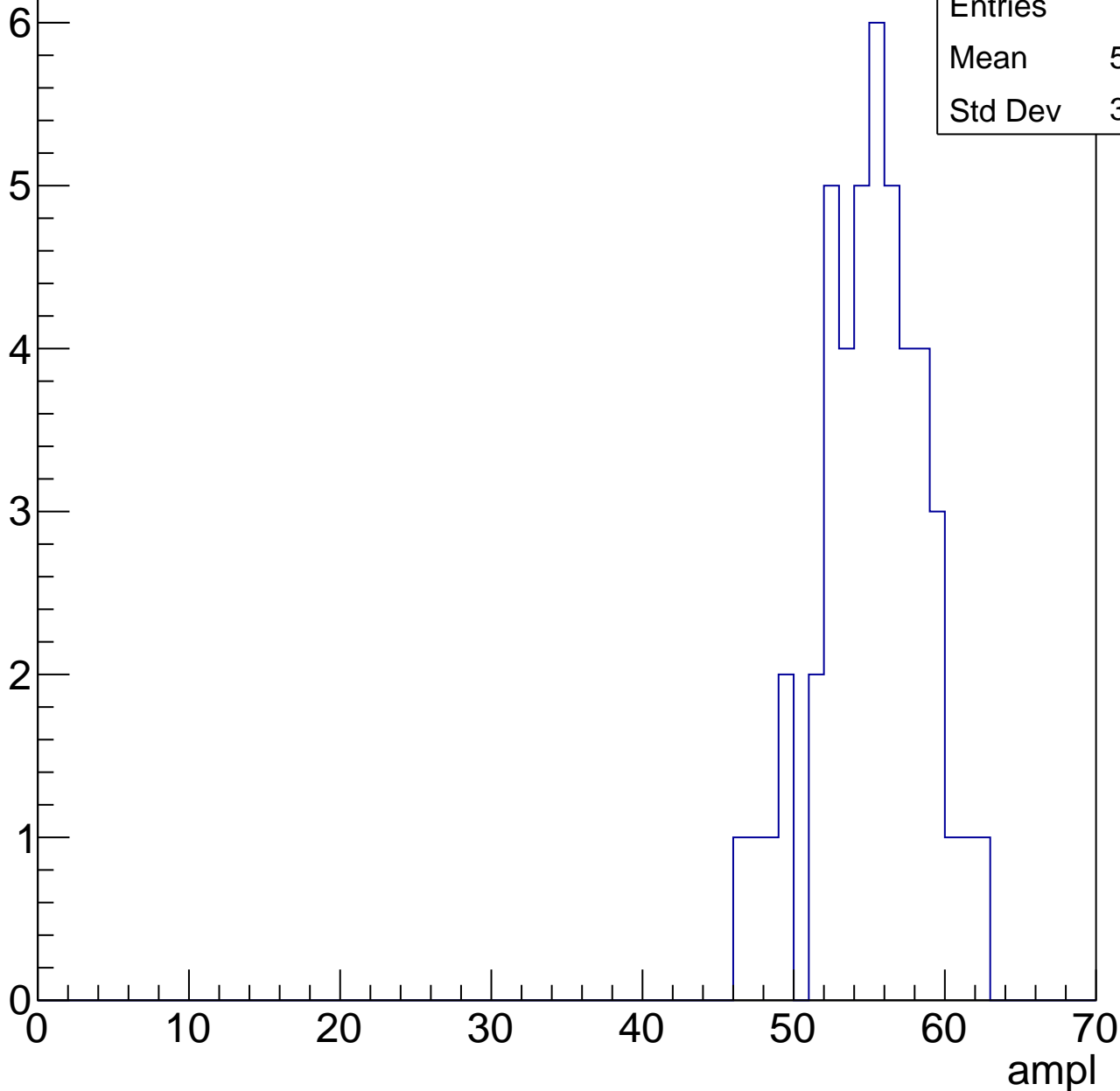


# B0L102S, U17-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	46
Mean	54.63
Std Dev	3.529

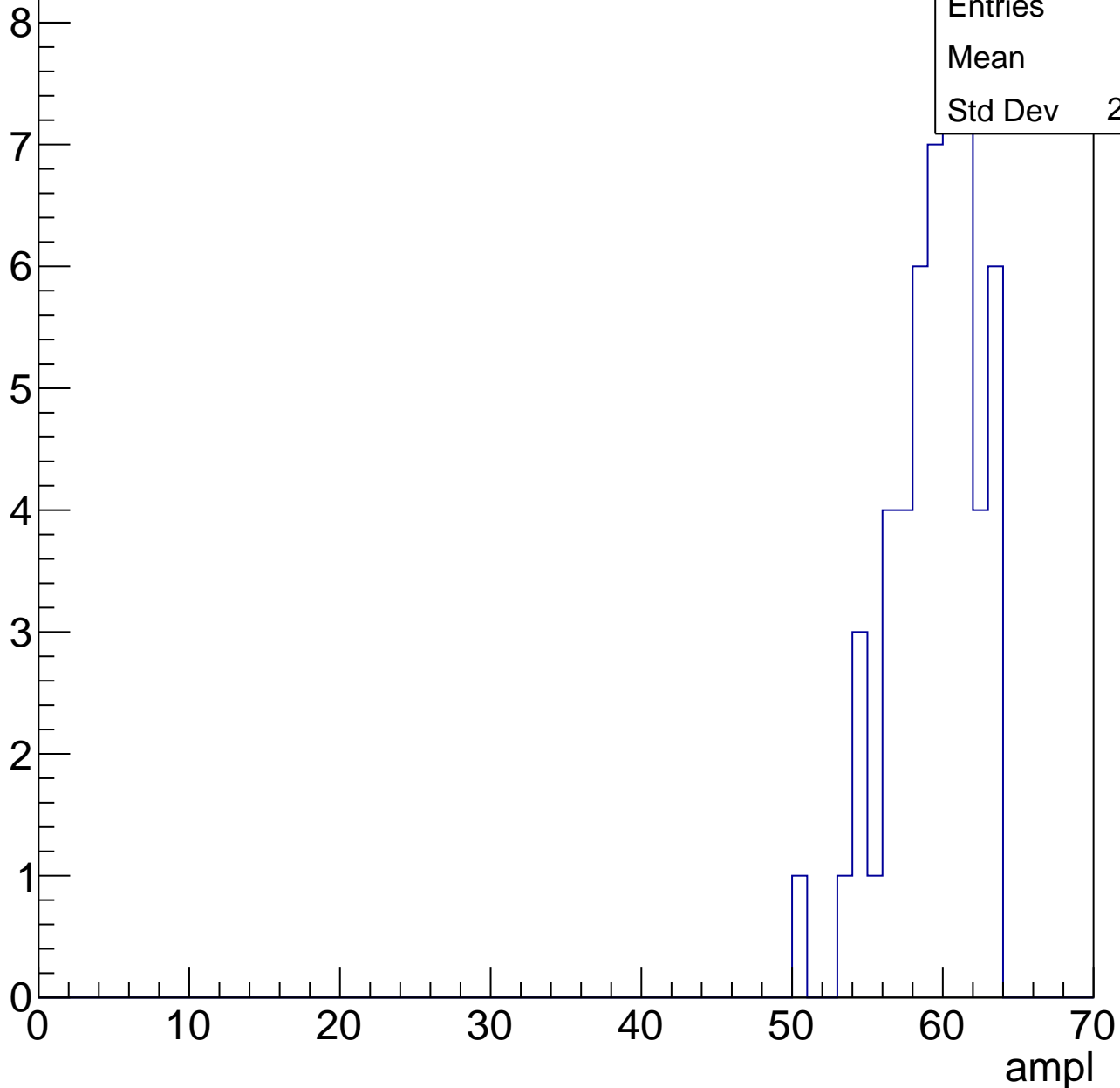


# B0L102S, U17-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

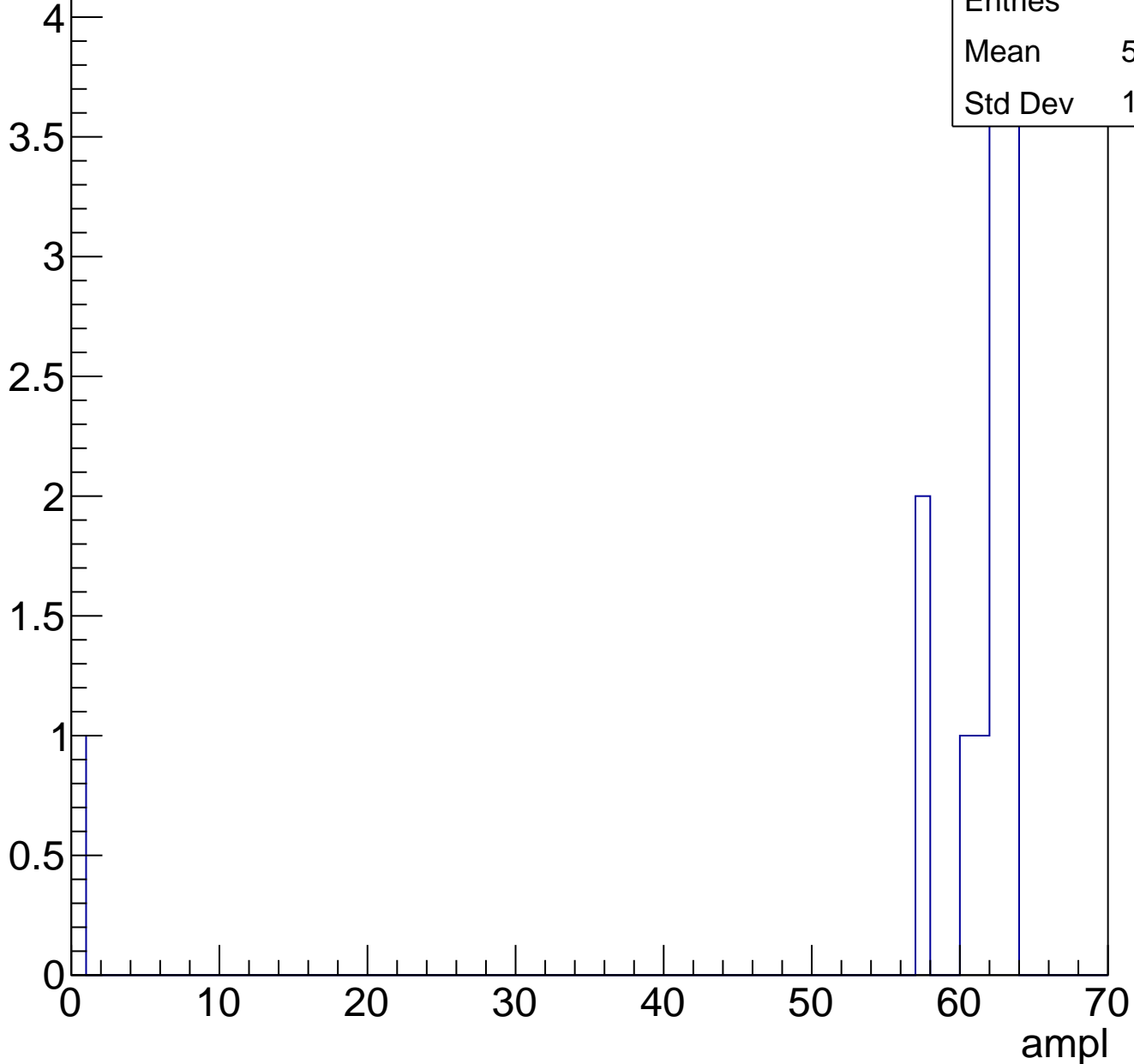
Entries	53
Mean	59
Std Dev	2.875



# B0L102S, U17-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch85, adc0

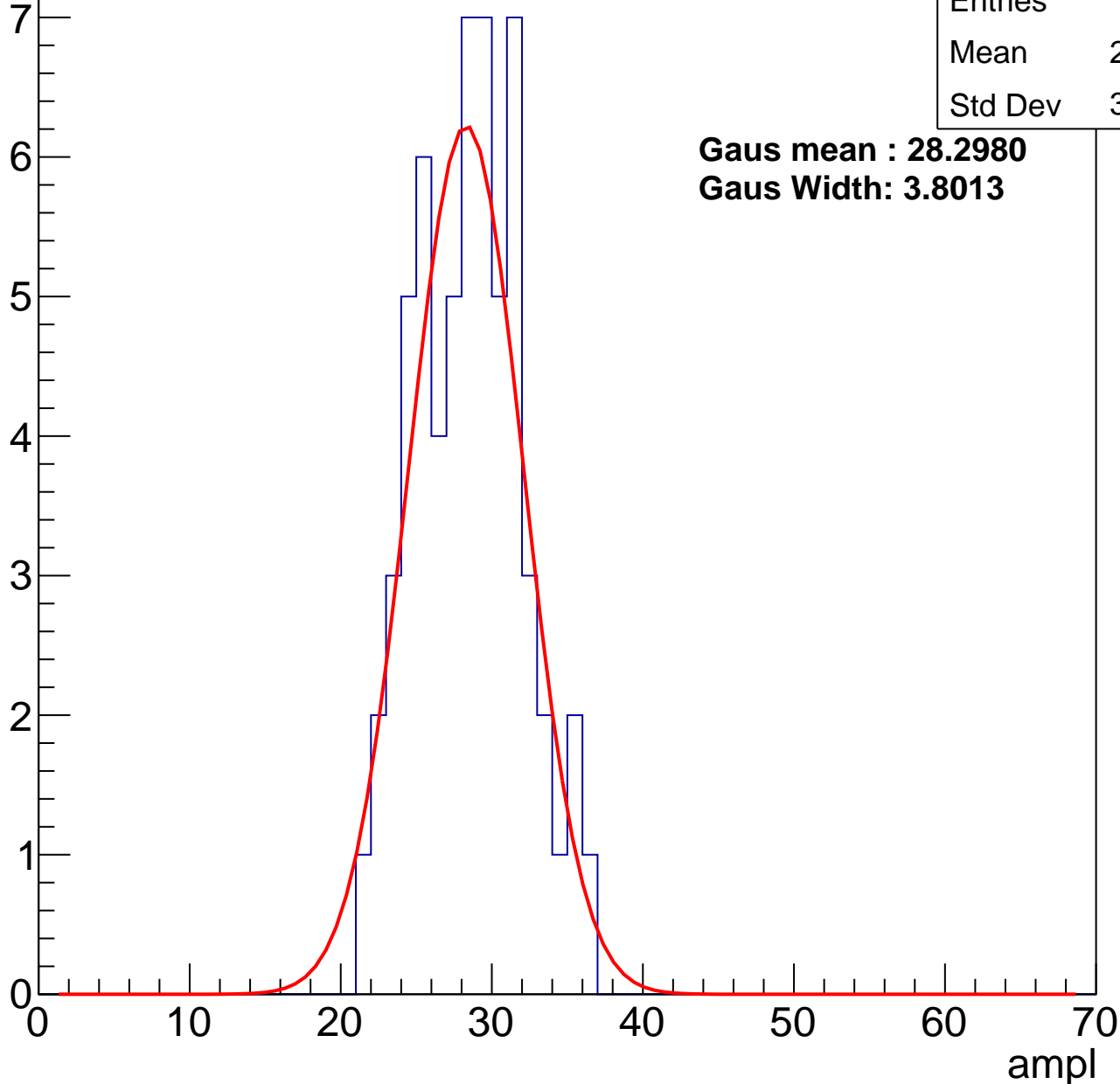
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	28.05
Std Dev	3.476

**Gaus mean : 28.2980**

**Gaus Width: 3.8013**



# B0L102S, U17-ch85, adc1

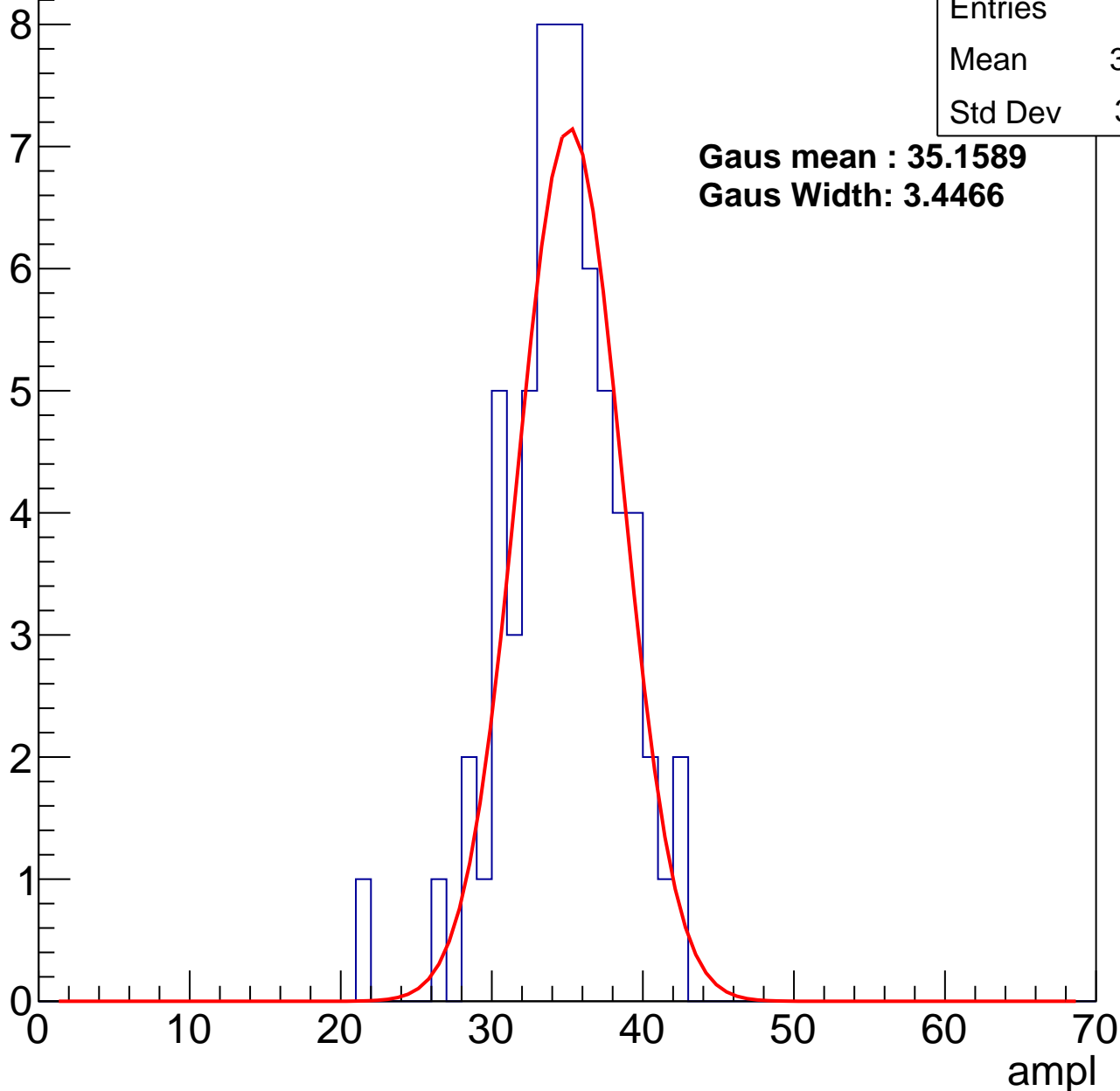
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.32
Std Dev	3.791

**Gaus mean : 35.1589**

**Gaus Width: 3.4466**



# B0L102S, U17-ch85, adc2

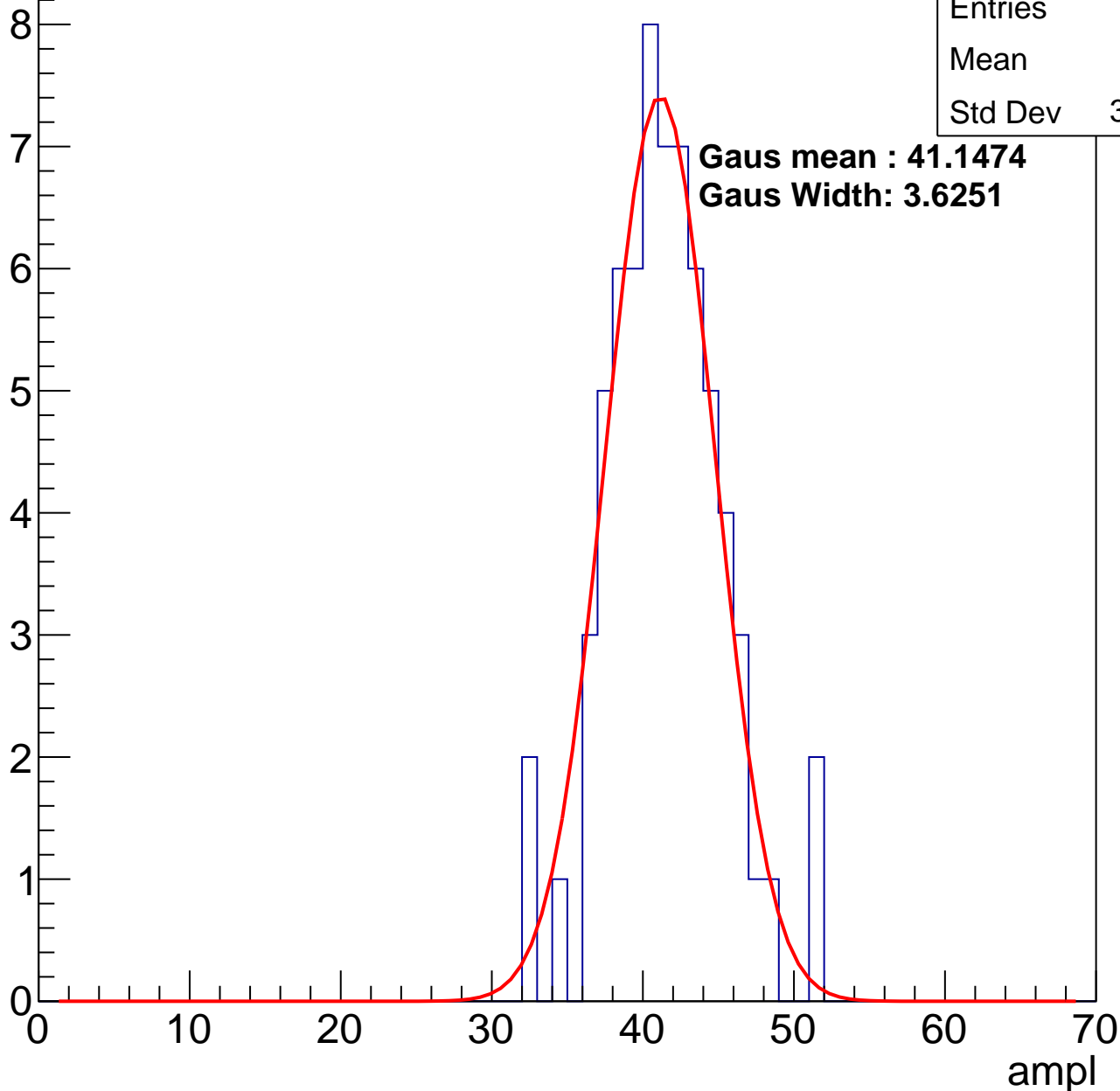
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	41
Std Dev	3.769

**Gaus mean : 41.1474**

**Gaus Width: 3.6251**



# B0L102S, U17-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	76
Mean	48.28
Std Dev	3.515

Entry

10

8

6

4

2

0

0

10

20

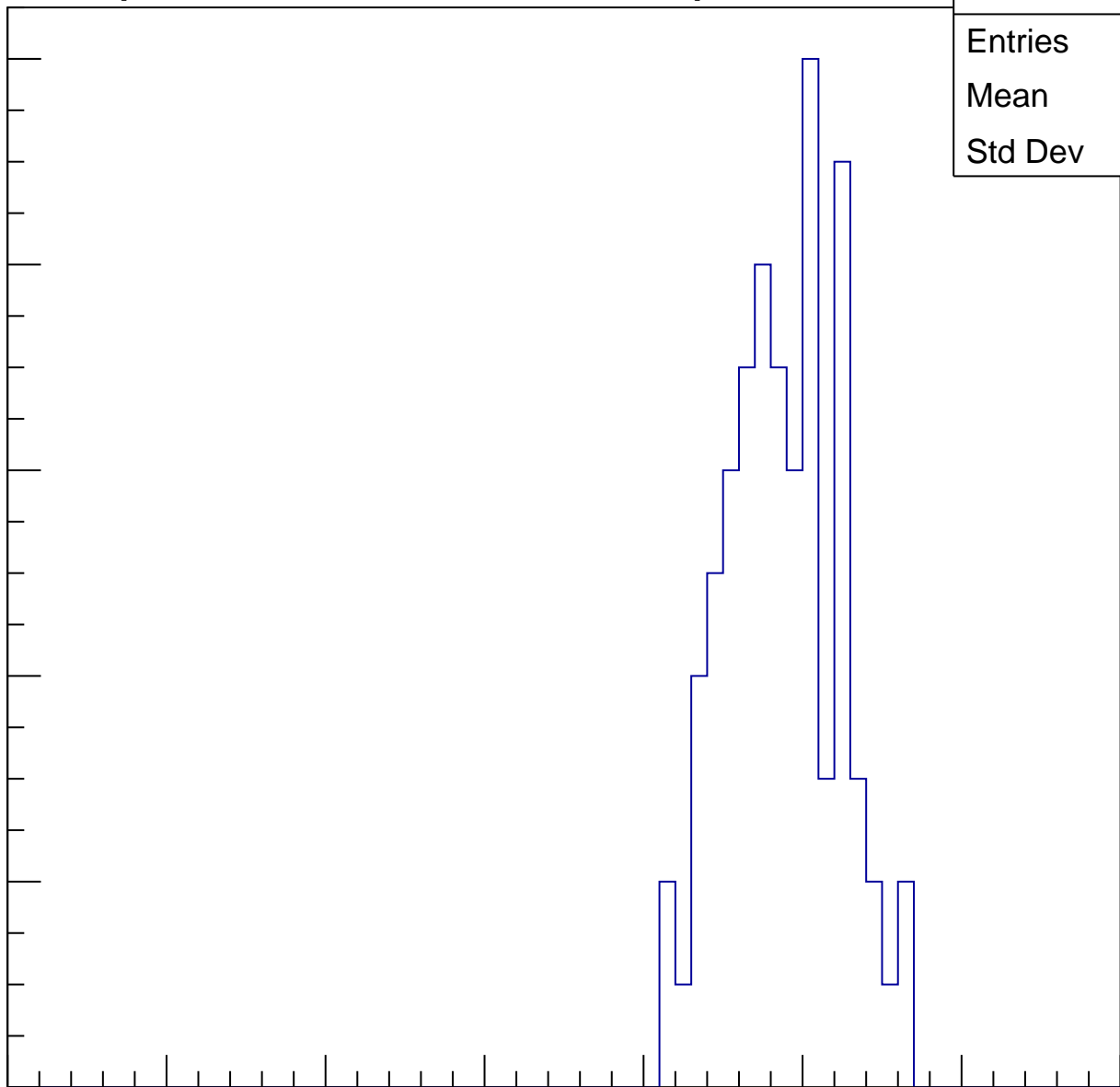
30

40

50

60

ampl

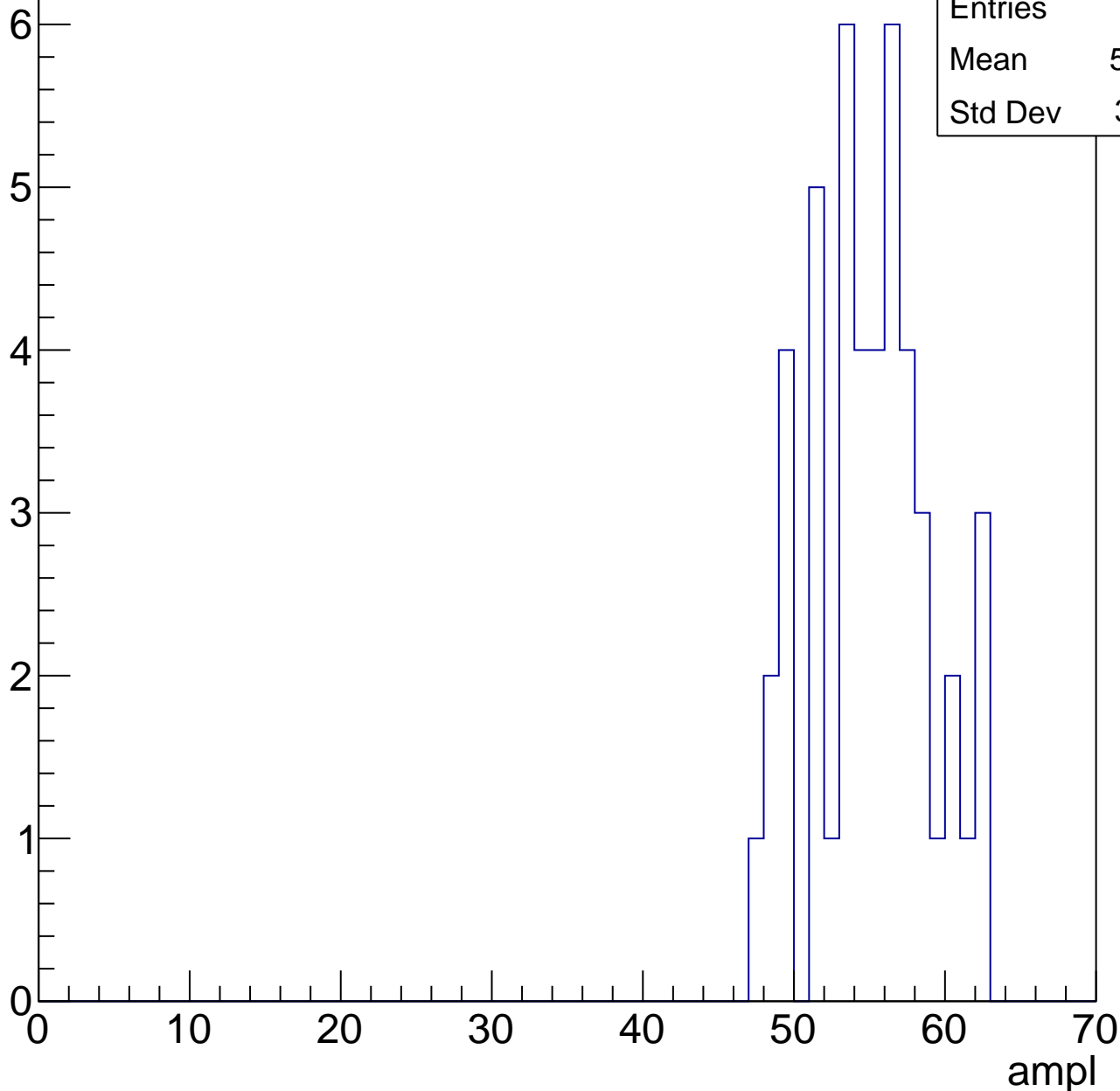


# B0L102S, U17-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	54.55
Std Dev	3.891

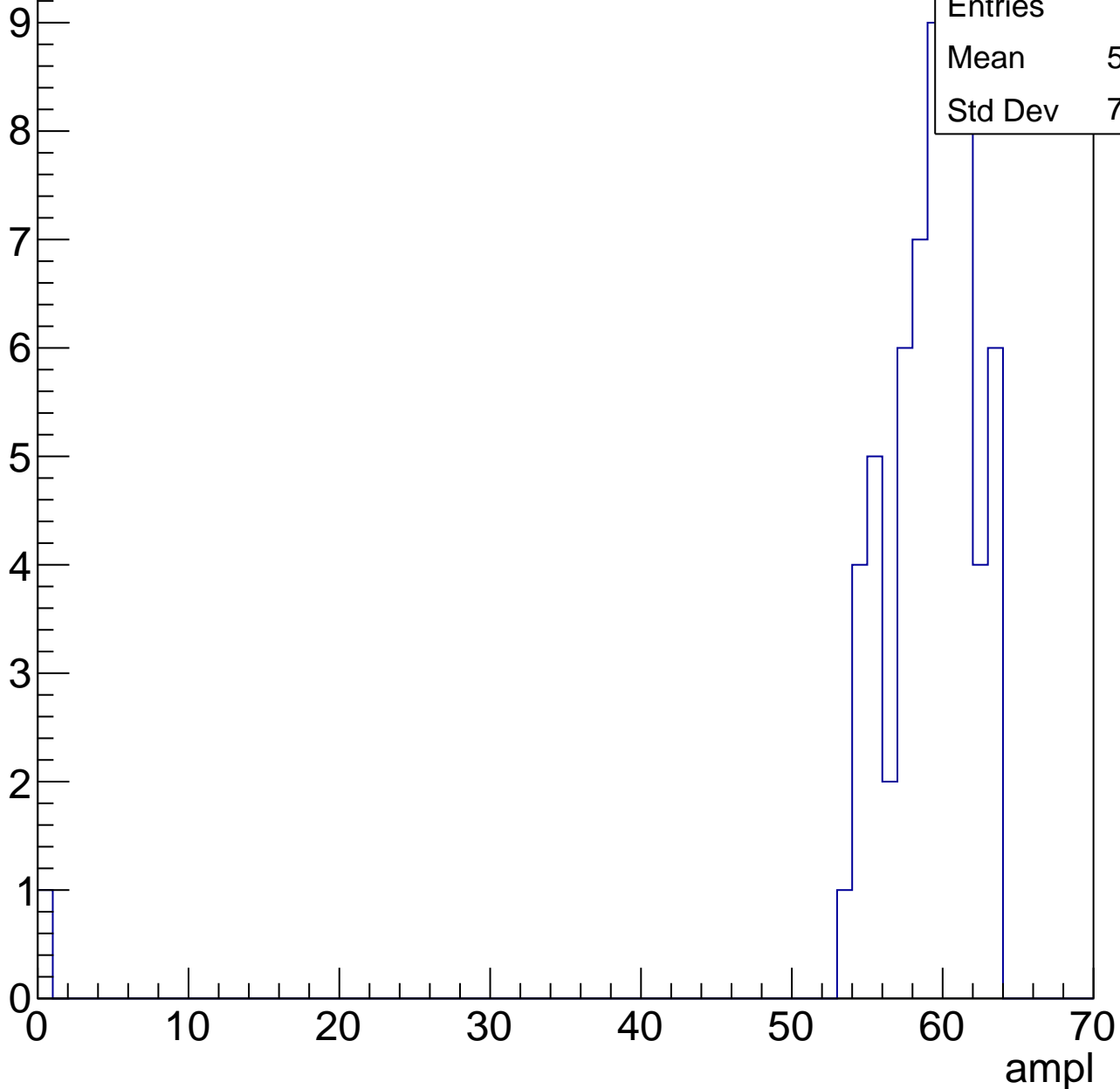


# B0L102S, U17-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	57.85
Std Dev	7.932



# B0L102S, U17-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



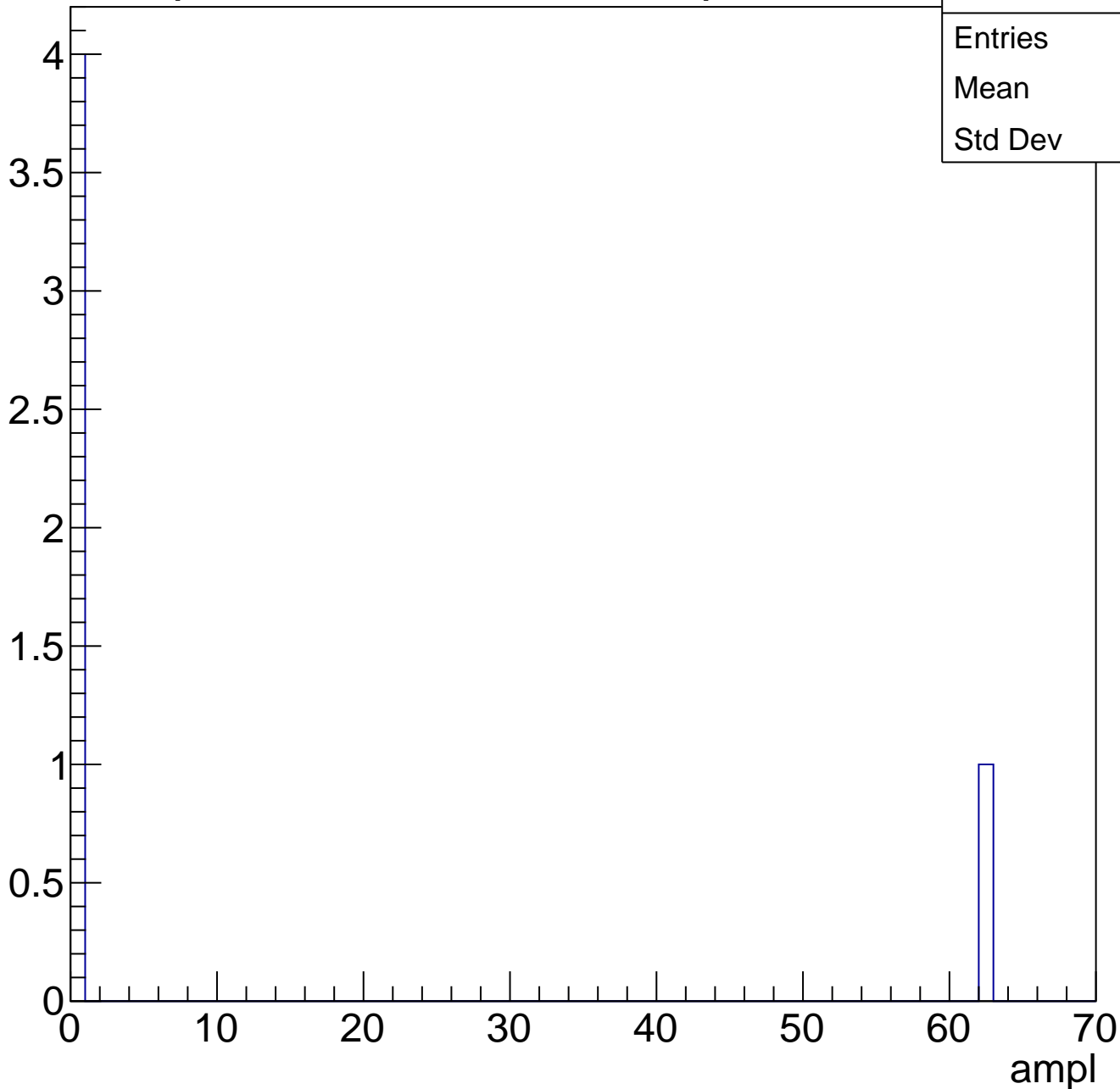
Entries	7
Mean	62.43
Std Dev	0.7284



# B0L102S, U17-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch86, adc0

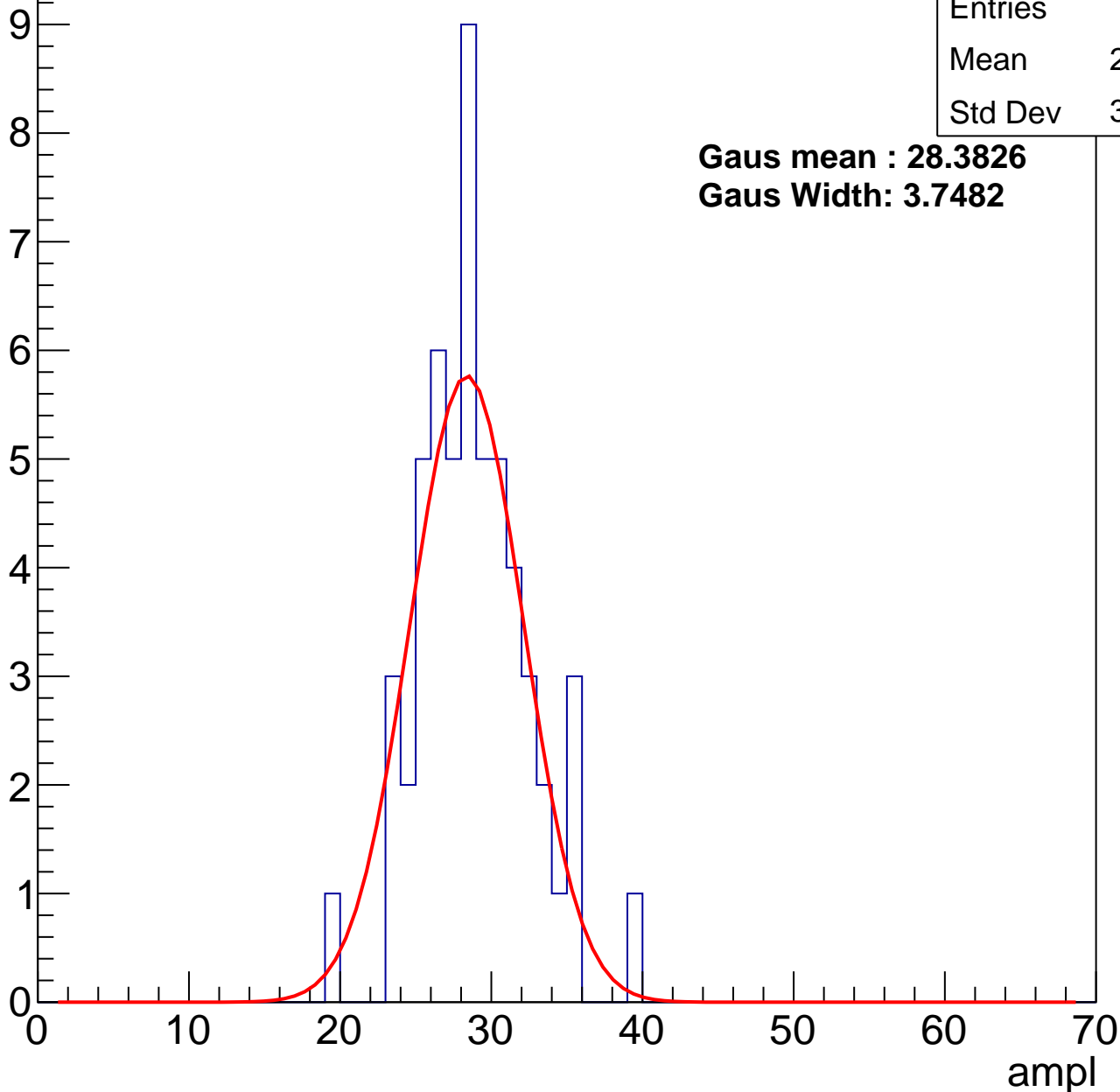
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	28.42
Std Dev	3.607

**Gaus mean : 28.3826**

**Gaus Width: 3.7482**



# B0L102S, U17-ch86, adc1

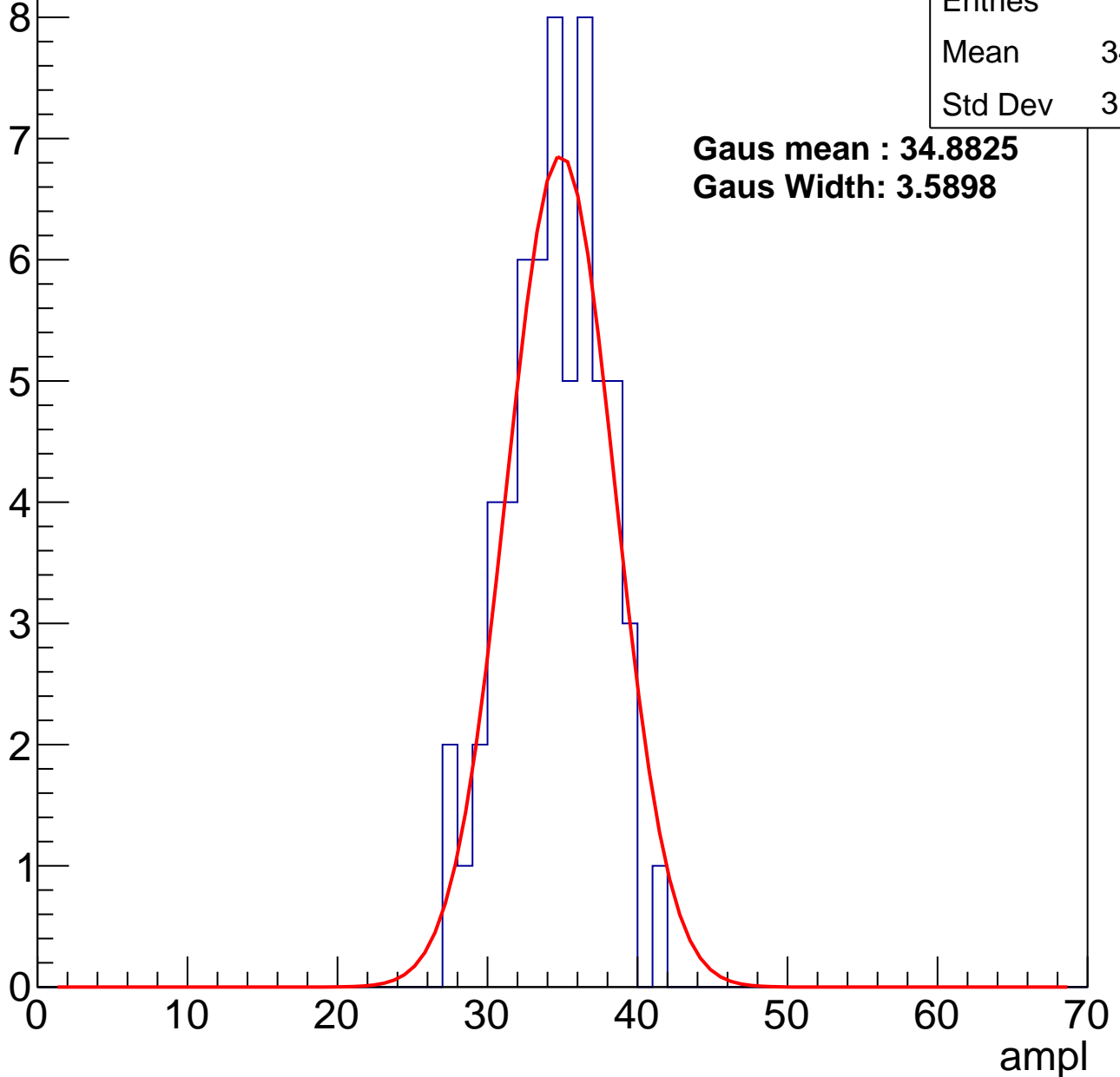
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	34.03
Std Dev	3.162

**Gaus mean : 34.8825**

**Gaus Width: 3.5898**



# B0L102S, U17-ch86, adc2

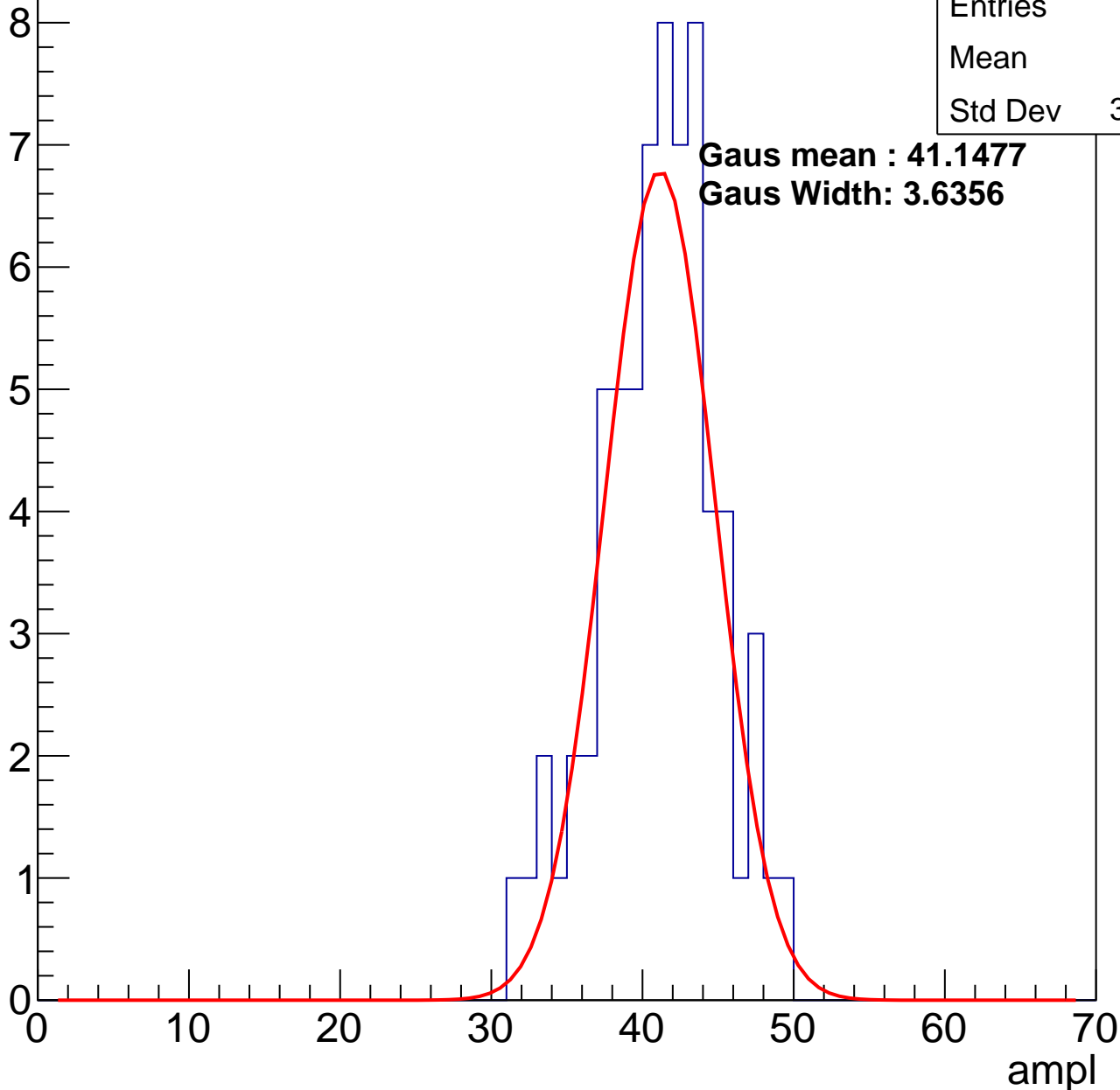
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	40.6
Std Dev	3.847

**Gaus mean : 41.1477**

**Gaus Width: 3.6356**

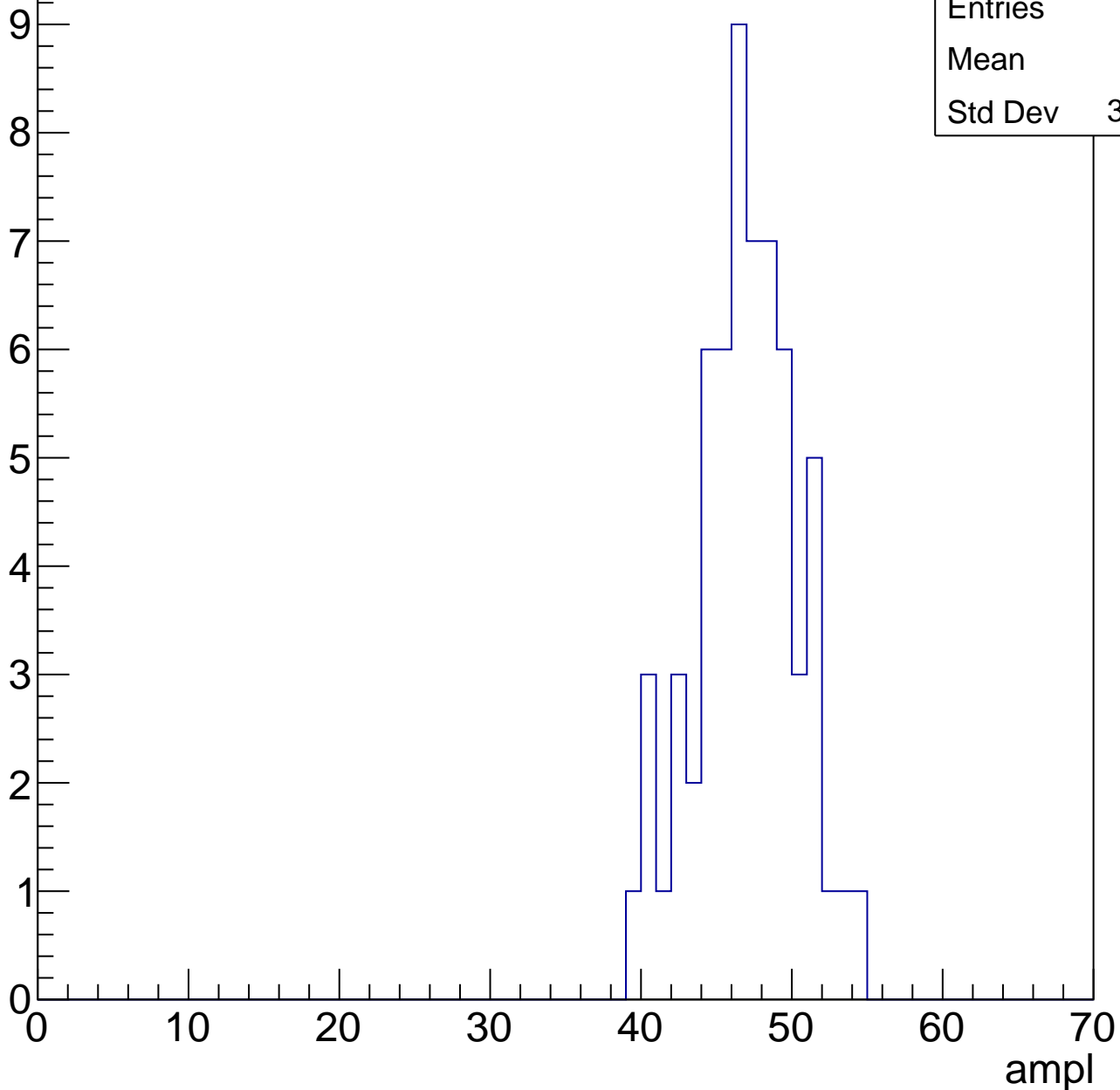


# B0L102S, U17-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	46.5
Std Dev	3.296

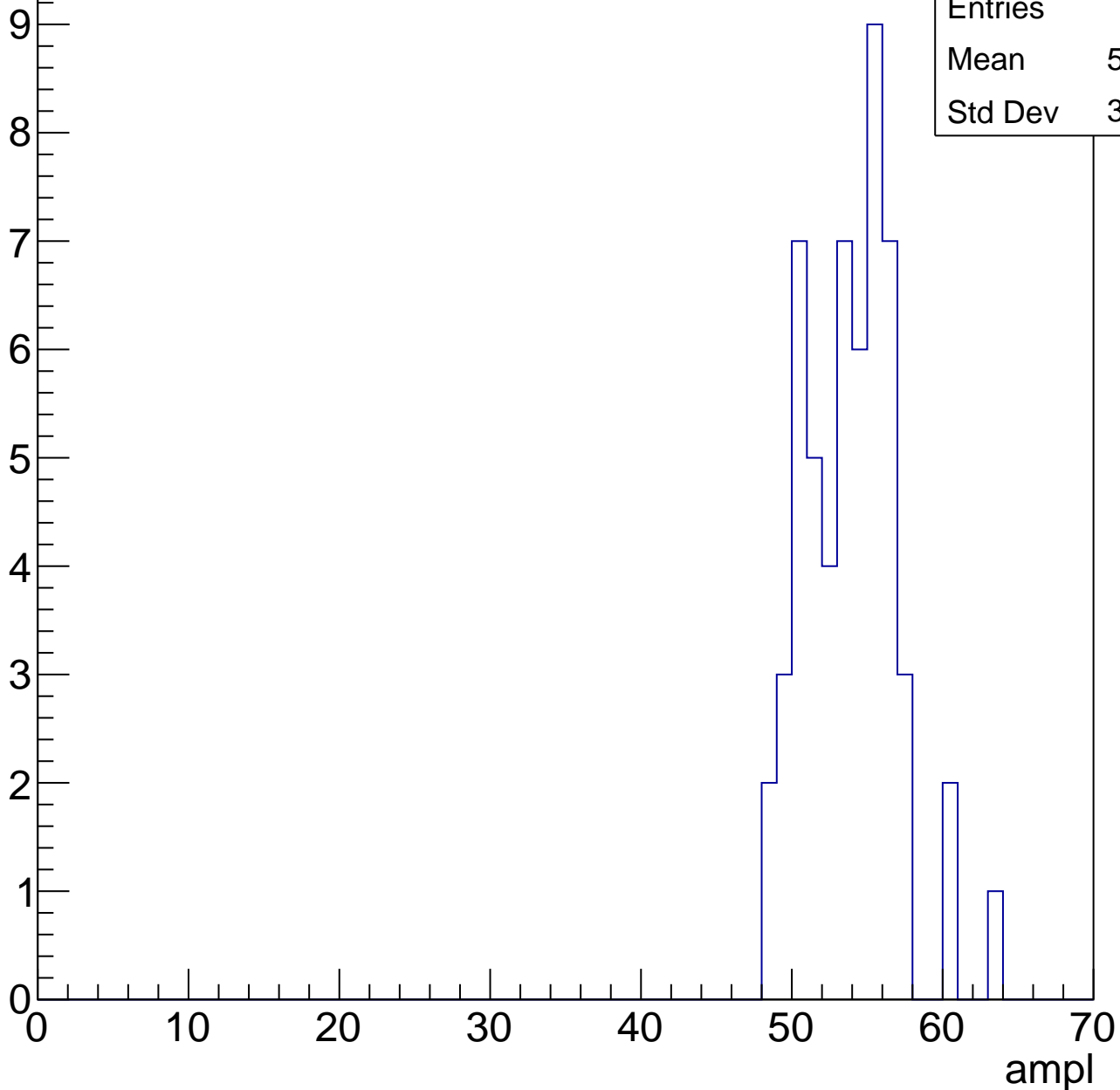


# B0L102S, U17-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	53.43
Std Dev	3.064



# B0L102S, U17-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	68
Mean	57.65
Std Dev	7.685

Entry

10

8

6

4

2

0

0

10

20

30

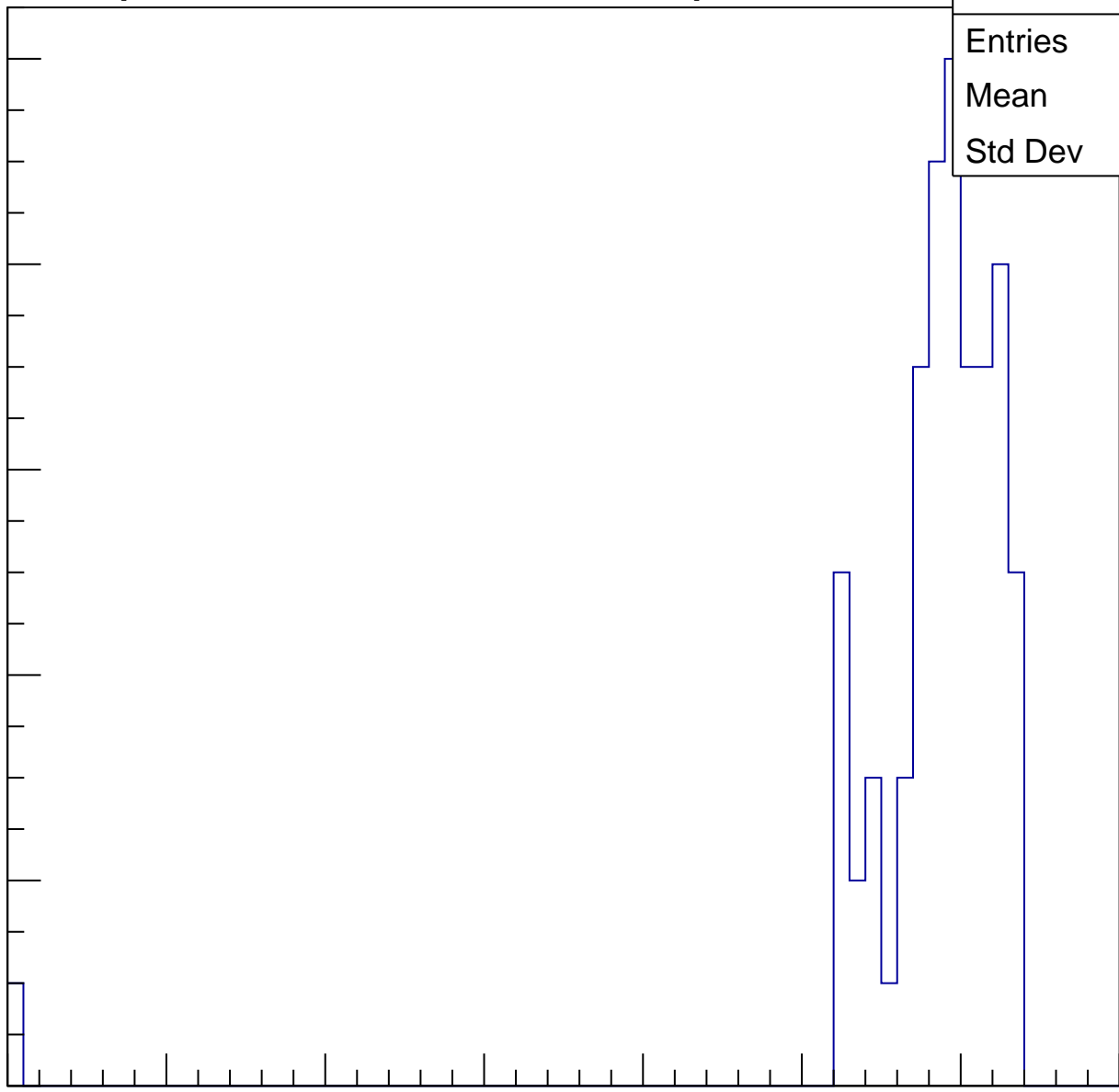
40

50

60

70

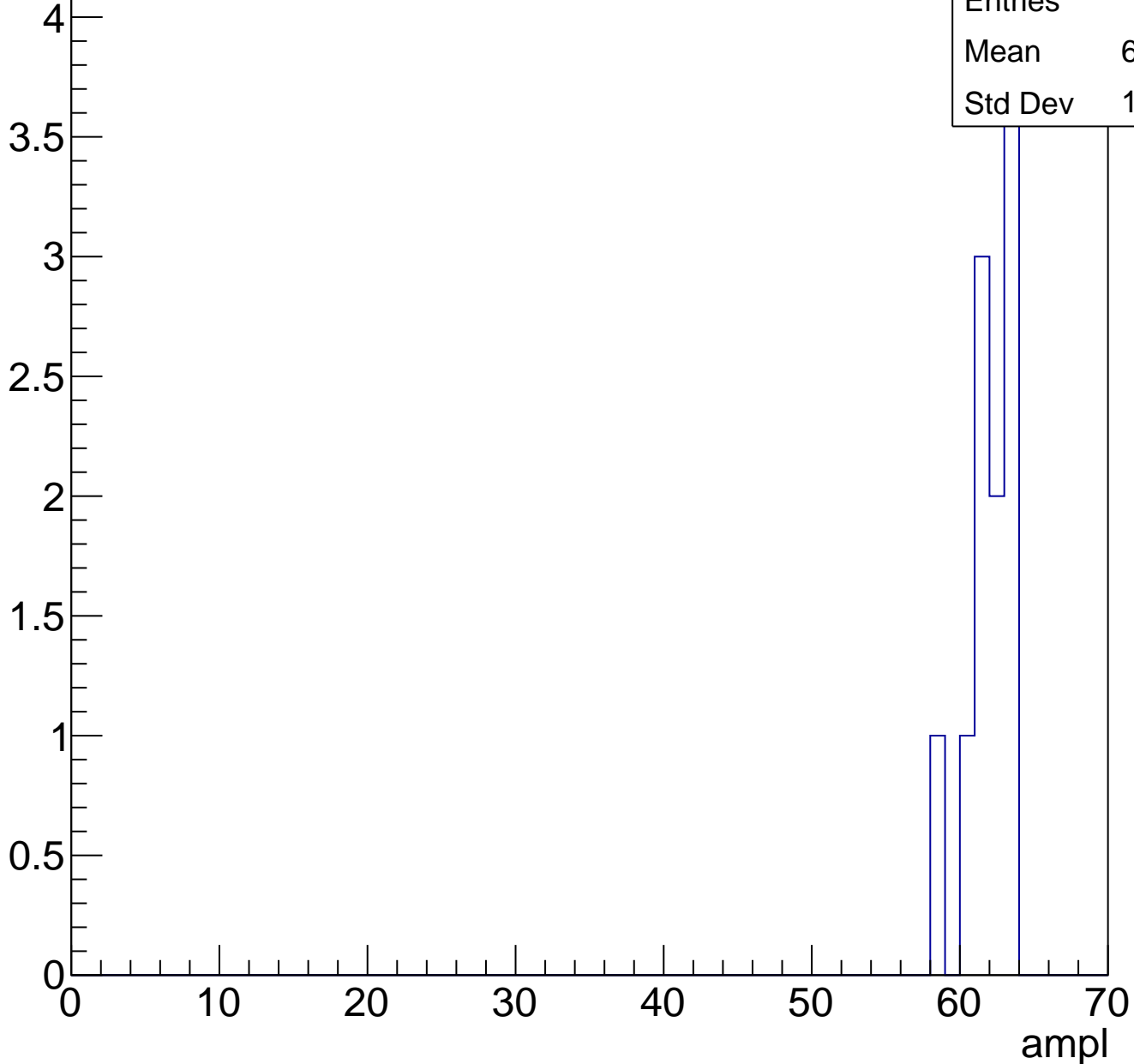
ampl



# B0L102S, U17-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch87, adc0

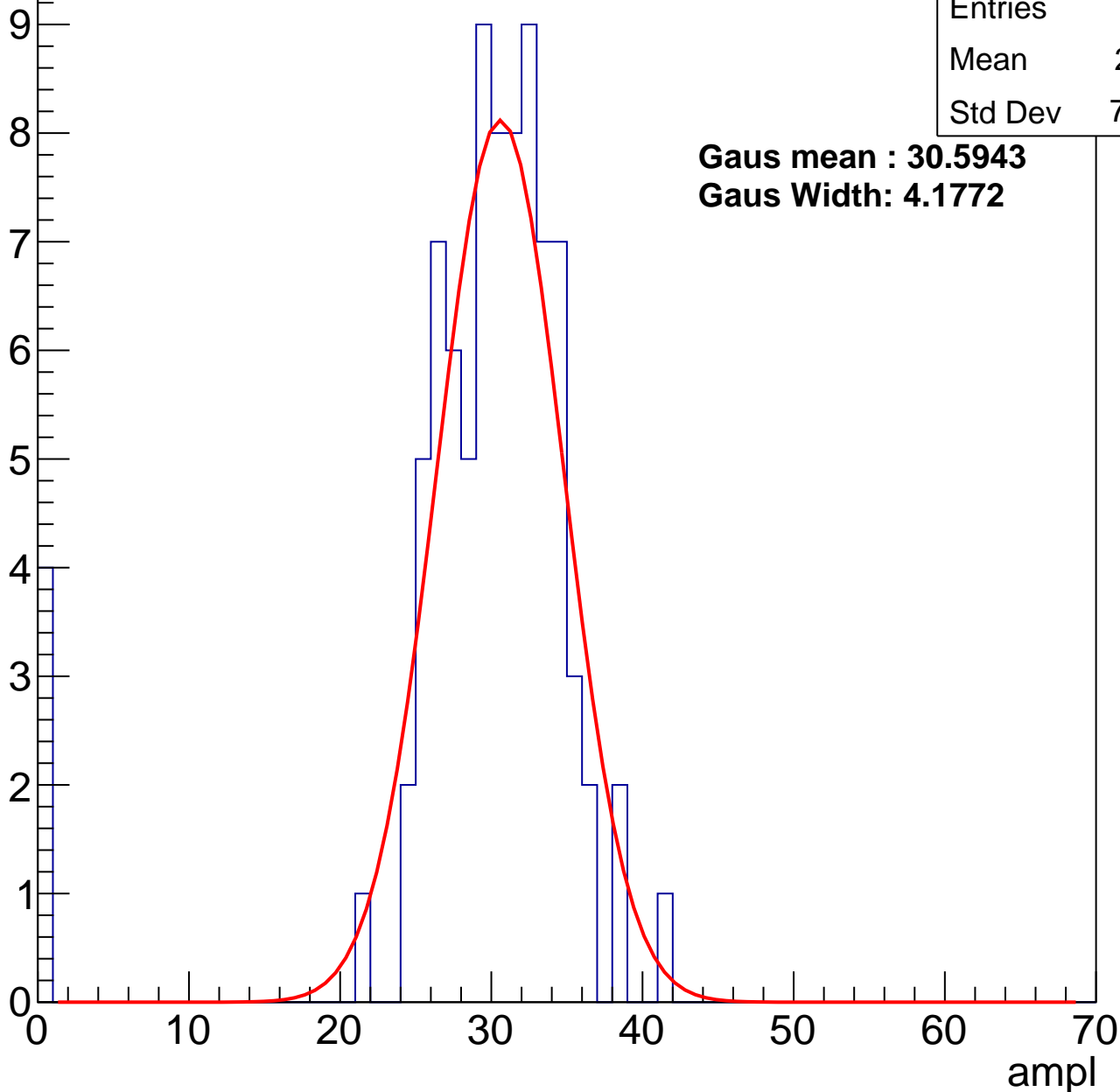
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	86
Mean	28.81
Std Dev	7.287

**Gaus mean : 30.5943**

**Gaus Width: 4.1772**



# B0L102S, U17-ch87, adc1

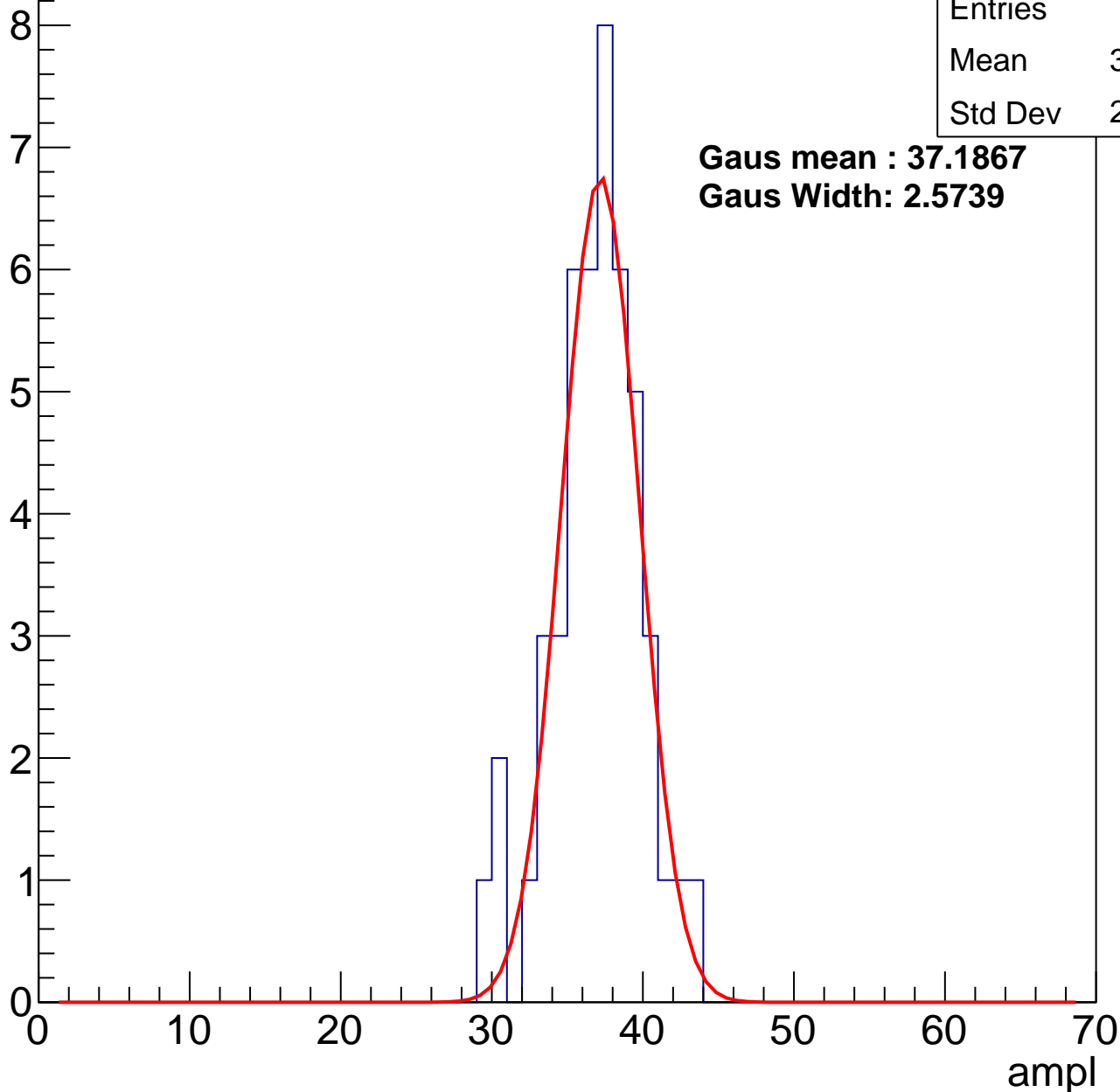
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	47
Mean	36.45
Std Dev	2.945

**Gaus mean : 37.1867**

**Gaus Width: 2.5739**



# B0L102S, U17-ch87, adc2

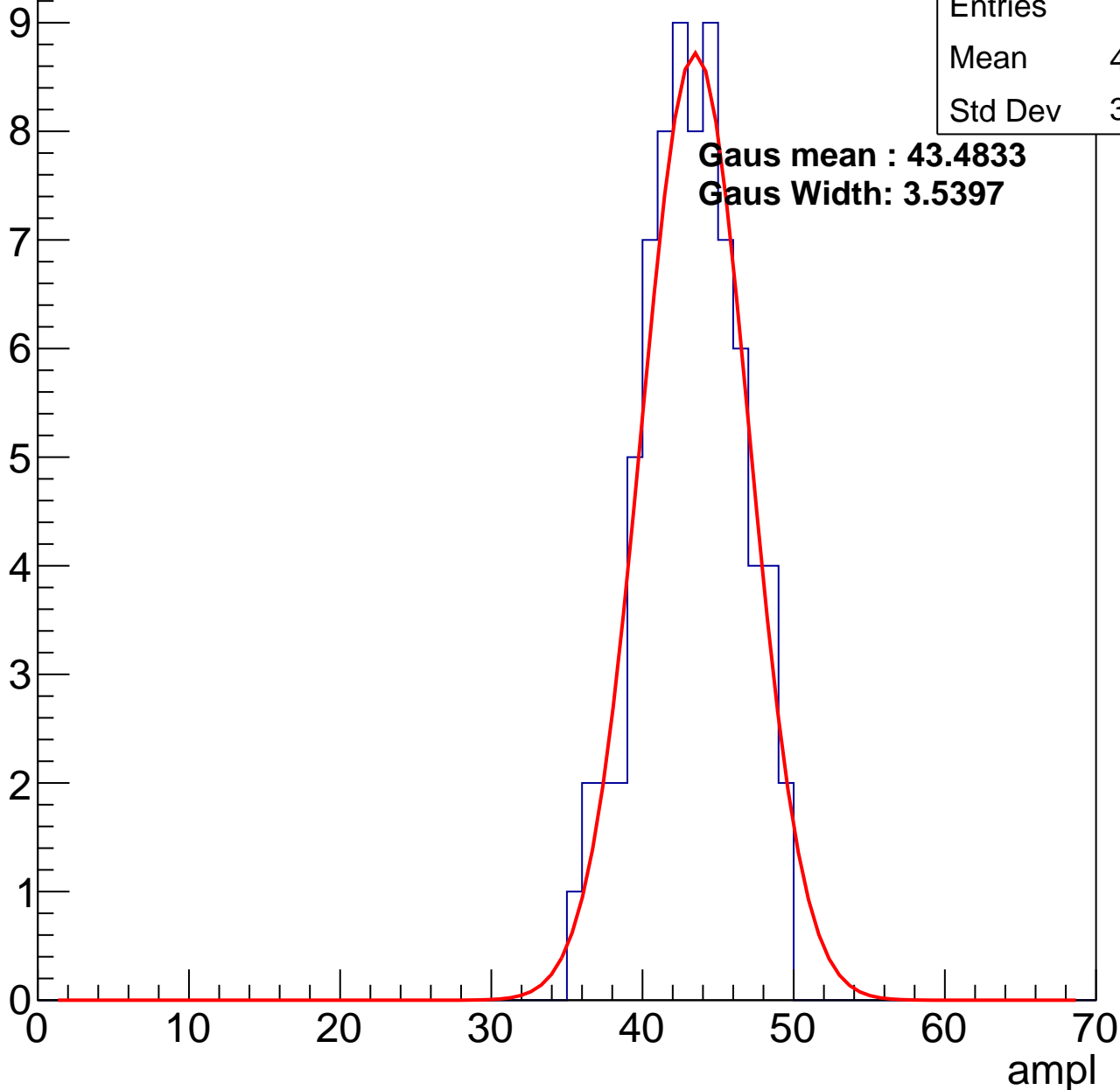
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	42.72
Std Dev	3.222

**Gaus mean : 43.4833**

**Gaus Width: 3.5397**

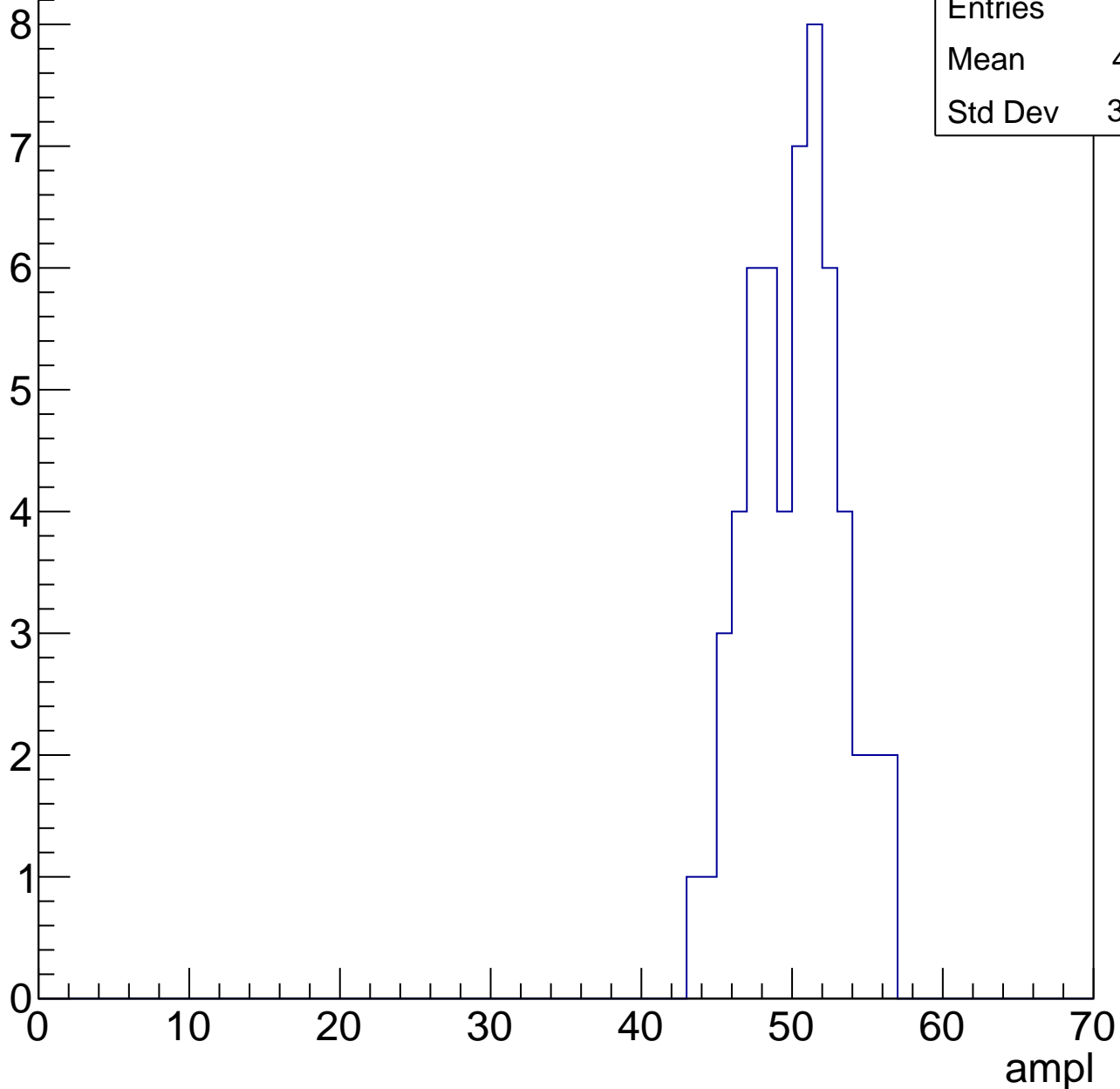


# B0L102S, U17-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	49.71
Std Dev	3.057

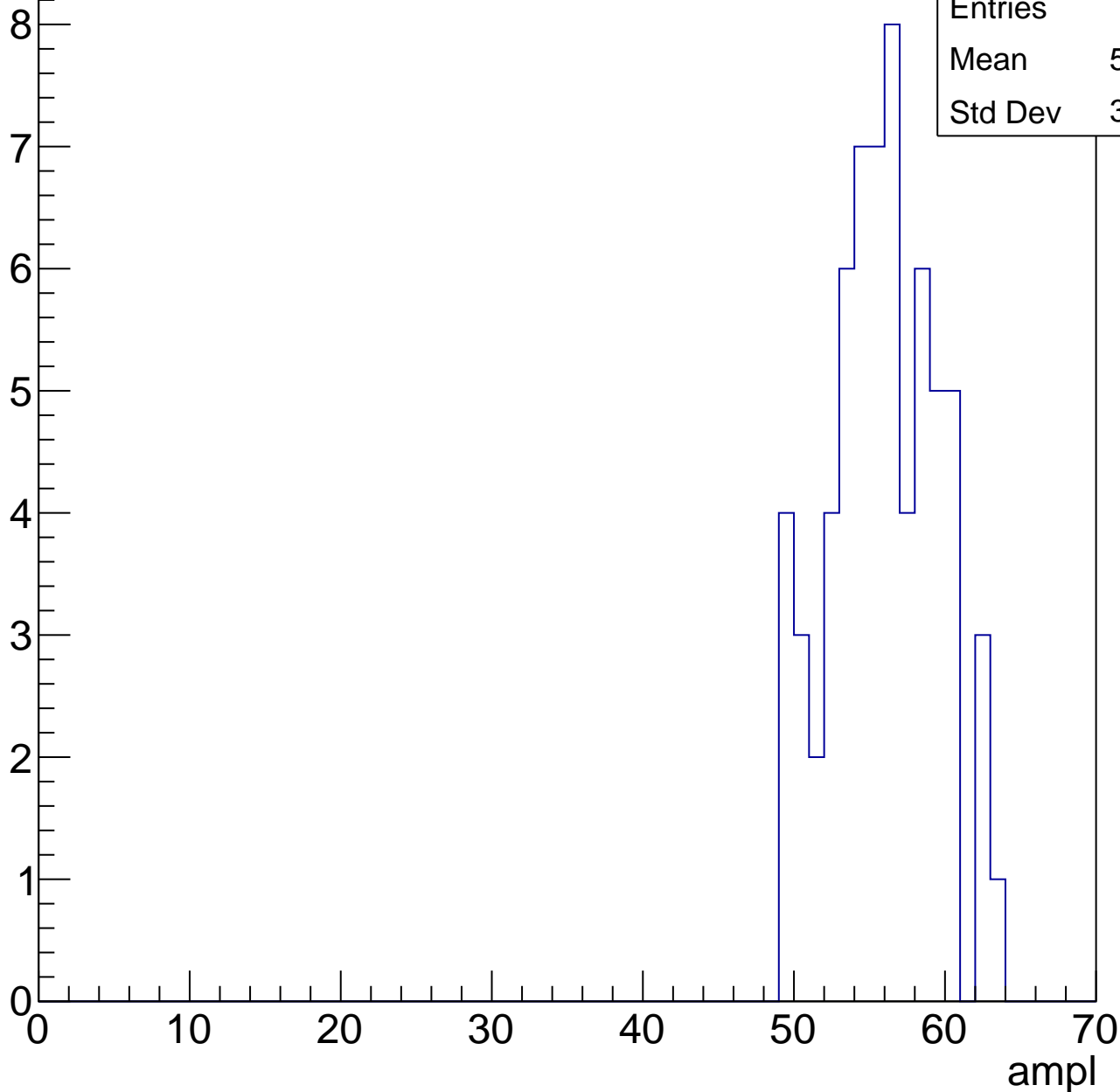


# B0L102S, U17-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	55.46
Std Dev	3.504

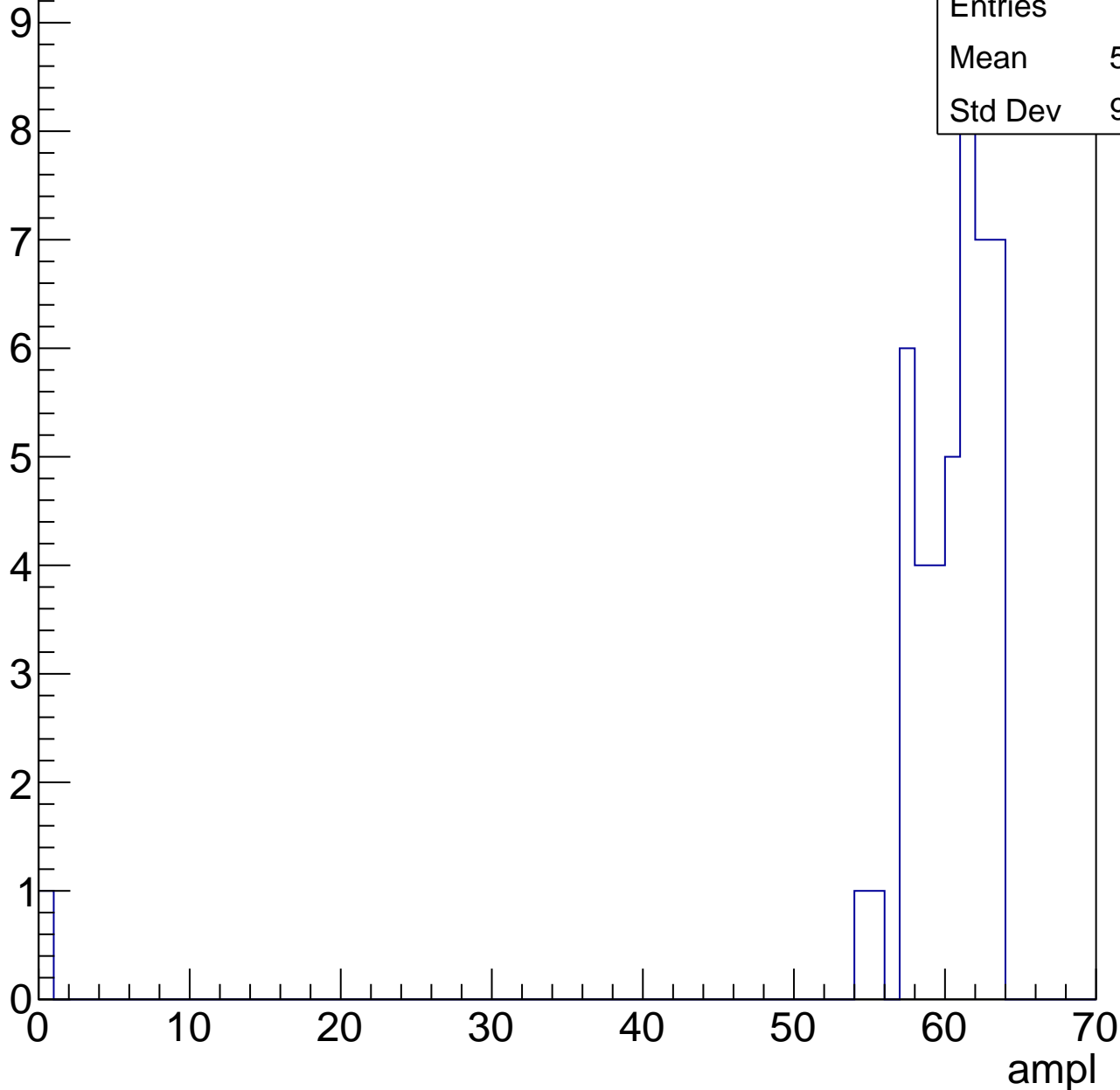


# B0L102S, U17-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

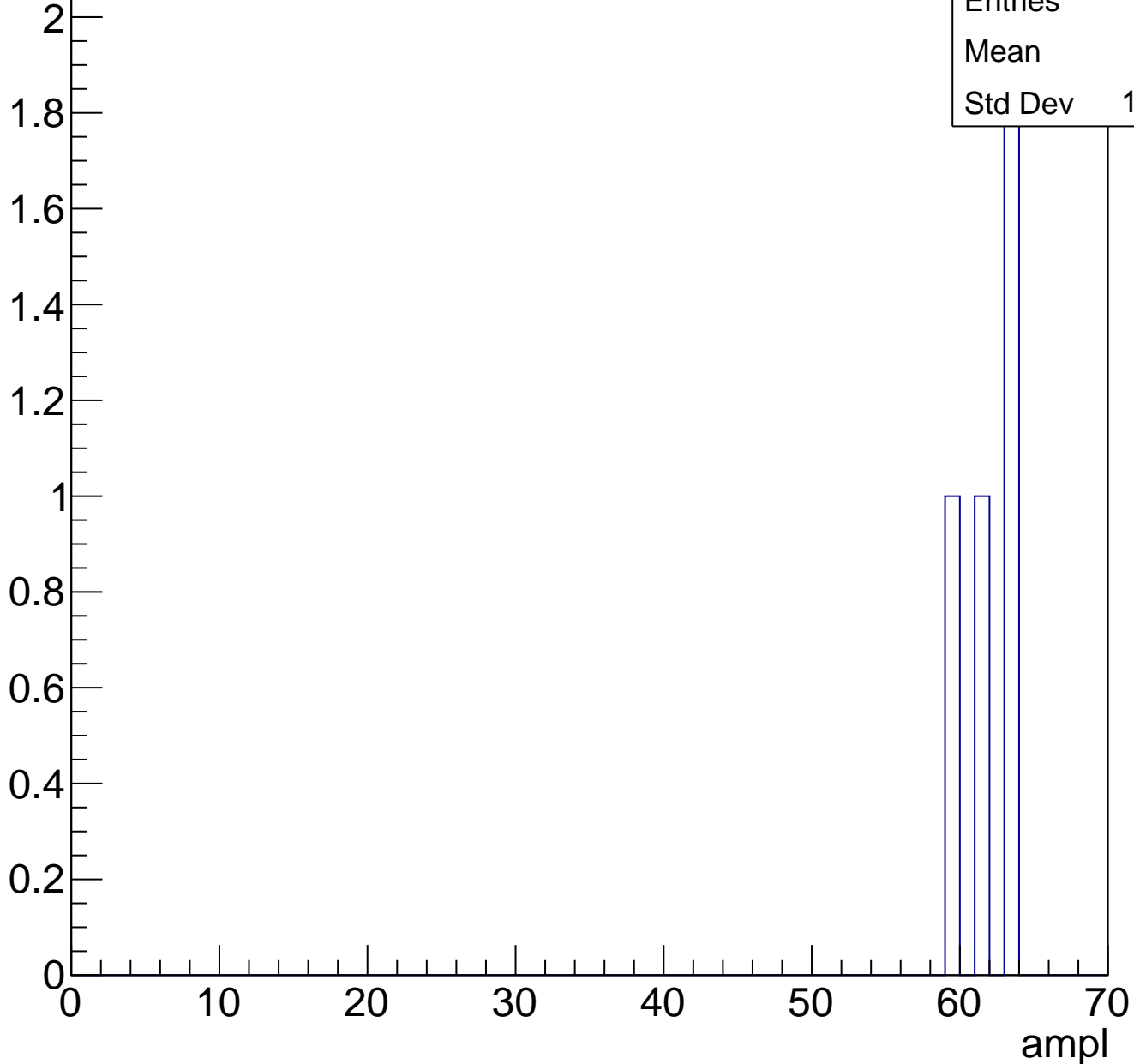
Entries	45
Mean	58.73
Std Dev	9.144



# B0L102S, U17-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch88, adc0

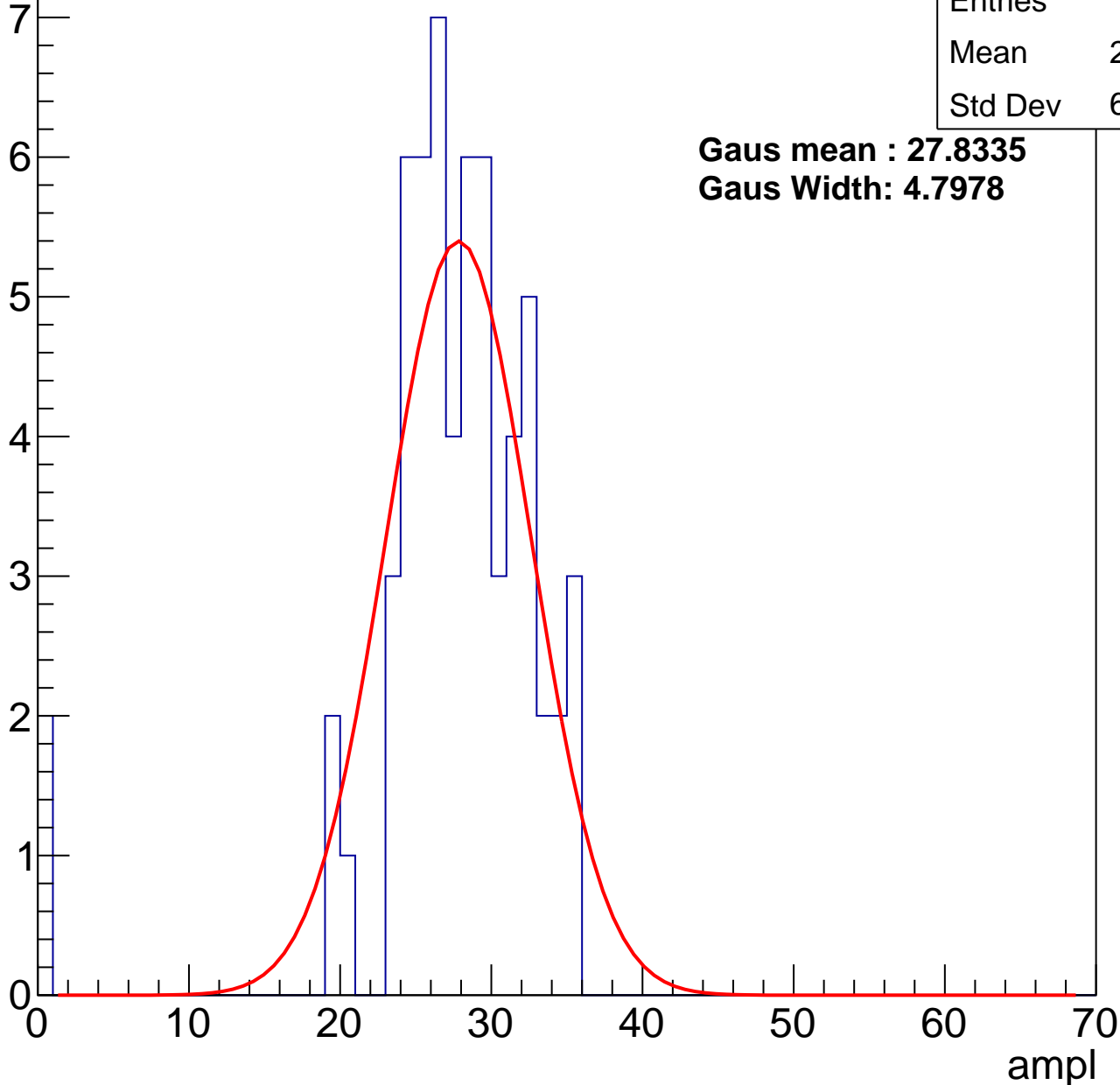
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	26.87
Std Dev	6.189

**Gaus mean : 27.8335**

**Gaus Width: 4.7978**



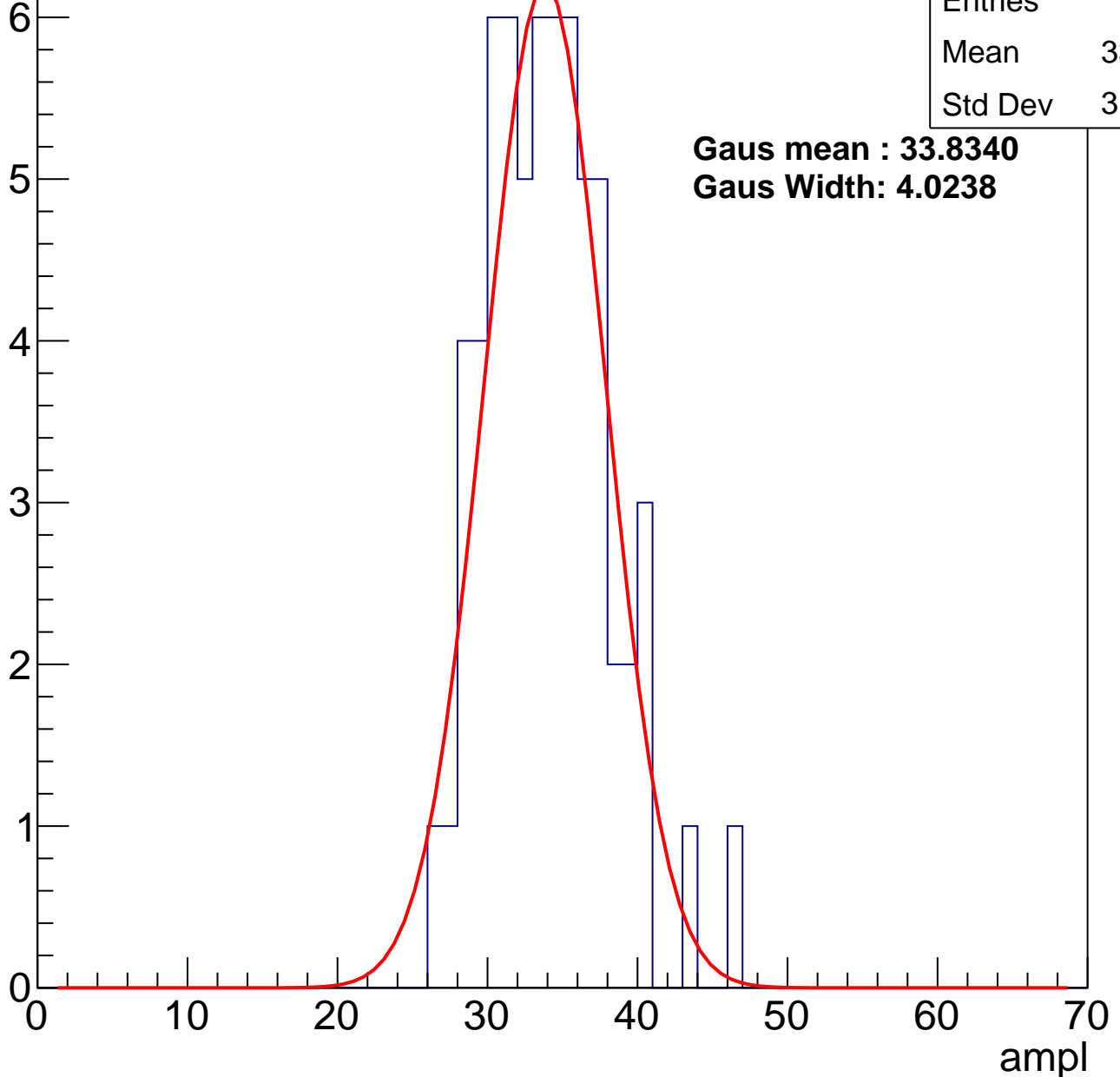
# B0L102S, U17-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	33.55
Std Dev	3.964

**Gaus mean : 33.8340**  
**Gaus Width: 4.0238**



# B0L102S, U17-ch88, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	88
Mean	41.25
Std Dev	3.964

**Gaus mean : 41.8627**

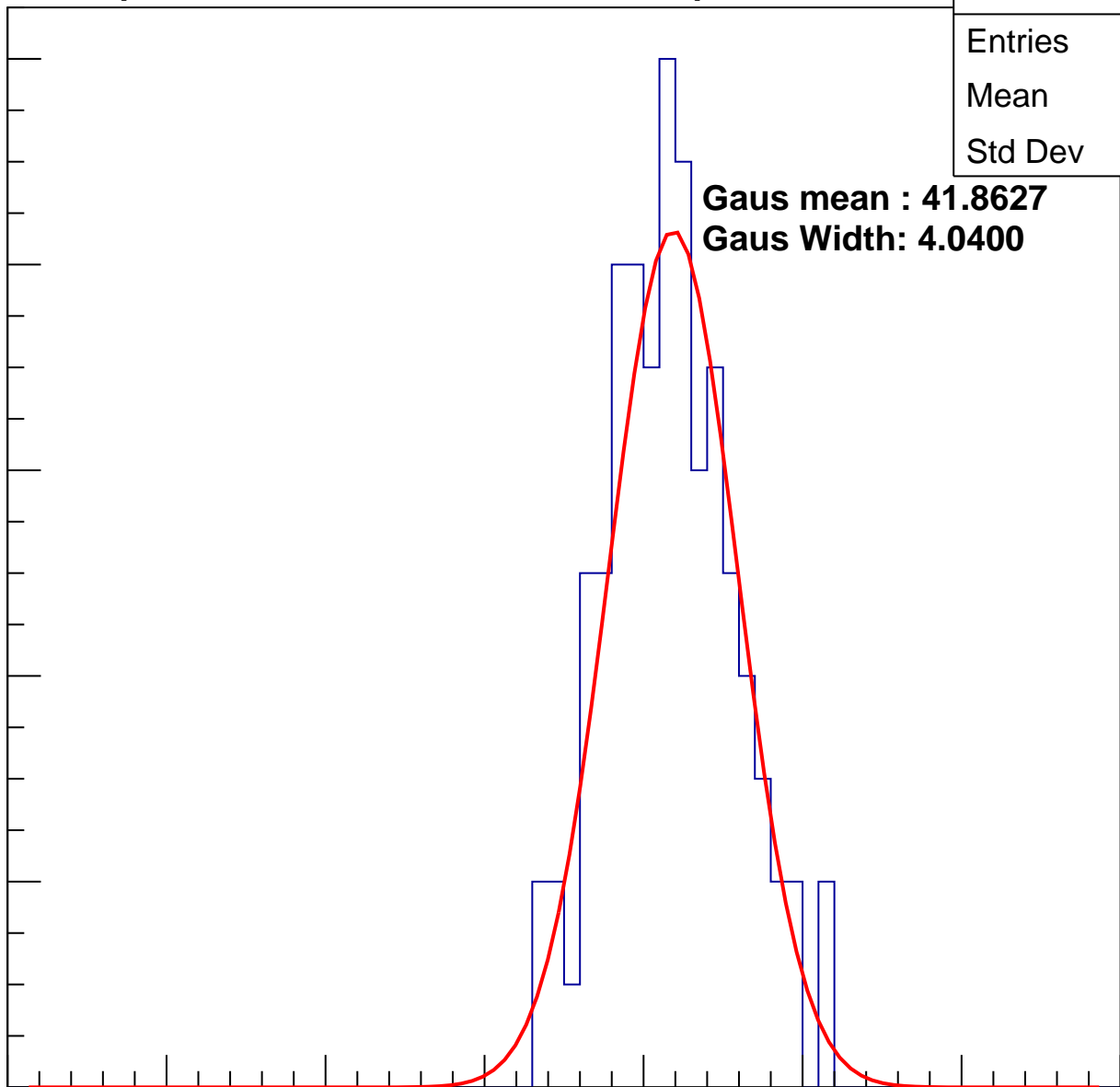
**Gaus Width: 4.0400**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

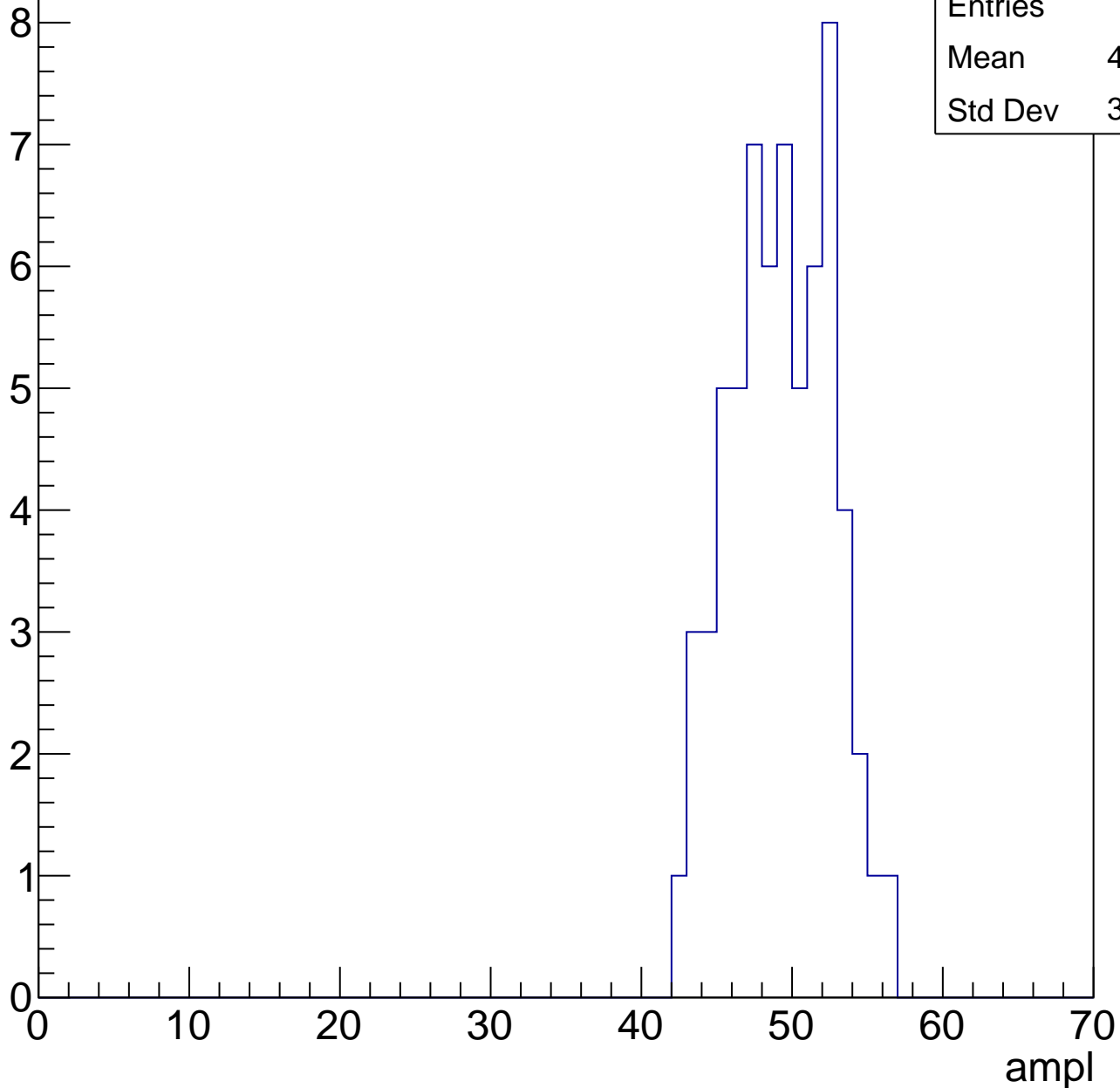


# B0L102S, U17-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

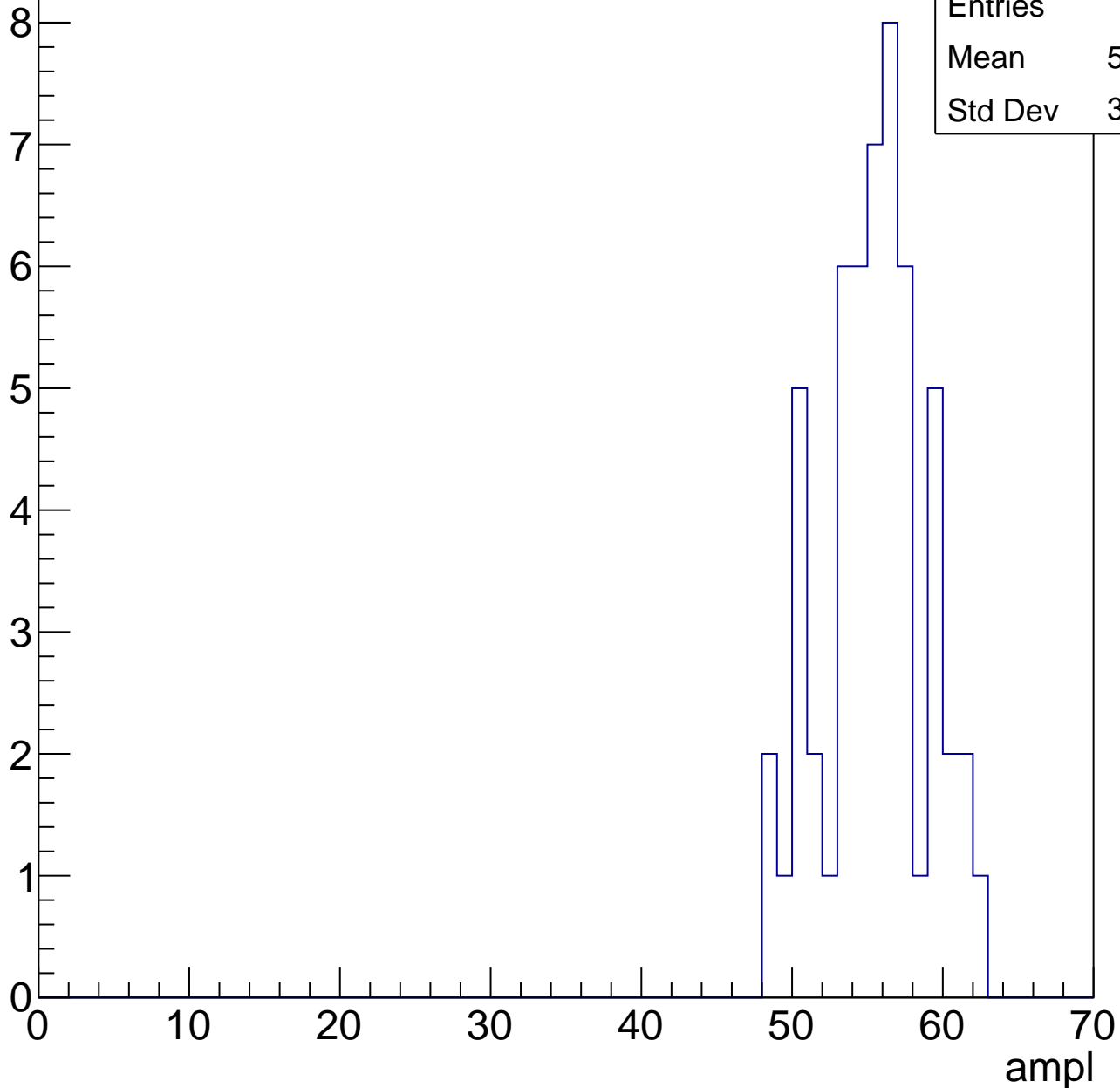
Entries	64
Mean	48.77
Std Dev	3.277



# B0L102S, U17-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



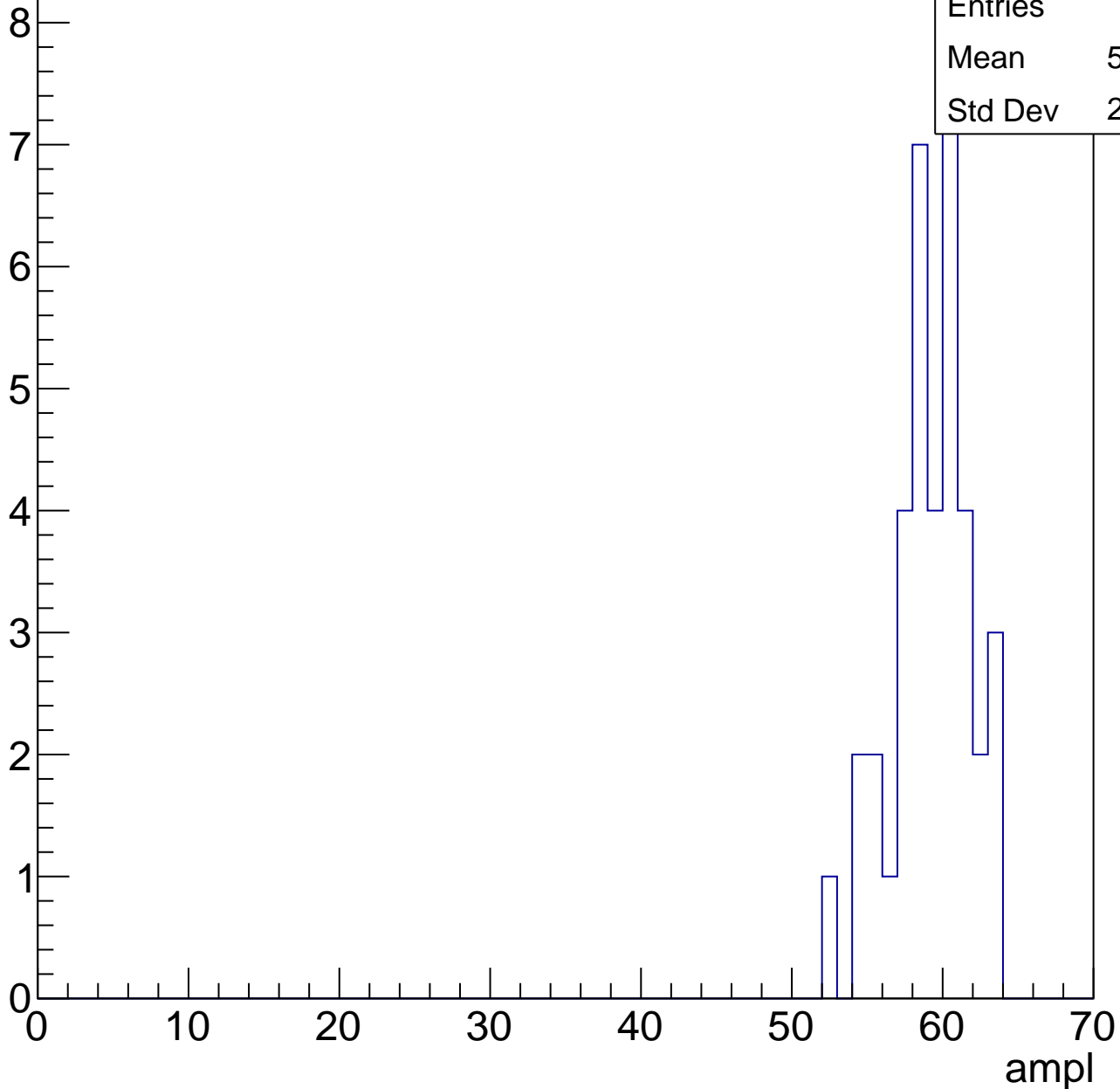
Entries	55
Mean	54.96
Std Dev	3.363

# B0L102S, U17-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	58.76
Std Dev	2.569

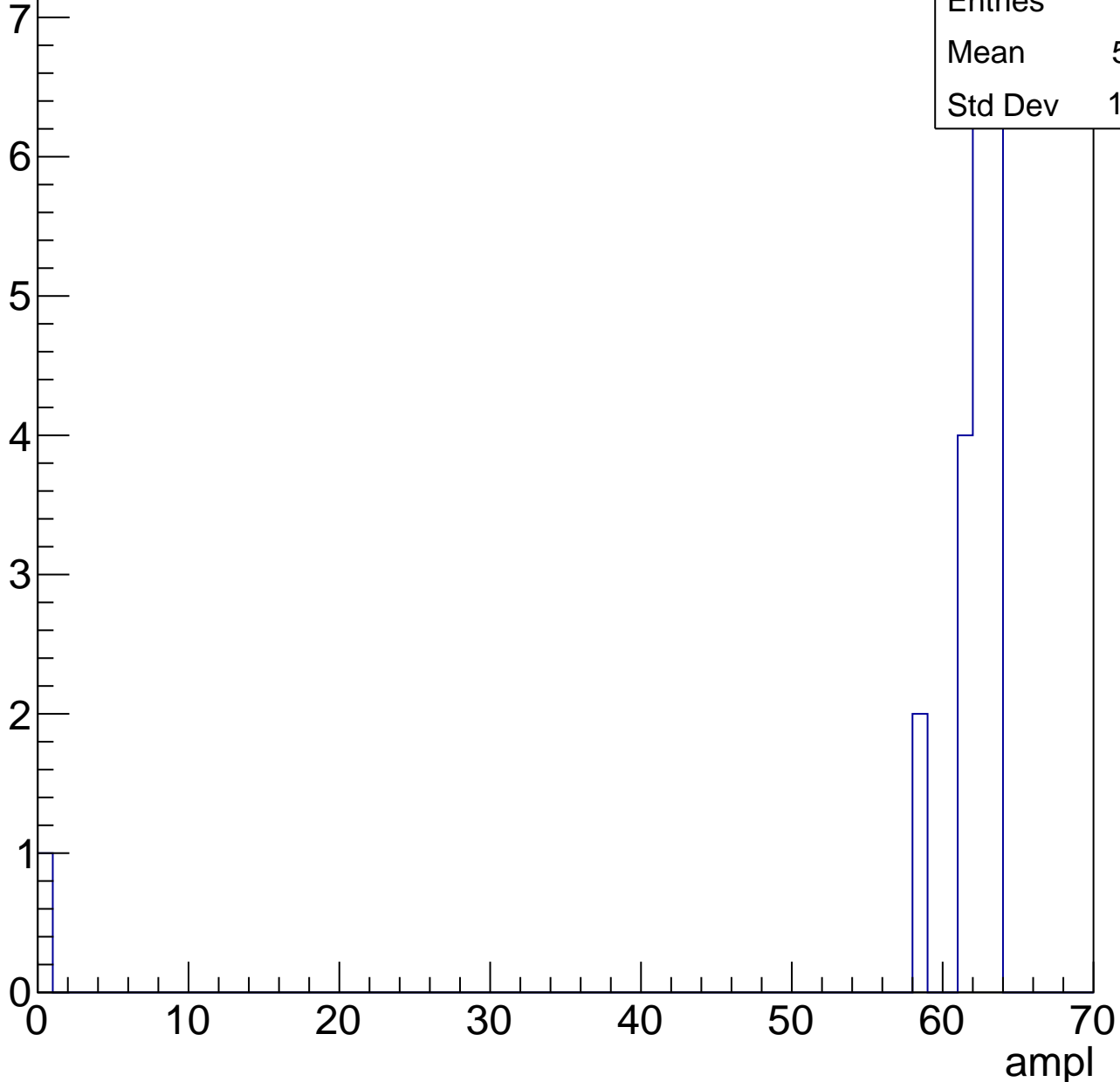


# B0L102S, U17-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	21
Mean	58.81
Std Dev	13.23

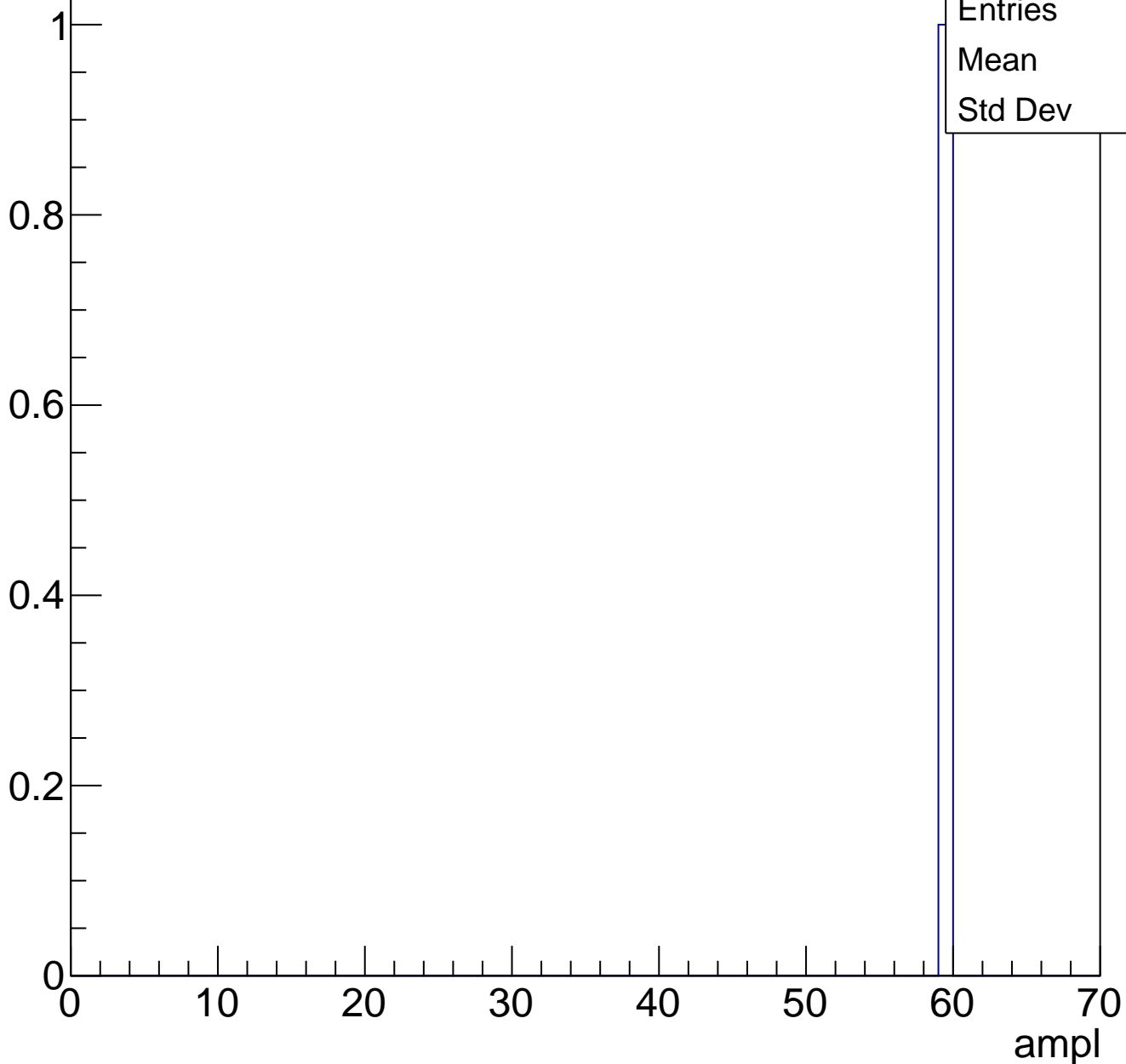




# B0L102S, U17-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch89, adc0

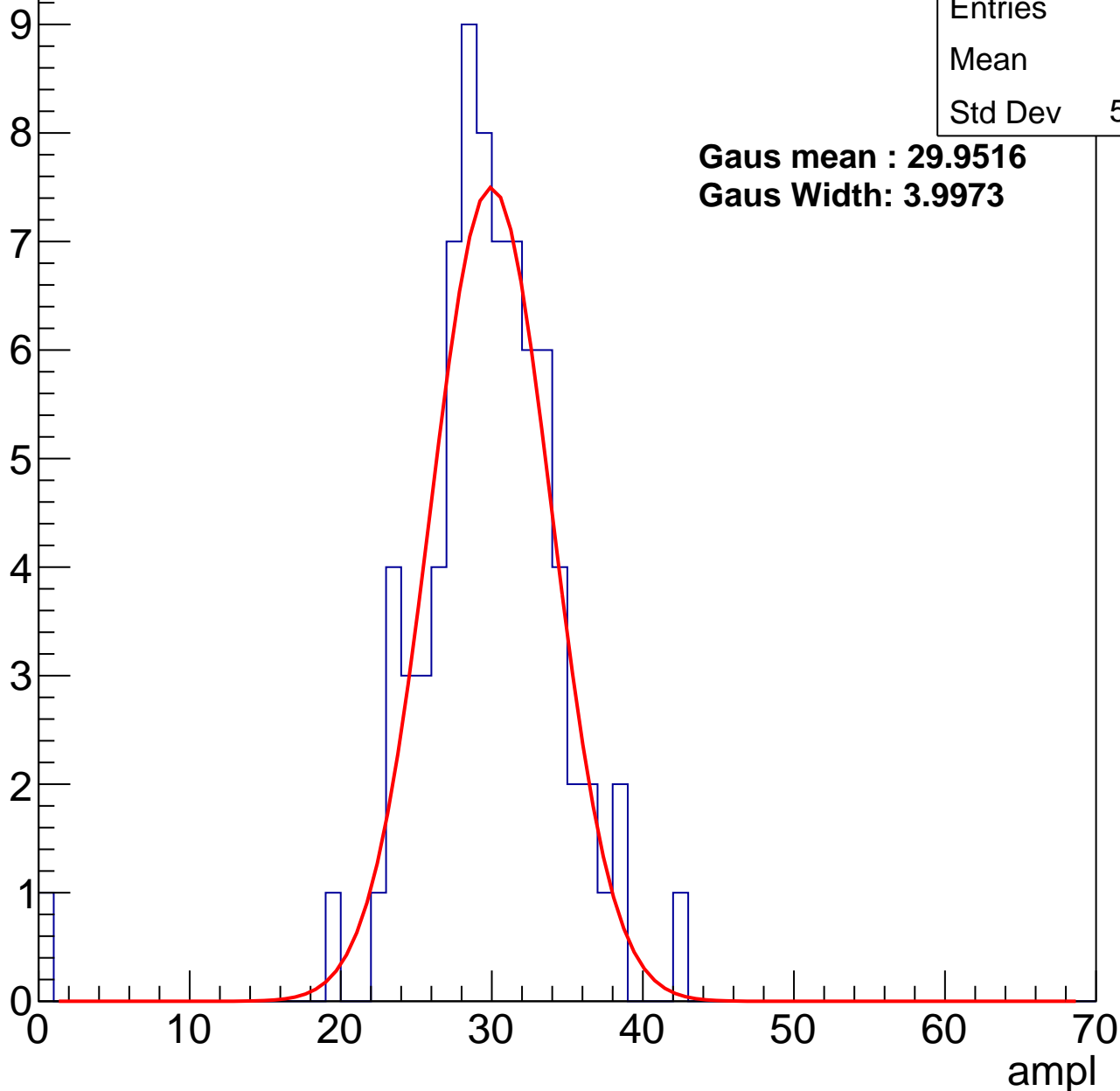
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	29.2
Std Dev	5.249

**Gaus mean : 29.9516**

**Gaus Width: 3.9973**



# B0L102S, U17-ch89, adc1

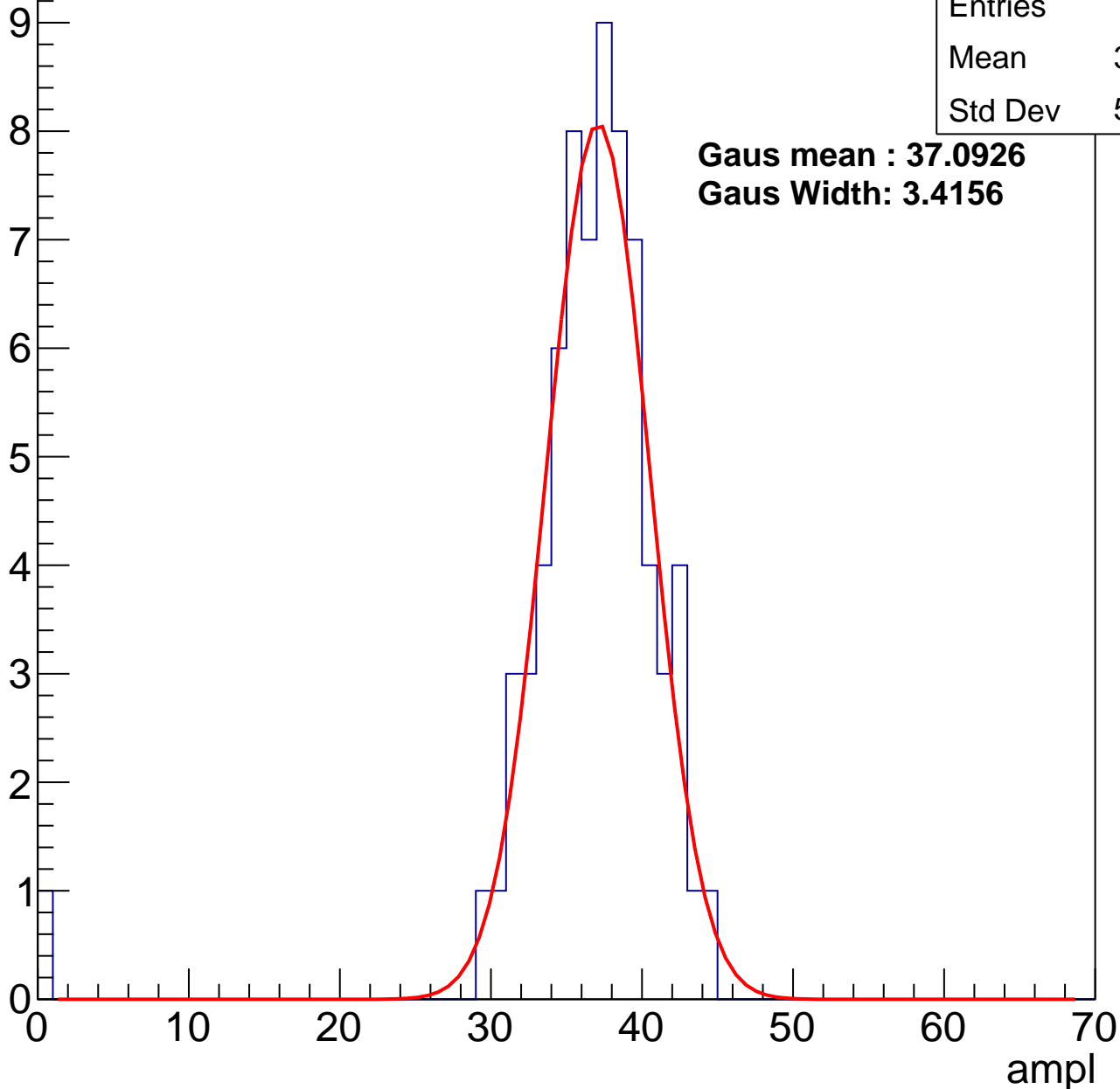
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.11
Std Dev	5.401

**Gaus mean : 37.0926**

**Gaus Width: 3.4156**



# B0L102S, U17-ch89, adc2

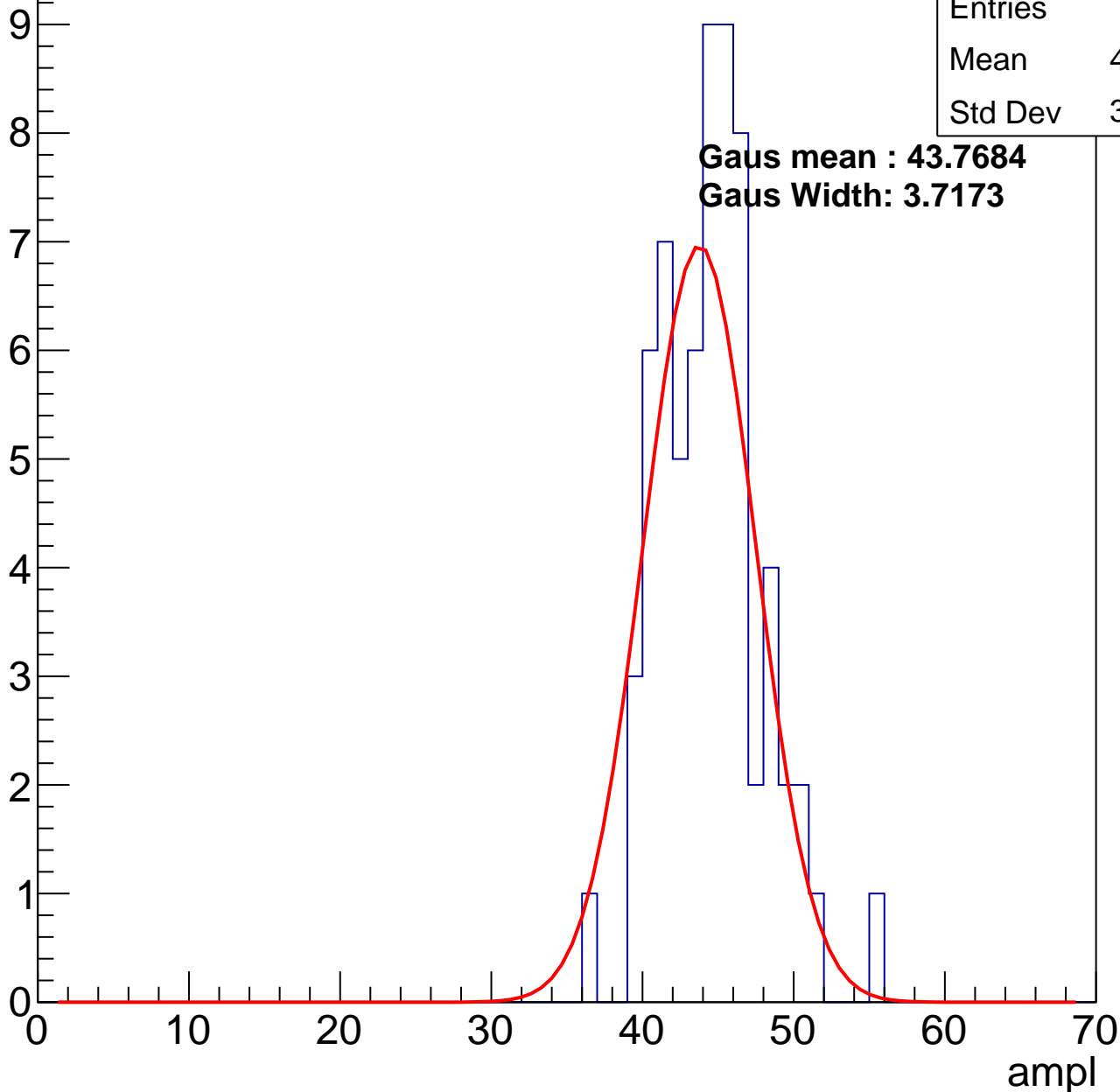
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	44.05
Std Dev	3.346

**Gaus mean : 43.7684**

**Gaus Width: 3.7173**

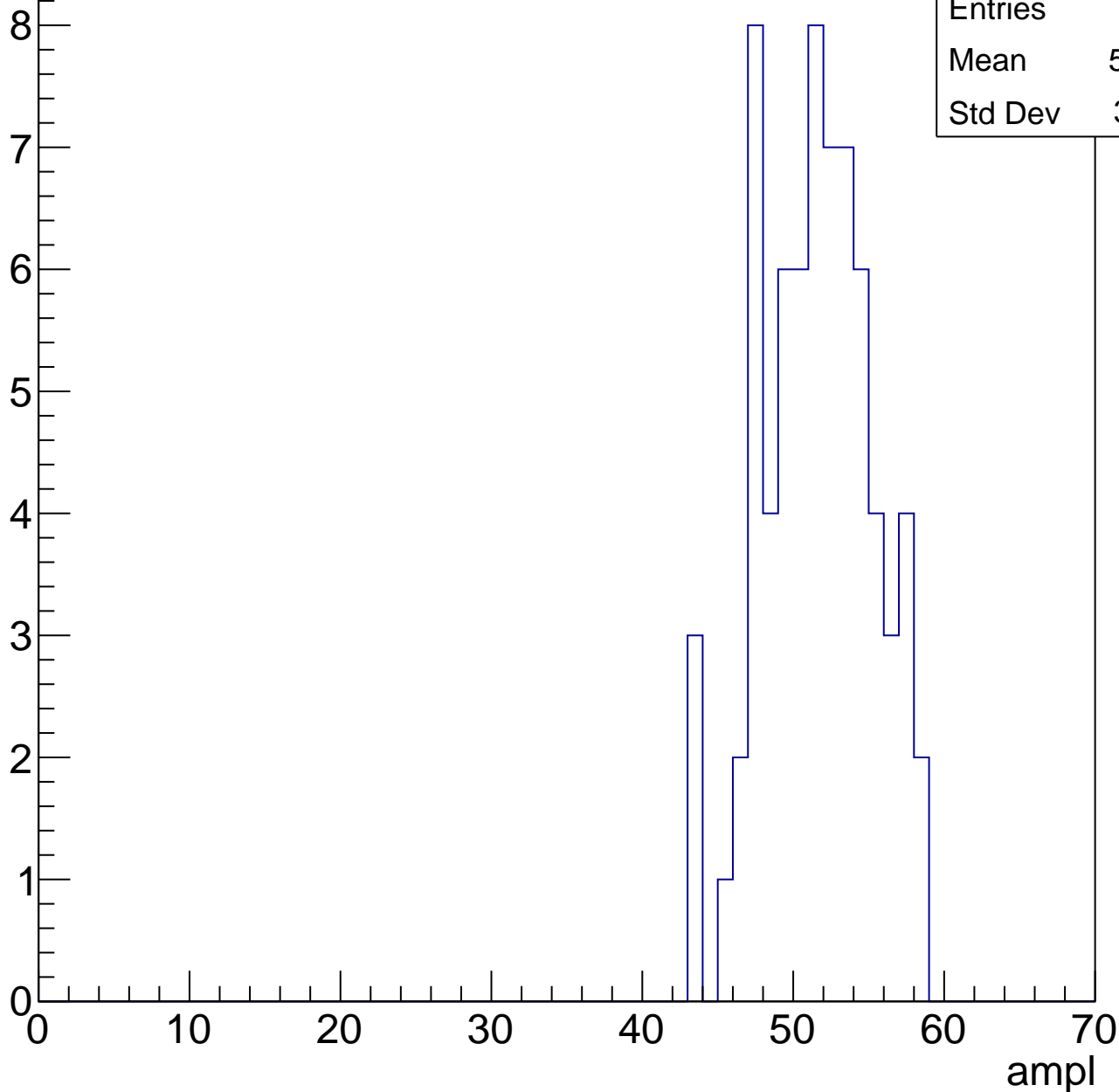


# B0L102S, U17-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	51.08
Std Dev	3.641

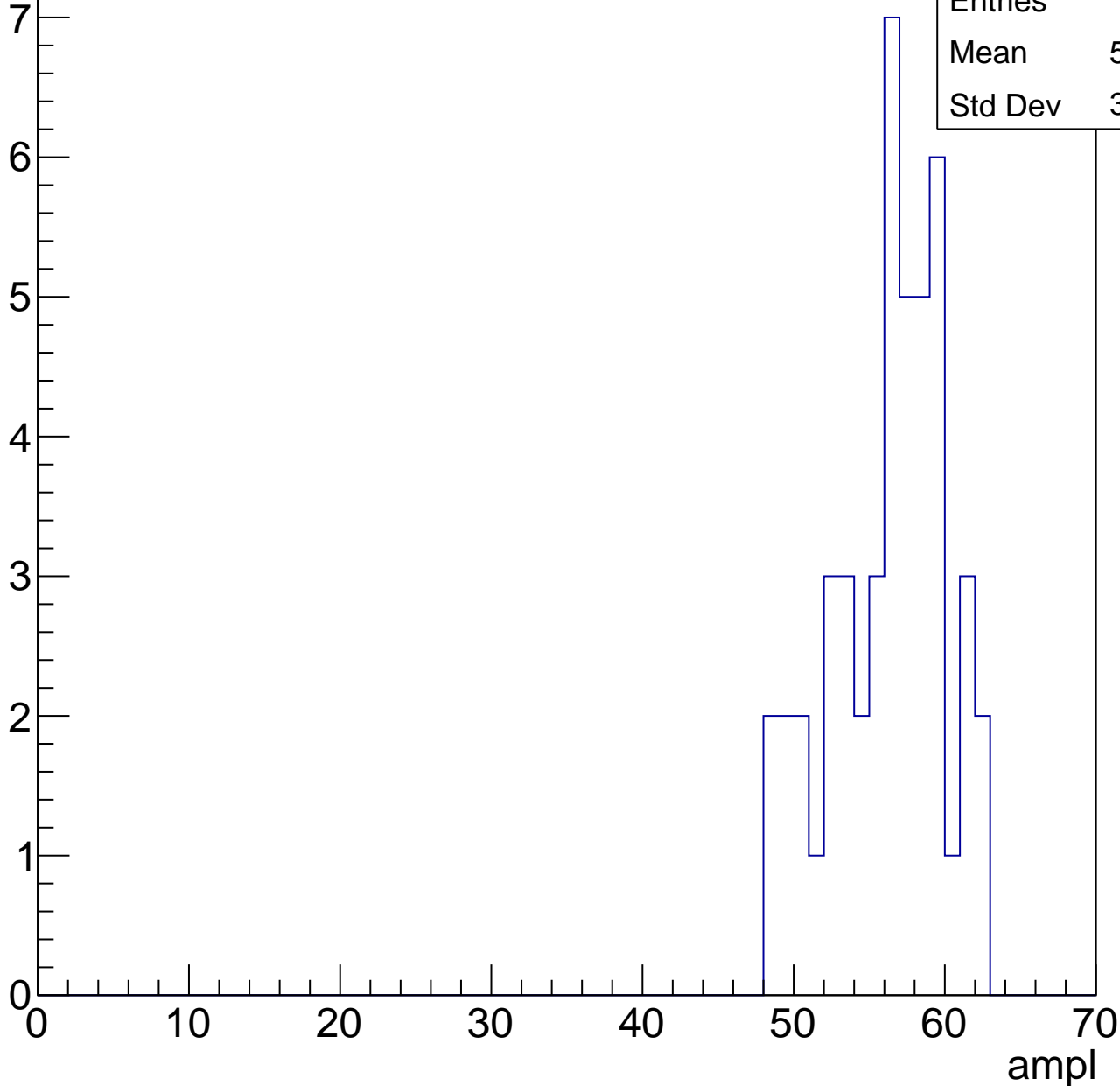


# B0L102S, U17-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

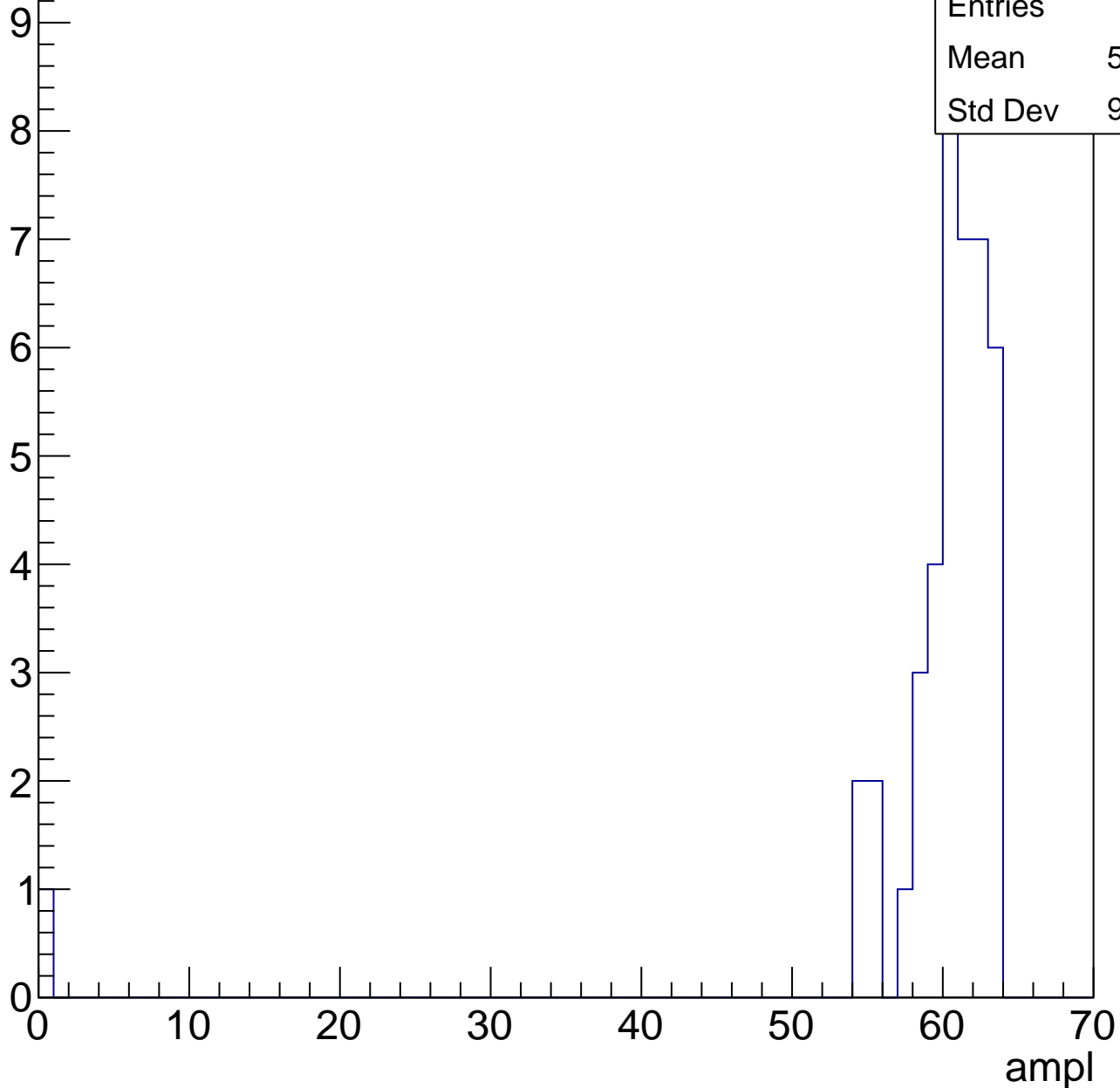
Entries	47
Mean	55.77
Std Dev	3.714



# B0L102S, U17-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch90, adc0

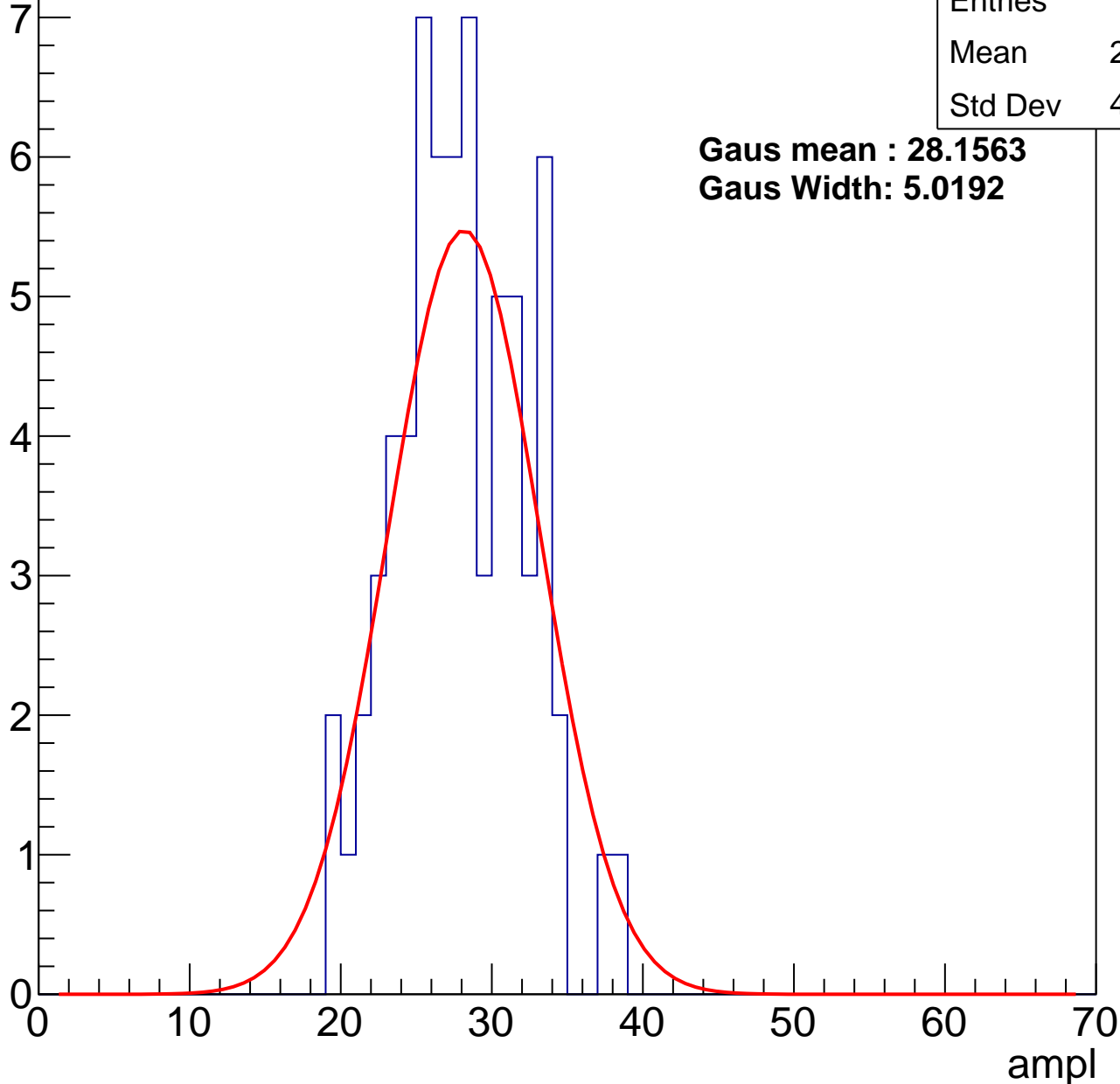
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	27.53
Std Dev	4.188

**Gaus mean : 28.1563**

**Gaus Width: 5.0192**



# B0L102S, U17-ch90, adc1

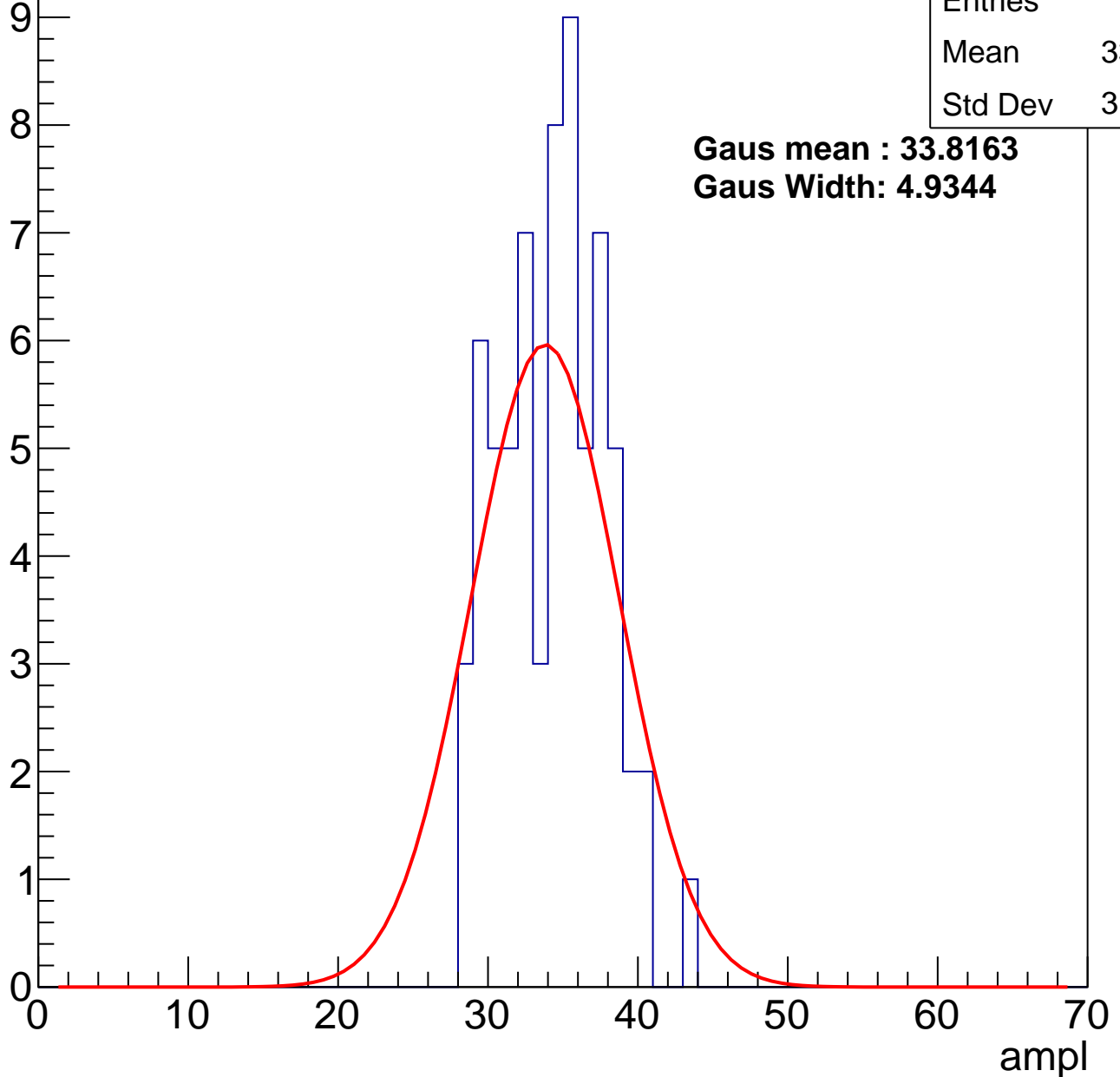
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	33.87
Std Dev	3.404

**Gaus mean : 33.8163**

**Gaus Width: 4.9344**



# B0L102S, U17-ch90, adc2

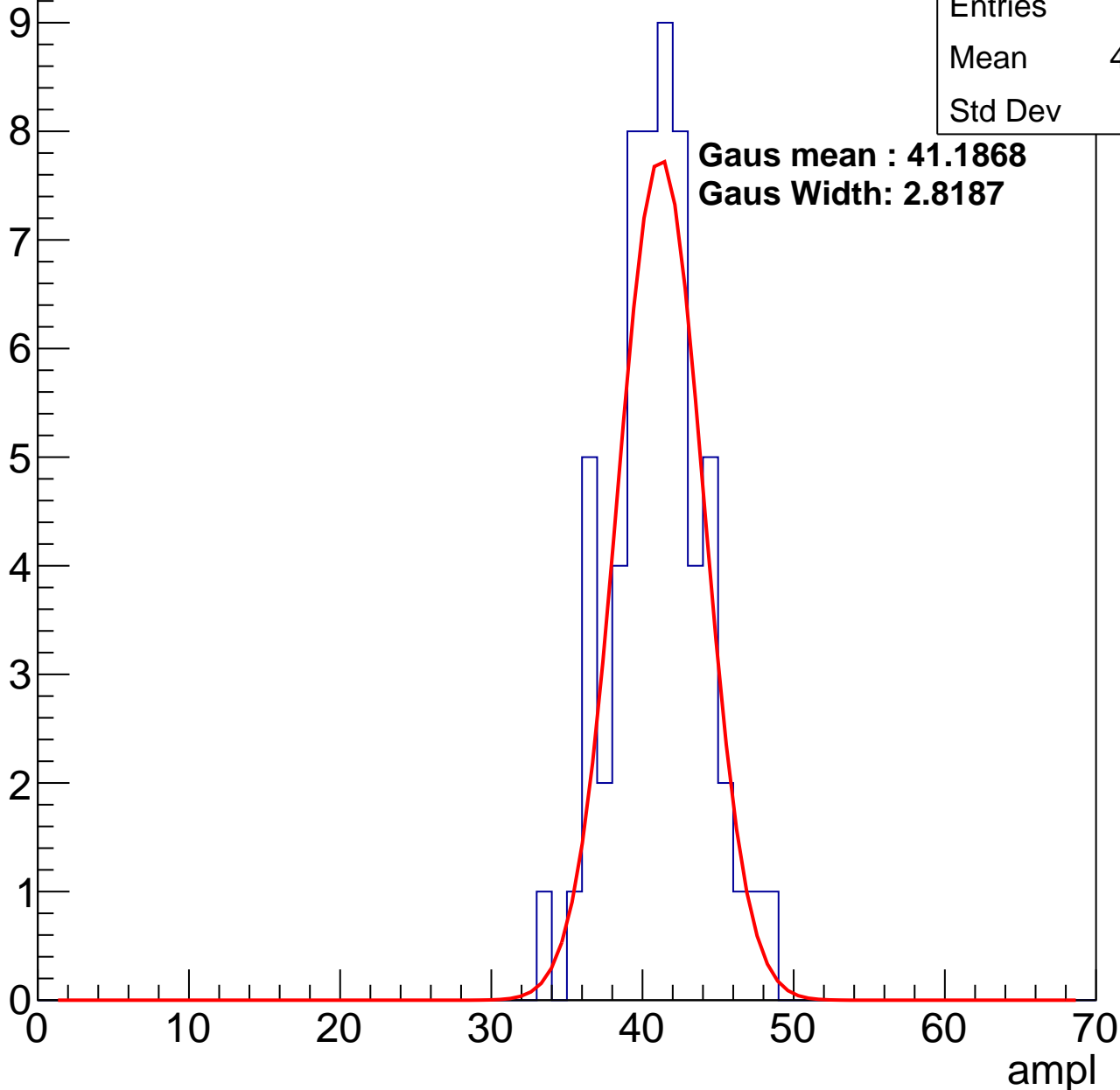
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	40.57
Std Dev	2.98

**Gaus mean : 41.1868**

**Gaus Width: 2.8187**

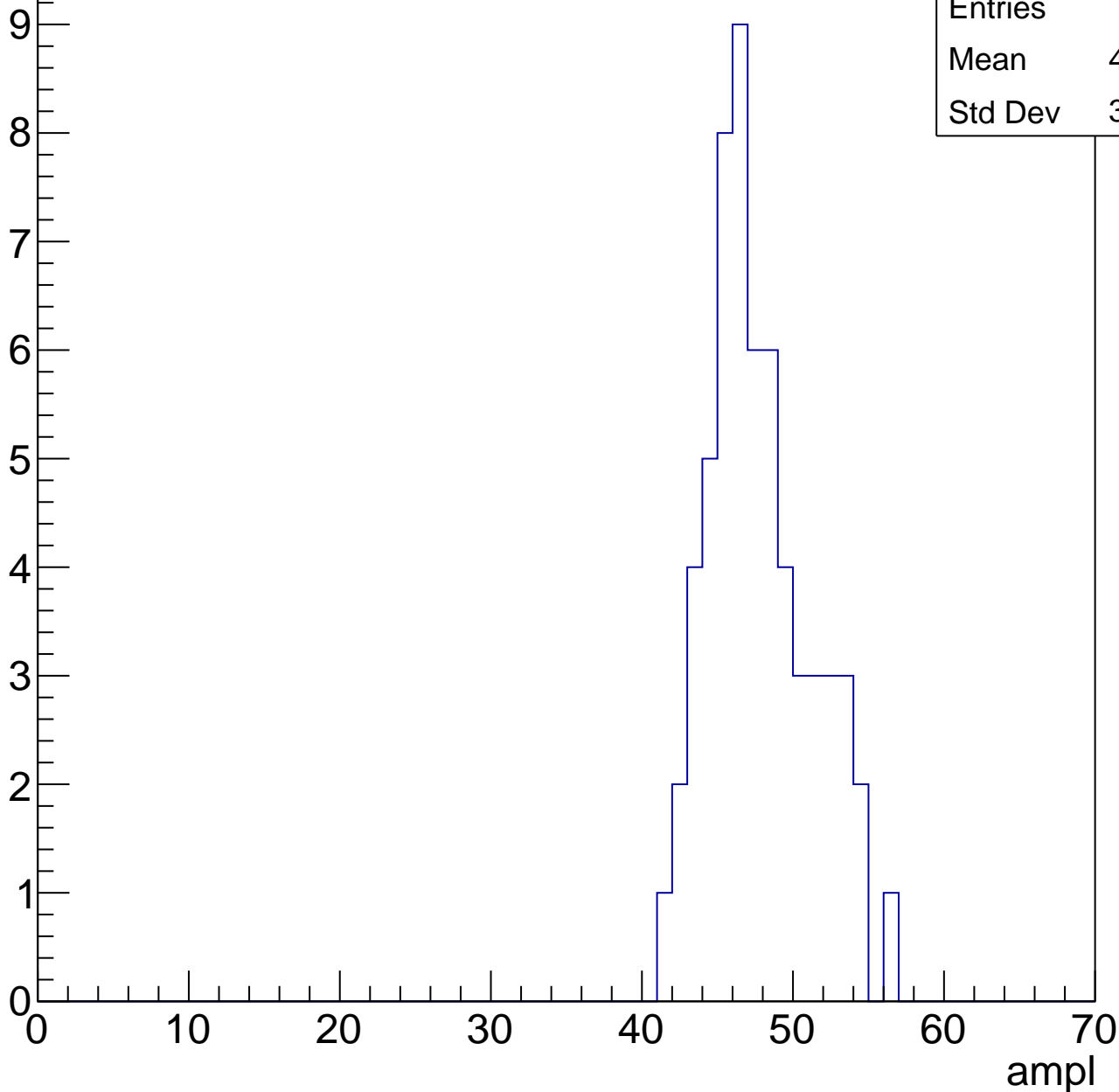


# B0L102S, U17-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	47.32
Std Dev	3.389

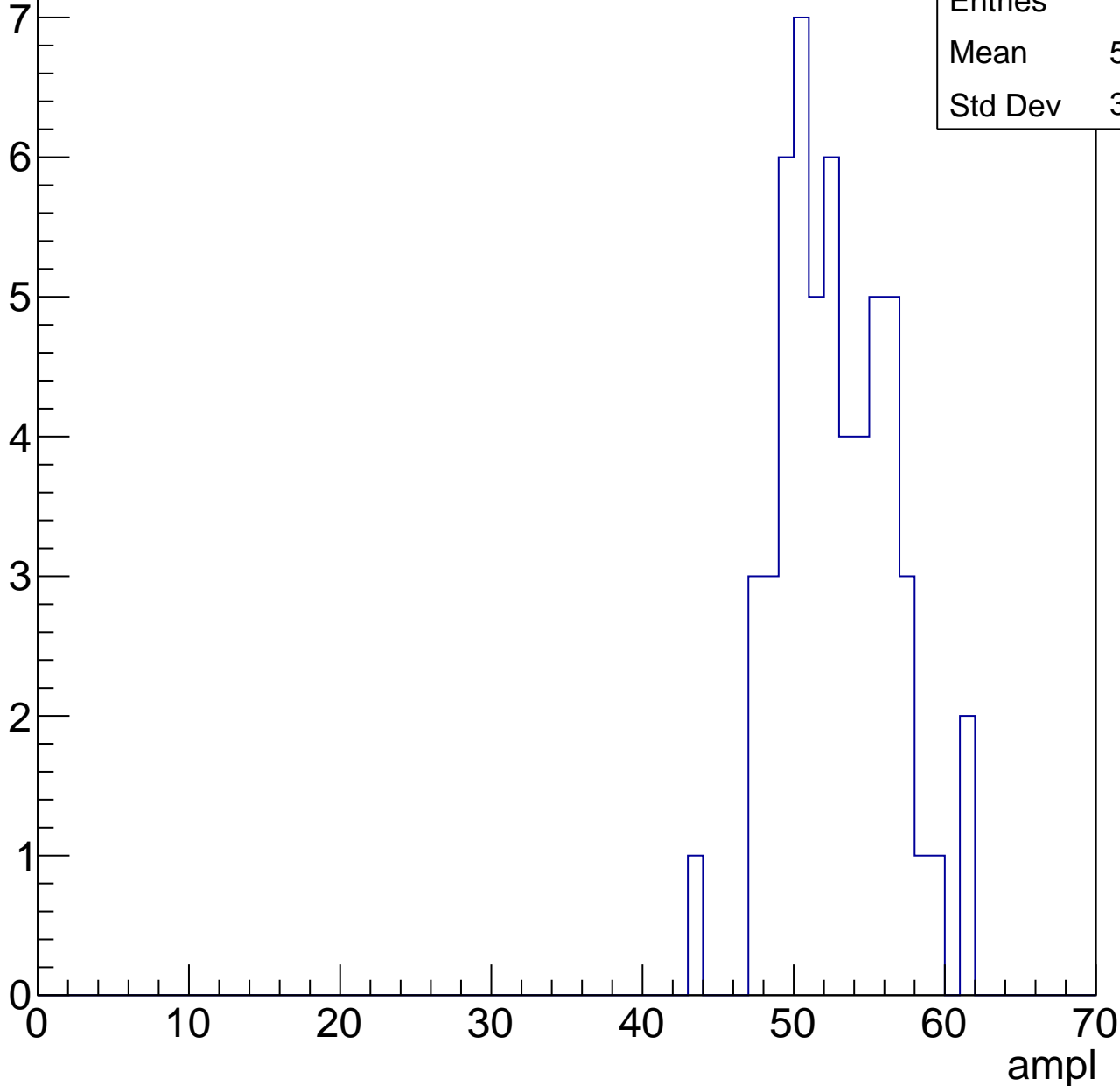


# B0L102S, U17-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	52.36
Std Dev	3.662



# B0L102S, U17-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

10

Entries 73

Mean 58.4

Std Dev 3.152

8

6

4

2

0

0

10

20

30

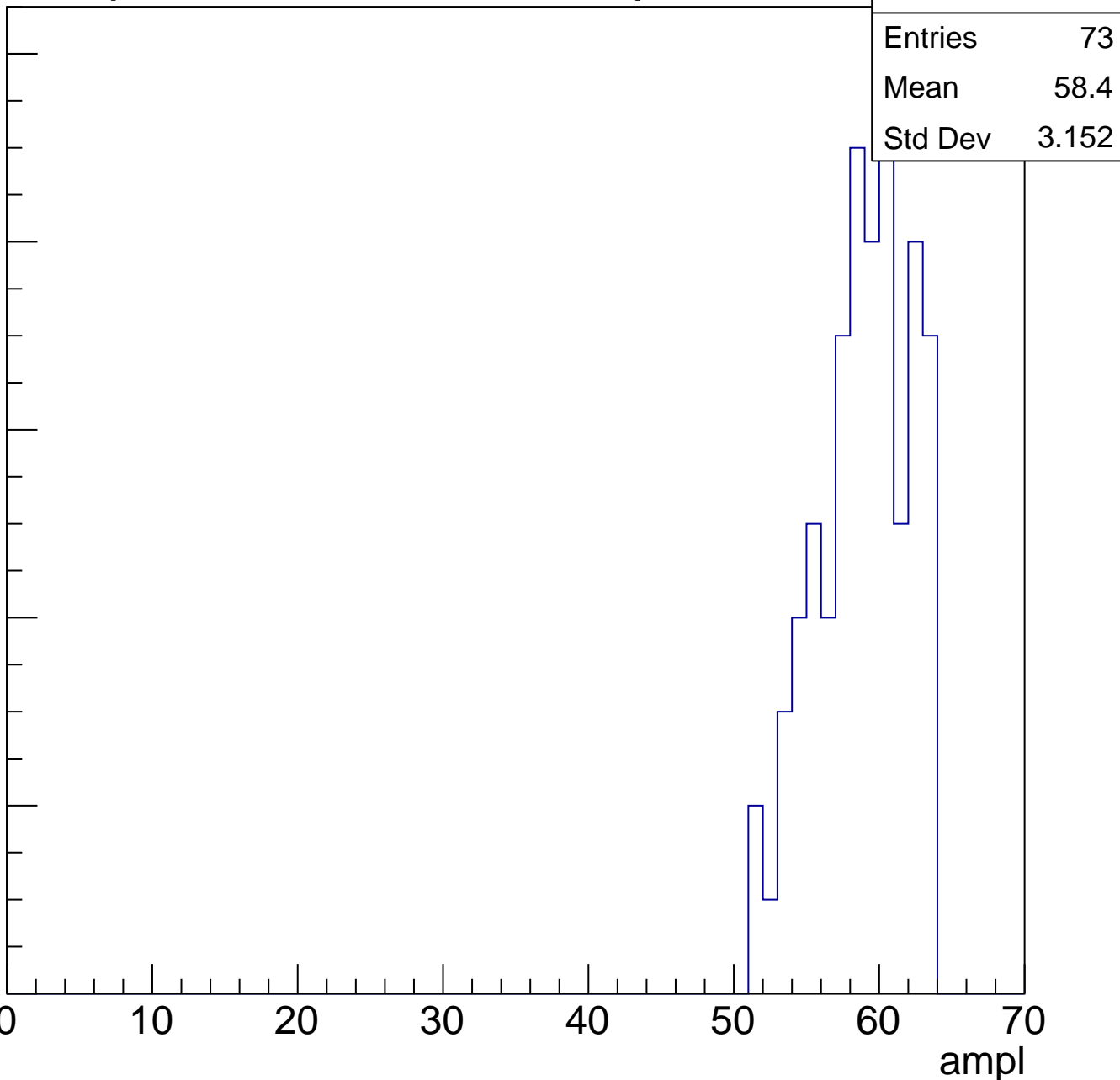
40

50

60

70

ampl



# B0L102S, U17-ch90, adc6

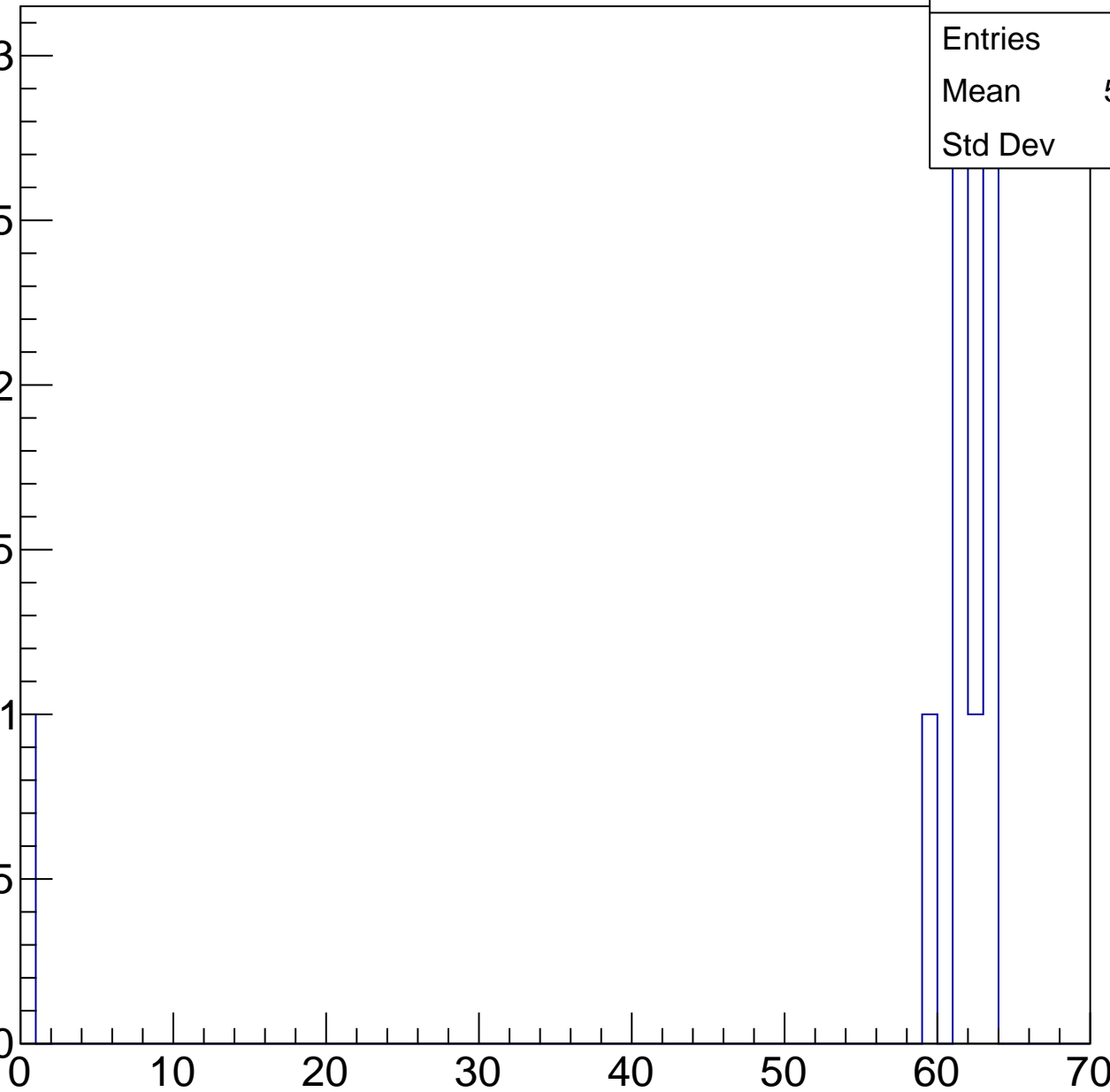
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	54.78
Std Dev	19.41

ampl





# B0L102S, U17-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch91, adc0

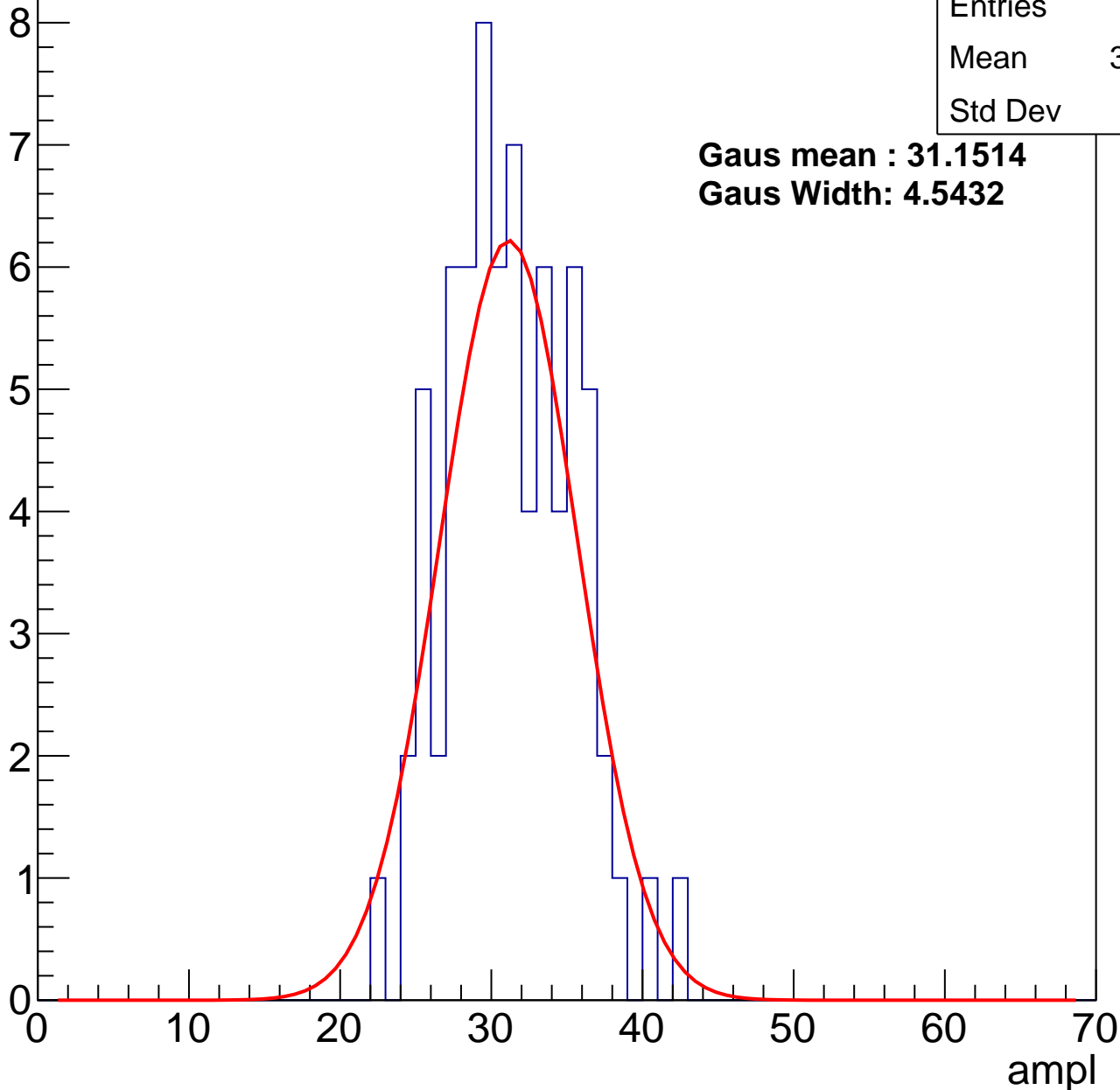
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	30.85
Std Dev	4.06

**Gaus mean : 31.1514**

**Gaus Width: 4.5432**



# B0L102S, U17-ch91, adc1

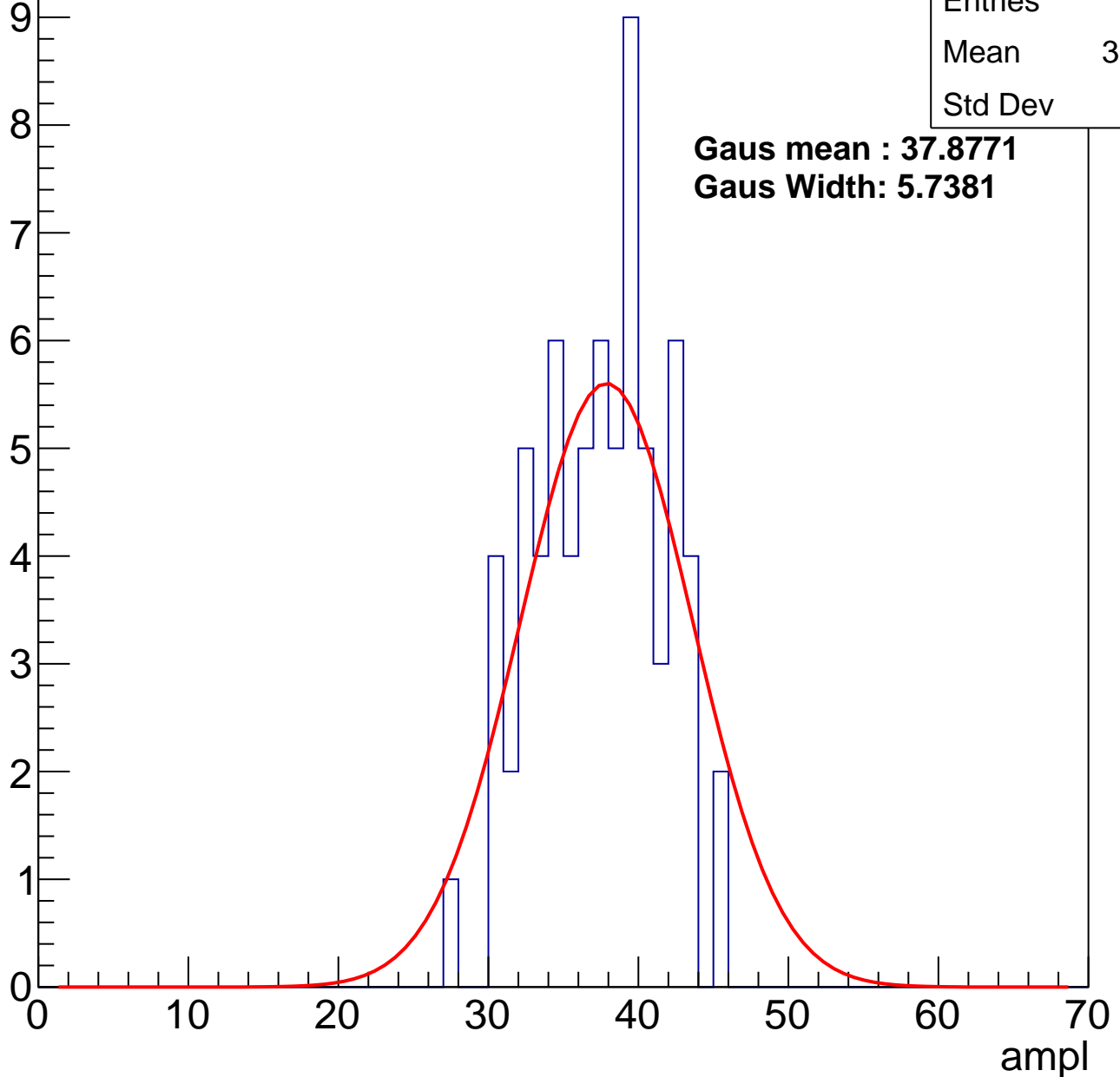
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.97
Std Dev	4.09

**Gaus mean : 37.8771**

**Gaus Width: 5.7381**



# B0L102S, U17-ch91, adc2

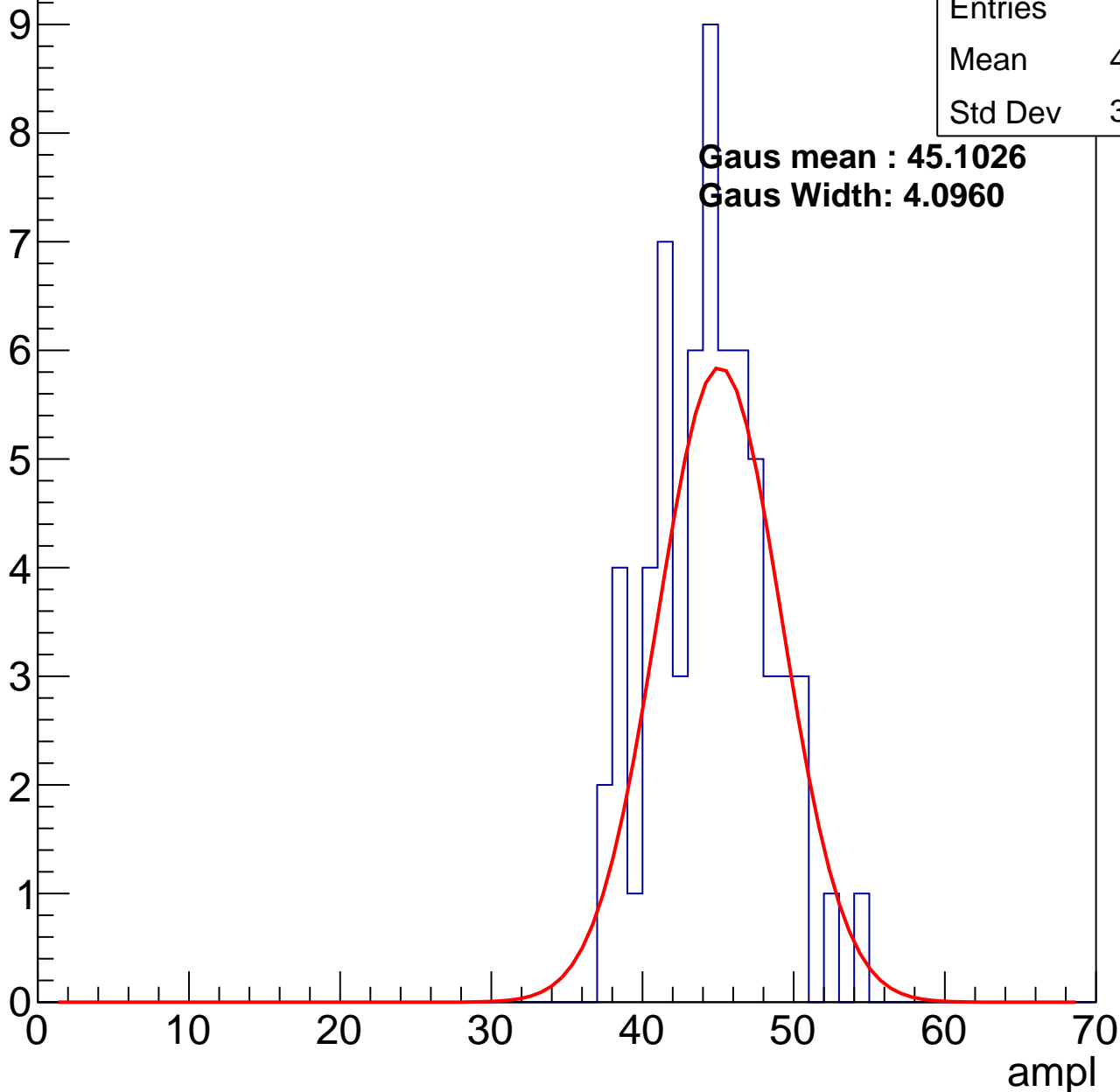
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	44.06
Std Dev	3.712

**Gaus mean : 45.1026**

**Gaus Width: 4.0960**

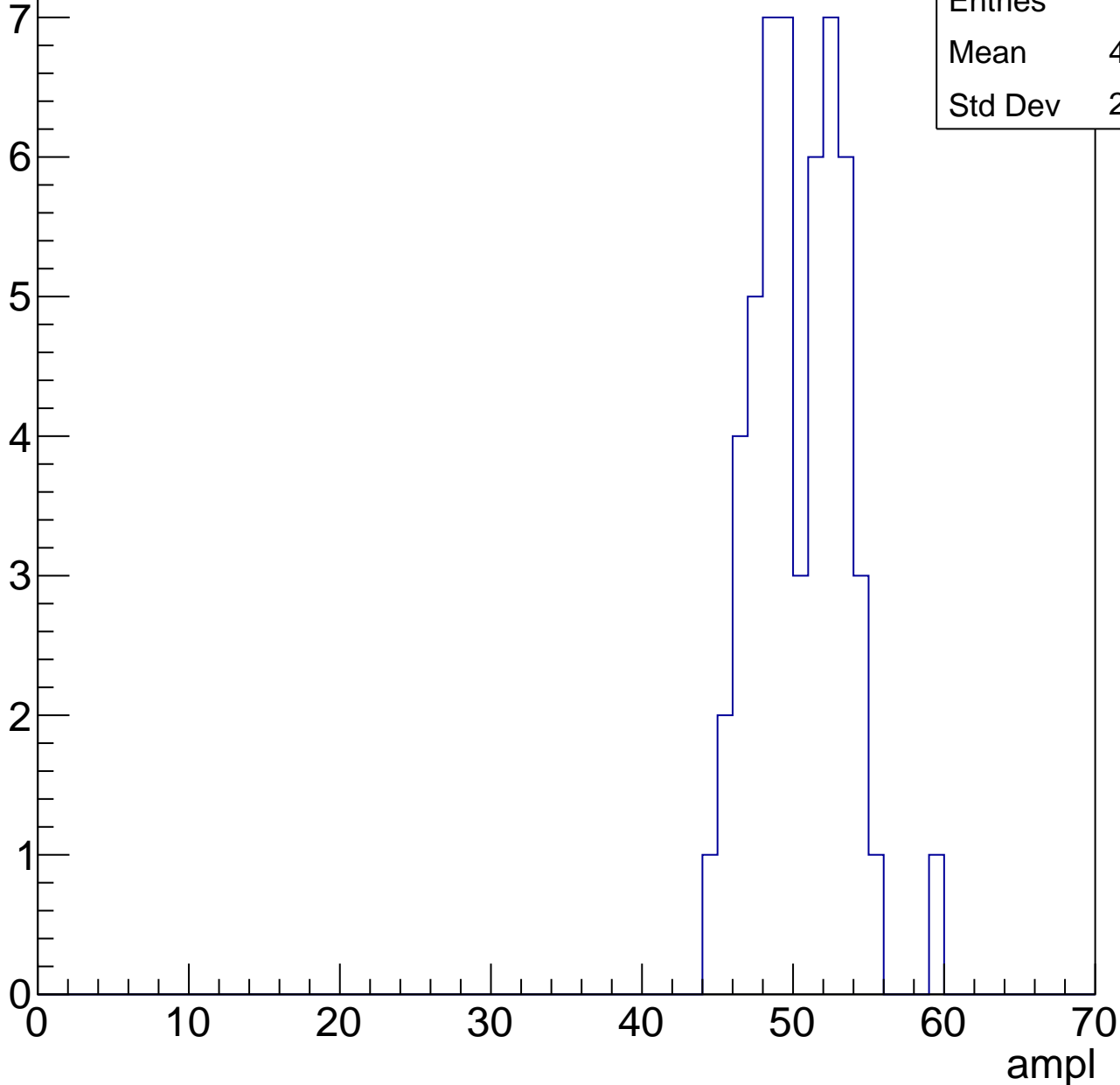


# B0L102S, U17-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	49.92
Std Dev	2.983

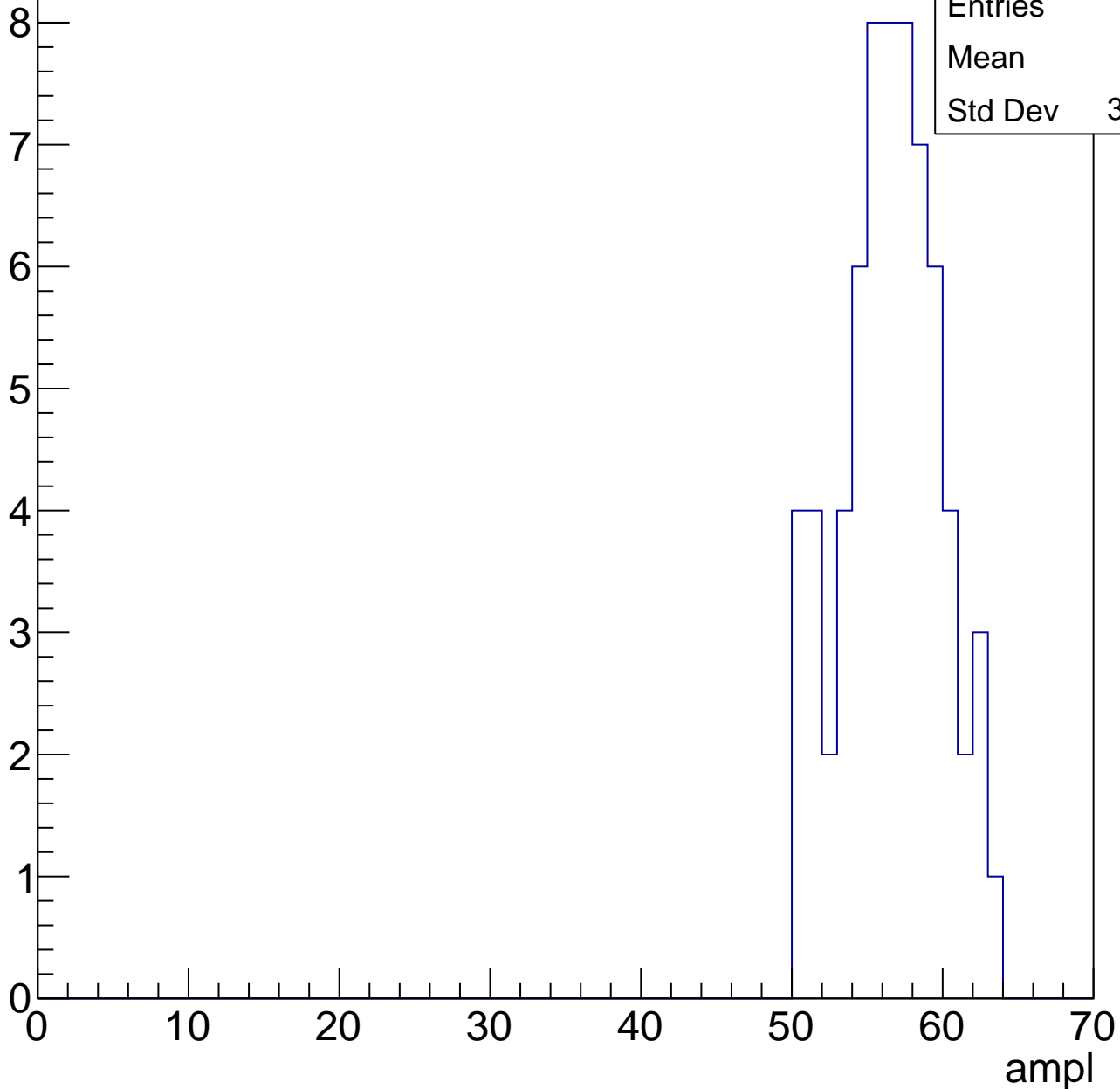


# B0L102S, U17-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	56.1
Std Dev	3.242

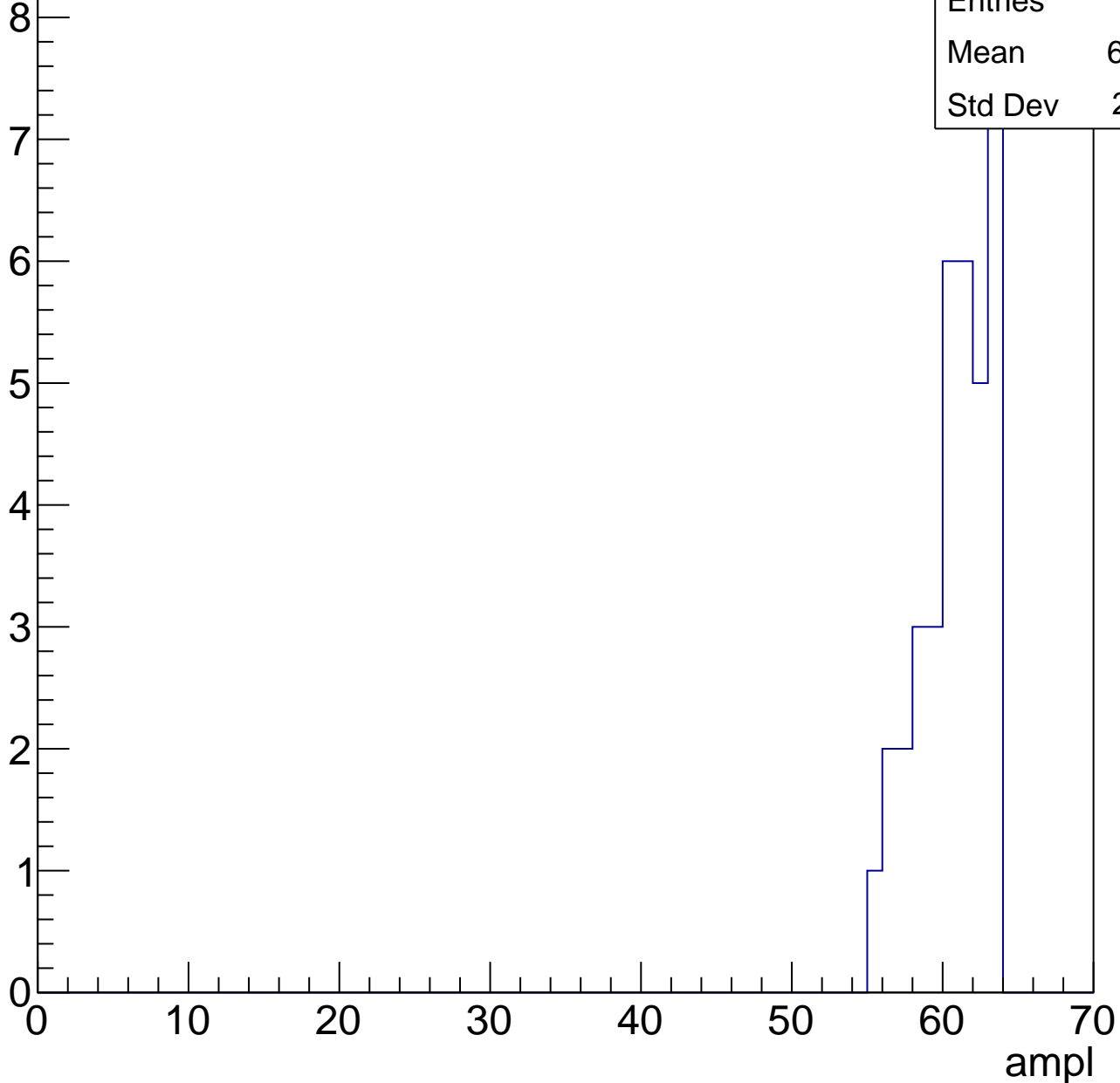


# B0L102S, U17-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	36
Mean	60.33
Std Dev	2.261



# B0L102S, U17-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

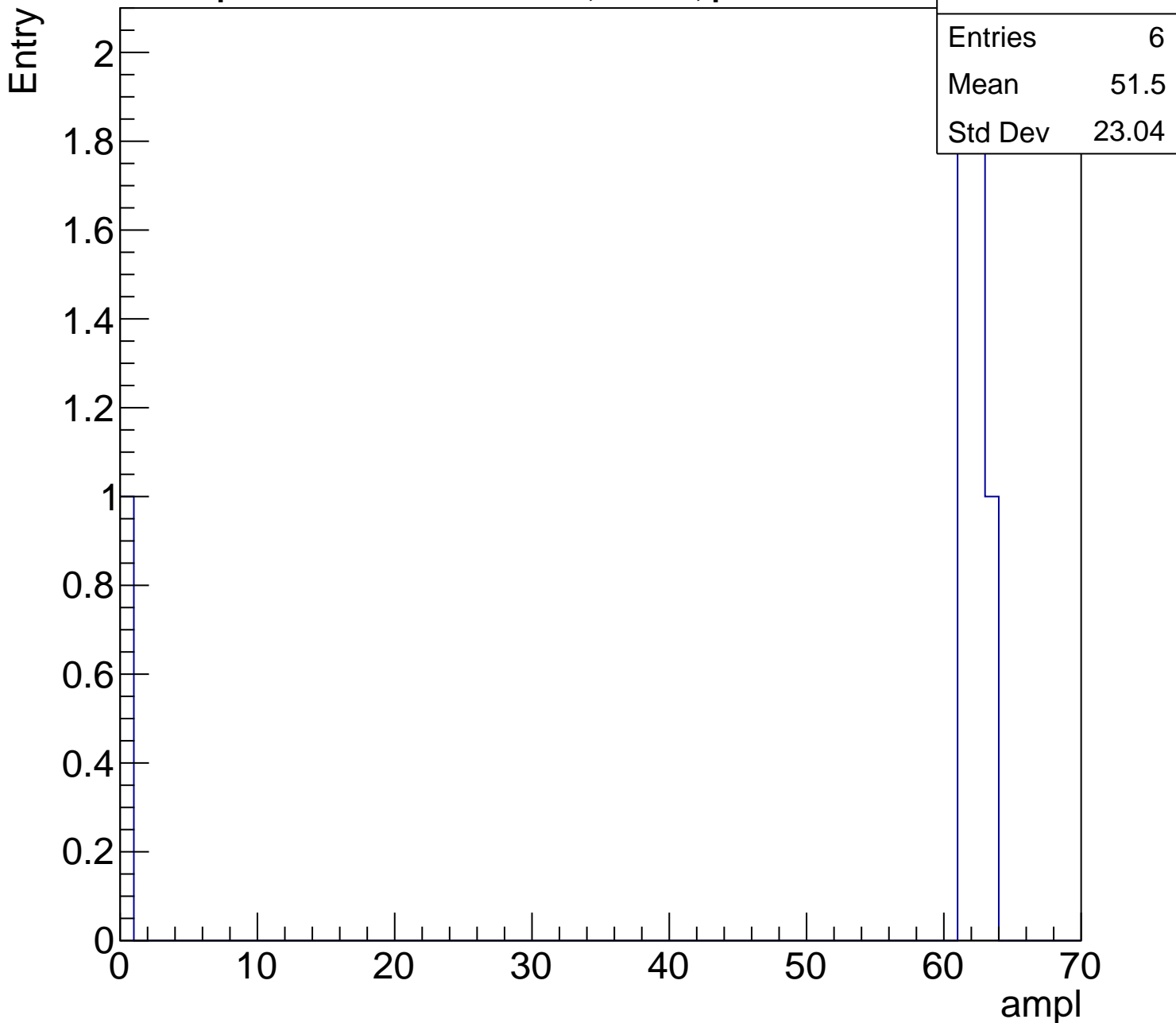
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.04

0 10 20 30 40 50 60 70

ampl





# B0L102S, U17-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch92, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	74
Mean	27.24
Std Dev	5.611

**Gaus mean : 27.7872**

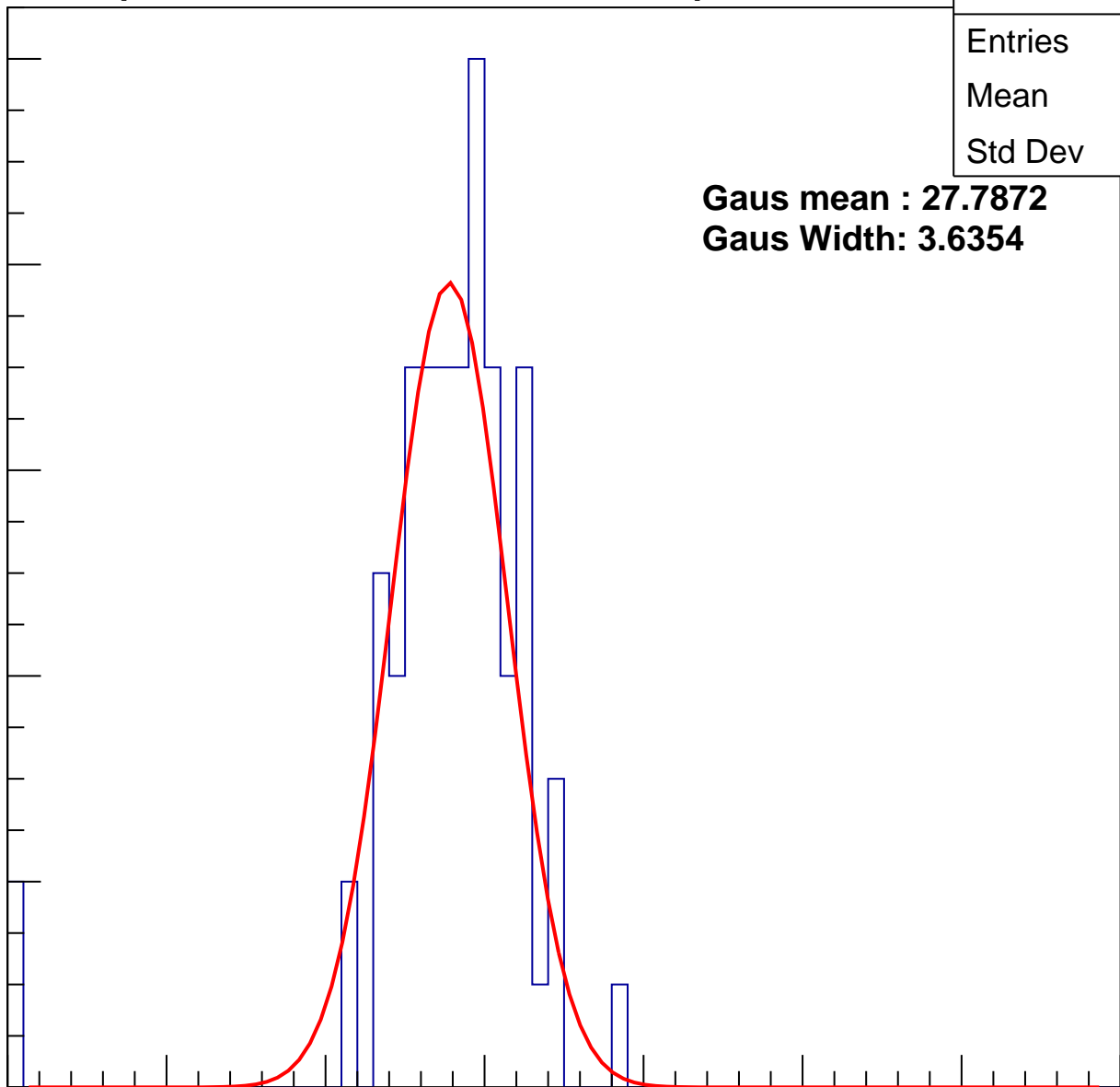
**Gaus Width: 3.6354**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch92, adc1

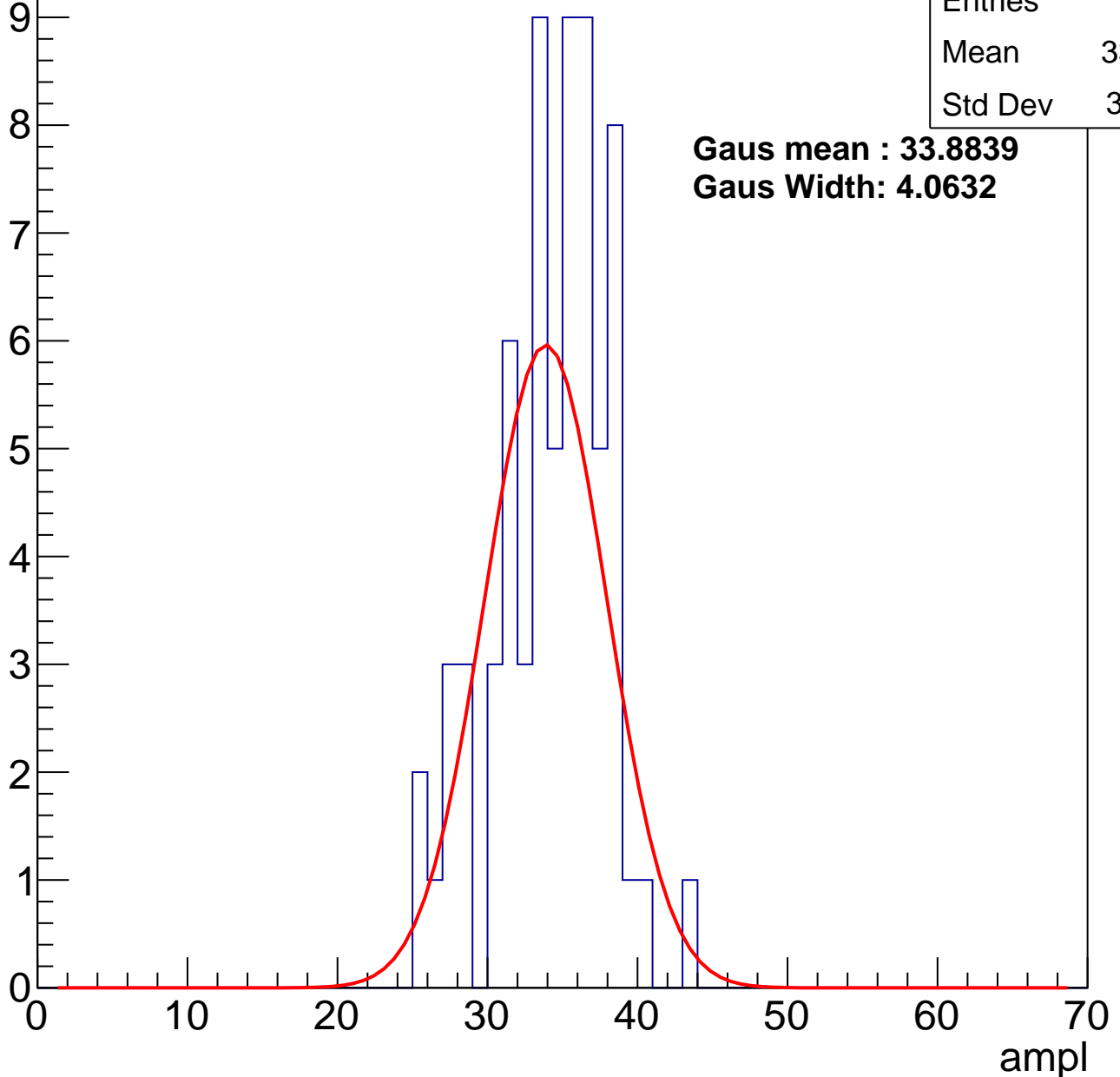
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	33.77
Std Dev	3.731

**Gaus mean : 33.8839**

**Gaus Width: 4.0632**



# B0L102S, U17-ch92, adc2

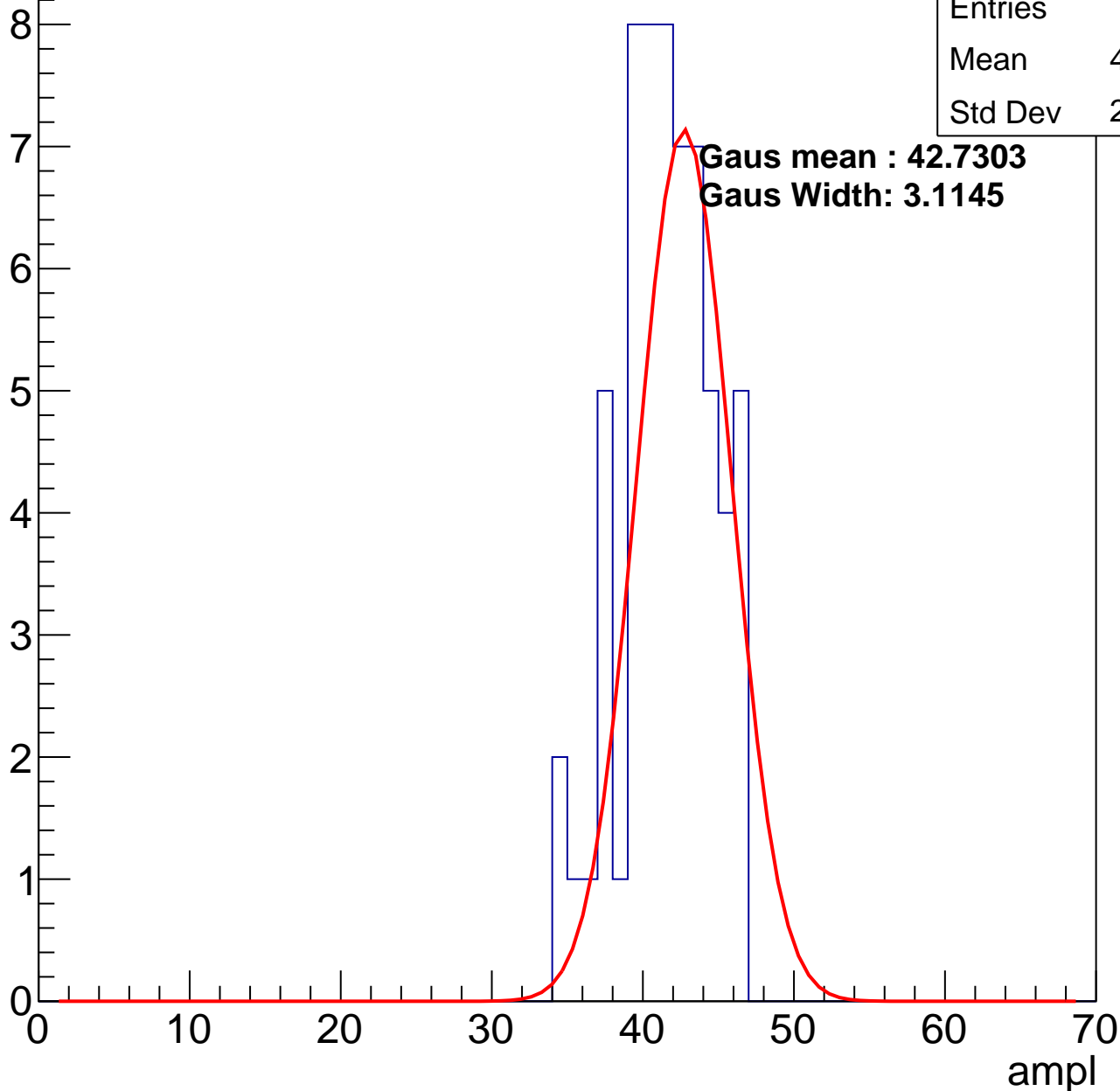
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	41.08
Std Dev	2.996

**Gaus mean : 42.7303**

**Gaus Width: 3.1145**

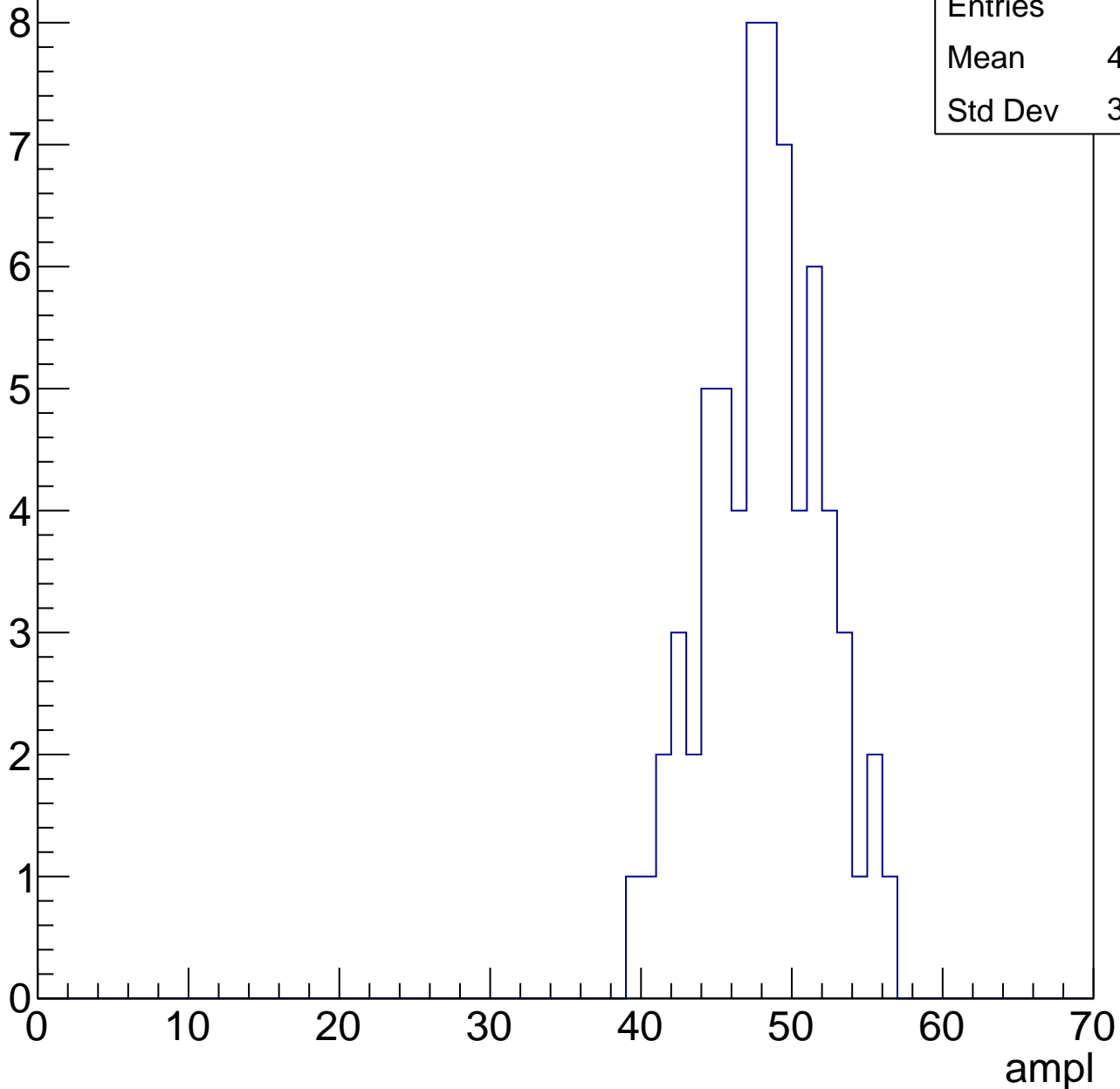


# B0L102S, U17-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	47.73
Std Dev	3.784

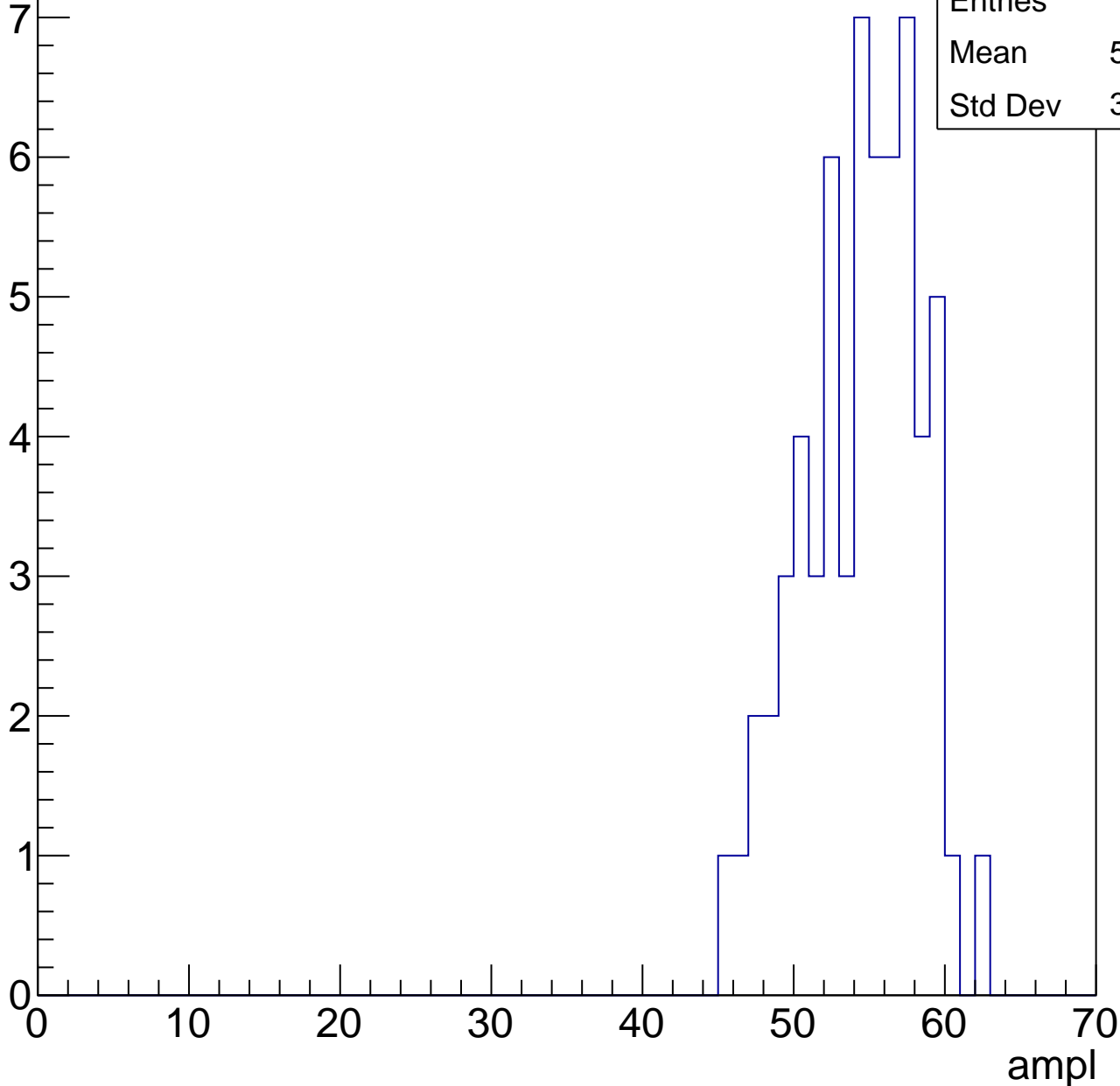


# B0L102S, U17-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	53.94
Std Dev	3.788

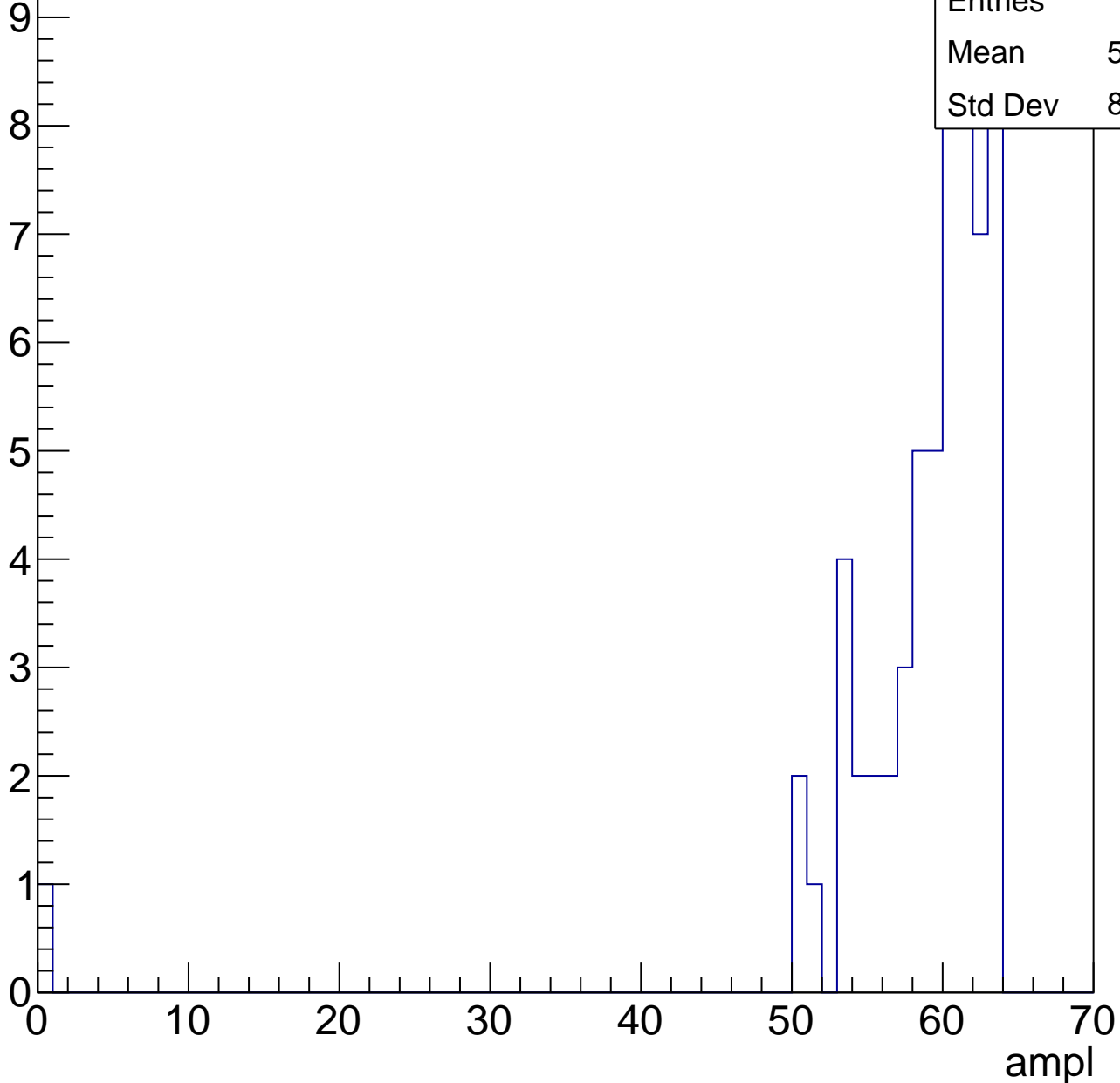


# B0L102S, U17-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	57.93
Std Dev	8.302



# B0L102S, U17-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

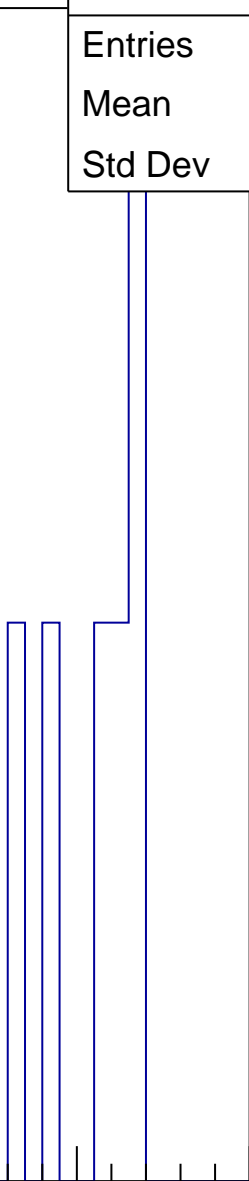
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	60.5
Std Dev	2.63

0 10 20 30 40 50 60 70

ampl

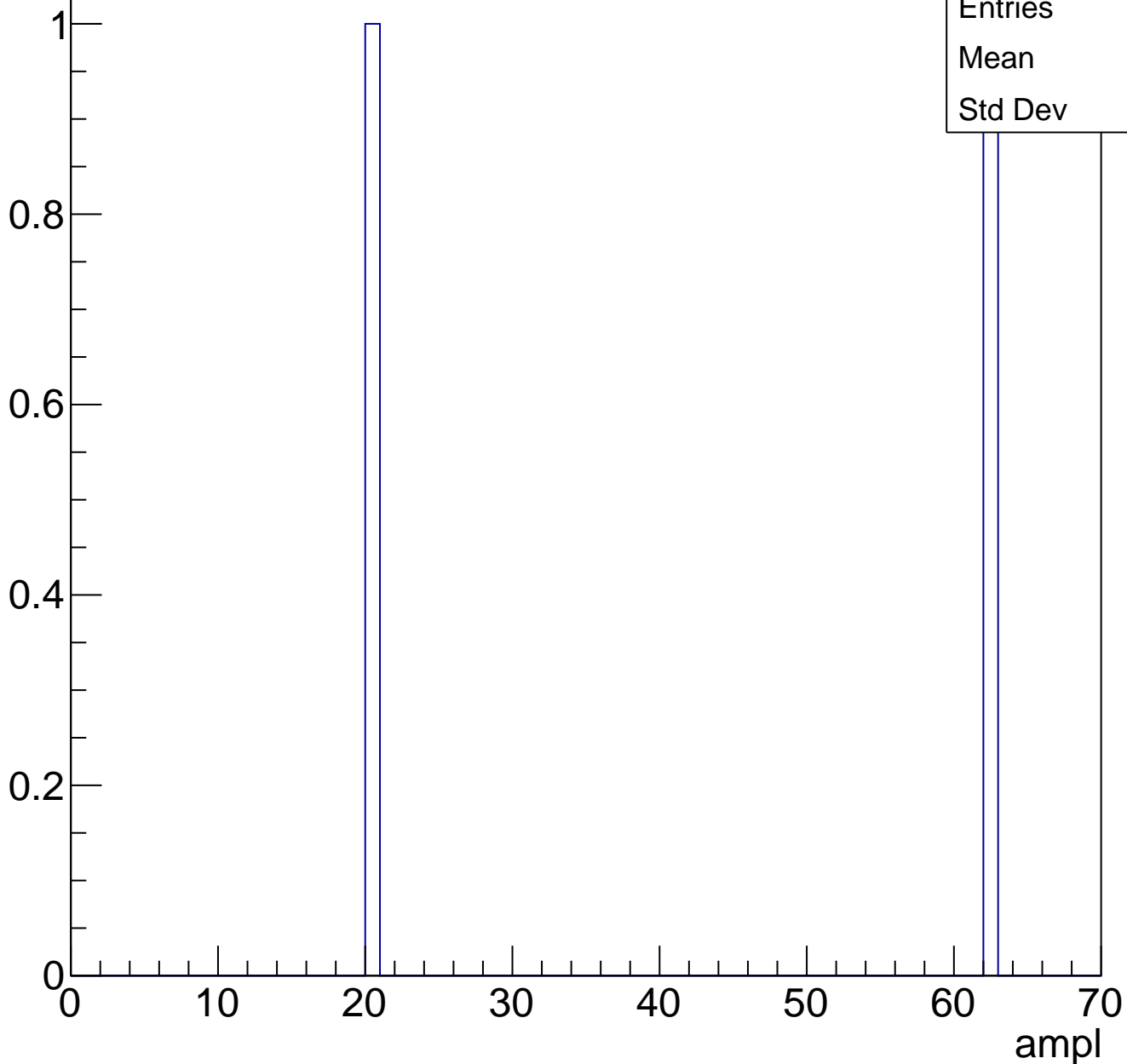




# B0L102S, U17-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	41
Std Dev	21

# B0L102S, U17-ch93, adc0

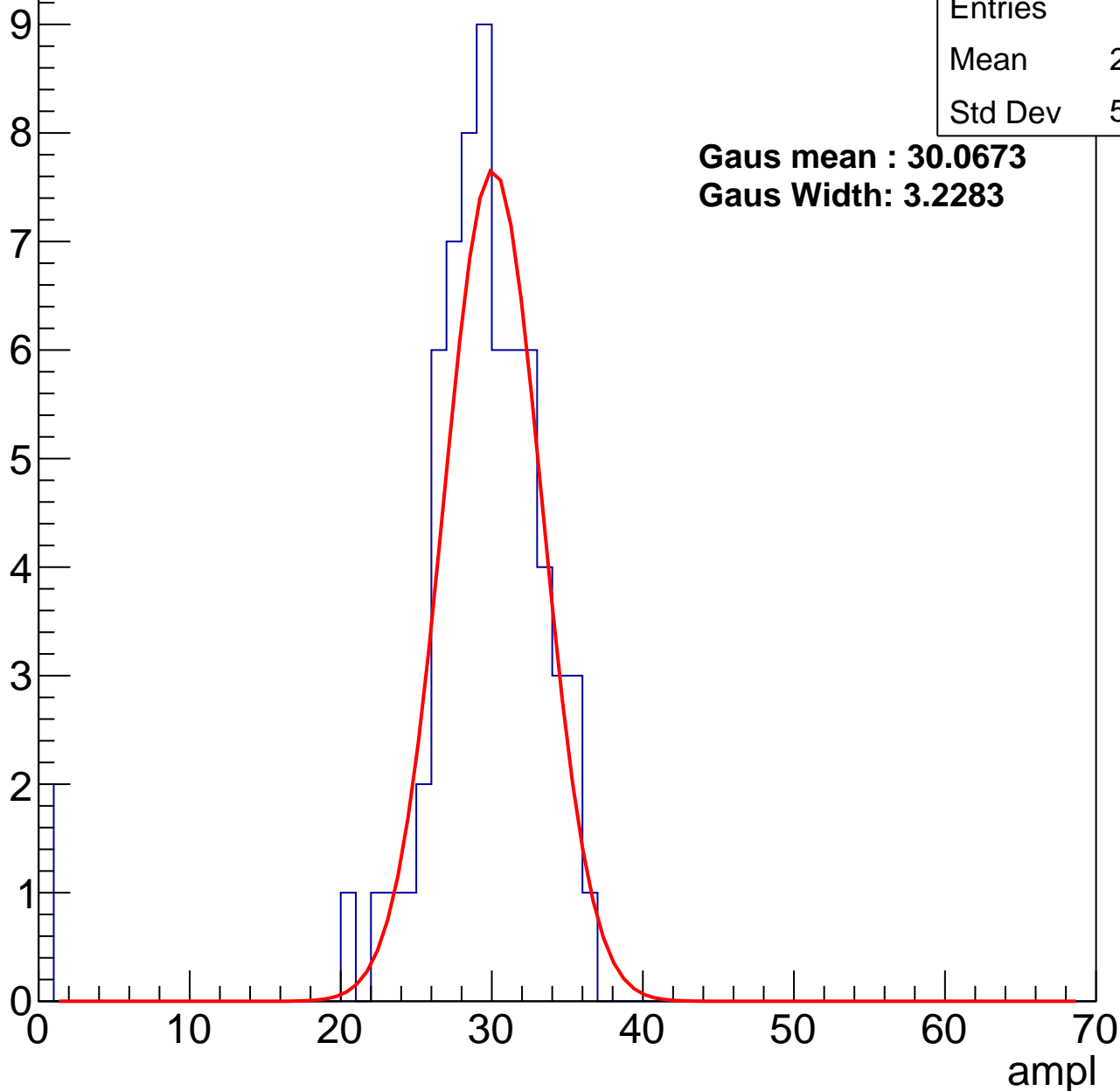
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	28.39
Std Dev	5.922

**Gaus mean : 30.0673**

**Gaus Width: 3.2283**



# B0L102S, U17-ch93, adc1

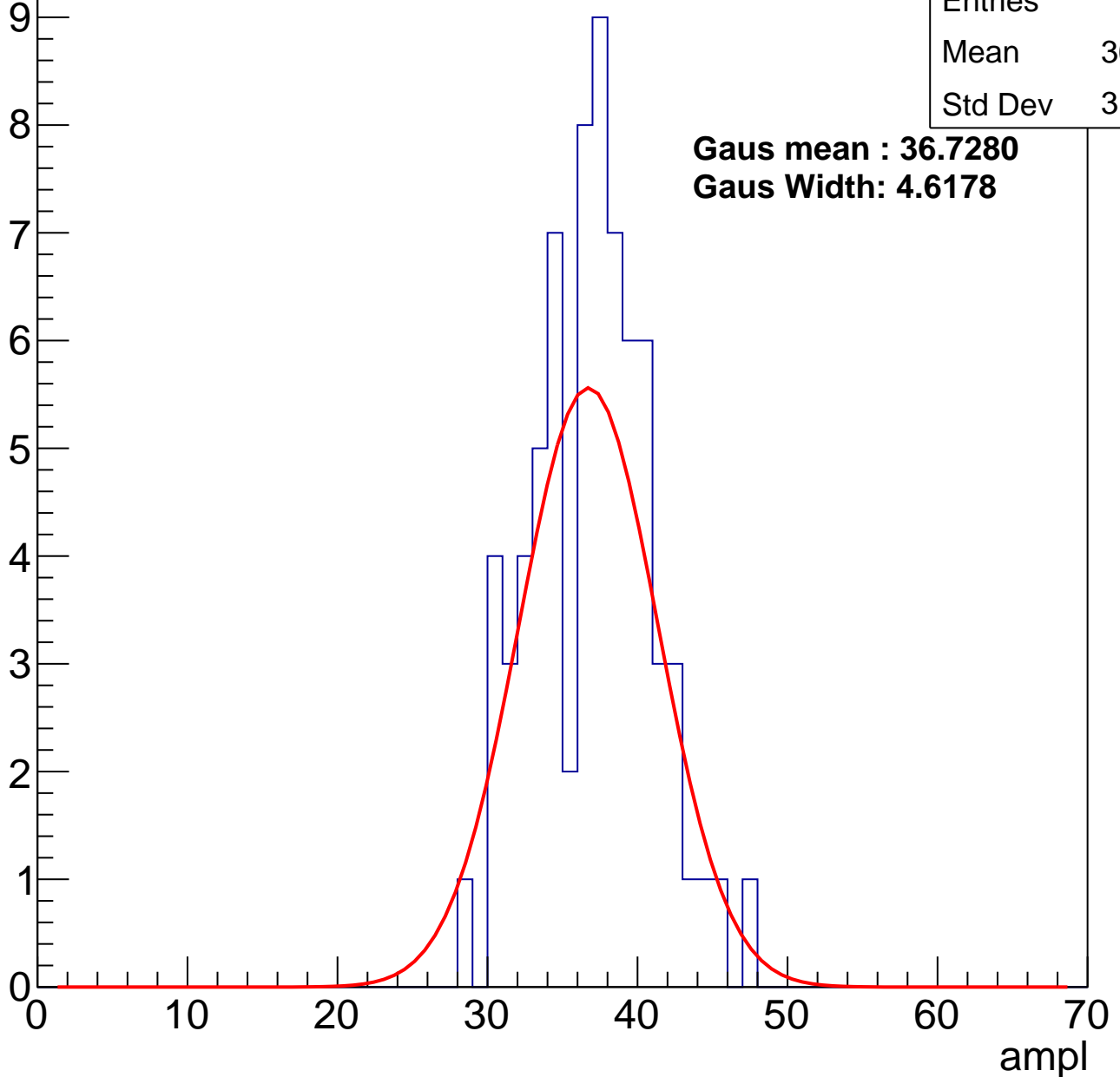
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	36.54
Std Dev	3.884

**Gaus mean : 36.7280**

**Gaus Width: 4.6178**

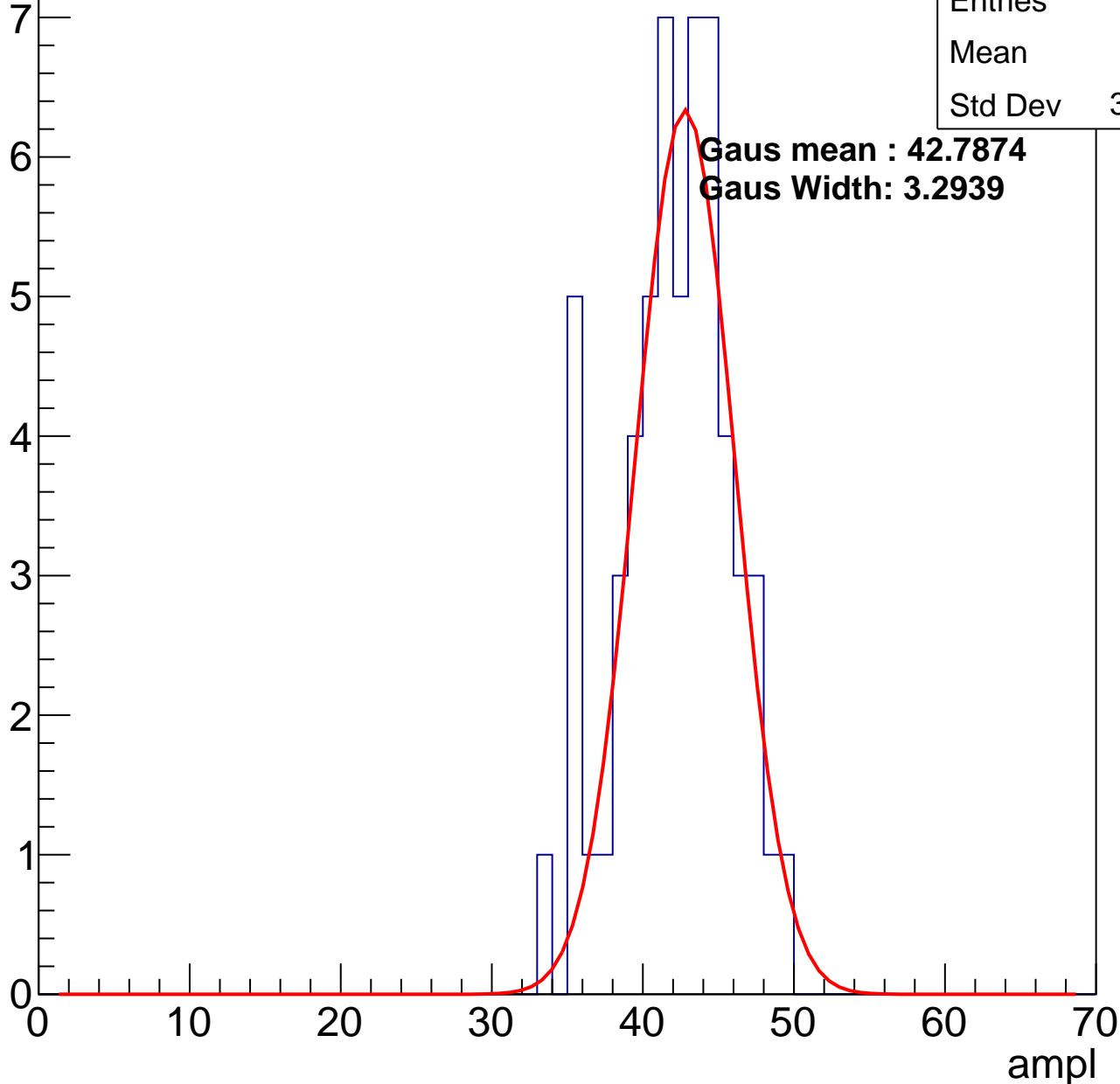


# B0L102S, U17-ch93, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

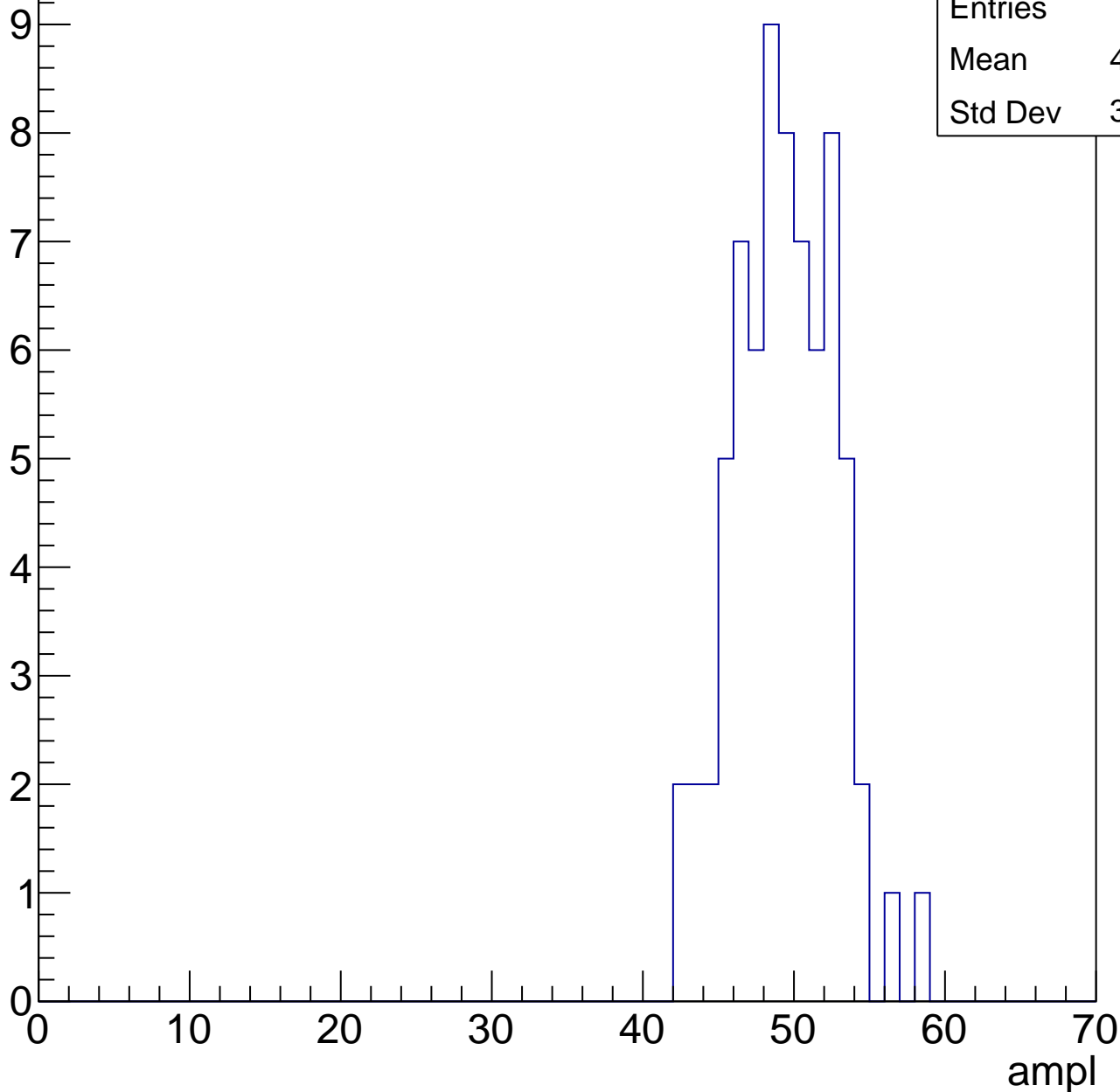
Entries	58
Mean	41.6
Std Dev	3.639



# B0L102S, U17-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

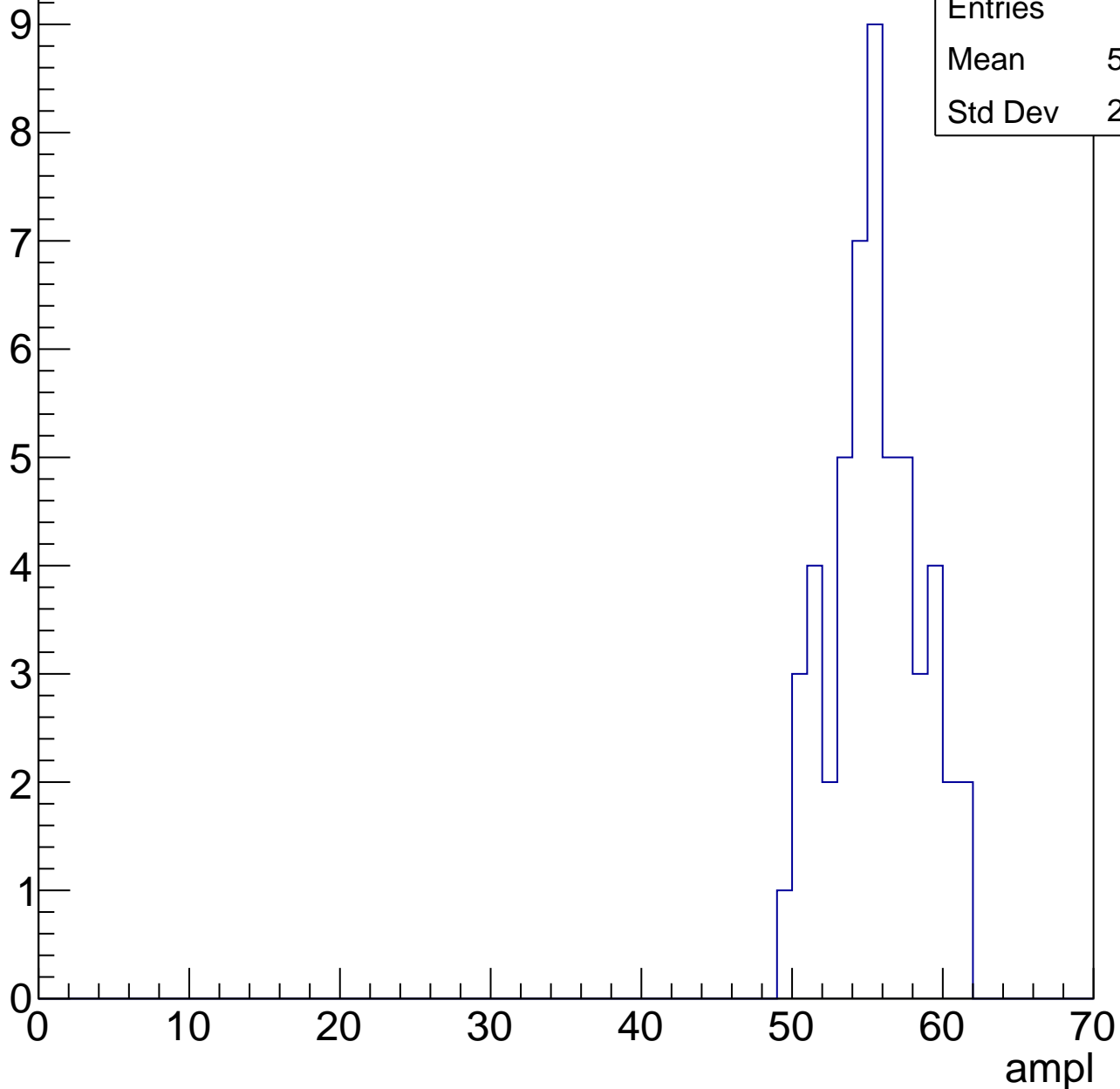


Entries	71
Mean	48.87
Std Dev	3.267

# B0L102S, U17-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

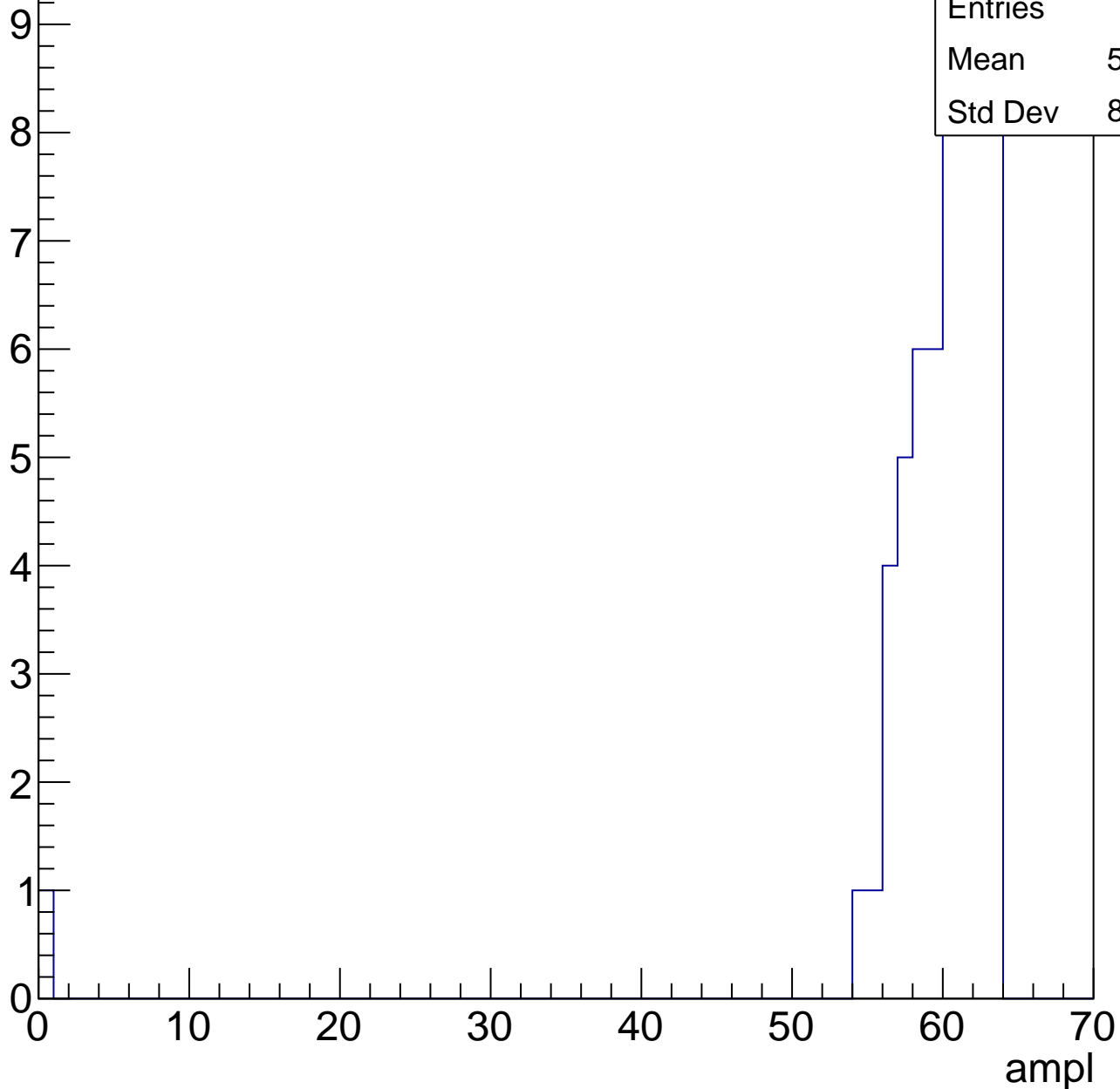


Entries	52
Mean	55.04
Std Dev	2.968

# B0L102S, U17-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

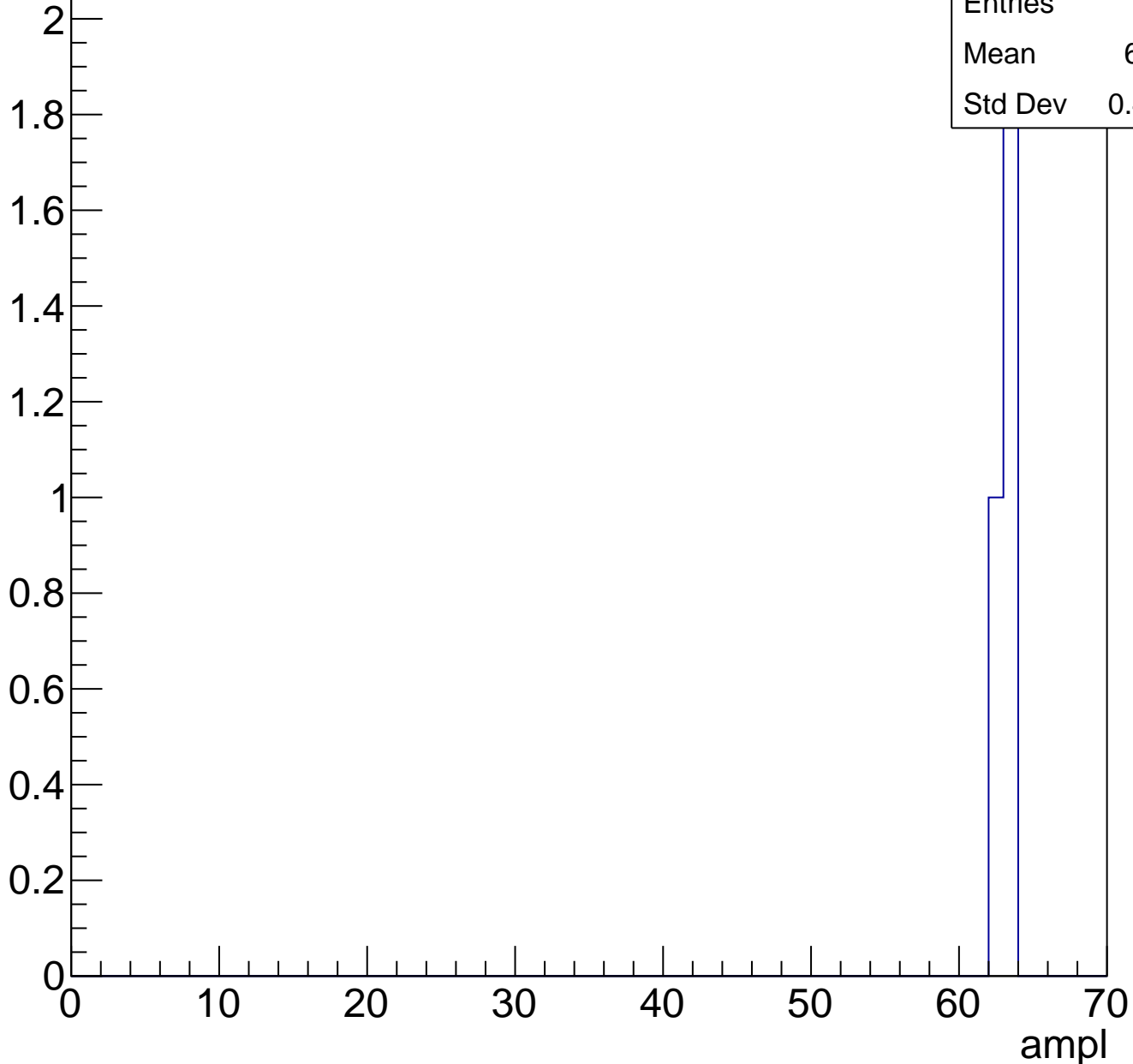
Entry



# B0L102S, U17-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch94, adc0

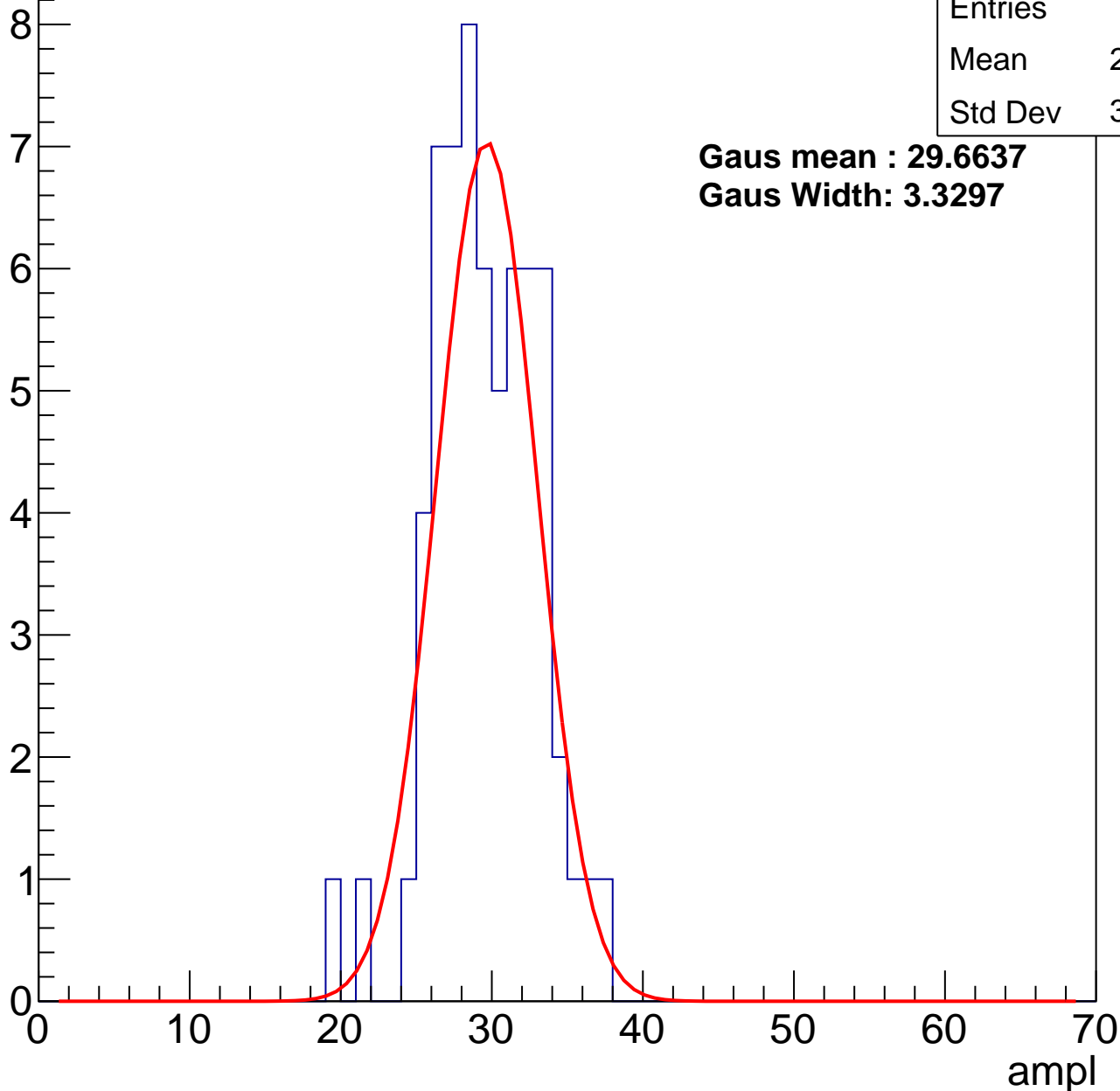
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	29.13
Std Dev	3.402

**Gaus mean : 29.6637**

**Gaus Width: 3.3297**



# B0L102S, U17-ch94, adc1

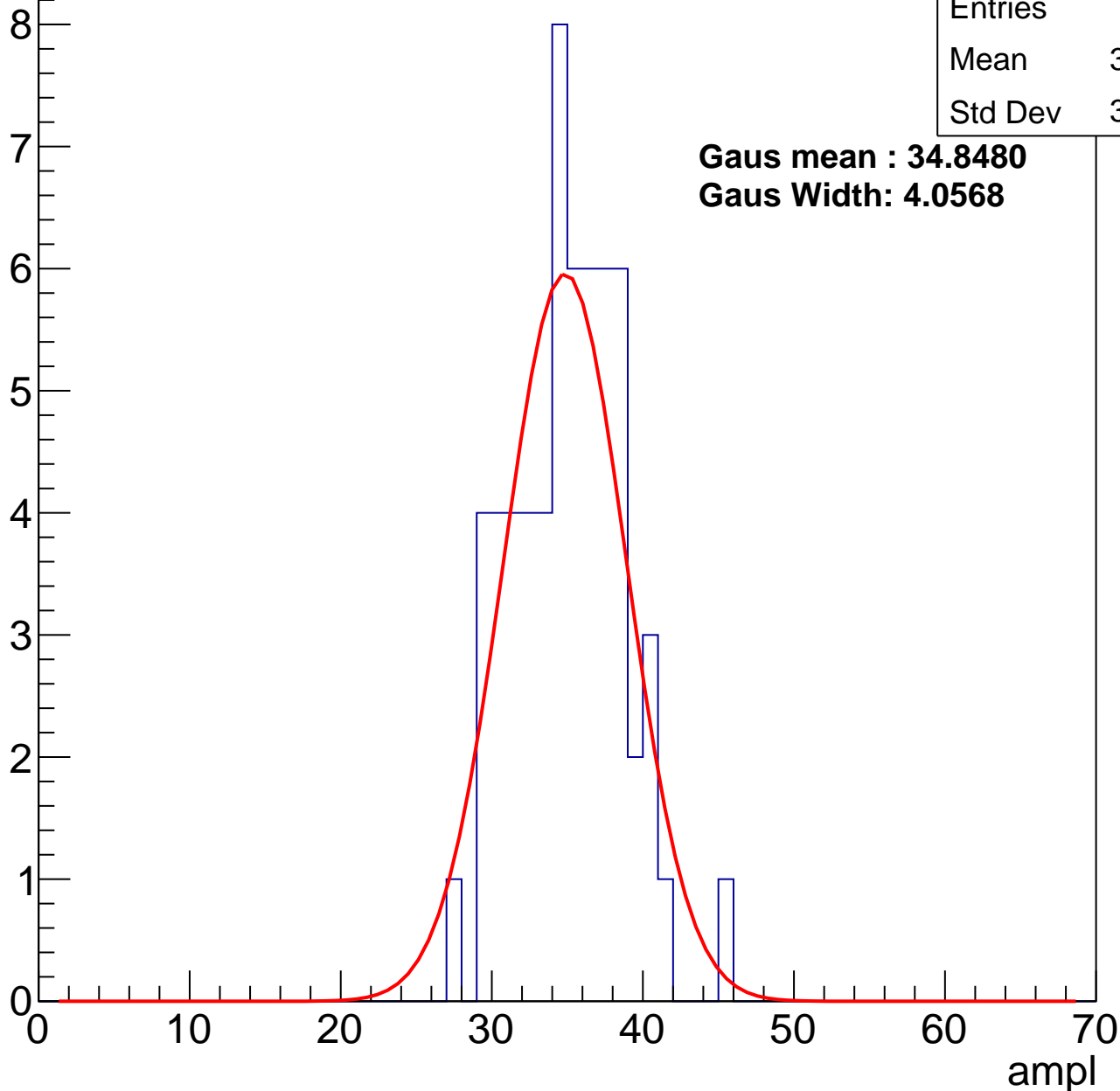
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	34.65
Std Dev	3.539

**Gaus mean : 34.8480**

**Gaus Width: 4.0568**



# B0L102S, U17-ch94, adc2

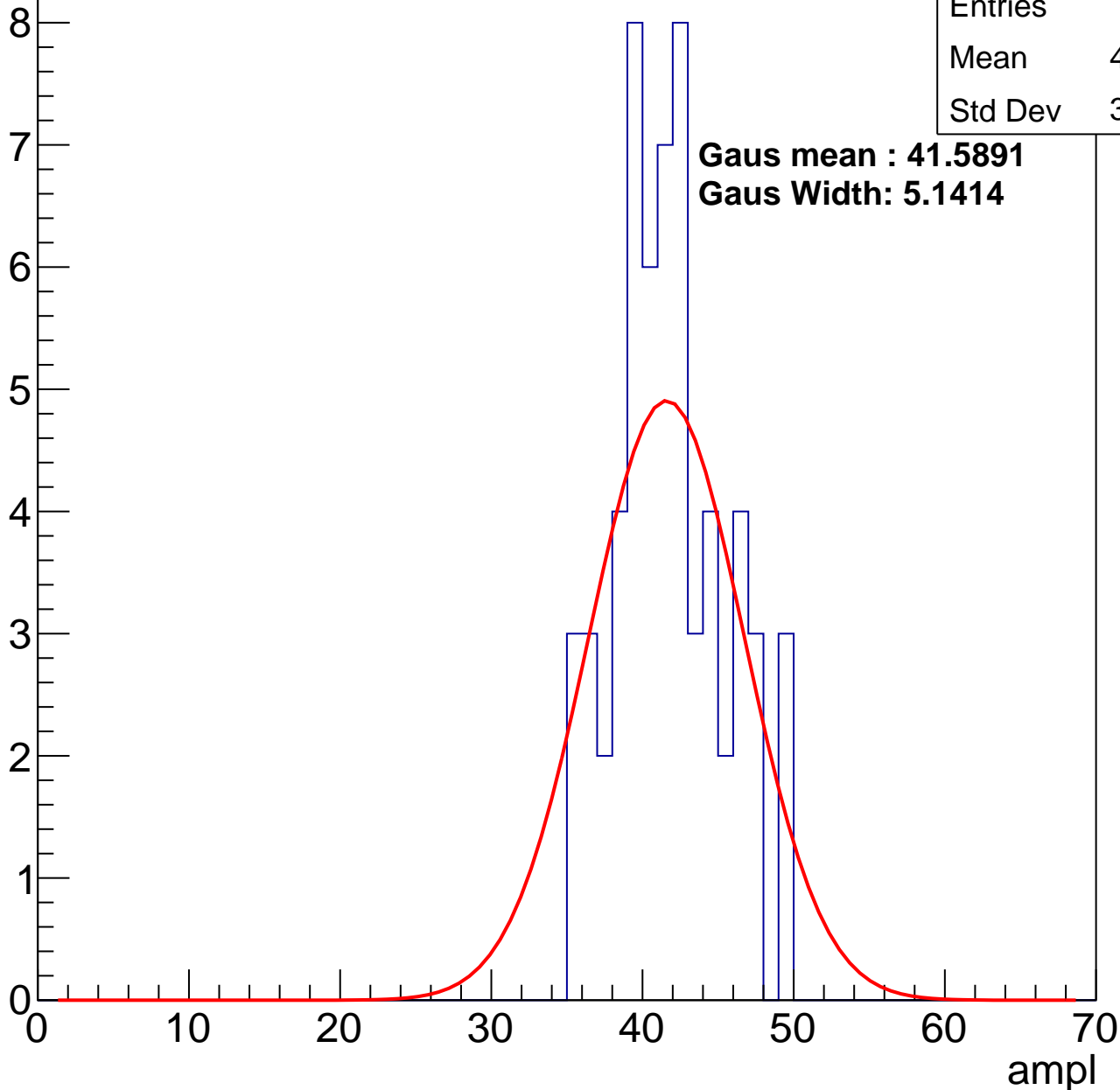
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.35
Std Dev	3.582

**Gaus mean : 41.5891**

**Gaus Width: 5.1414**

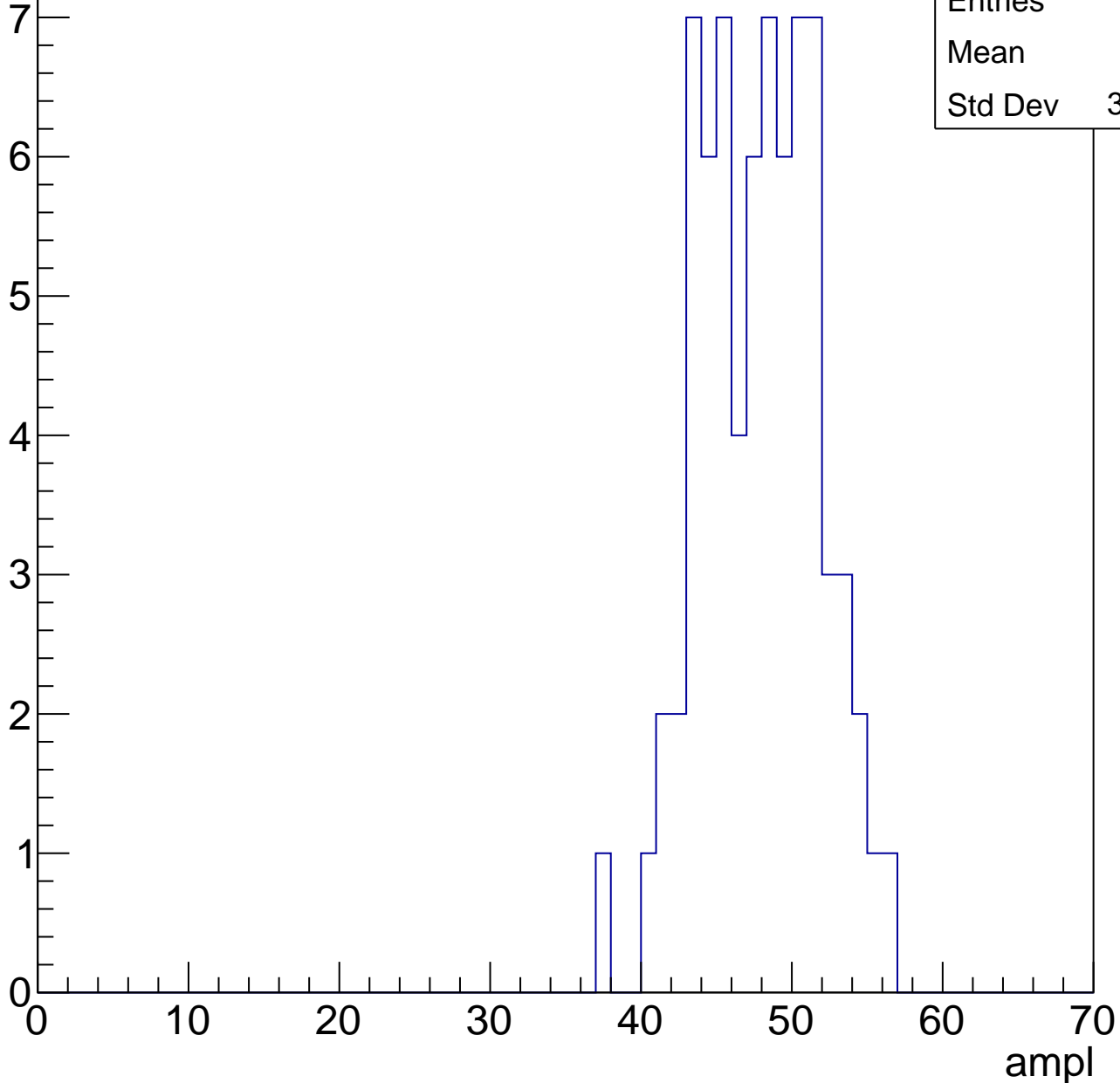


# B0L102S, U17-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	47.4
Std Dev	3.863

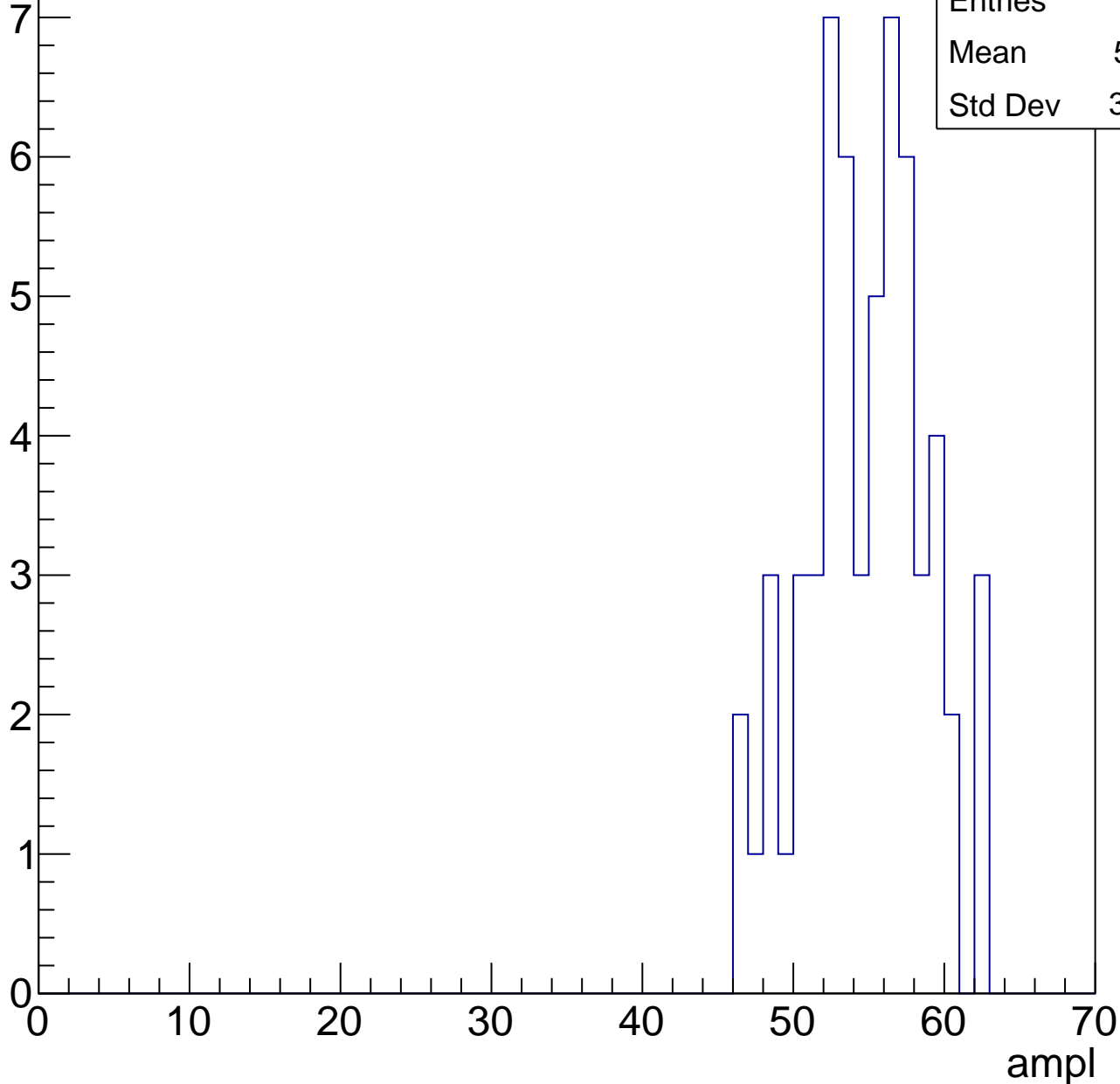


# B0L102S, U17-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.31
Std Dev	3.933

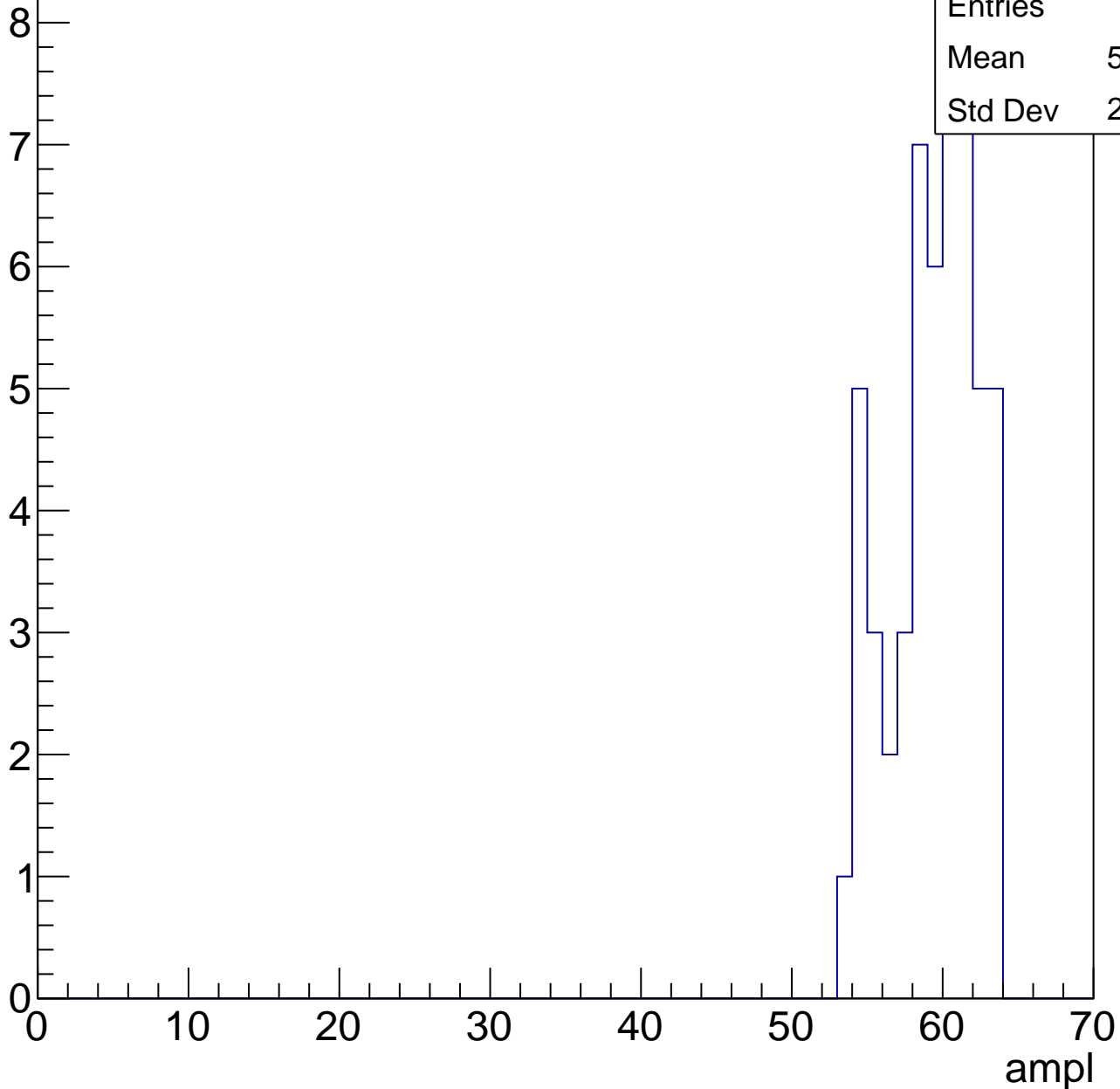


# B0L102S, U17-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	58.94
Std Dev	2.784

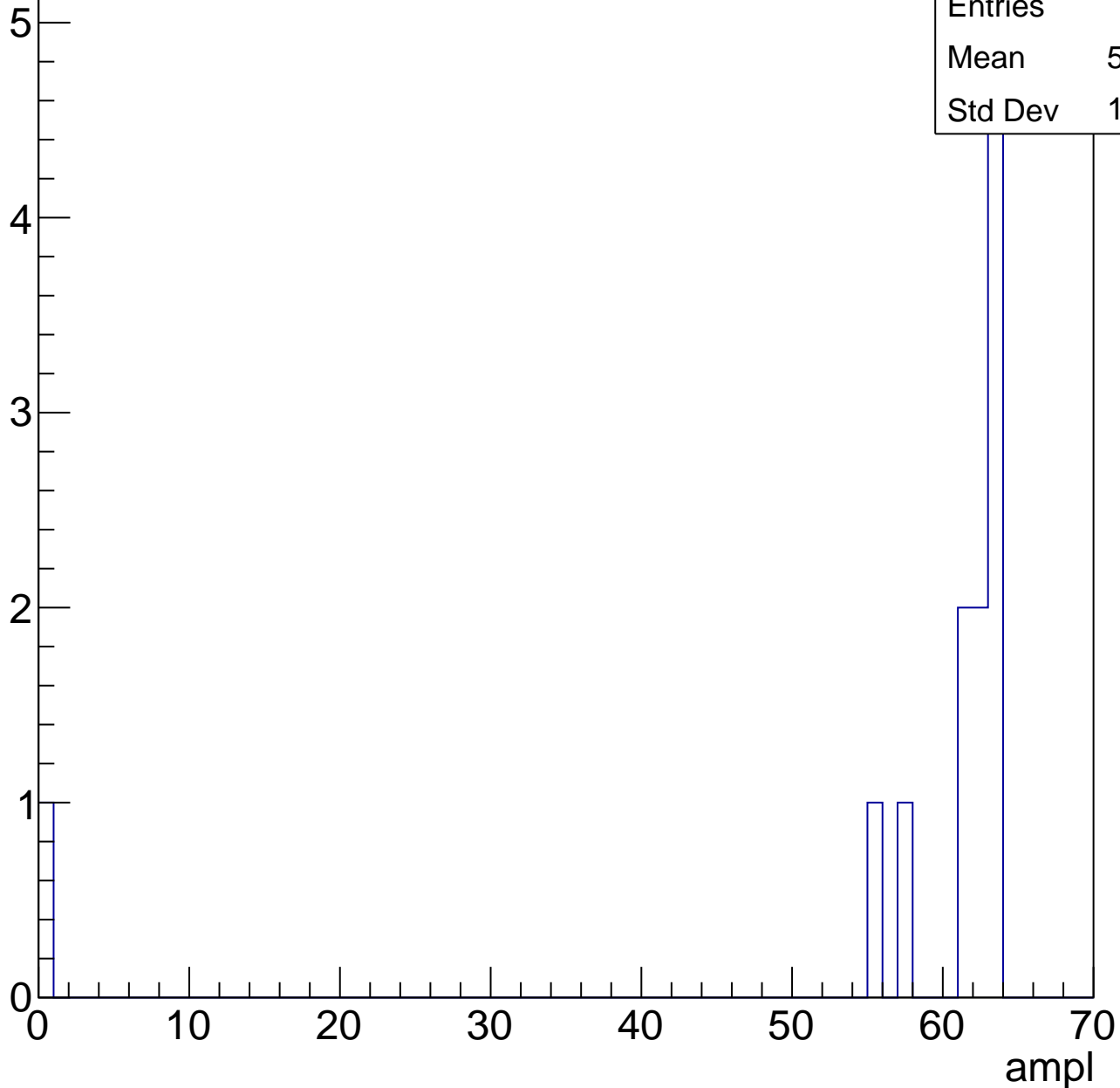


# B0L102S, U17-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	12
Mean	56.08
Std Dev	17.09





# B0L102S, U17-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	97
Mean	27.07
Std Dev	6.674

**Gaus mean : 28.8643**

**Gaus Width: 3.6220**

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

# B0L102S, U17-ch95, adc1

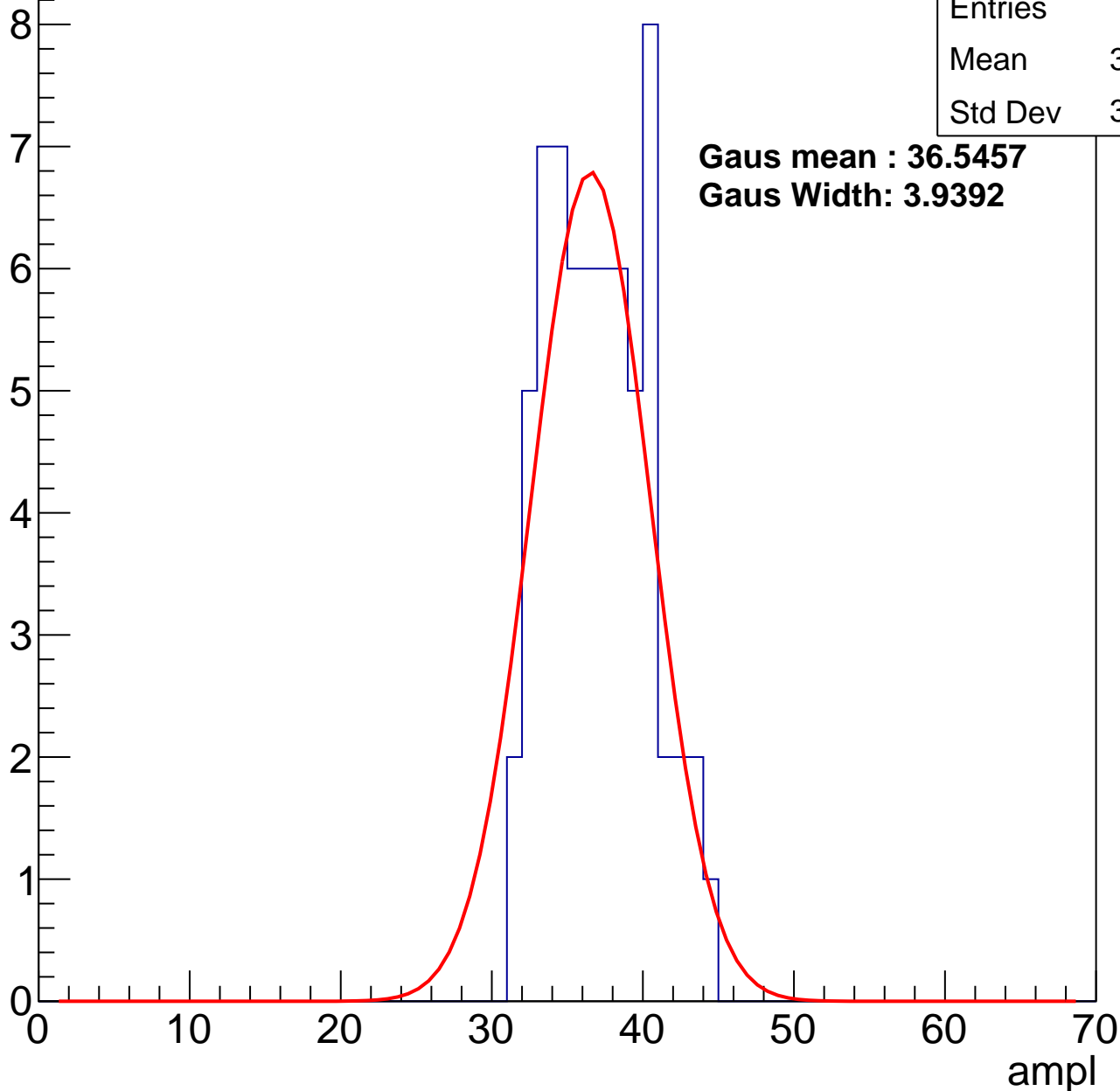
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.58
Std Dev	3.262

**Gaus mean : 36.5457**

**Gaus Width: 3.9392**



# B0L102S, U17-ch95, adc2

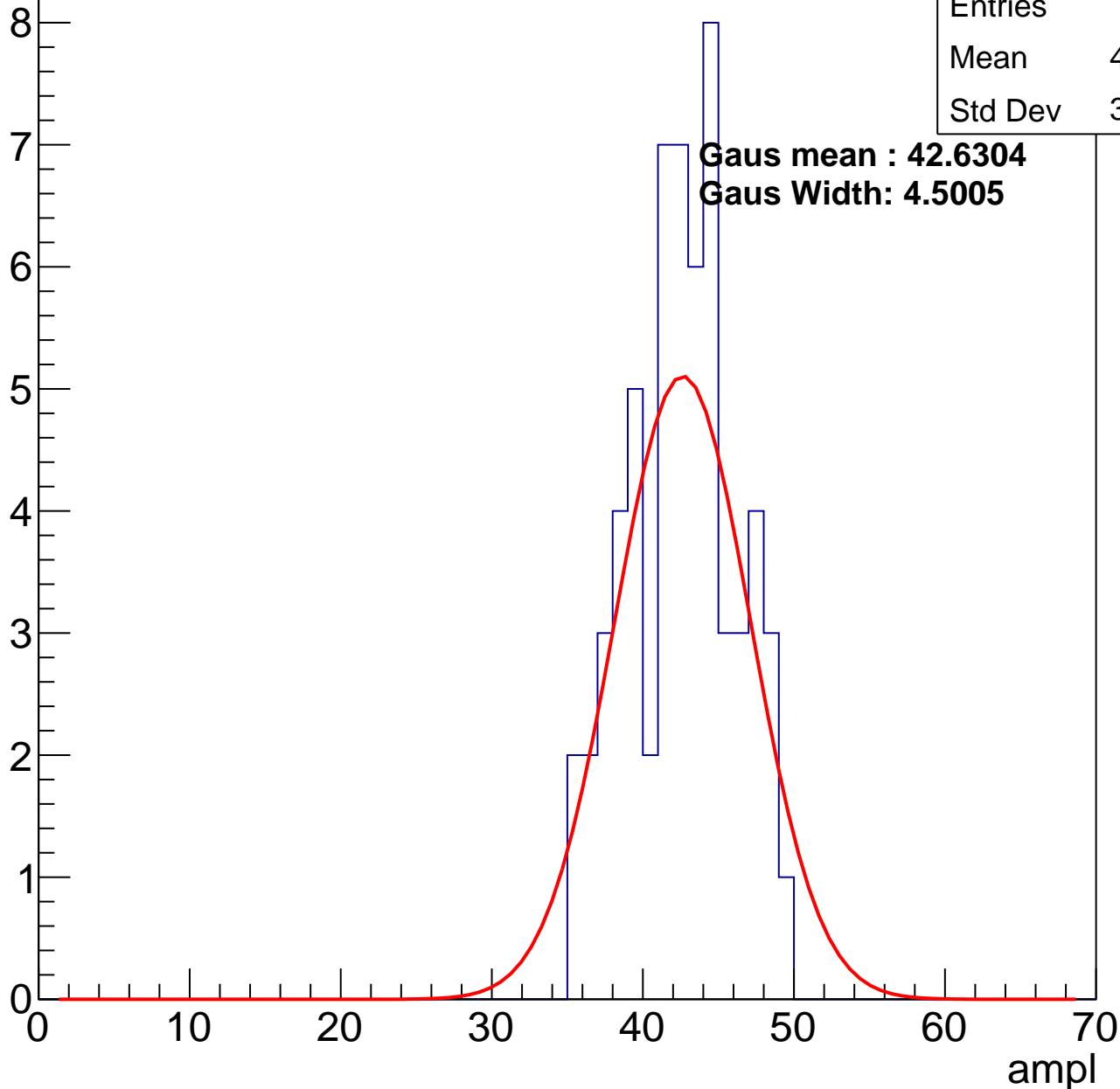
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.08
Std Dev	3.509

**Gaus mean : 42.6304**

**Gaus Width: 4.5005**

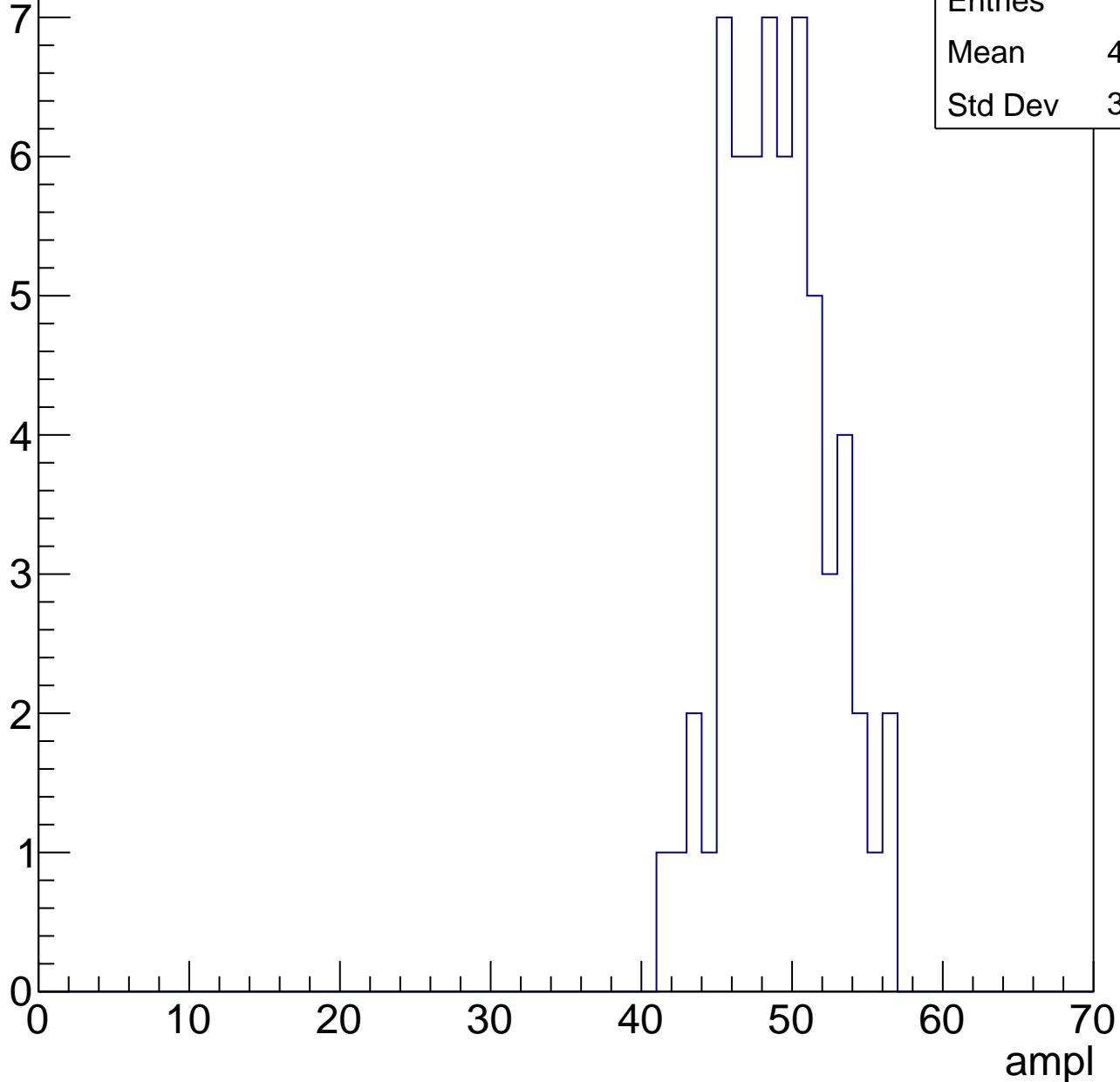


# B0L102S, U17-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

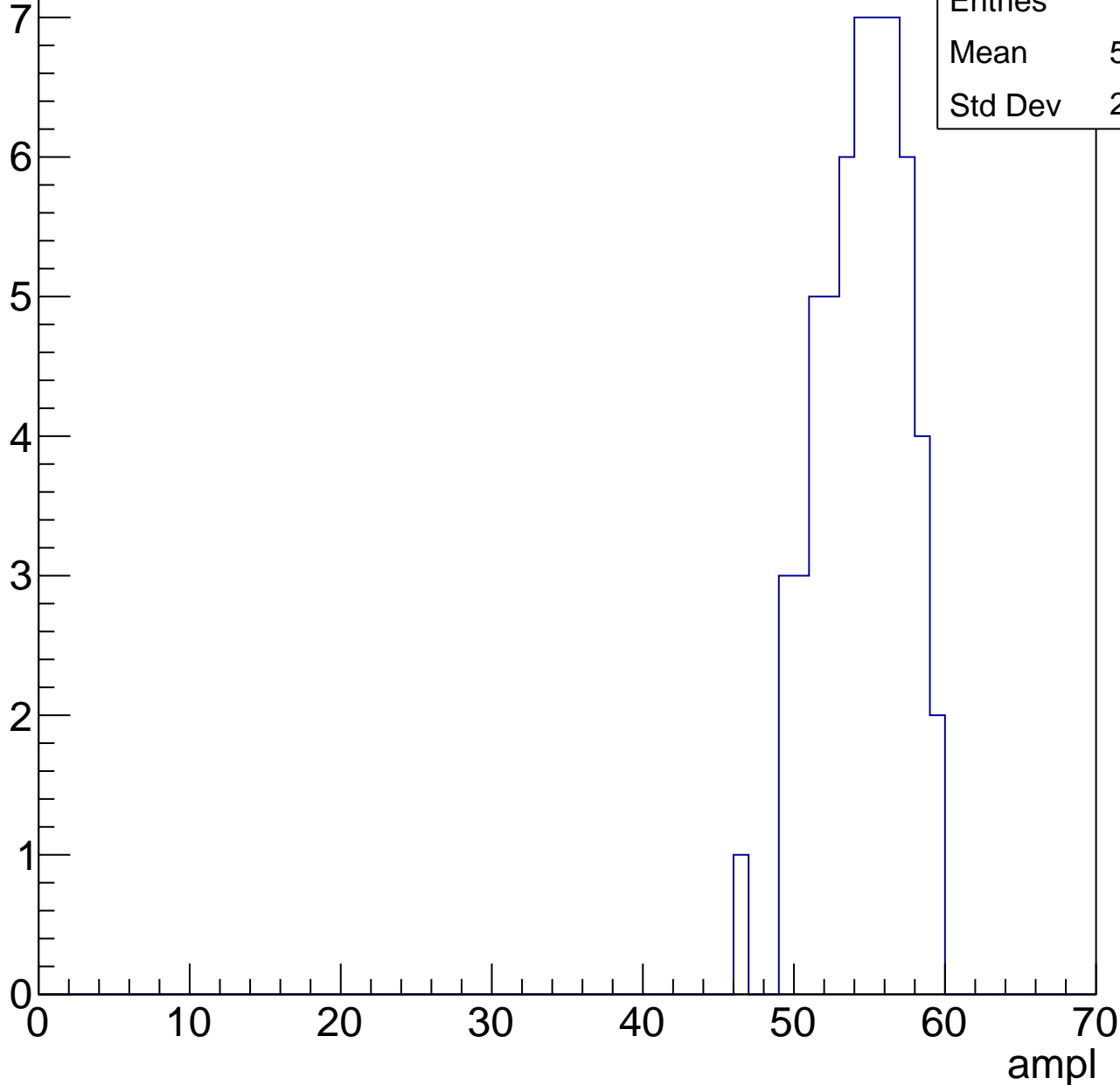
Entries	61
Mean	48.59
Std Dev	3.385



# B0L102S, U17-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

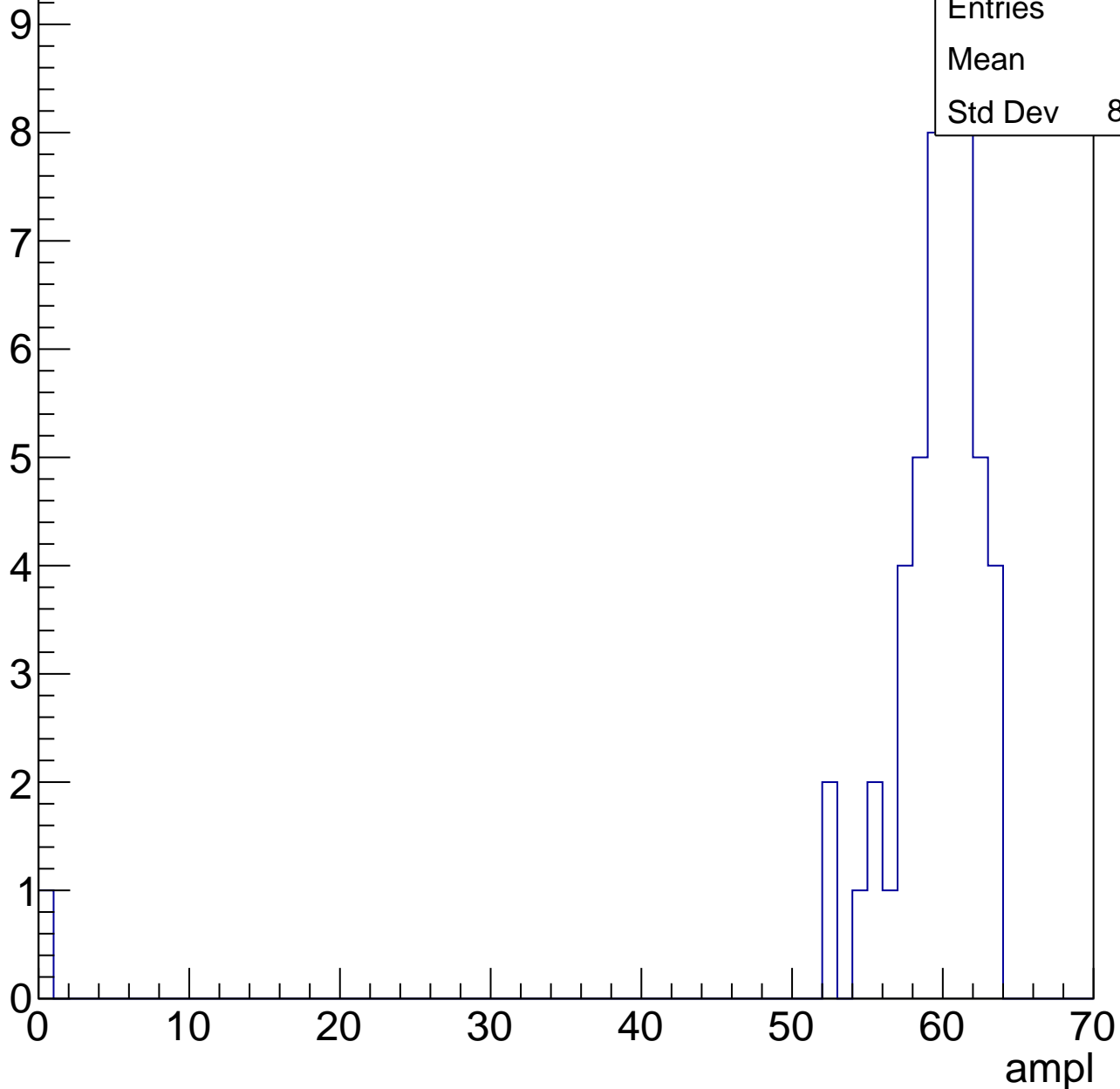
Entry



# B0L102S, U17-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

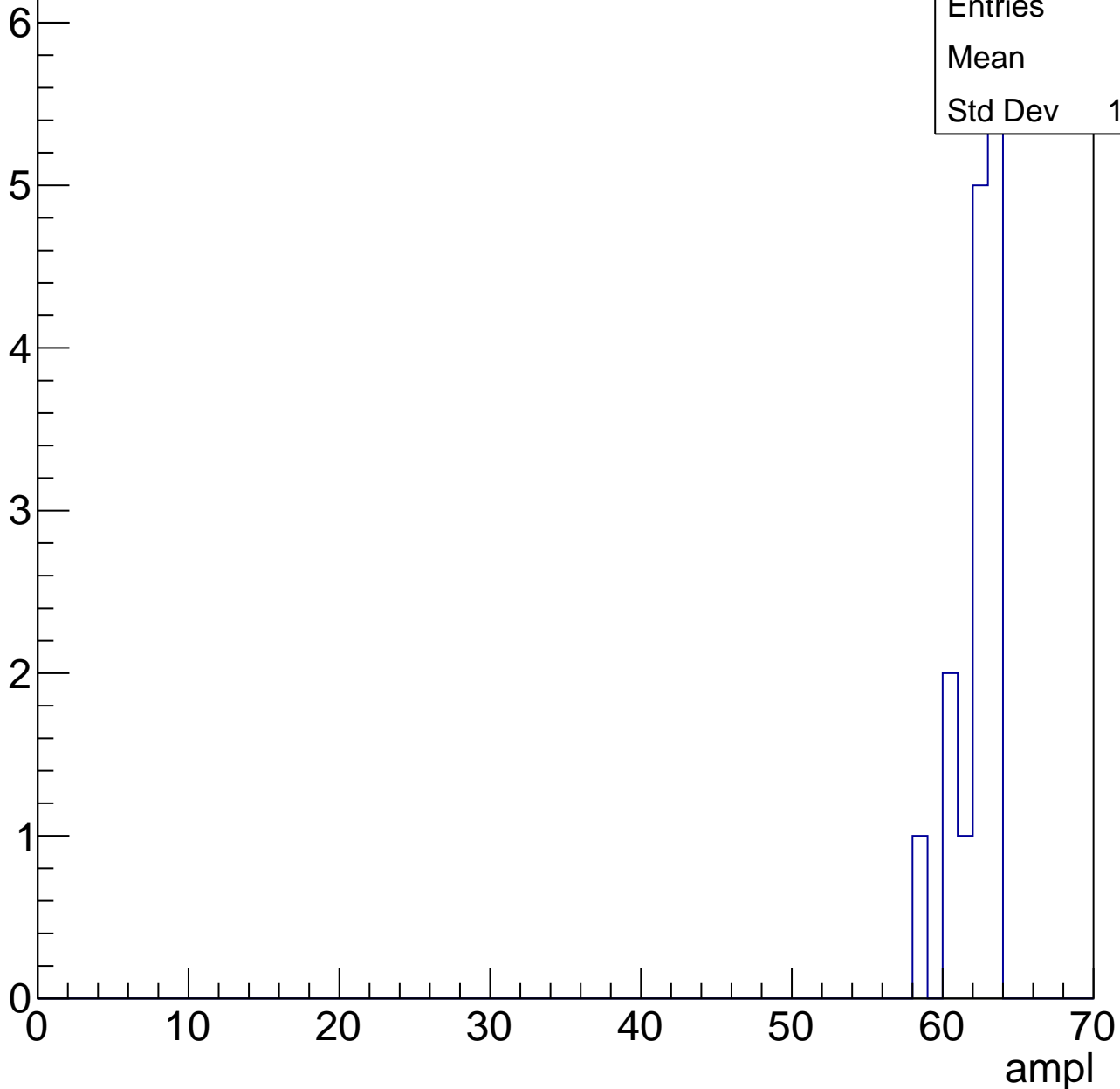


# B0L102S, U17-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	15
Mean	61.8
Std Dev	1.424





# B0L102S, U17-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch96, adc0

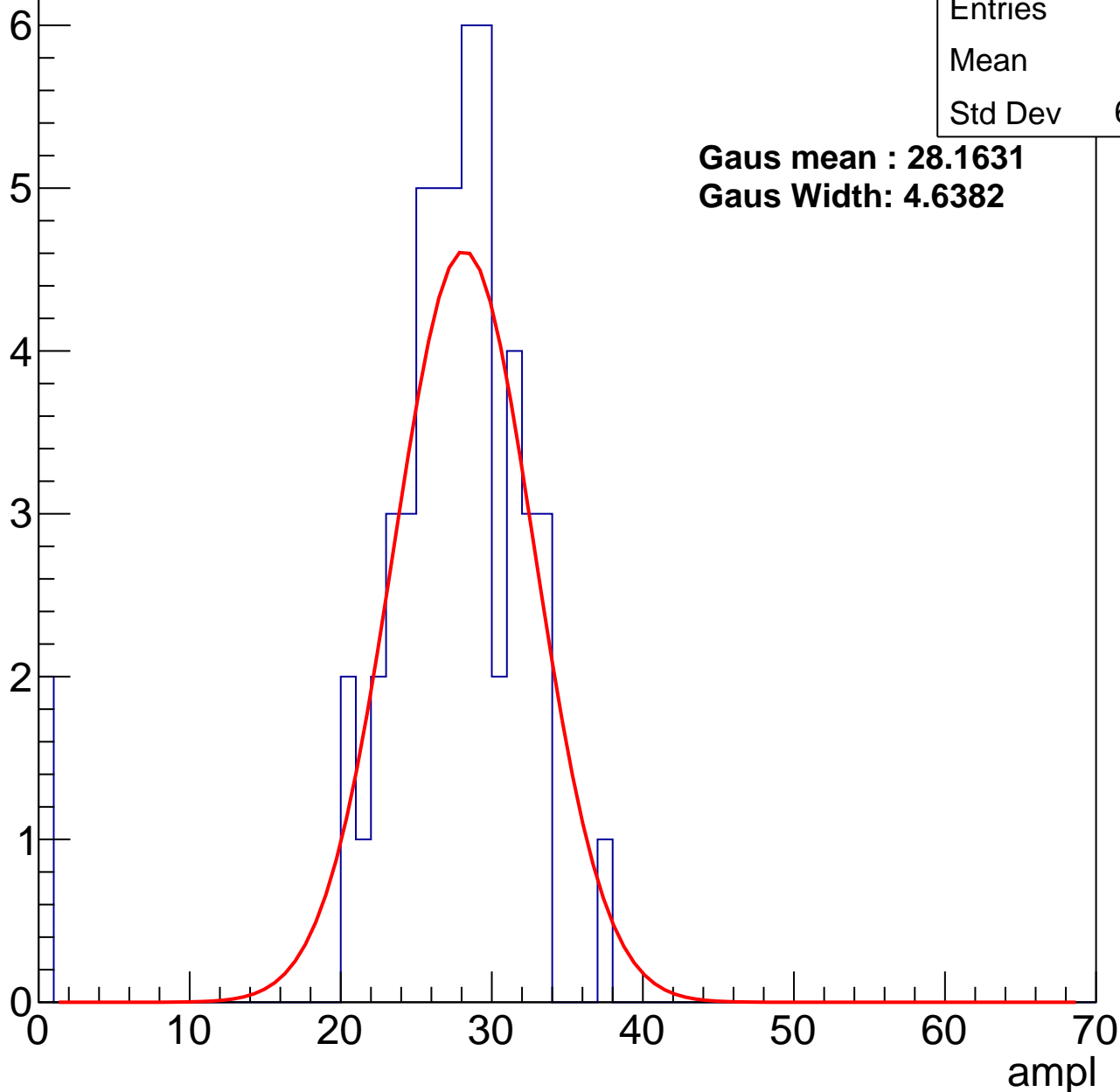
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	26.3
Std Dev	6.311

**Gaus mean : 28.1631**

**Gaus Width: 4.6382**



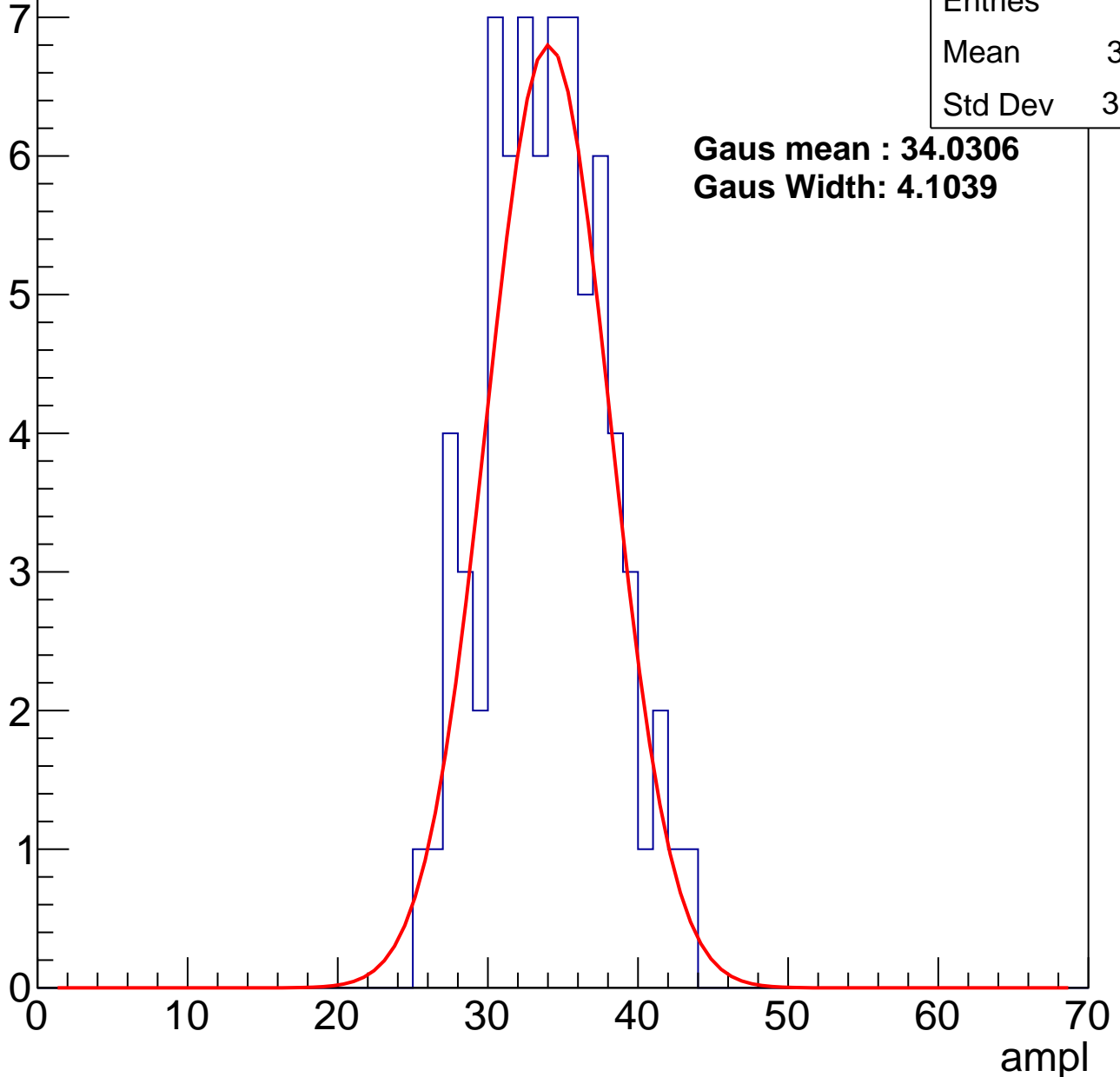
# B0L102S, U17-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	33.51
Std Dev	3.994

**Gaus mean : 34.0306**  
**Gaus Width: 4.1039**



# B0L102S, U17-ch96, adc2

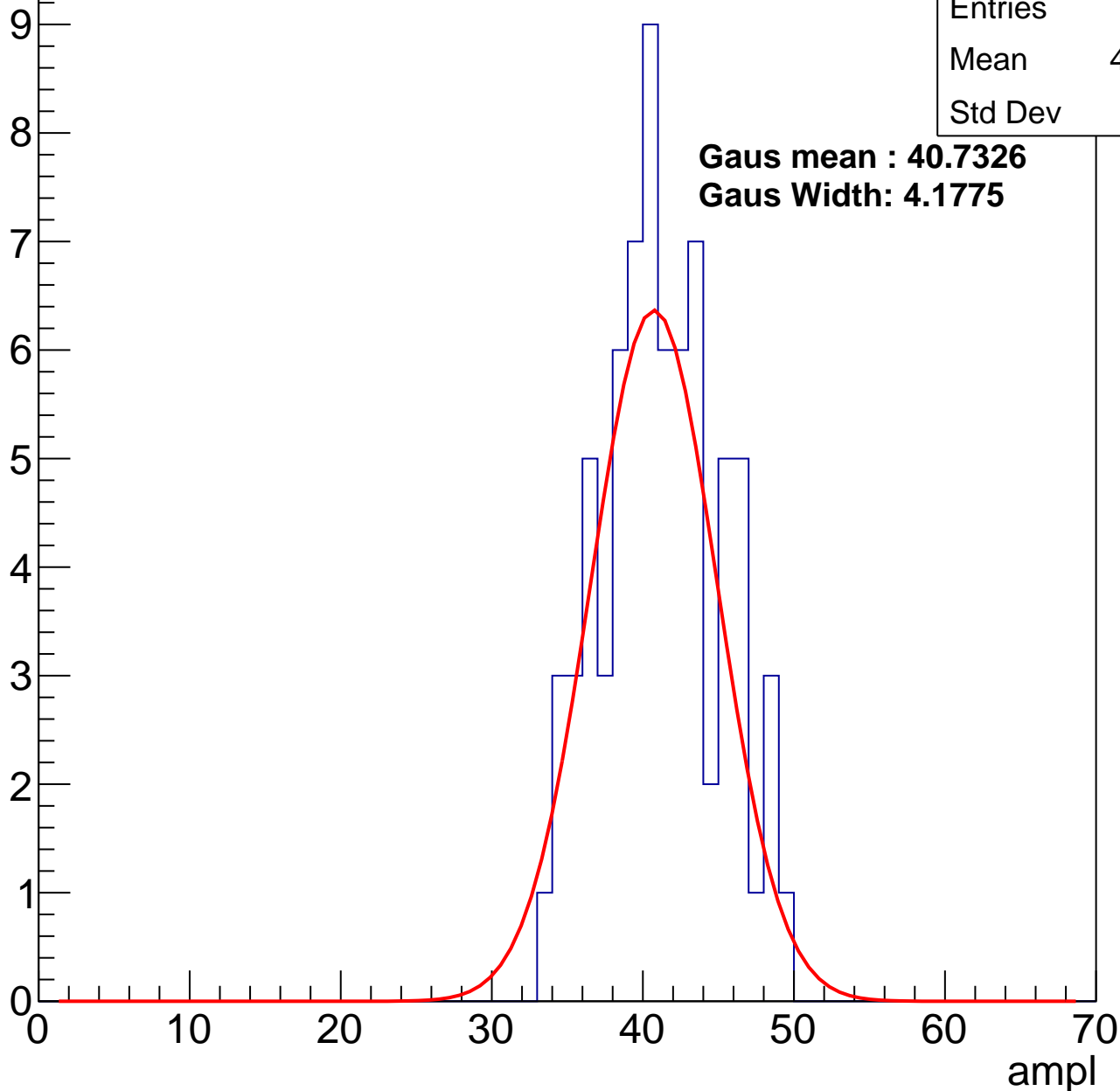
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	40.74
Std Dev	3.85

**Gaus mean : 40.7326**

**Gaus Width: 4.1775**

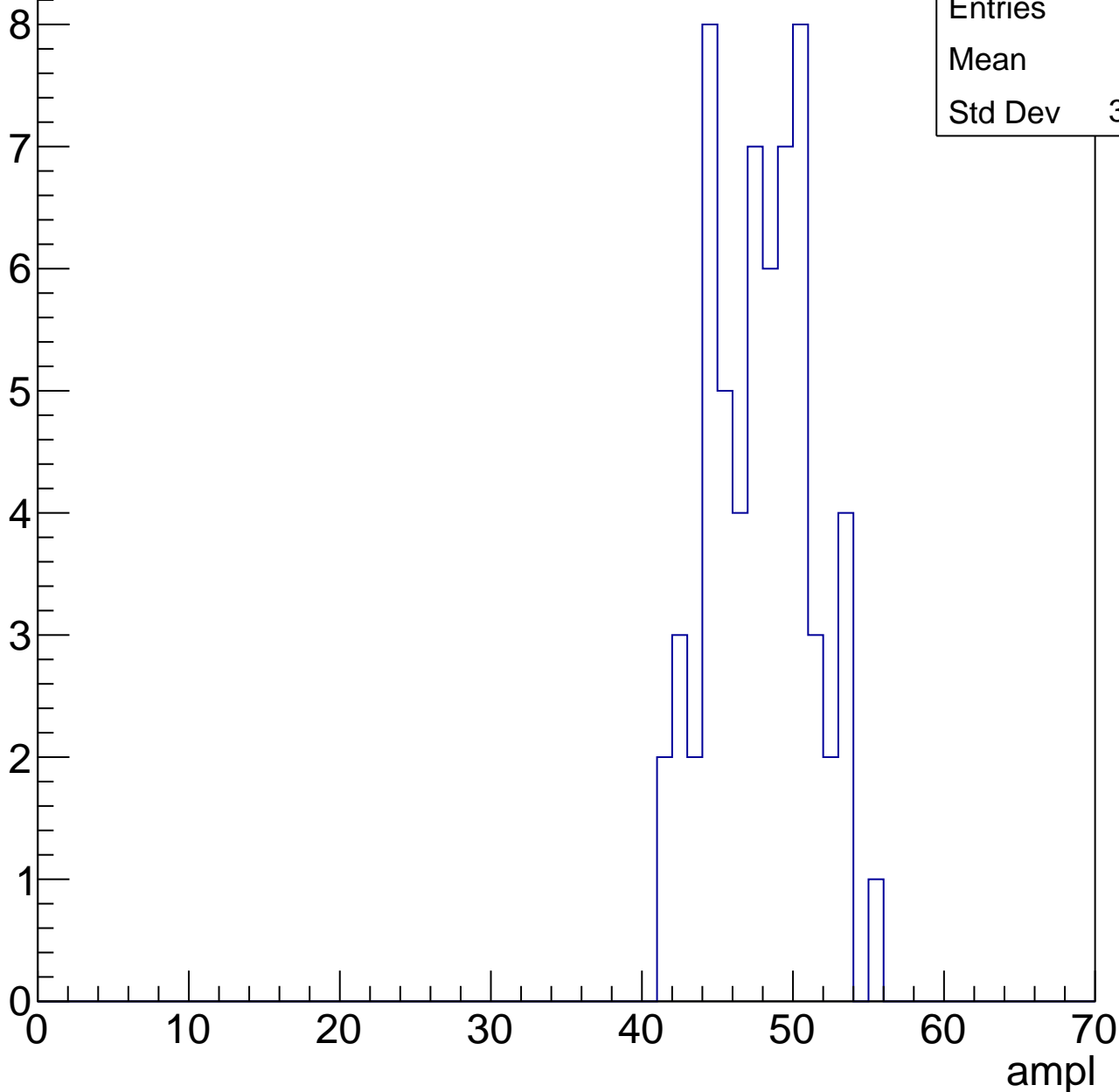


# B0L102S, U17-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	47.4
Std Dev	3.304

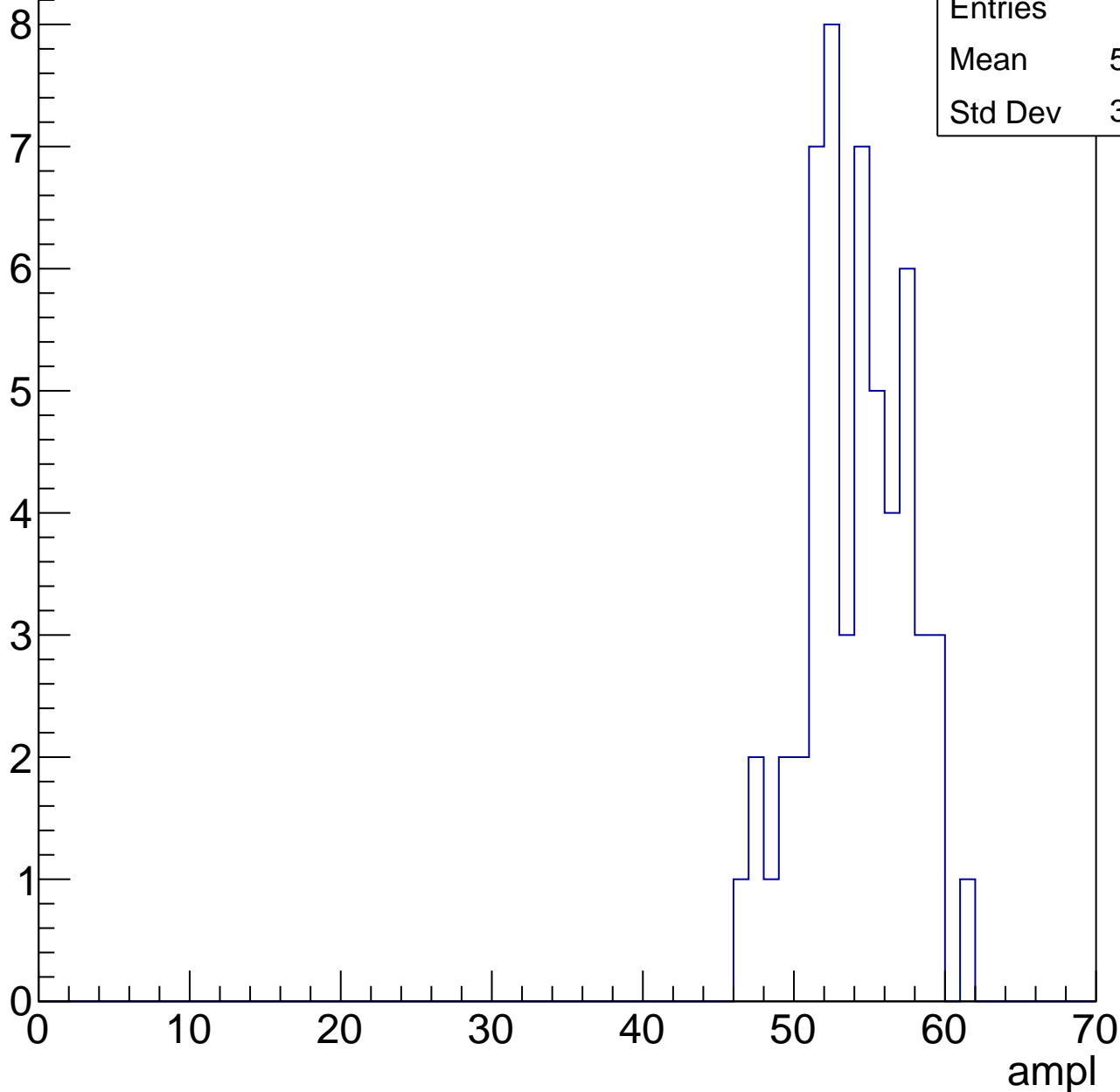


# B0L102S, U17-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	53.62
Std Dev	3.349

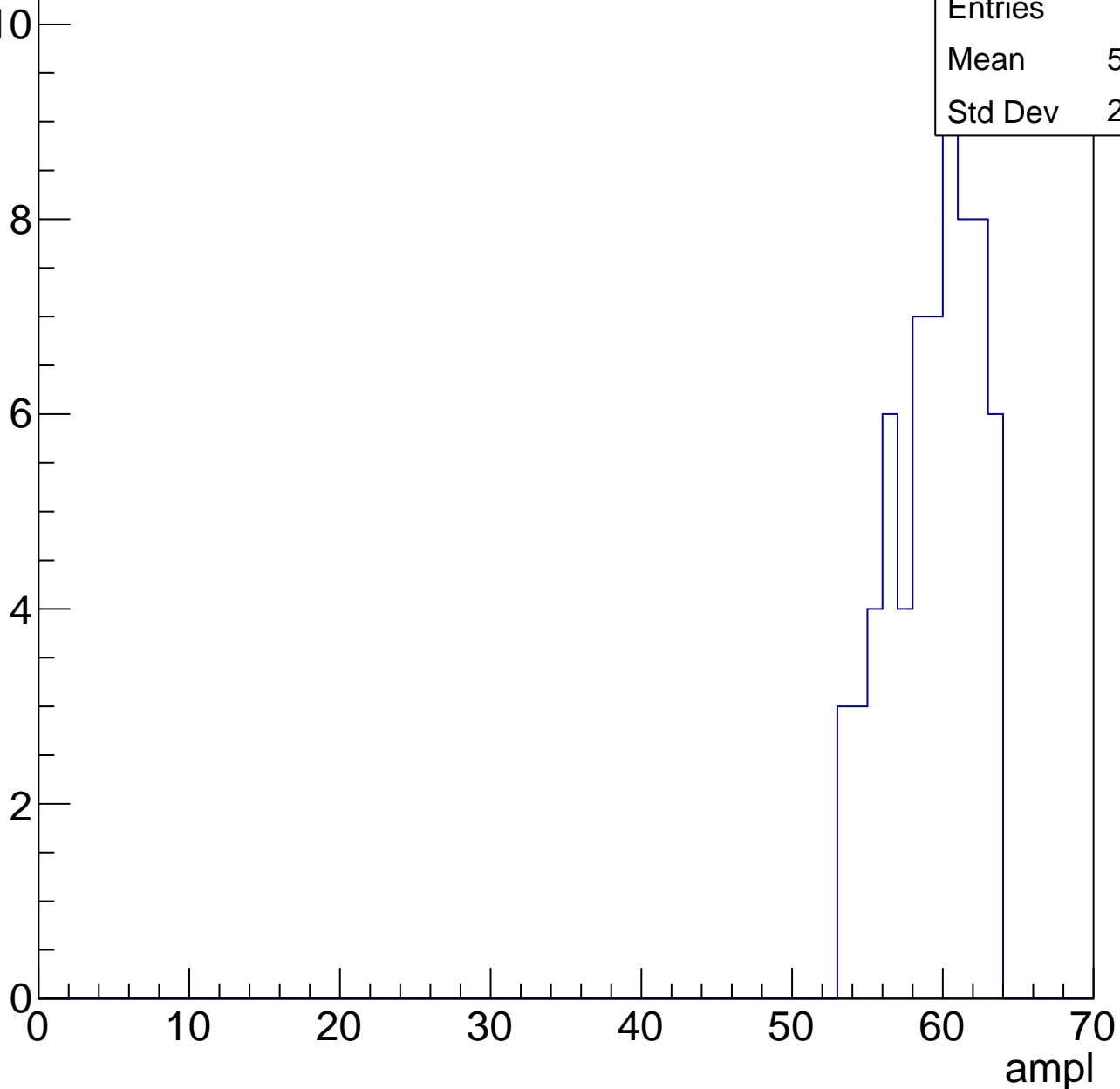


# B0L102S, U17-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

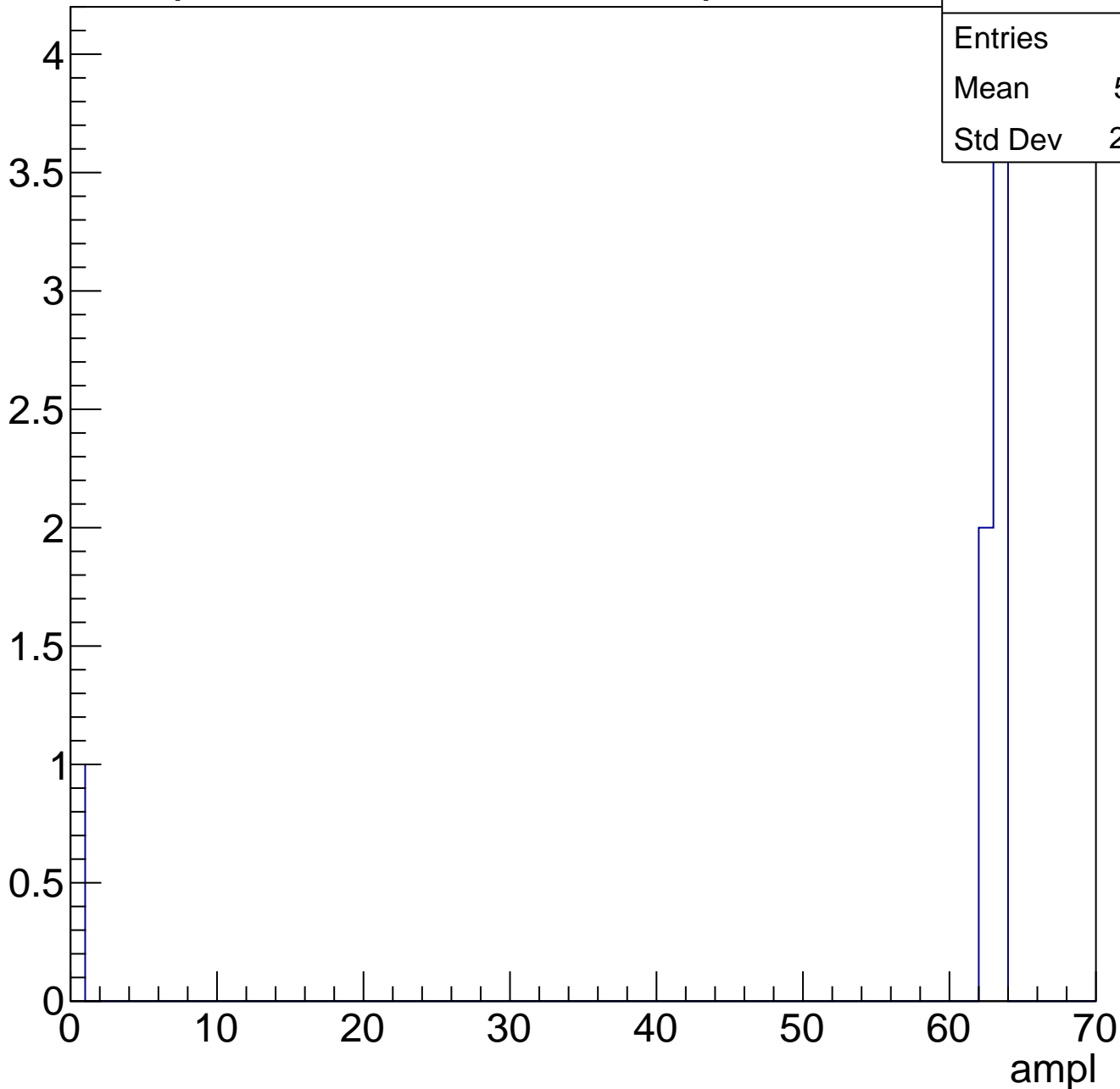
Entries	66
Mean	58.88
Std Dev	2.842



# B0L102S, U17-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

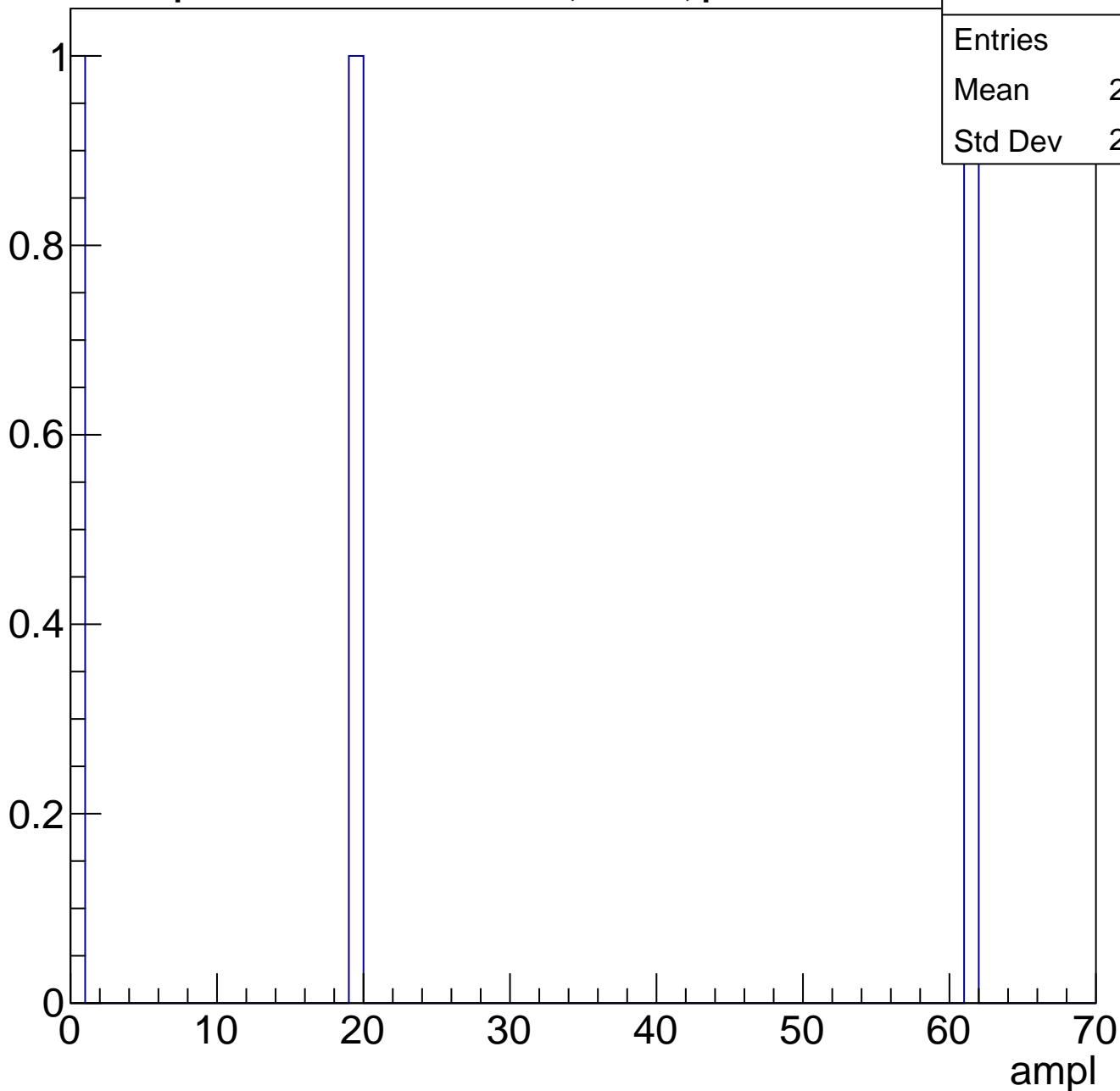




# B0L102S, U17-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	26.67
Std Dev	25.49

# B0L102S, U17-ch97, adc0

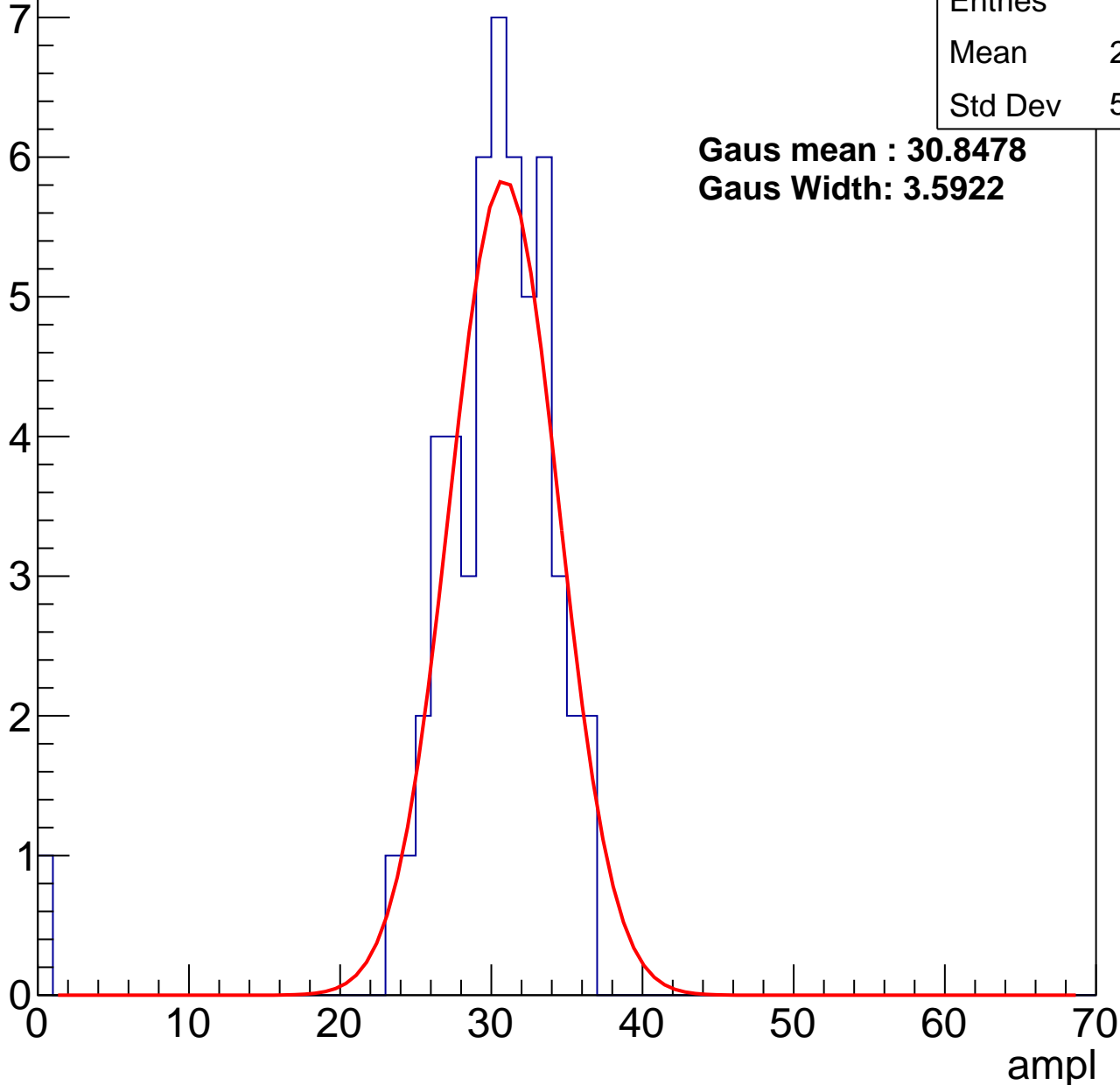
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	29.53
Std Dev	5.123

**Gaus mean : 30.8478**

**Gaus Width: 3.5922**



# B0L102S, U17-ch97, adc1

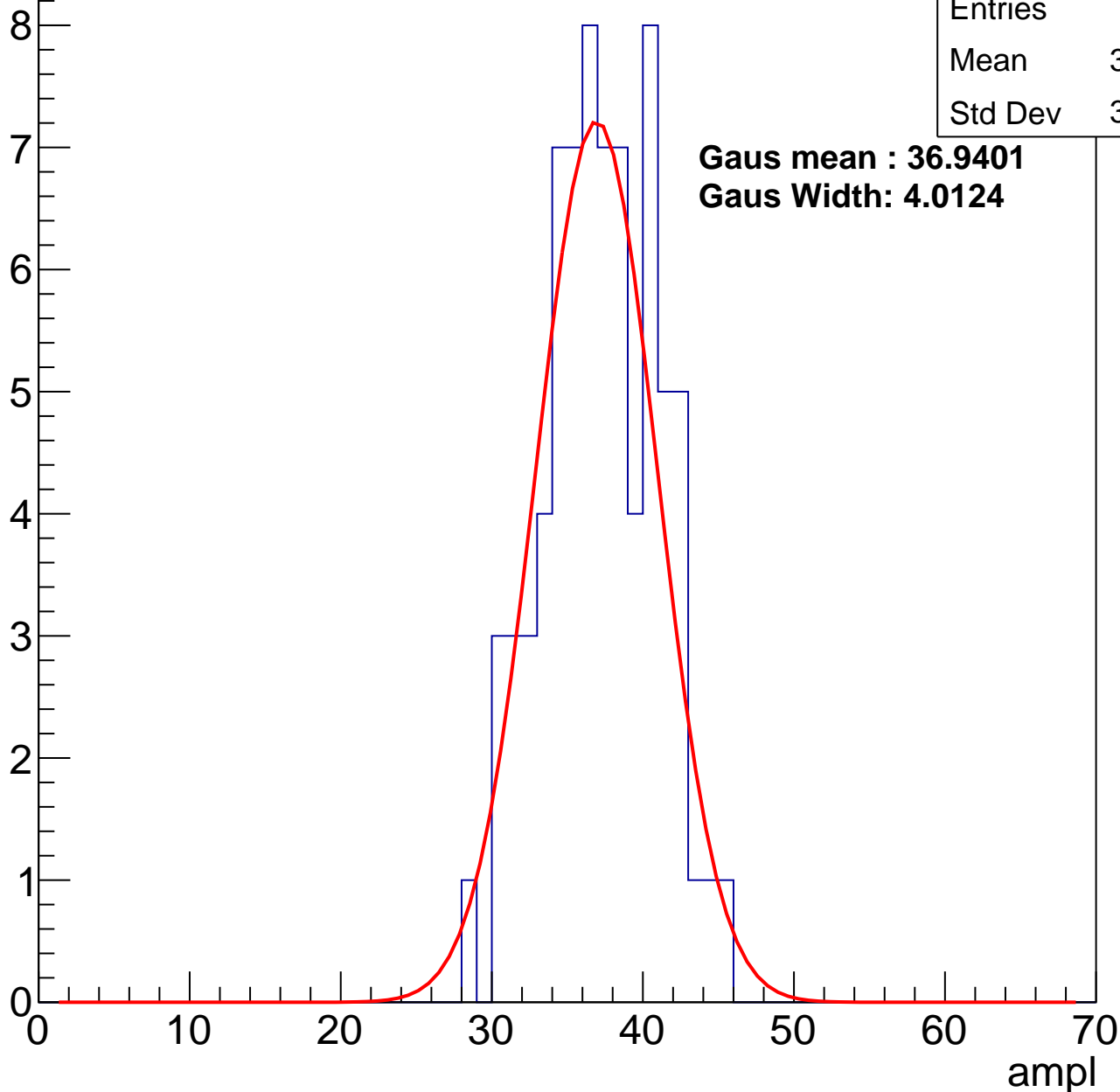
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	36.77
Std Dev	3.686

**Gaus mean : 36.9401**

**Gaus Width: 4.0124**



# B0L102S, U17-ch97, adc2

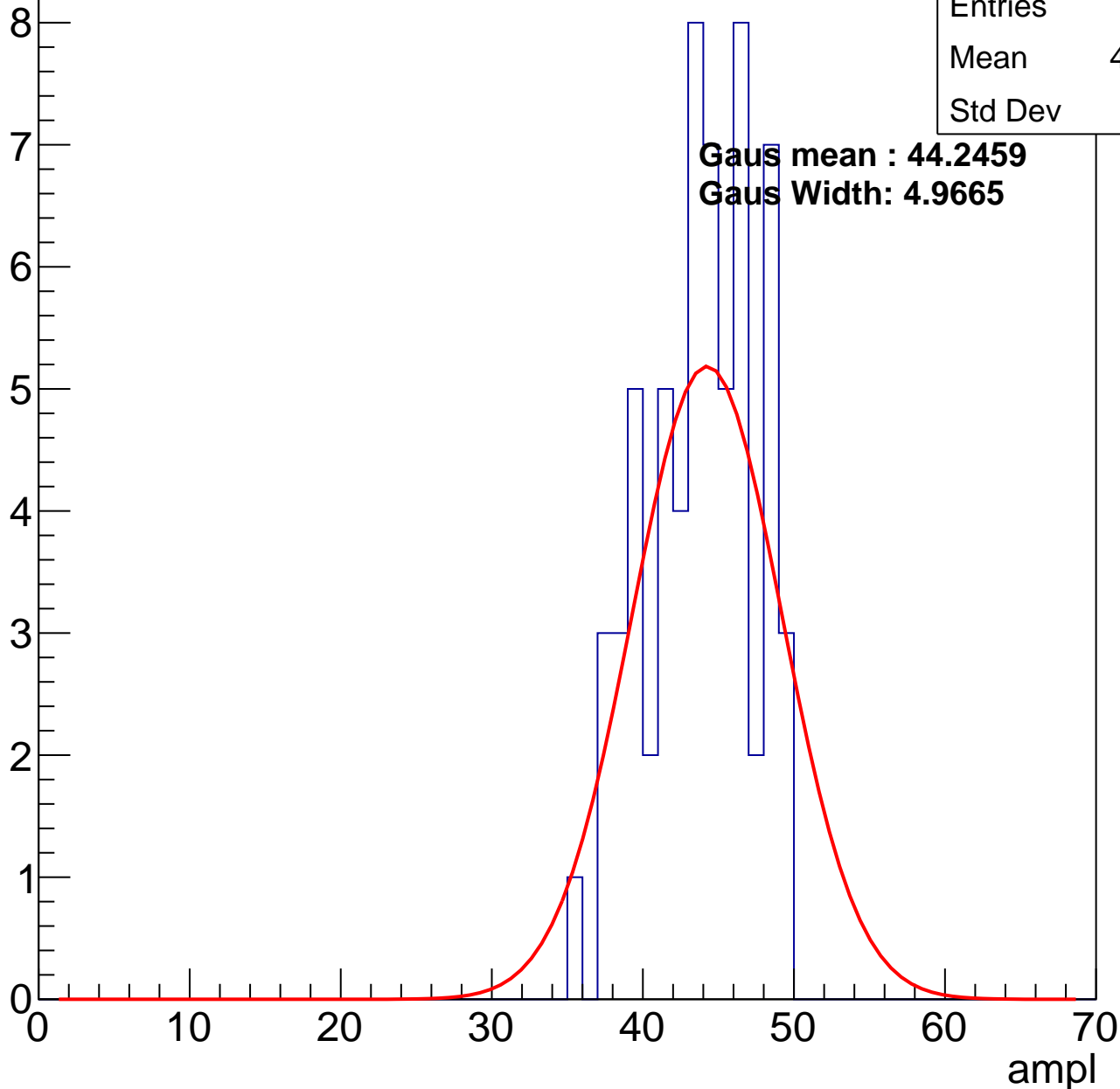
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	43.33
Std Dev	3.51

**Gaus mean : 44.2459**

**Gaus Width: 4.9665**

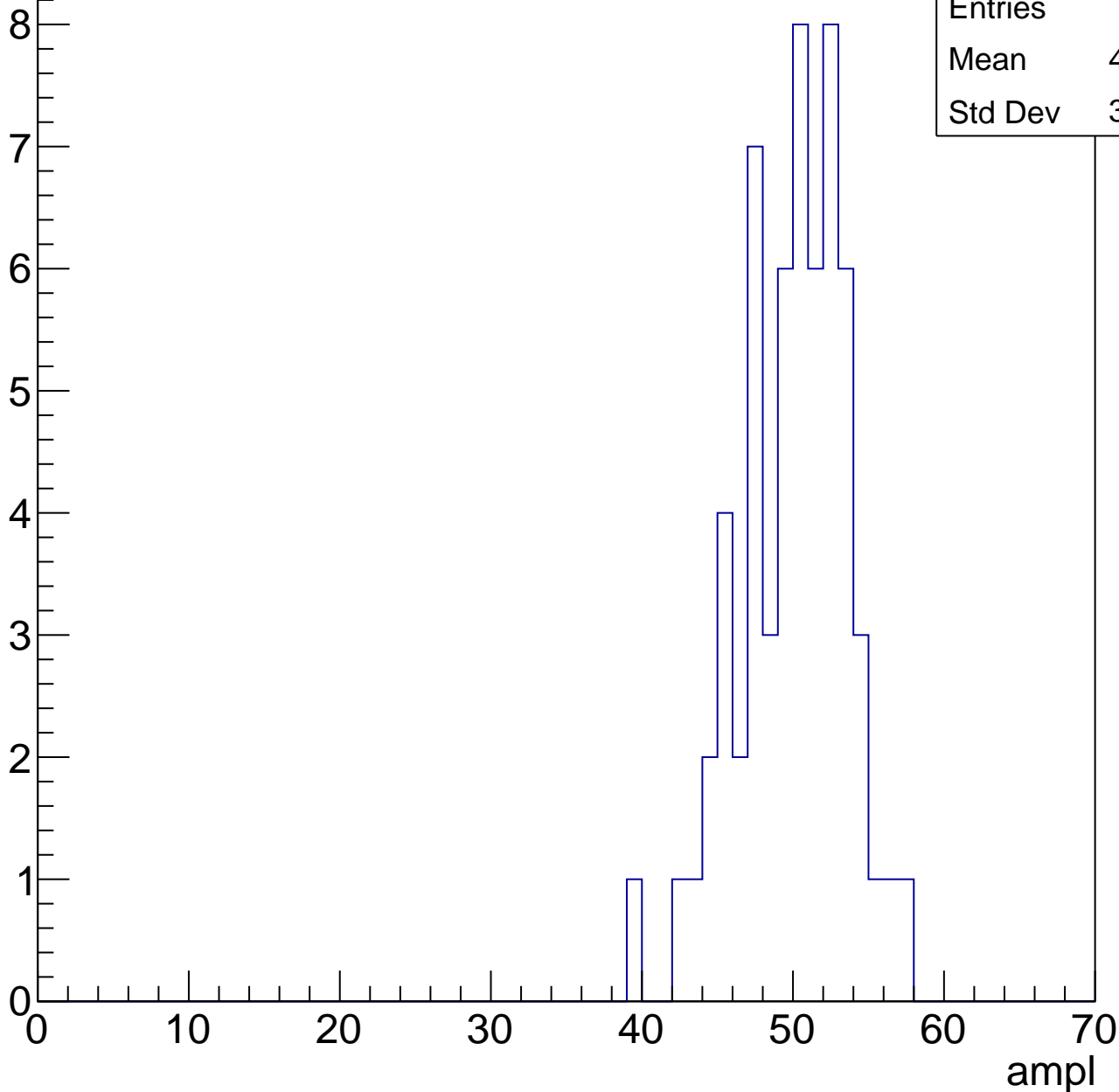


# B0L102S, U17-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	49.52
Std Dev	3.519

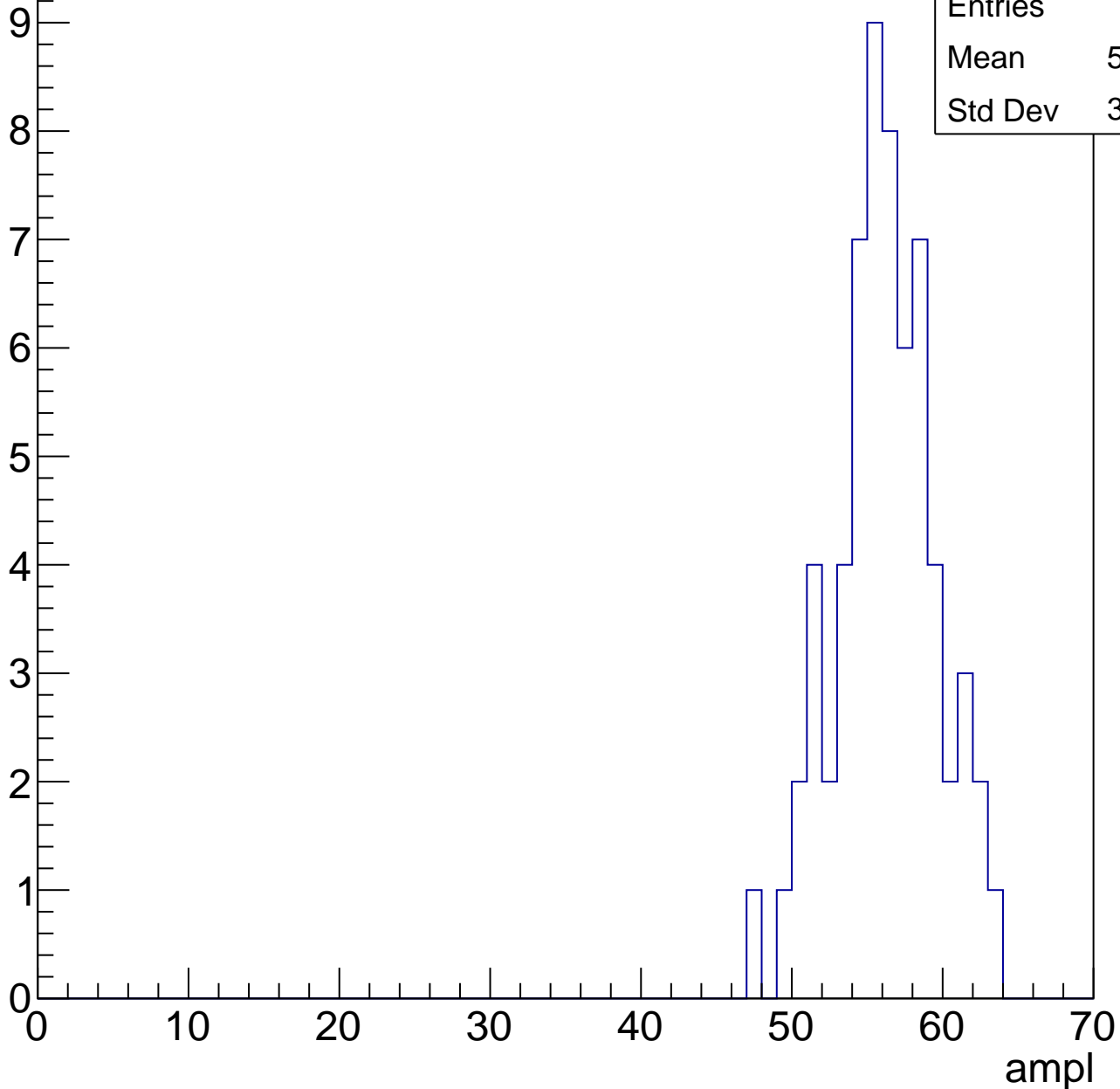


# B0L102S, U17-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

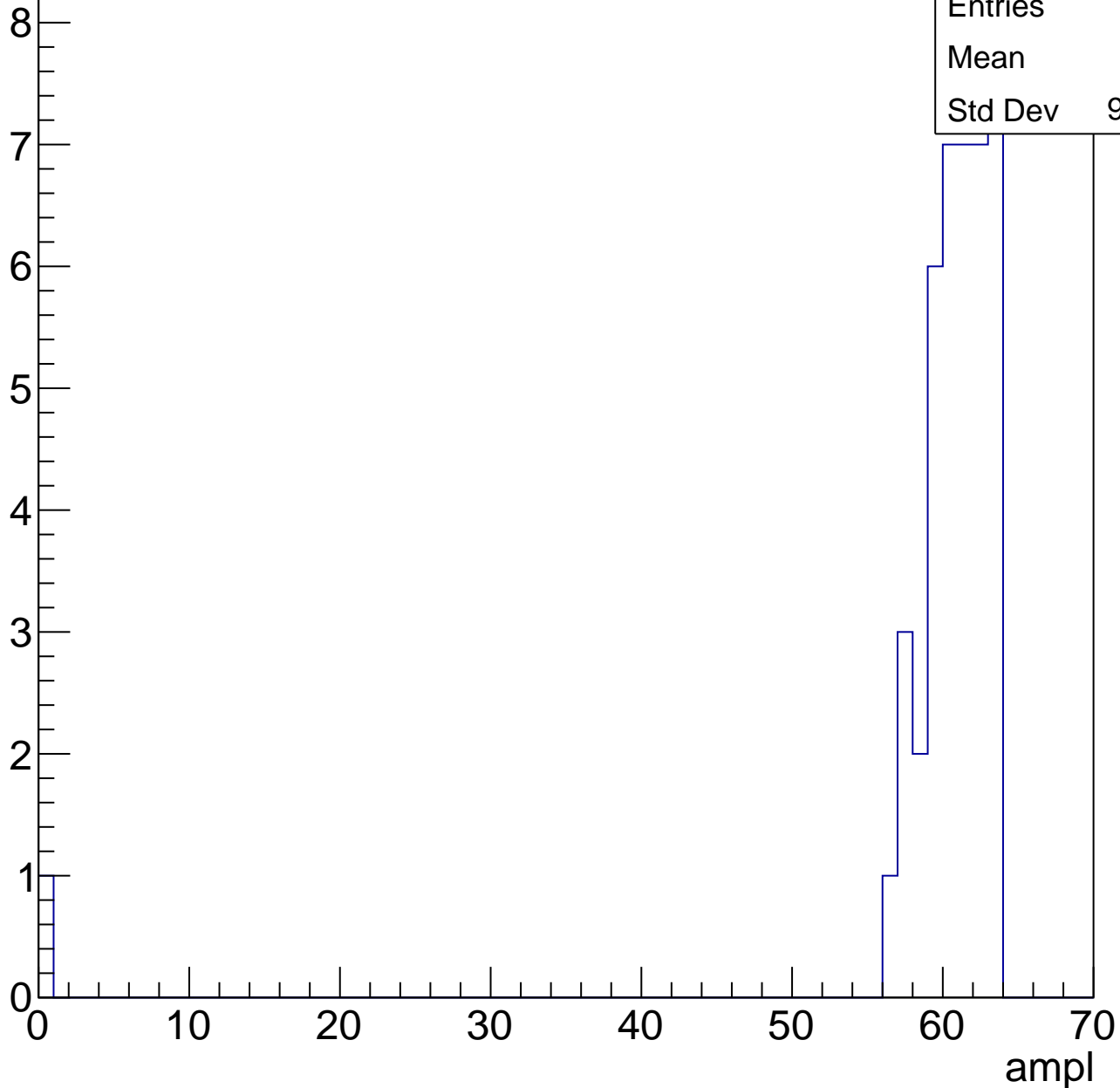
Entries	63
Mean	55.73
Std Dev	3.334



# B0L102S, U17-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

60.75

Std Dev

1.92



# B0L102S, U17-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch98, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	82
Mean	26.82
Std Dev	5.808

**Gaus mean : 28.2741**

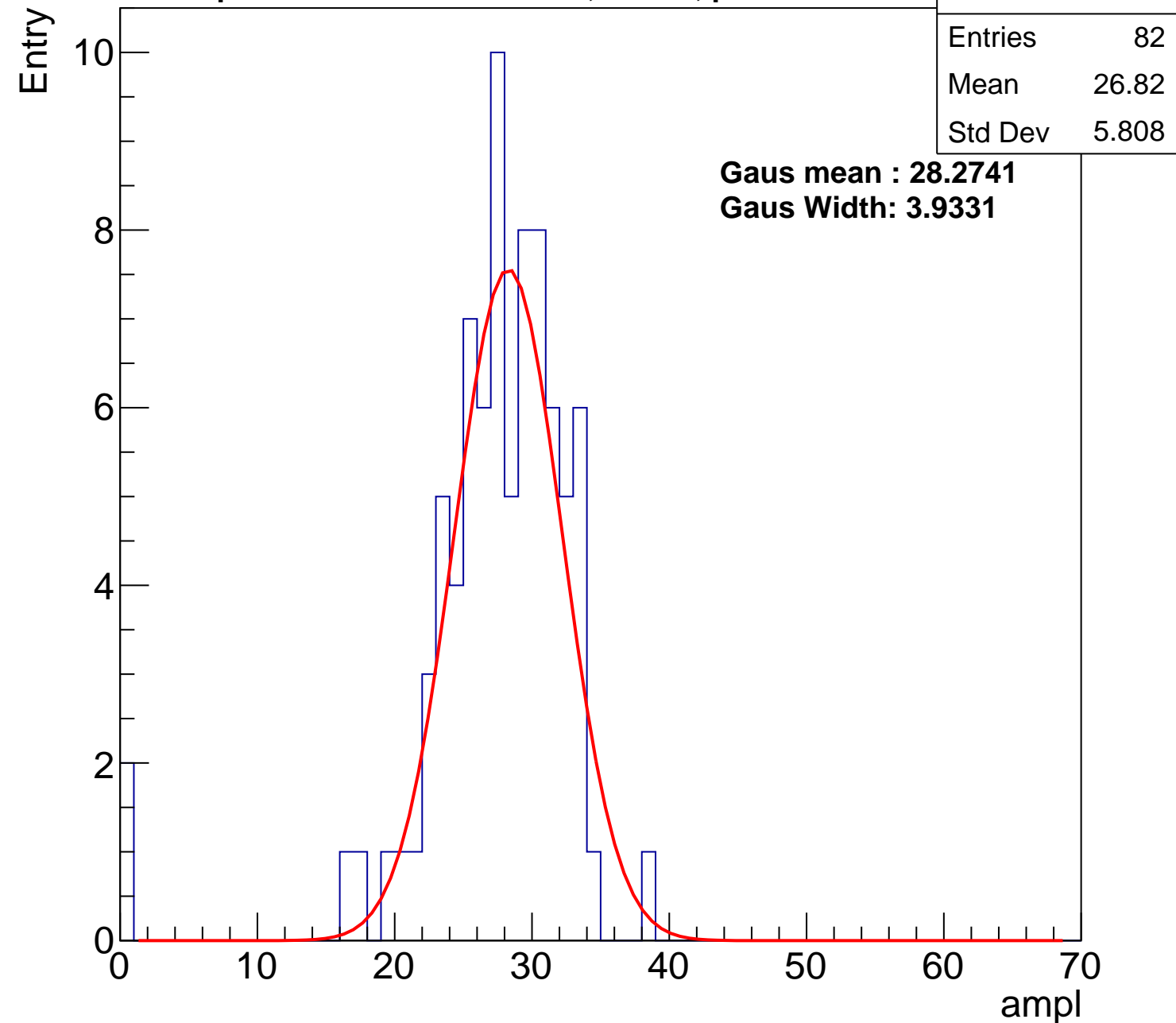
**Gaus Width: 3.9331**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch98, adc1

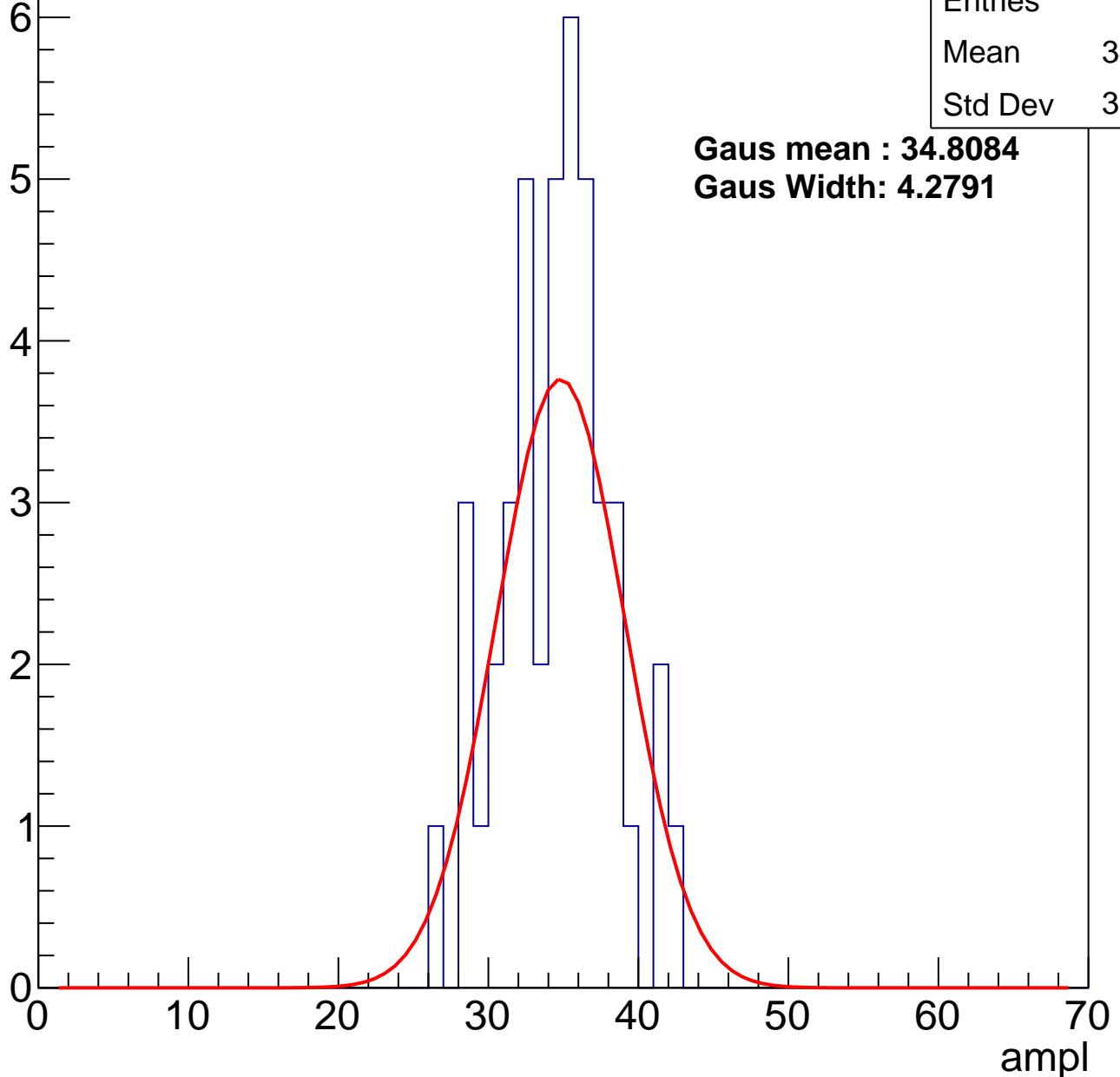
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	43
Mean	34.09
Std Dev	3.627

**Gaus mean : 34.8084**

**Gaus Width: 4.2791**



# B0L102S, U17-ch98, adc2

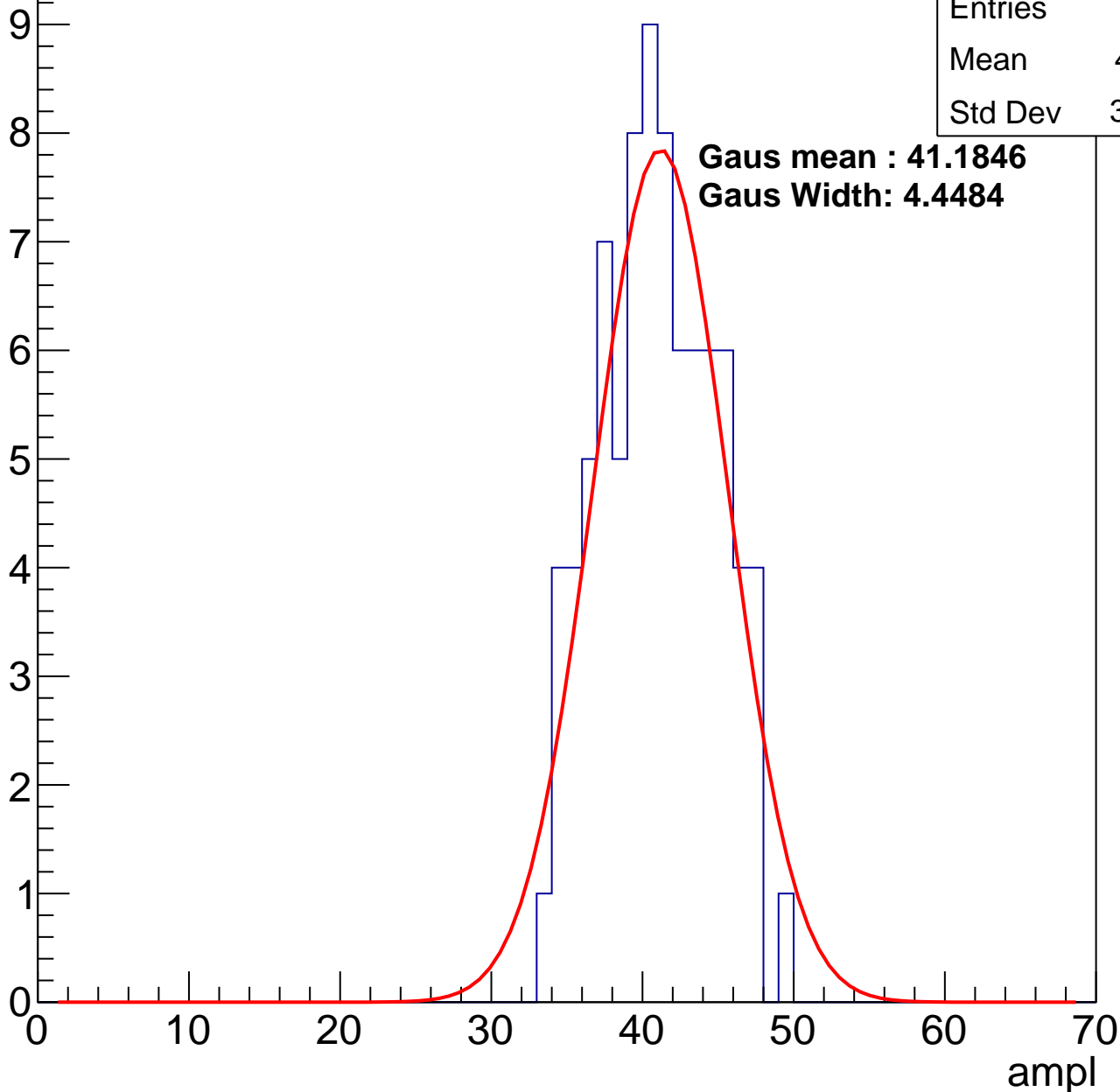
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	40.51
Std Dev	3.772

**Gaus mean : 41.1846**

**Gaus Width: 4.4484**

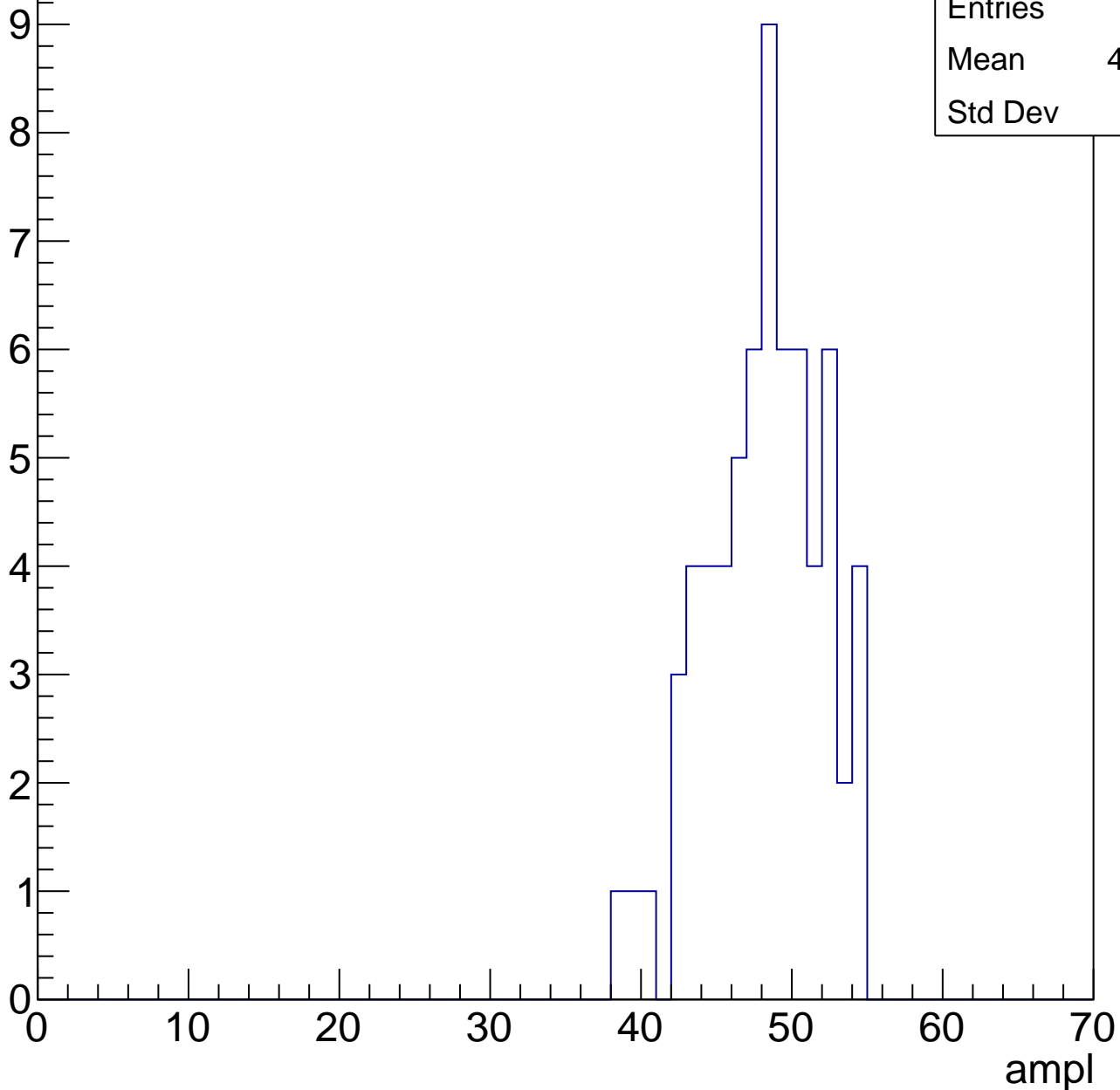


# B0L102S, U17-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.68
Std Dev	3.75

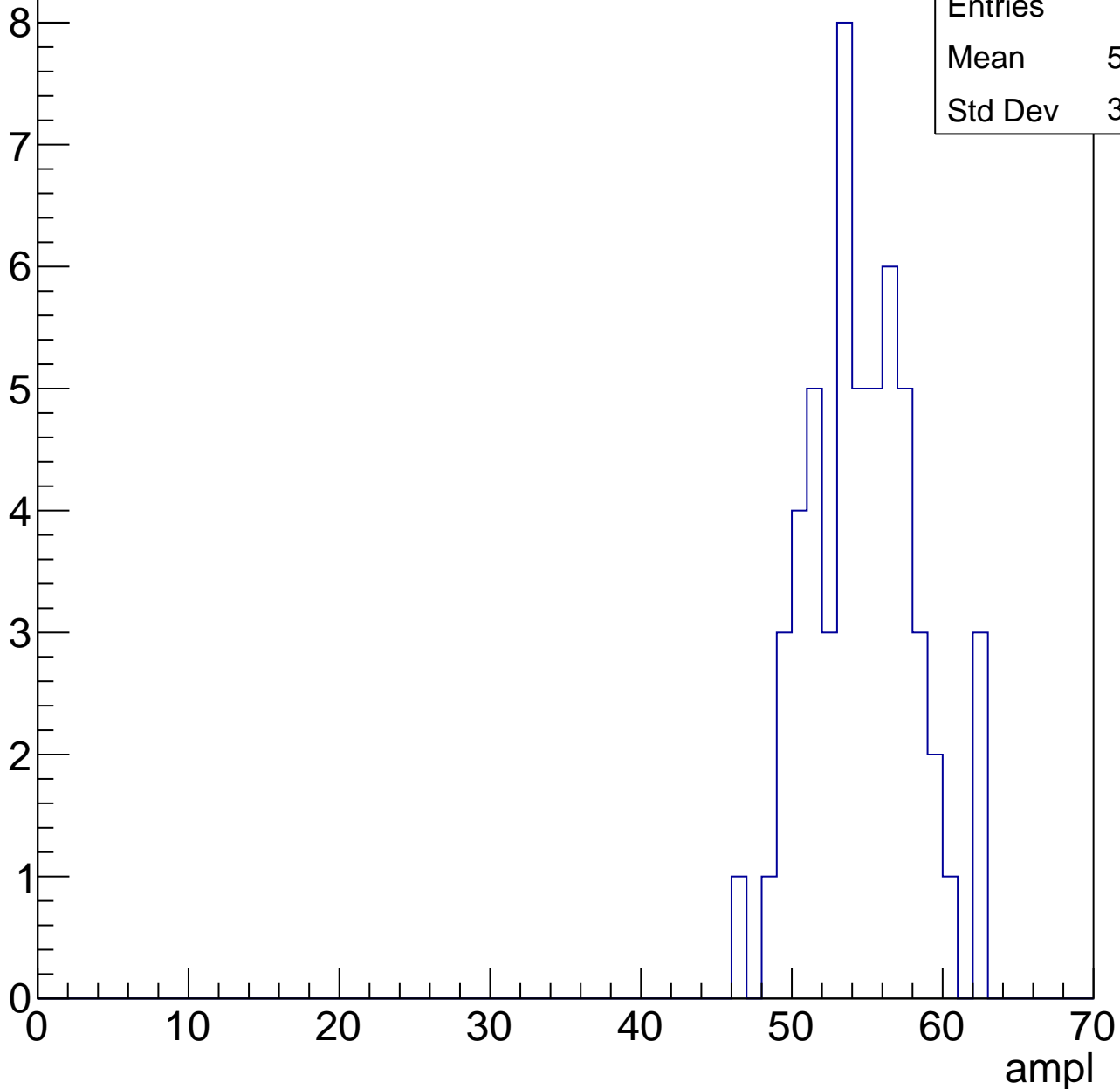


# B0L102S, U17-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

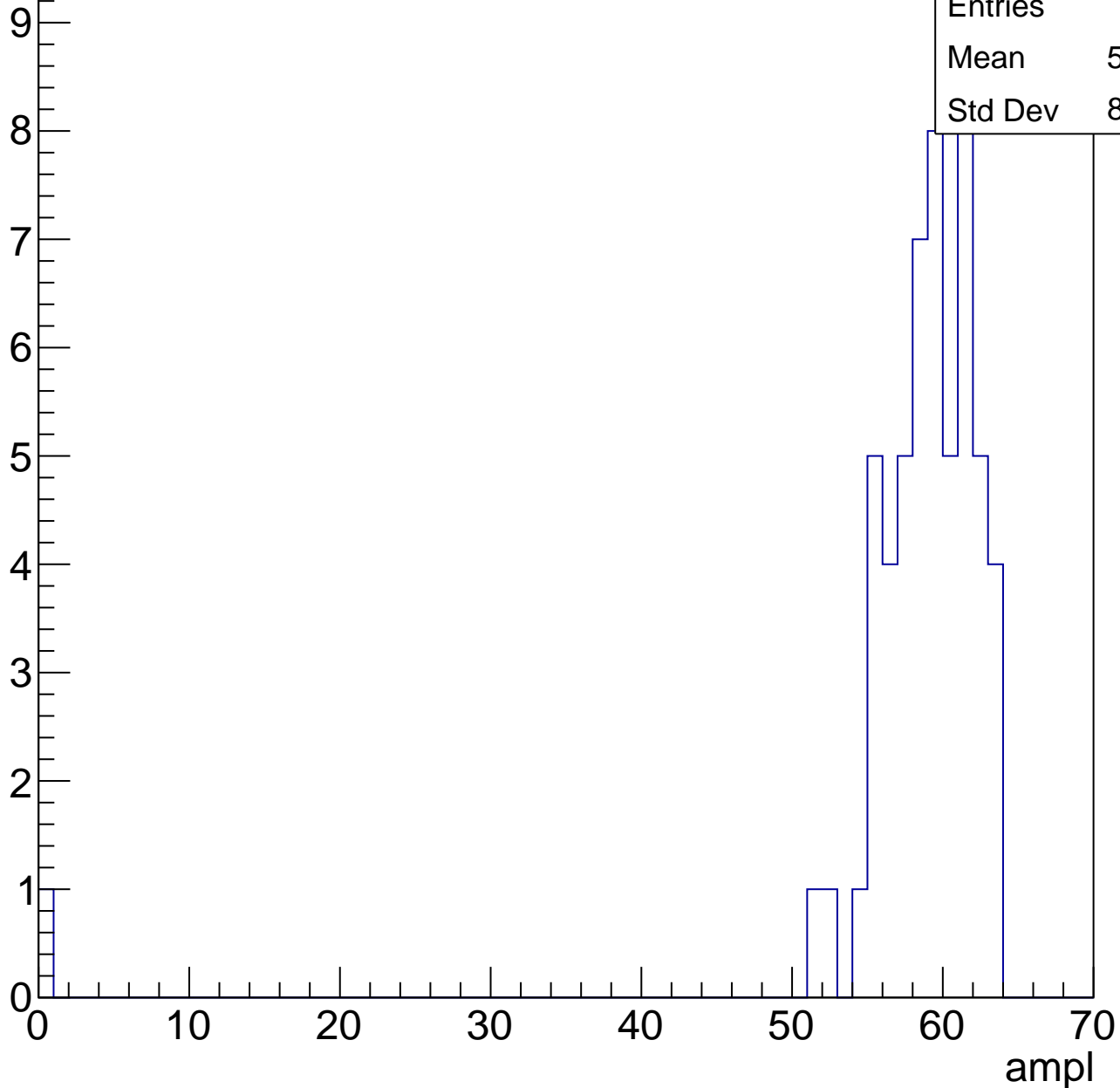
Entries	55
Mean	54.18
Std Dev	3.573



# B0L102S, U17-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

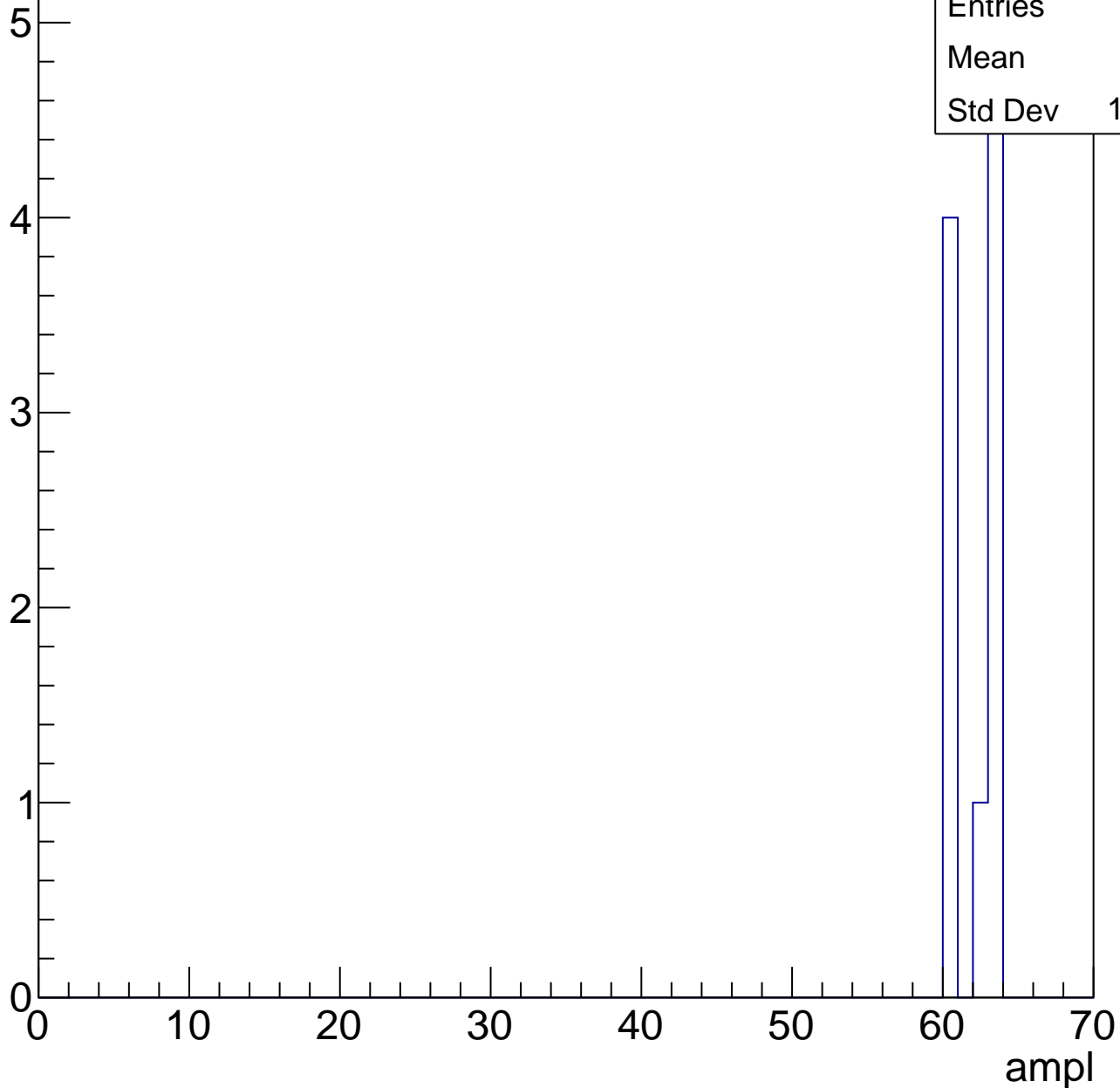


# B0L102S, U17-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	10
Mean	61.7
Std Dev	1.418

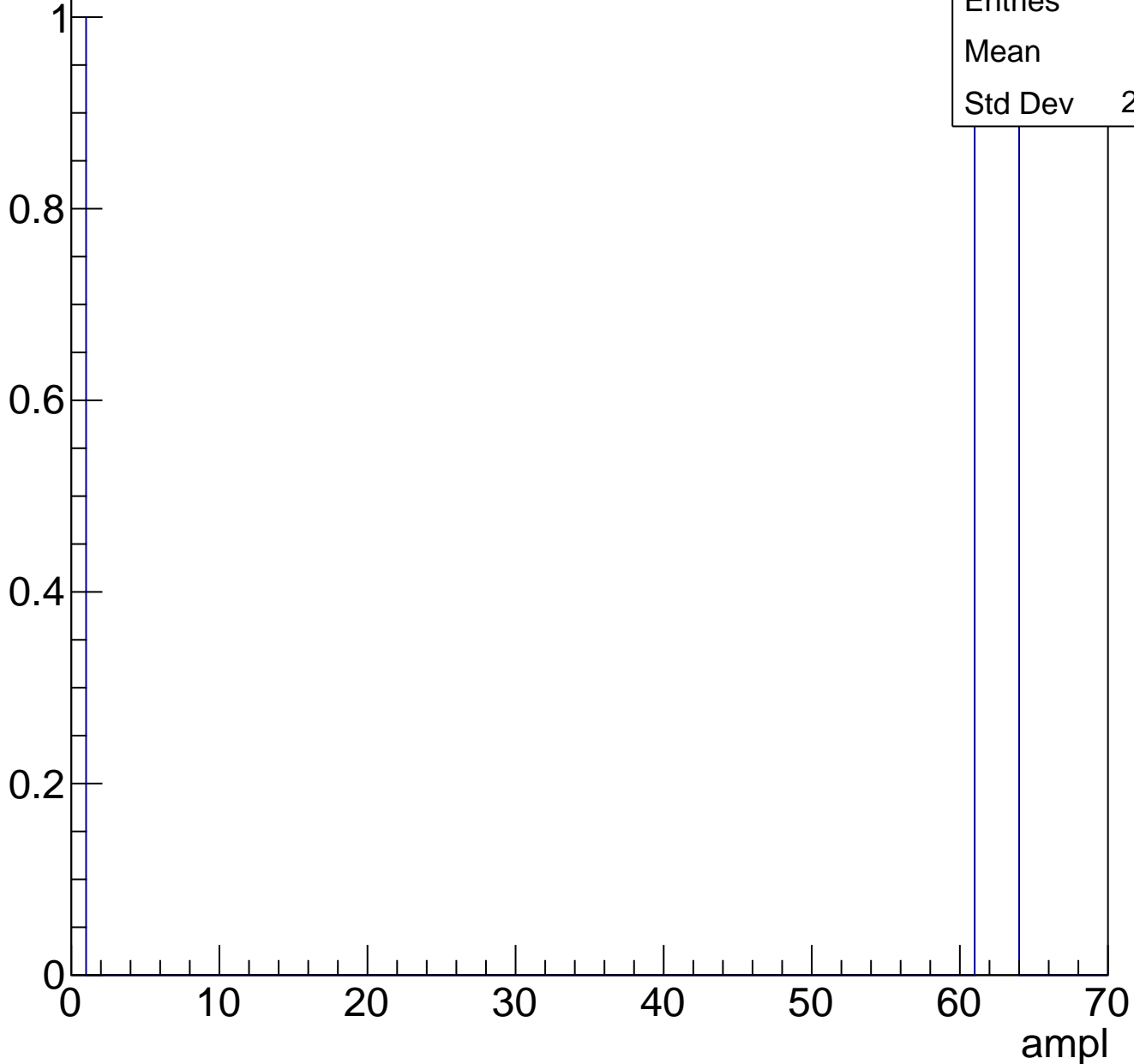




# B0L102S, U17-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch99, adc0

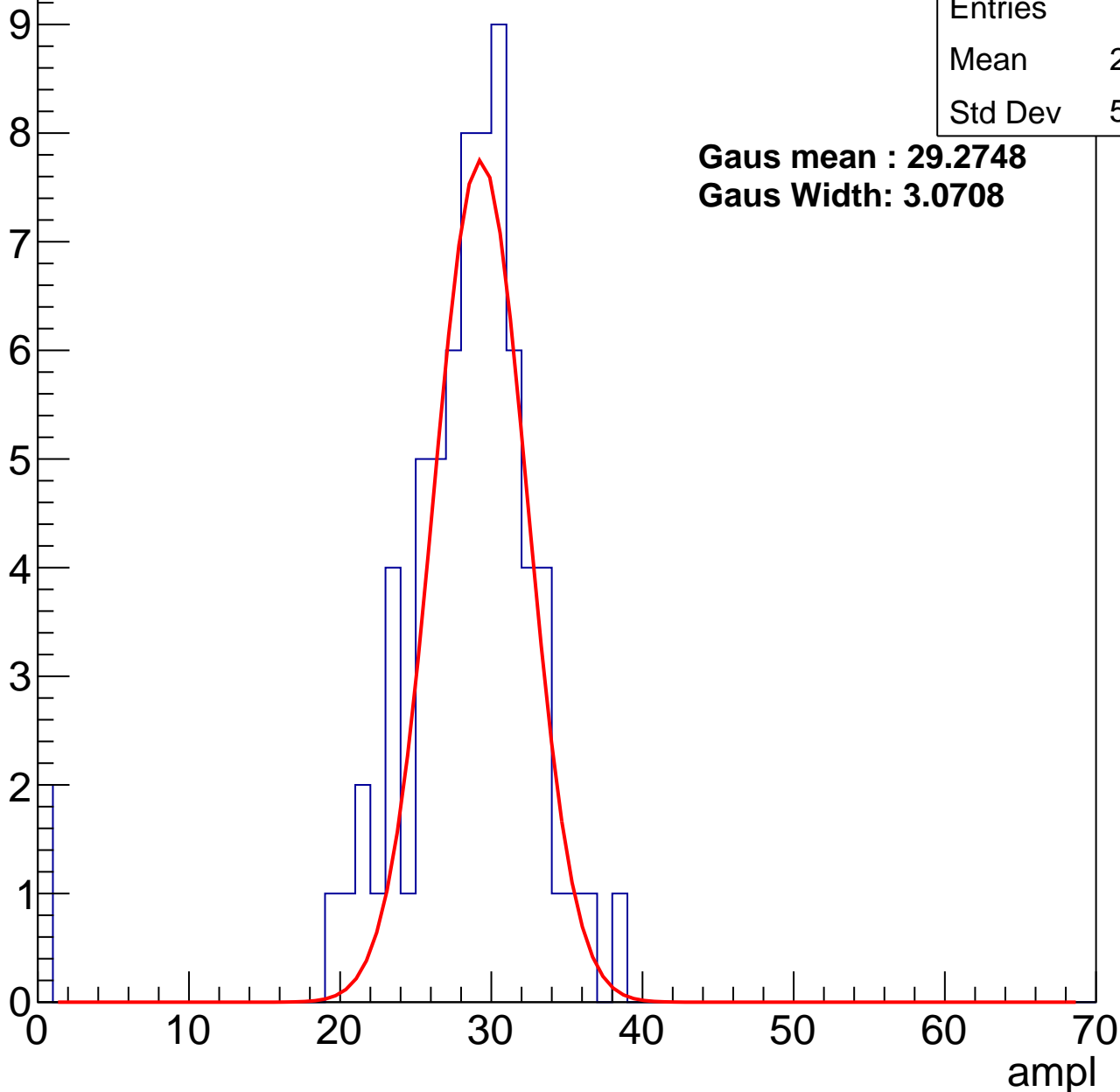
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	27.48
Std Dev	5.964

**Gaus mean : 29.2748**

**Gaus Width: 3.0708**



# B0L102S, U17-ch99, adc1

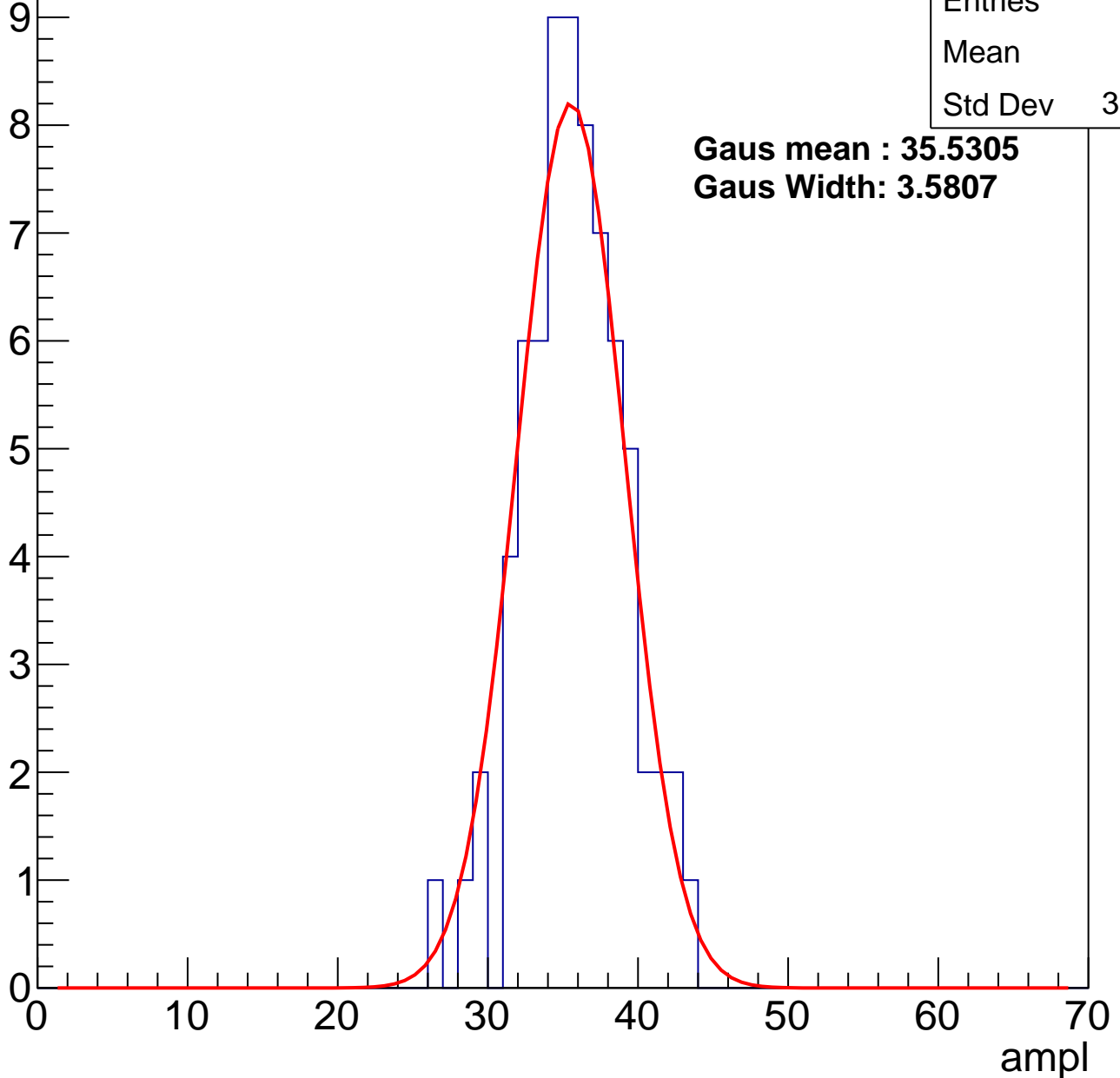
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	35.3
Std Dev	3.358

**Gaus mean : 35.5305**

**Gaus Width: 3.5807**



# B0L102S, U17-ch99, adc2

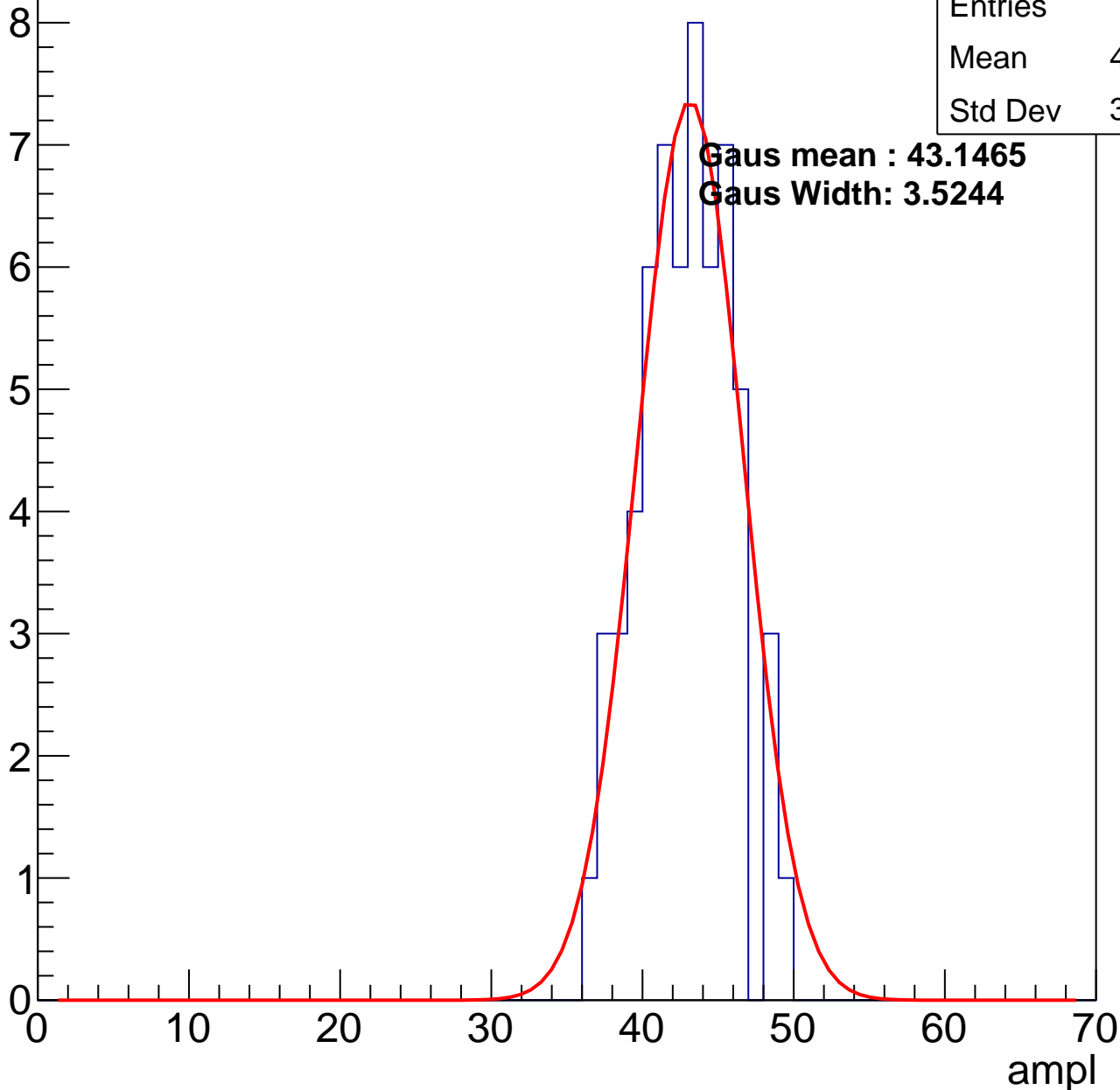
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.37
Std Dev	3.027

**Gaus mean : 43.1465**

**Gaus Width: 3.5244**

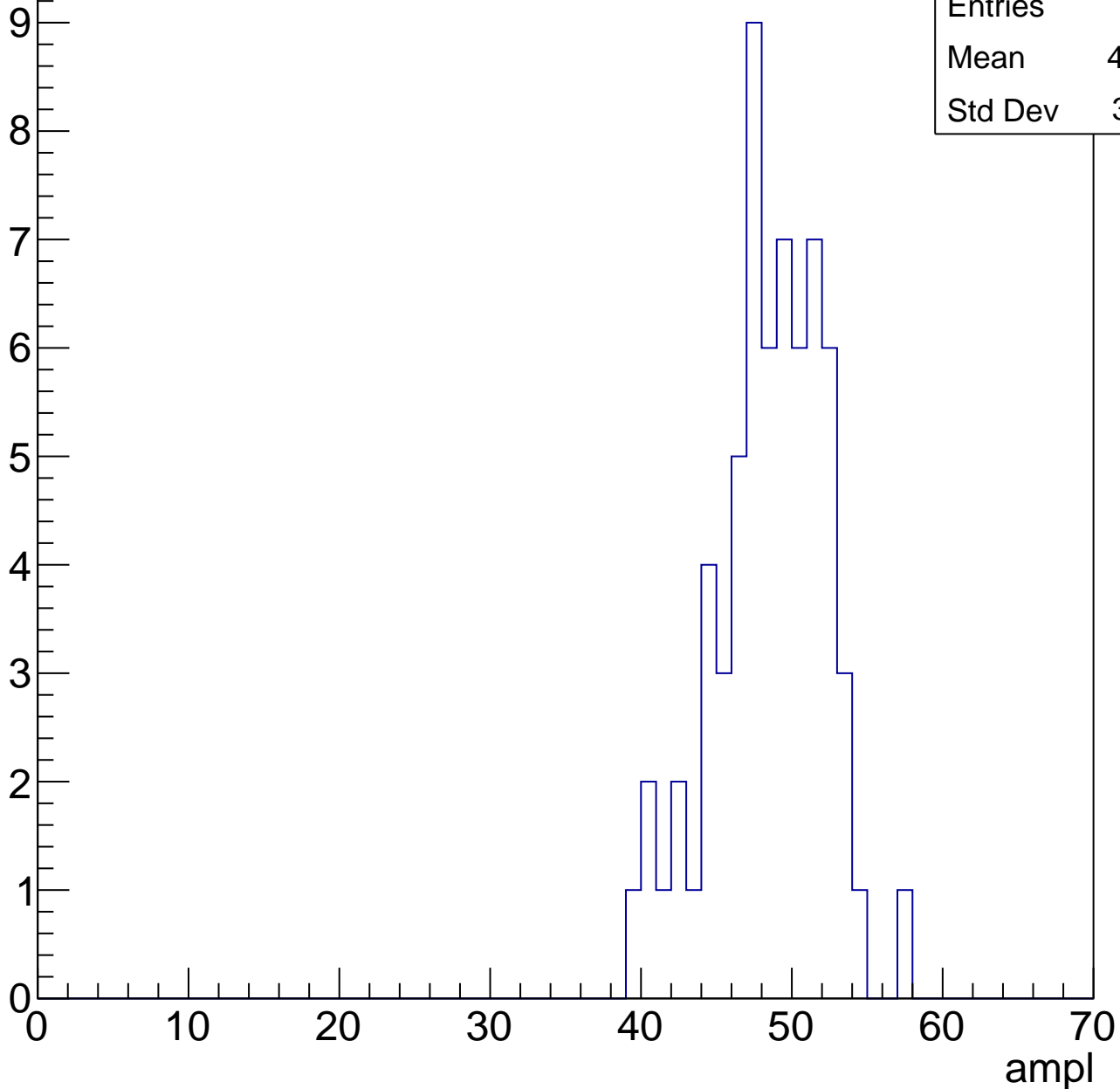


# B0L102S, U17-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

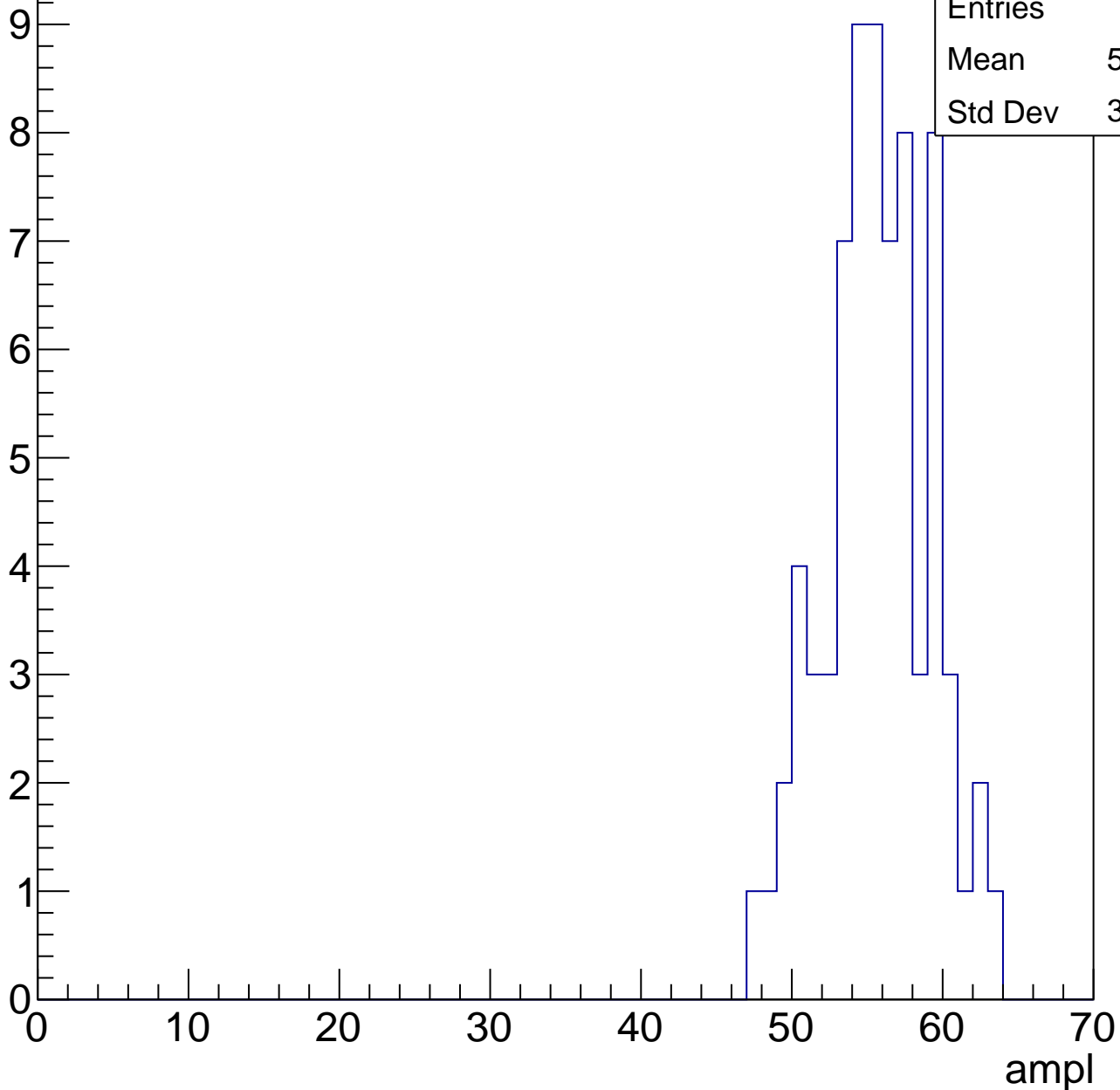
Entries	65
Mean	48.02
Std Dev	3.631



# B0L102S, U17-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



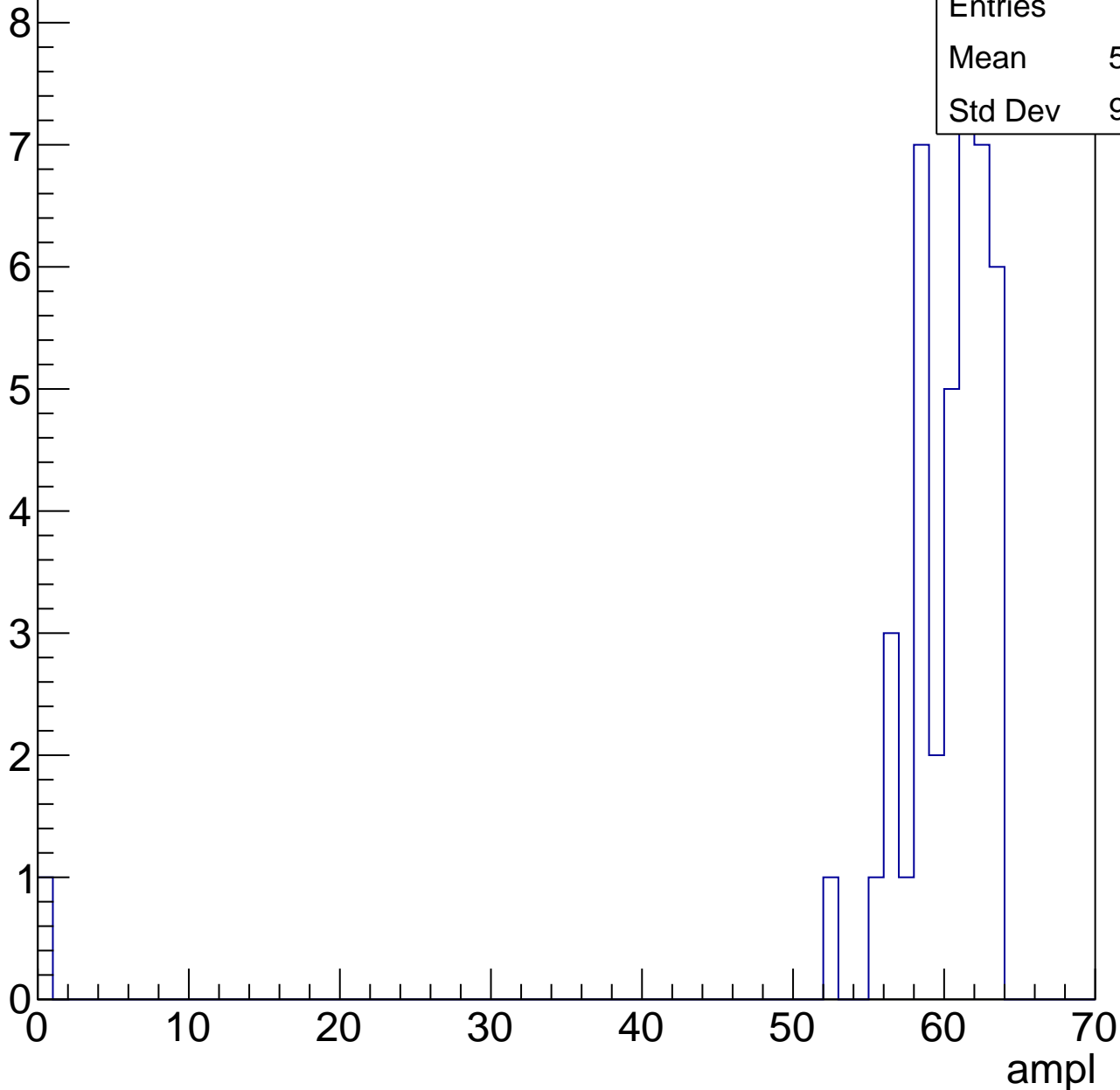
Entries	72
Mean	55.22
Std Dev	3.457

# B0L102S, U17-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

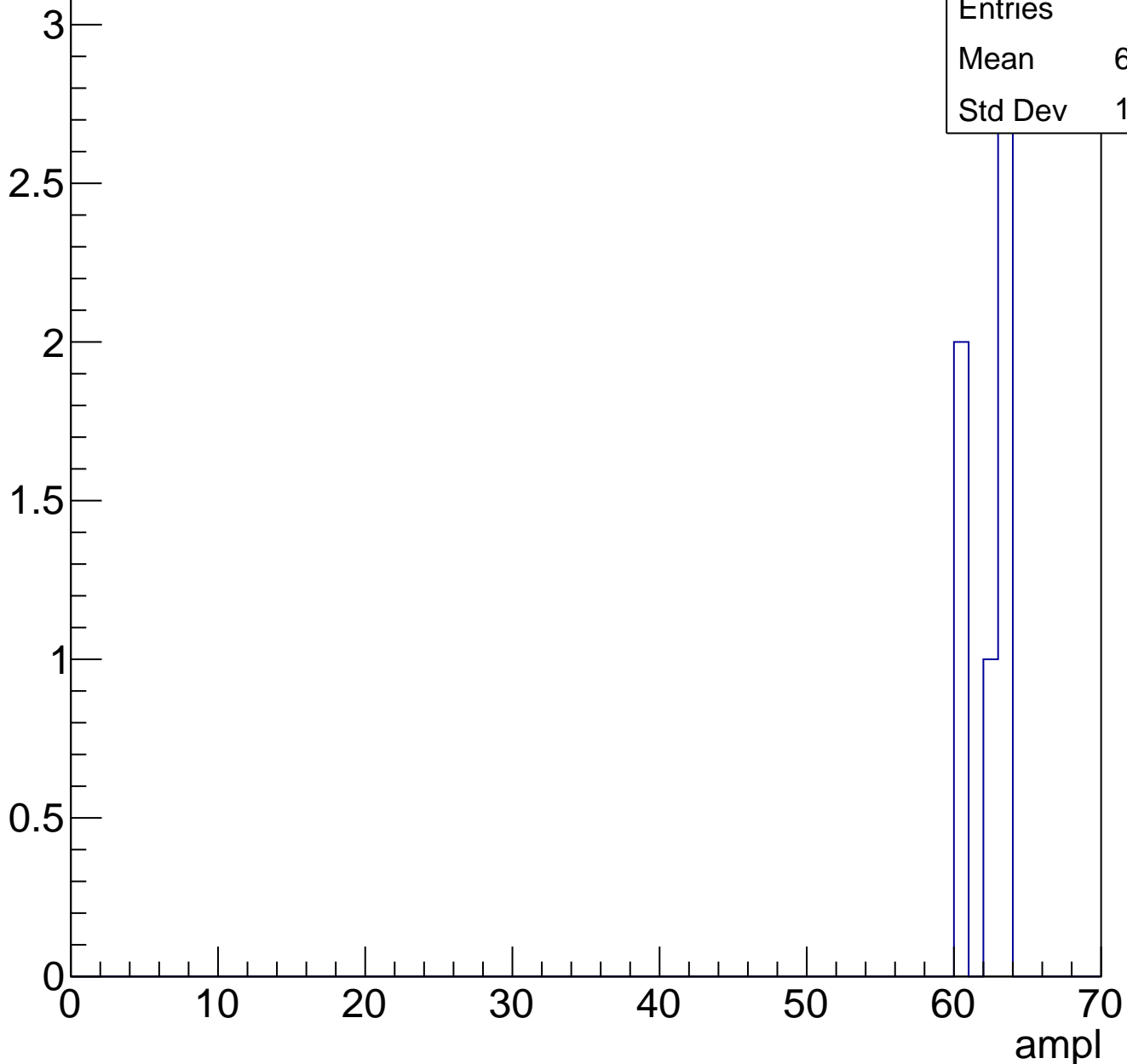
Entries	42
Mean	58.48
Std Dev	9.472



# B0L102S, U17-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch100, adc0

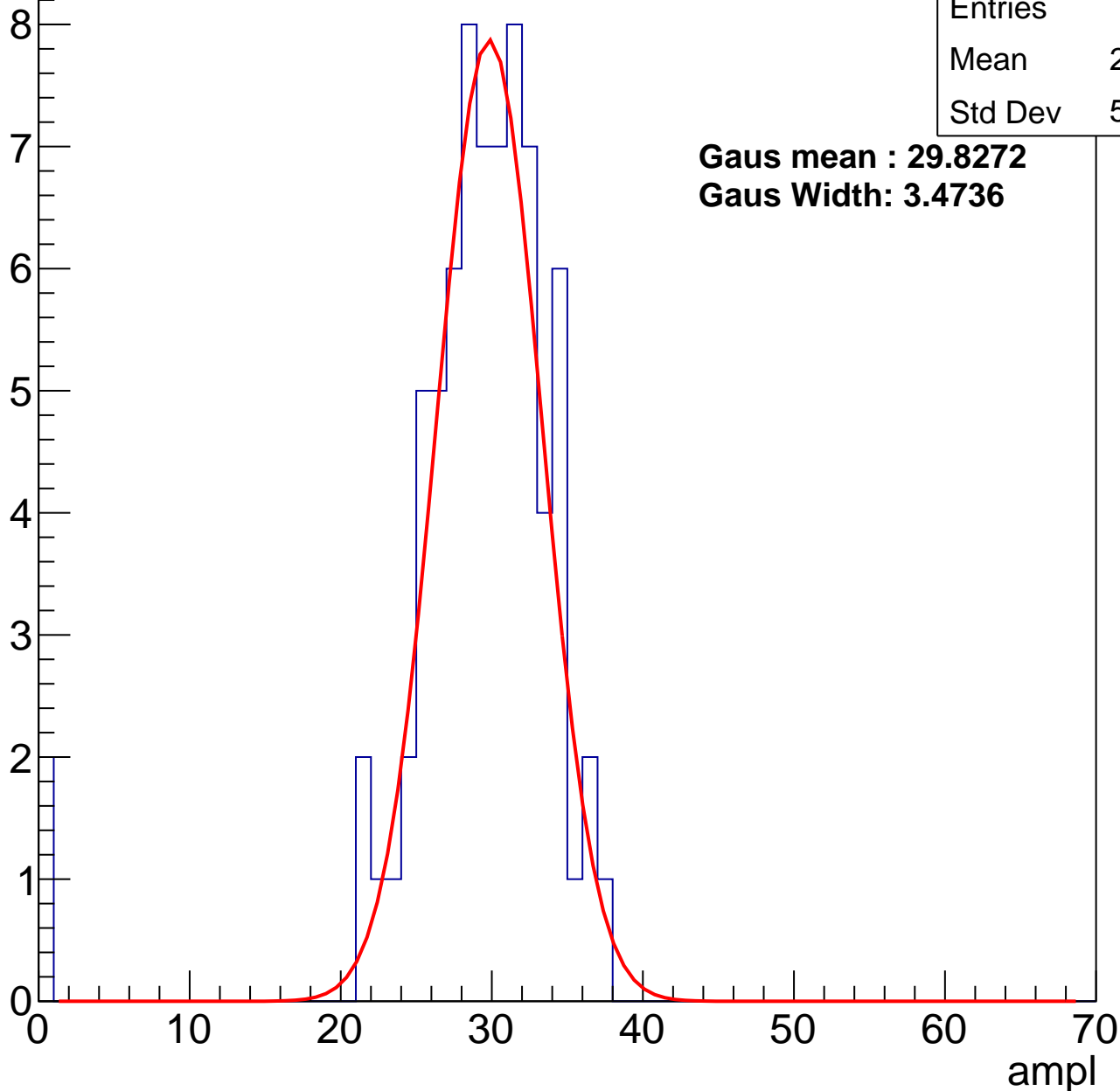
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	28.55
Std Dev	5.889

**Gaus mean : 29.8272**

**Gaus Width: 3.4736**



# B0L102S, U17-ch100, adc1

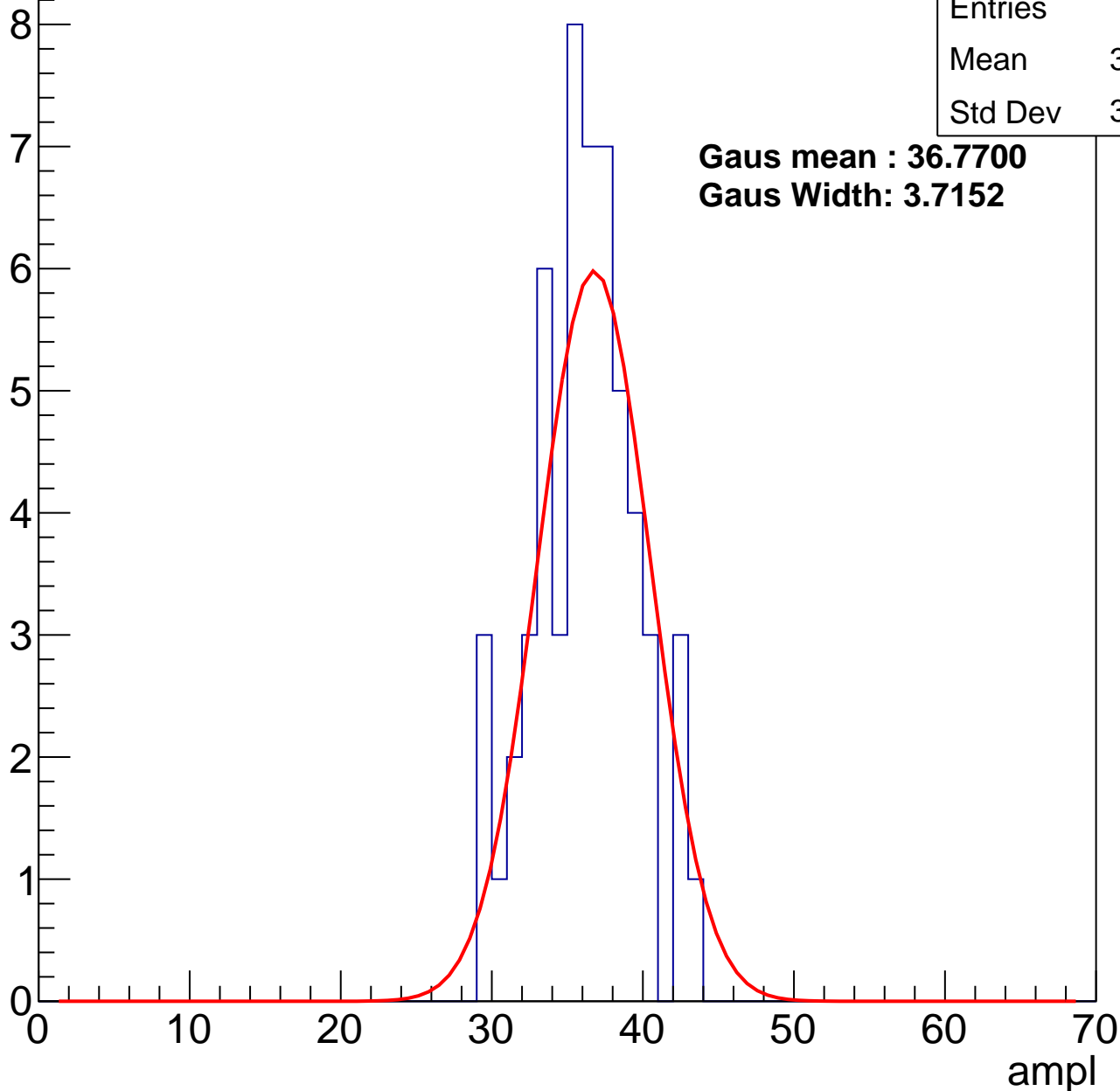
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	35.73
Std Dev	3.325

**Gaus mean : 36.7700**

**Gaus Width: 3.7152**



# B0L102S, U17-ch100, adc2

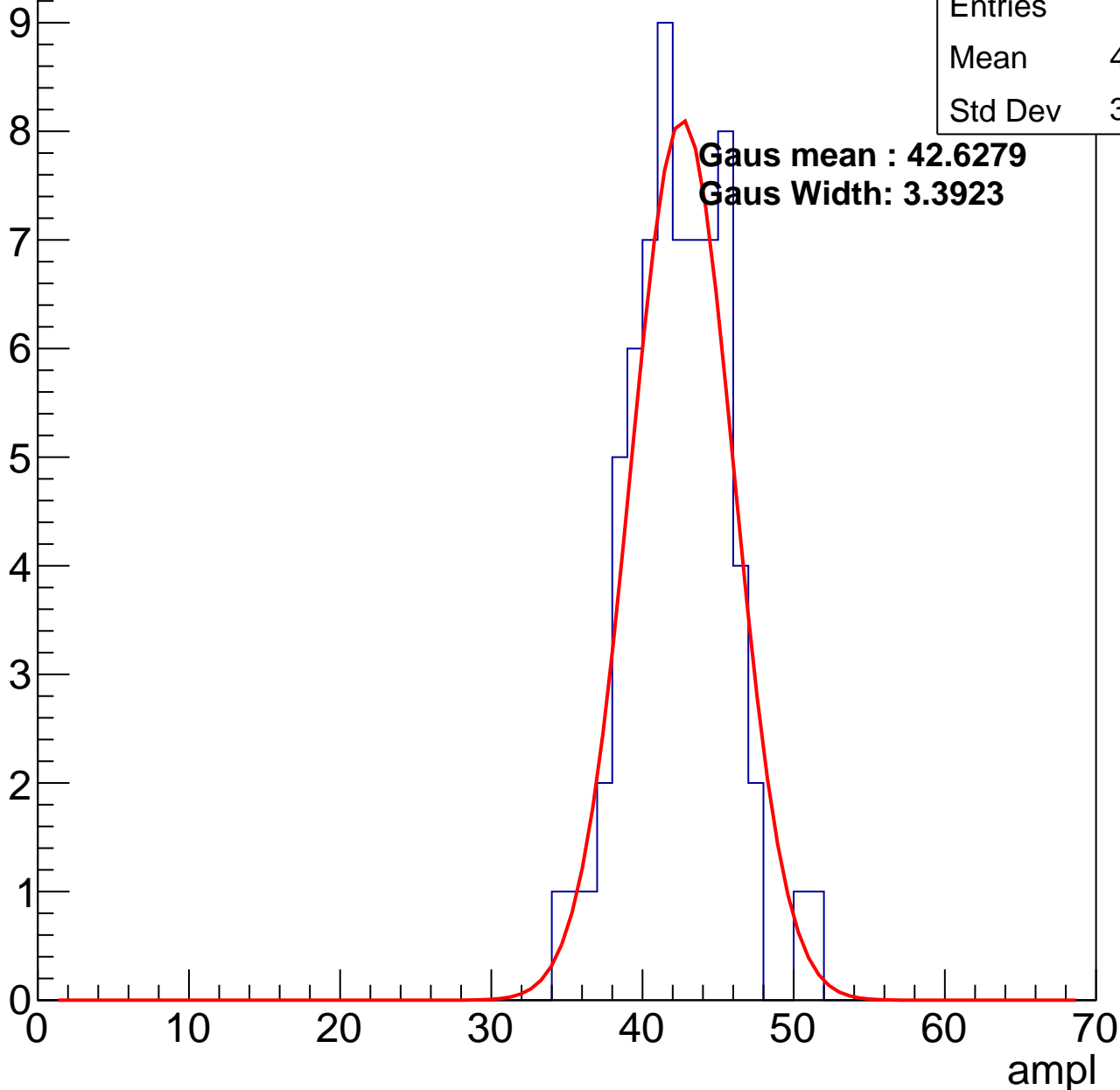
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	41.94
Std Dev	3.265

**Gaus mean : 42.6279**

**Gaus Width: 3.3923**

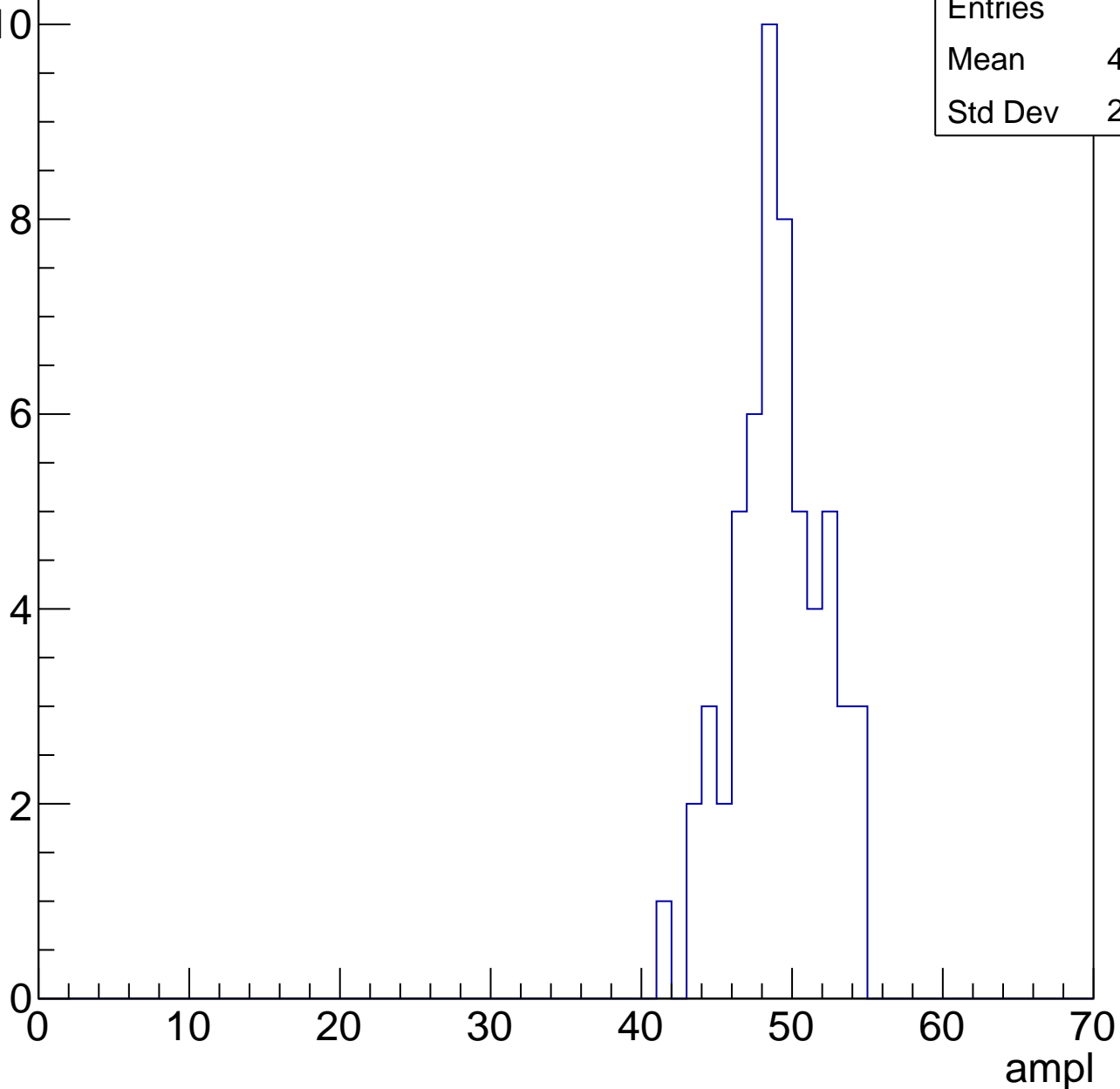


# B0L102S, U17-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.56
Std Dev	2.962

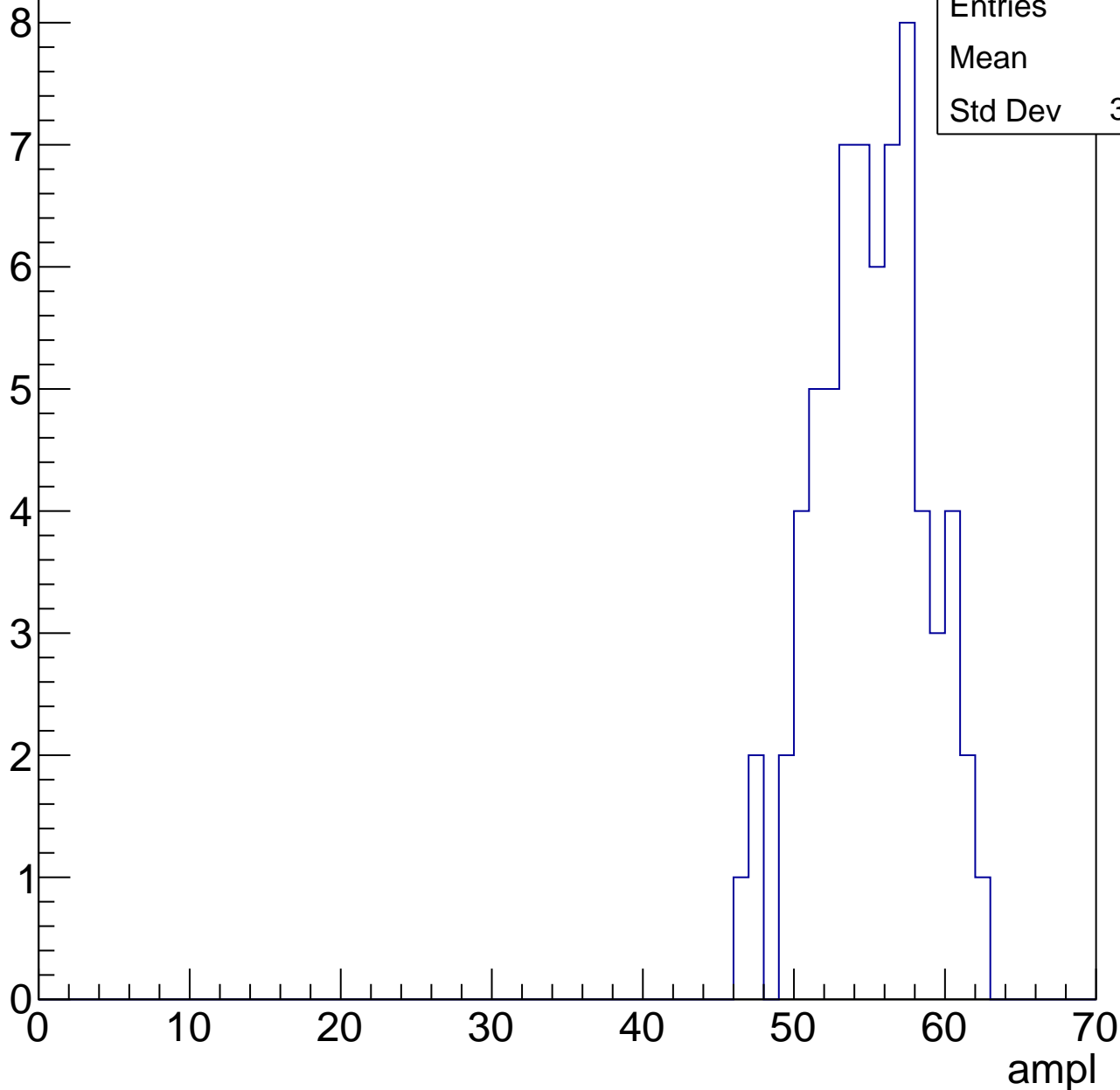


# B0L102S, U17-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	54.6
Std Dev	3.586

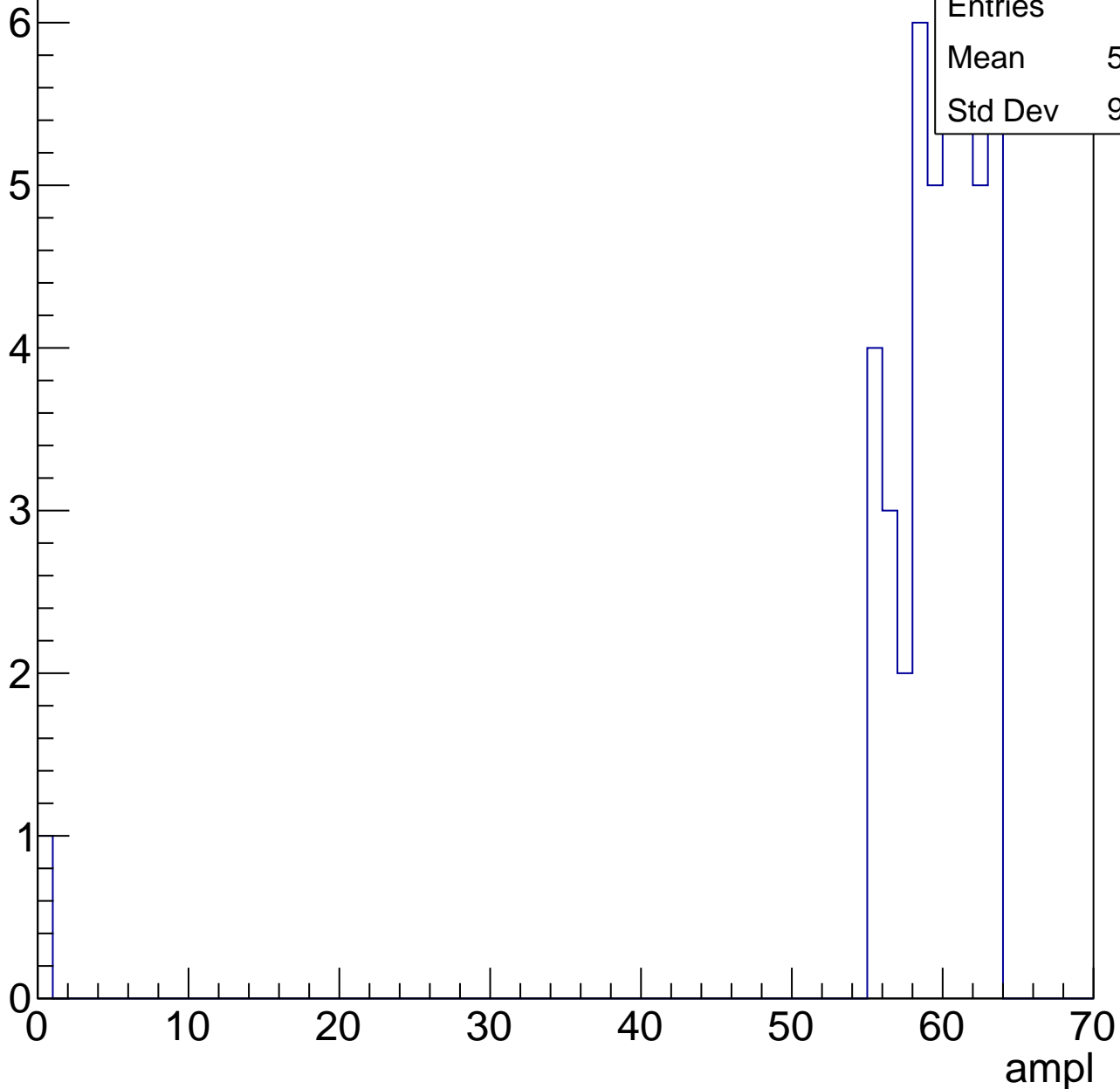


# B0L102S, U17-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

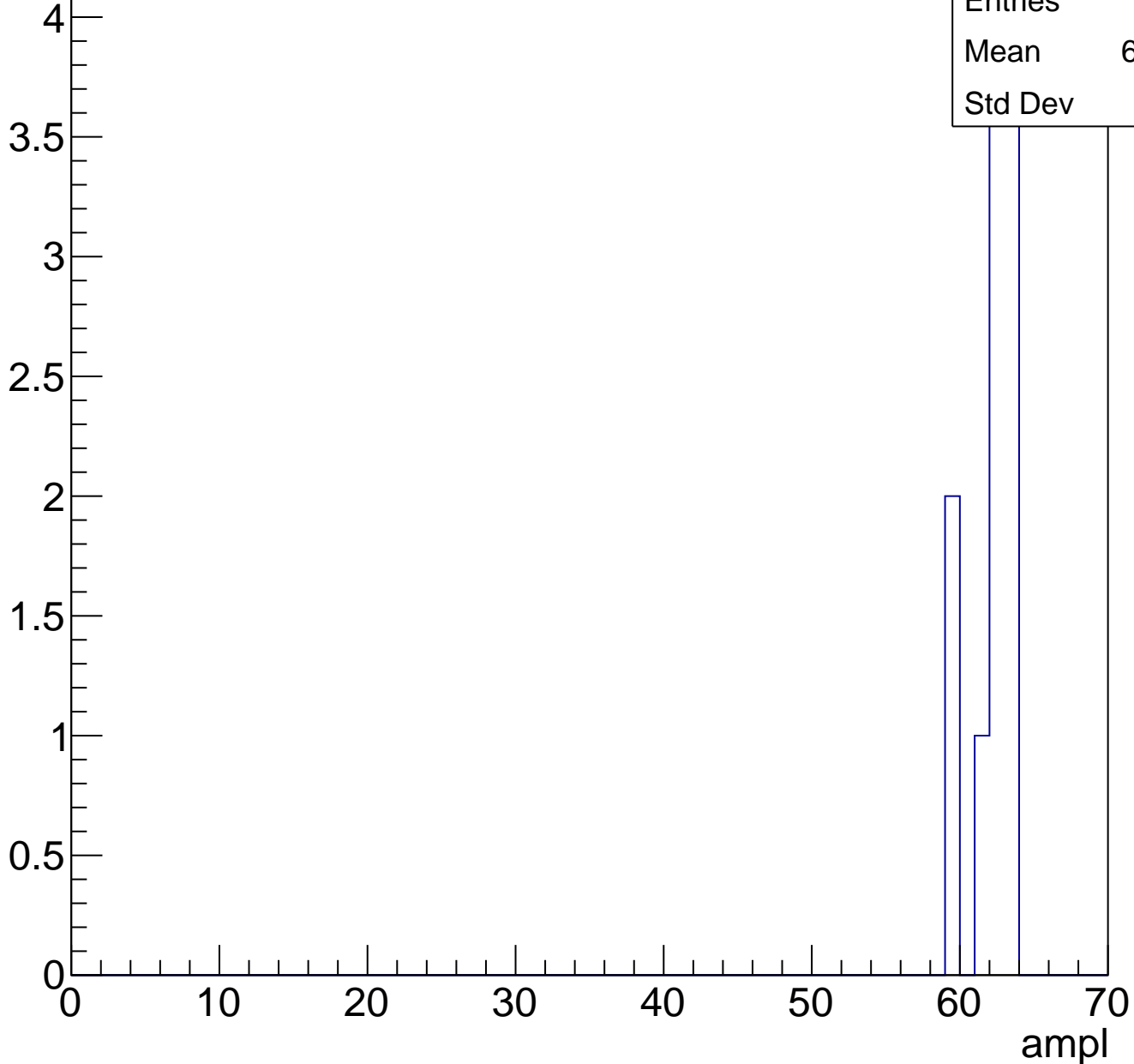
Entries	44
Mean	58.16
Std Dev	9.202



# B0L102S, U17-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	20.33
Std Dev	28.76

# B0L102S, U17-ch101, adc0

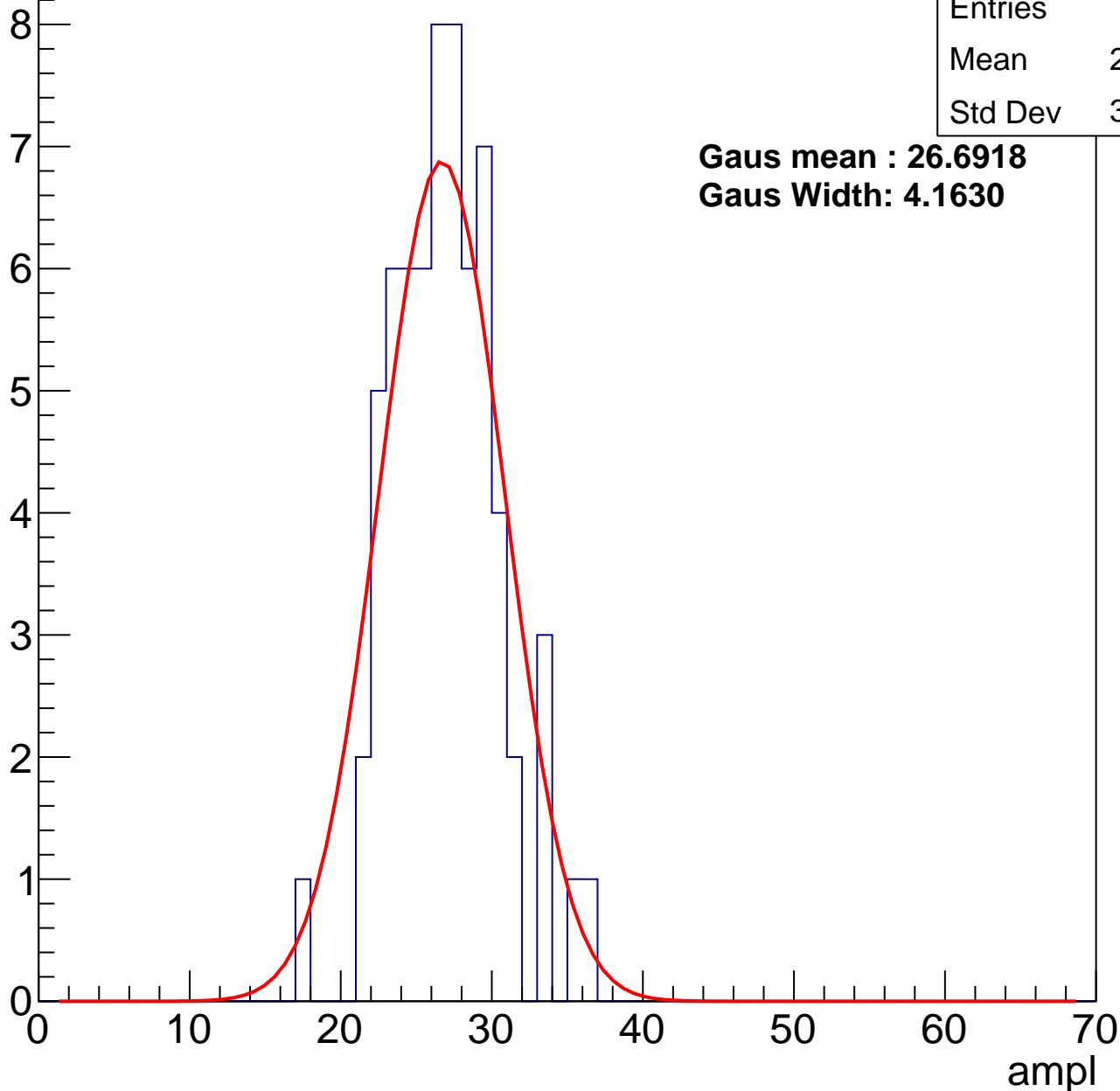
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	26.48
Std Dev	3.517

**Gaus mean : 26.6918**

**Gaus Width: 4.1630**



# B0L102S, U17-ch101, adc1

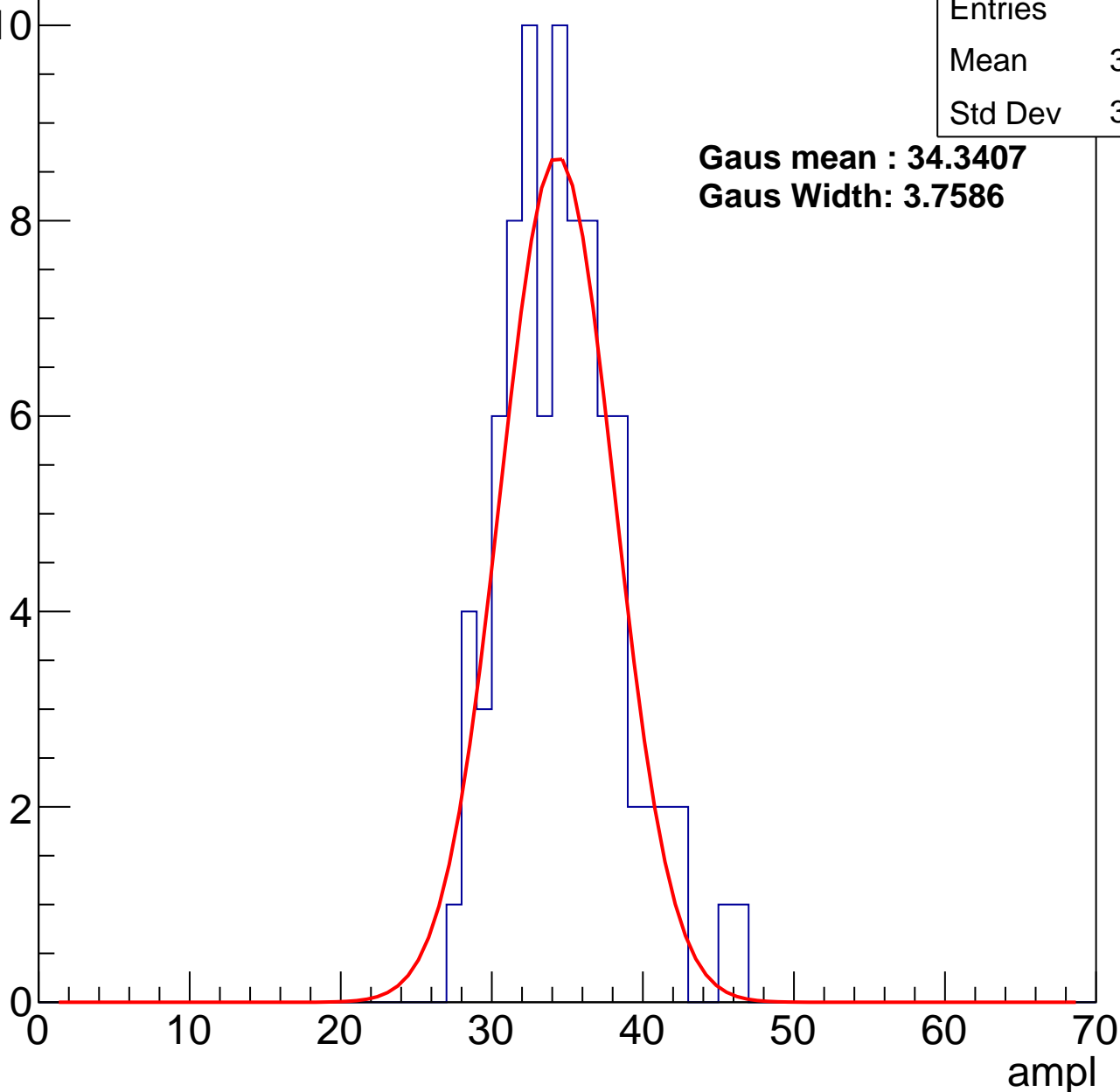
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	86
Mean	34.24
Std Dev	3.858

**Gaus mean : 34.3407**

**Gaus Width: 3.7586**



# B0L102S, U17-ch101, adc2

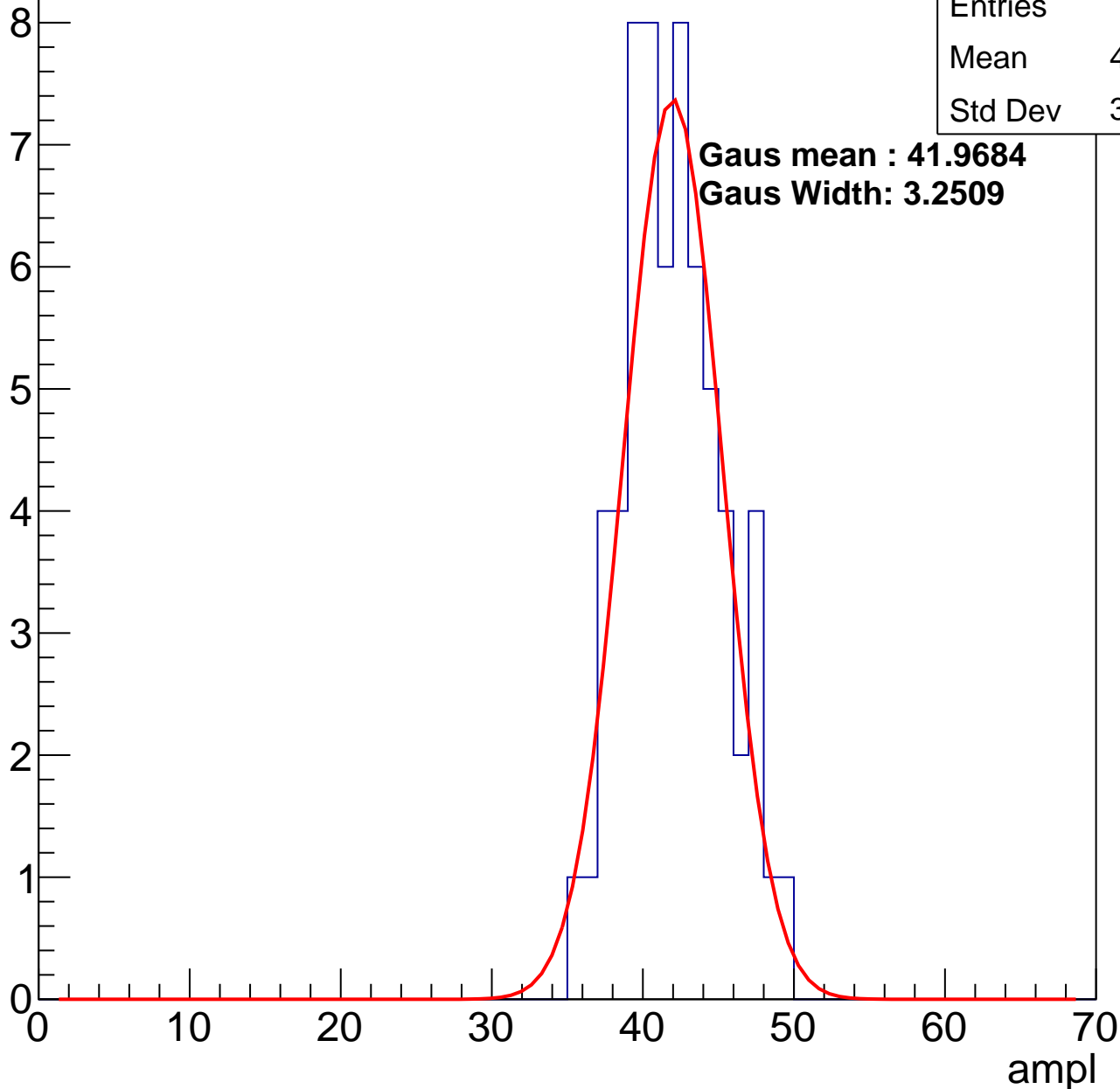
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	41.59
Std Dev	3.155

**Gaus mean : 41.9684**

**Gaus Width: 3.2509**

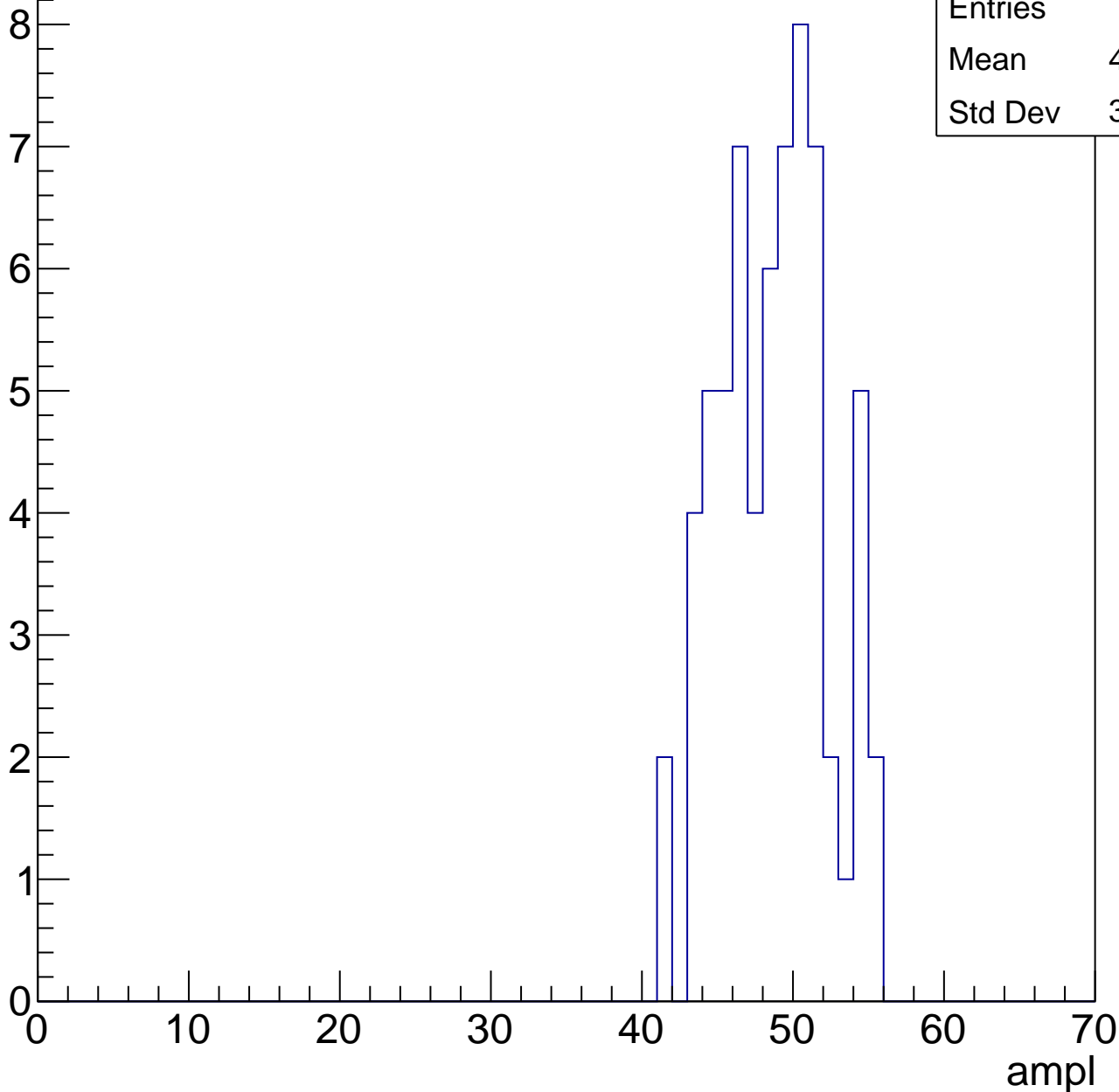


# B0L102S, U17-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

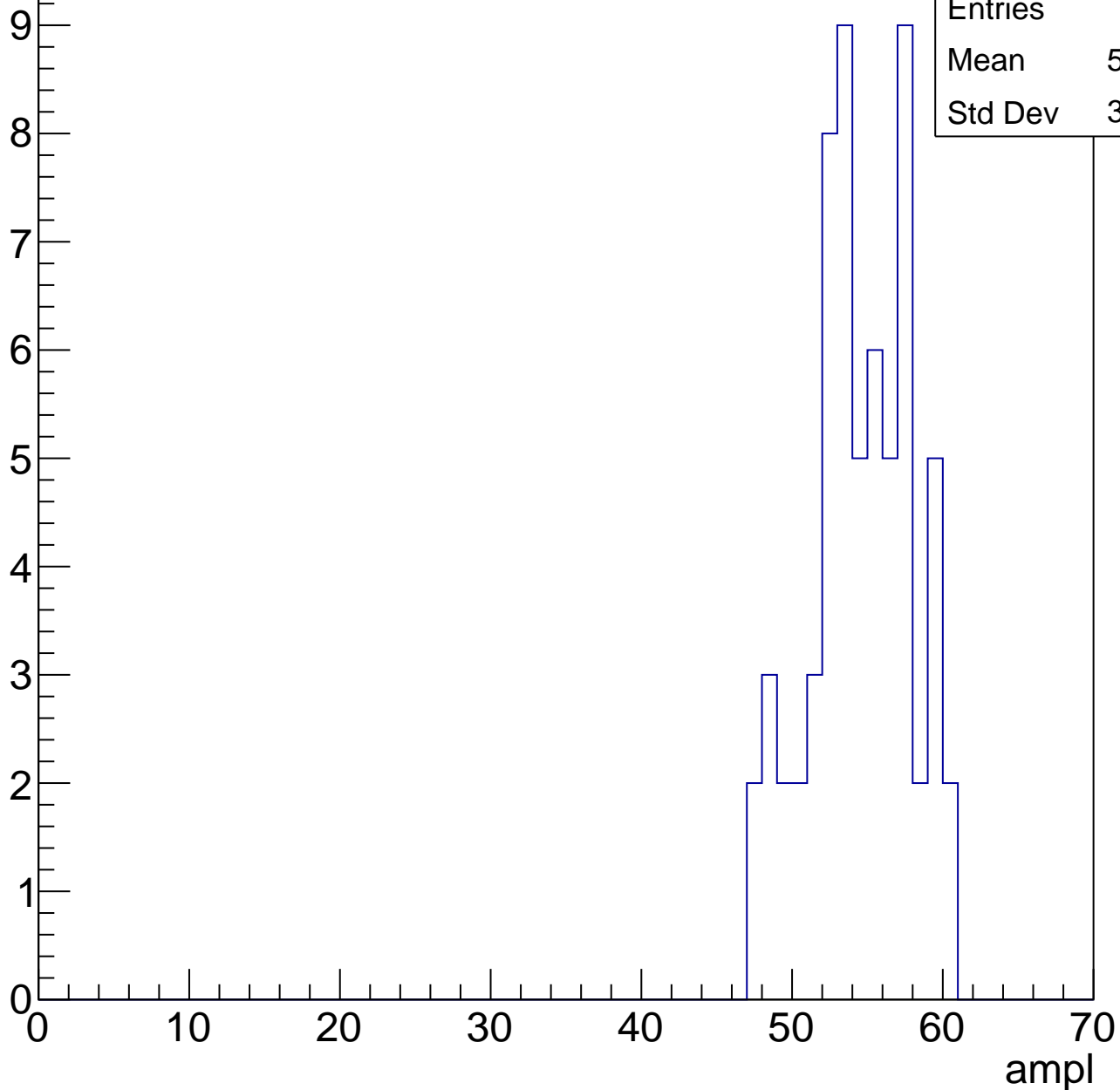
Entries	65
Mean	48.22
Std Dev	3.484



# B0L102S, U17-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



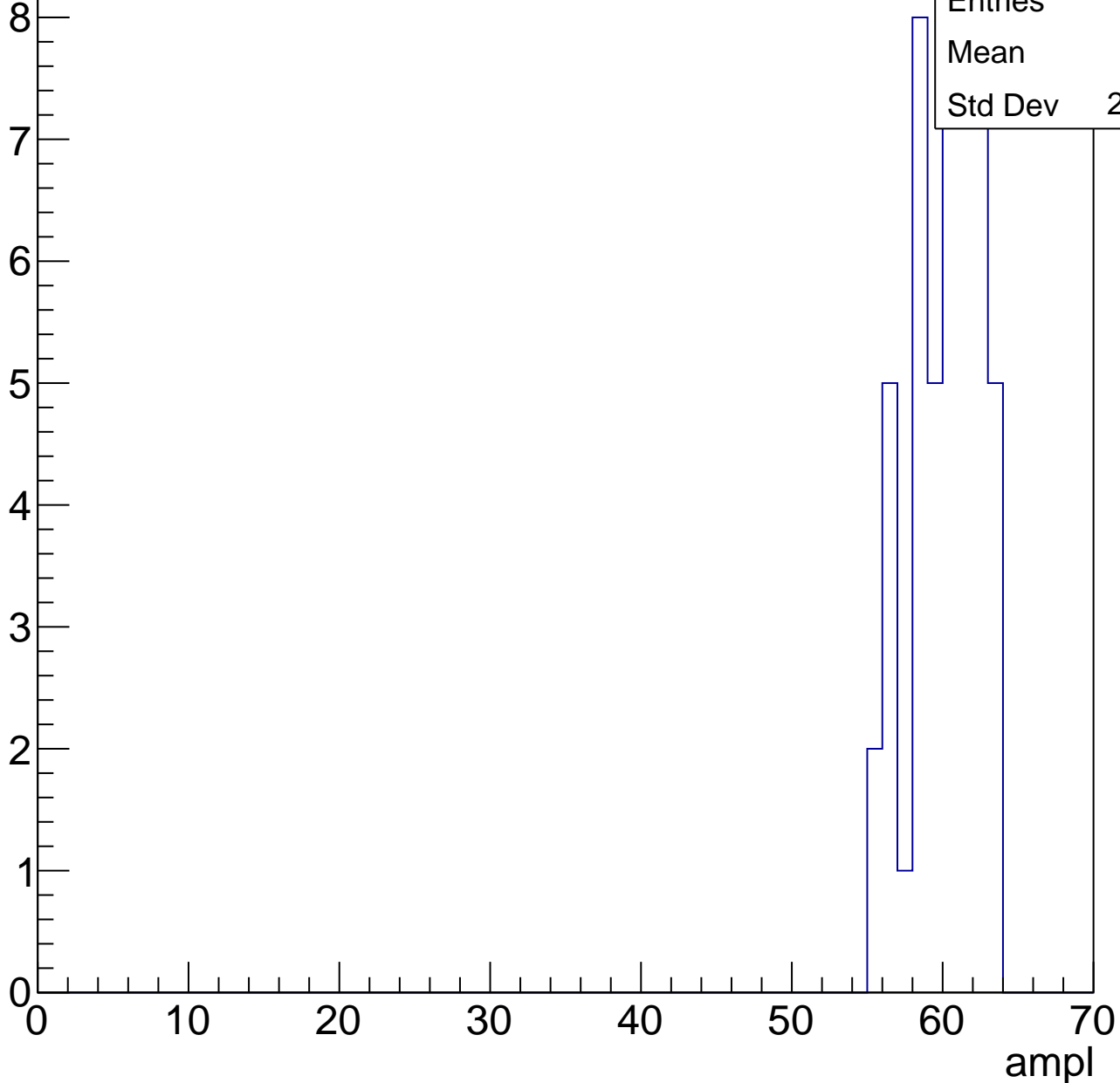
Entries	63
Mean	54.06
Std Dev	3.314

# B0L102S, U17-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	59.7
Std Dev	2.265

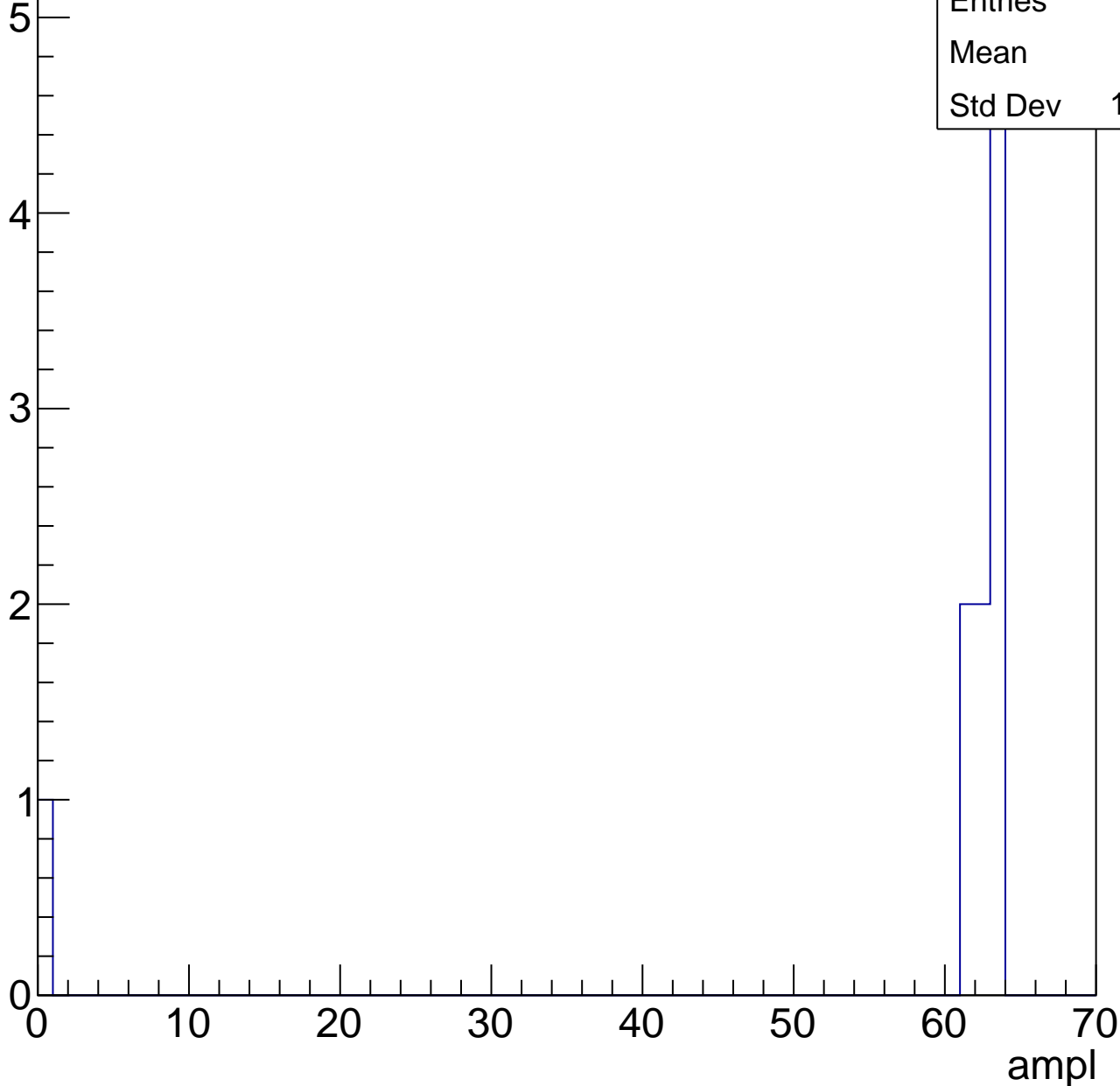


# B0L102S, U17-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	10
Mean	56.1
Std Dev	18.72

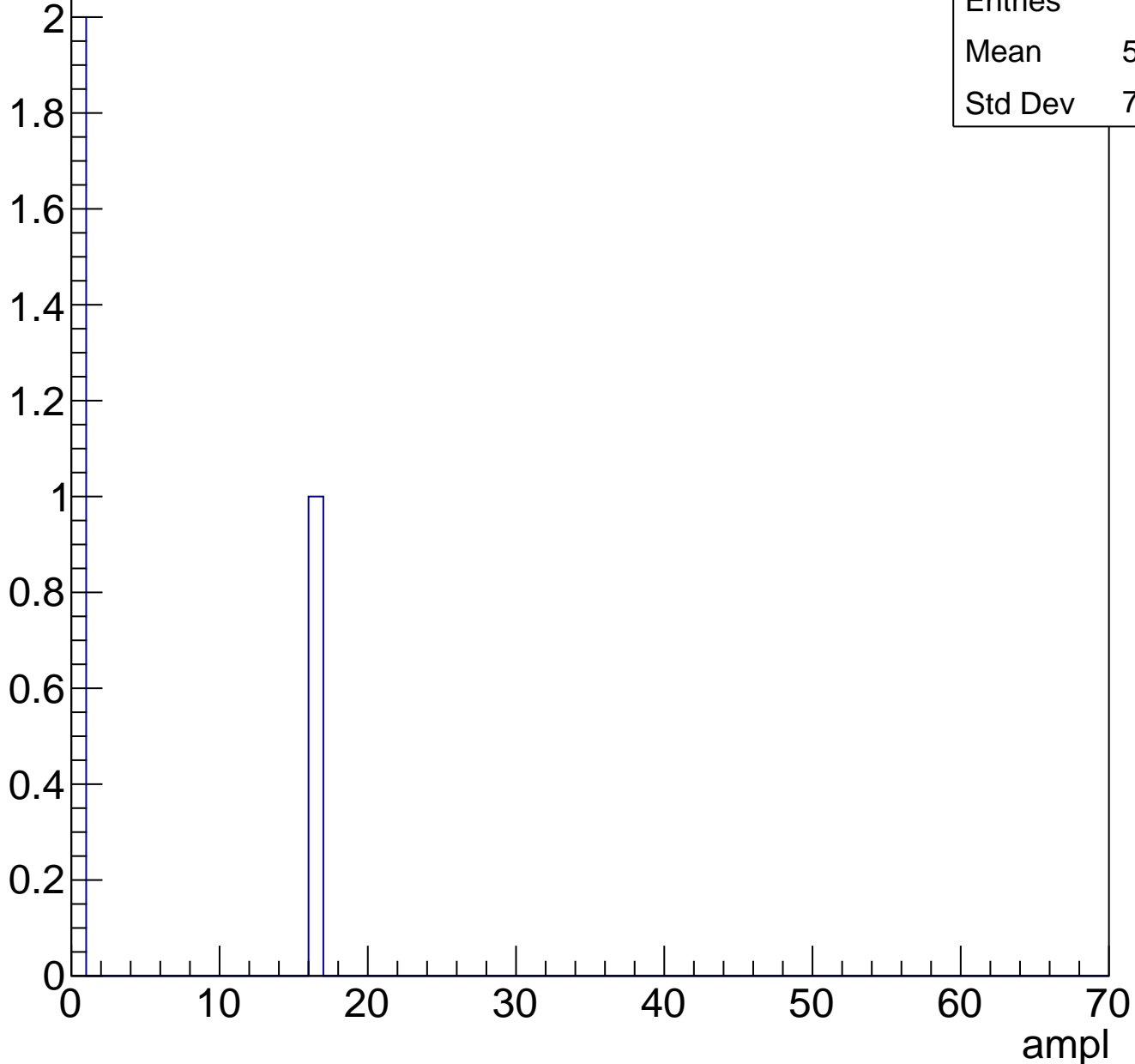




# B0L102S, U17-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	5.333
Std Dev	7.542

# B0L102S, U17-ch102, adc0

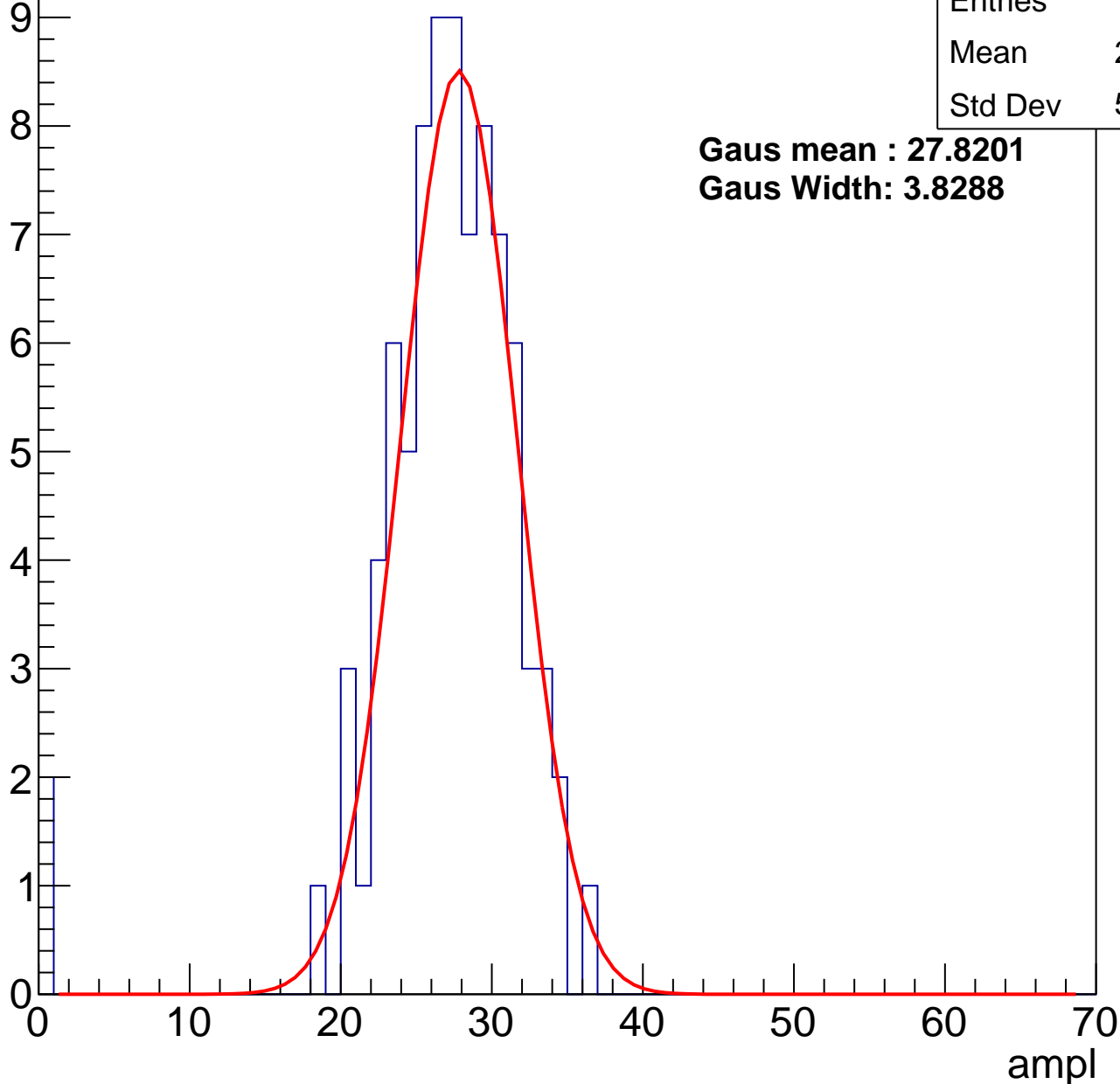
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	26.41
Std Dev	5.461

**Gaus mean : 27.8201**

**Gaus Width: 3.8288**



# B0L102S, U17-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	77
Mean	34.57
Std Dev	3.473

**Gaus mean : 35.0208**

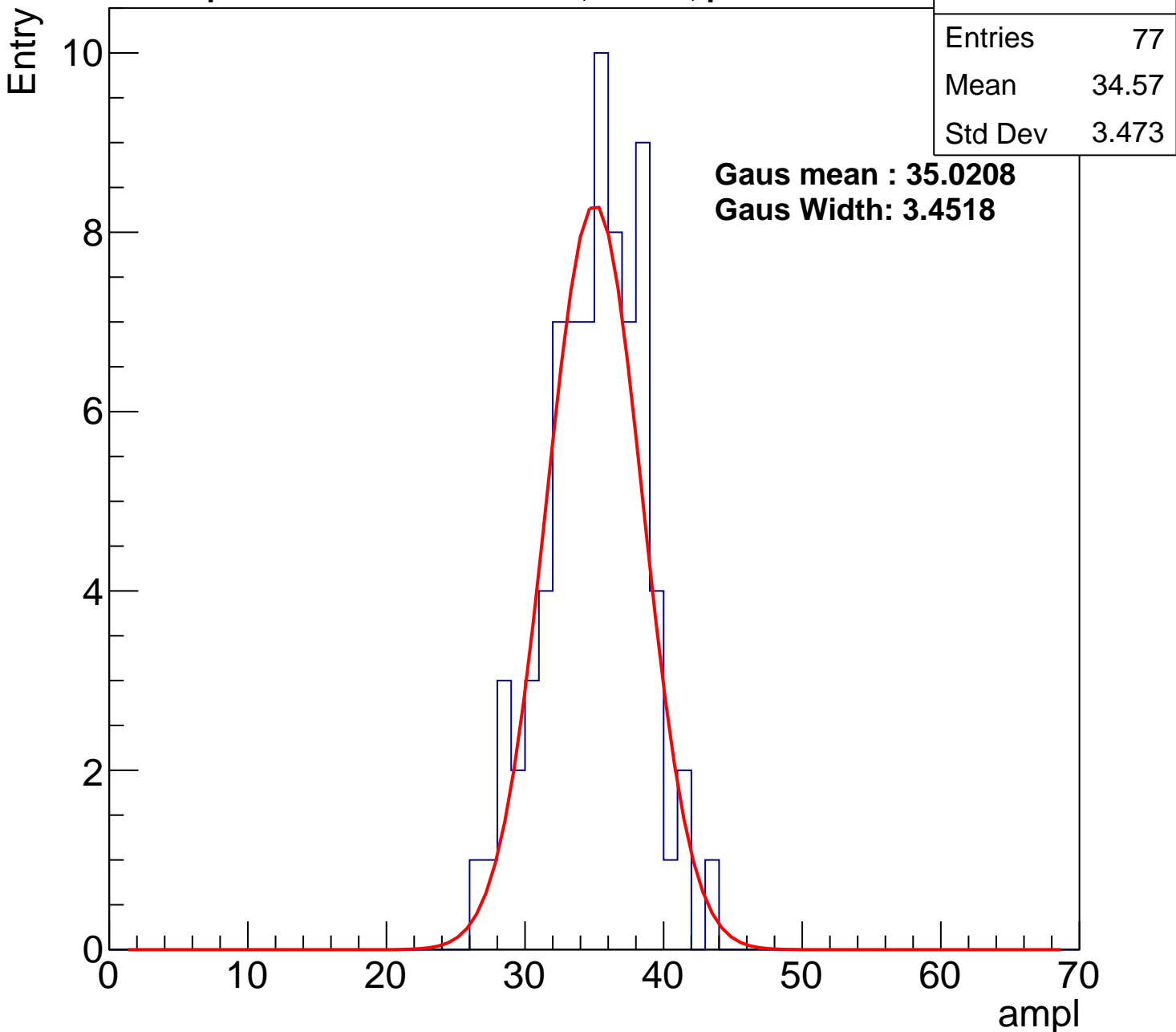
**Gaus Width: 3.4518**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch102, adc2

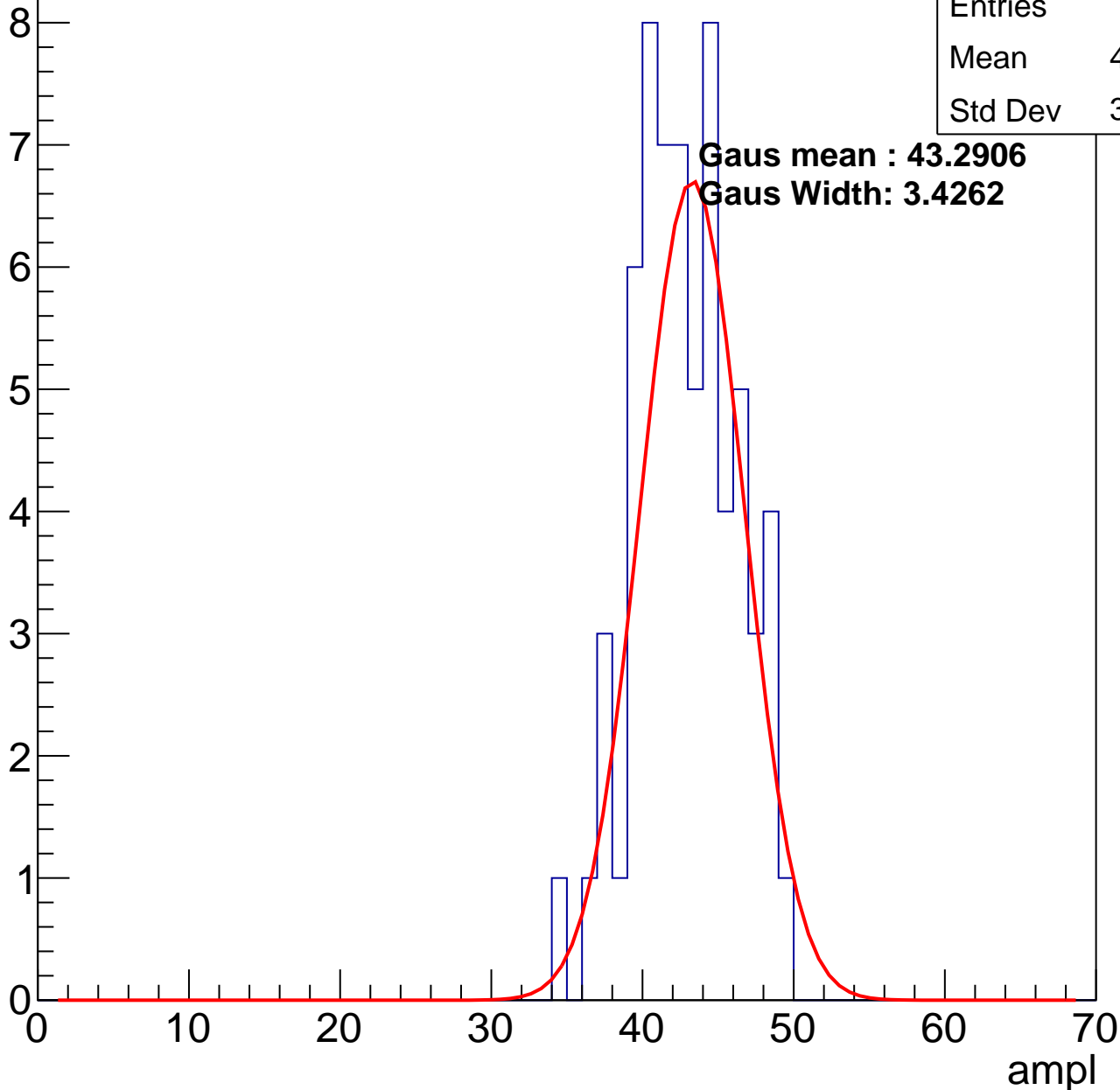
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.39
Std Dev	3.296

**Gaus mean : 43.2906**

**Gaus Width: 3.4262**

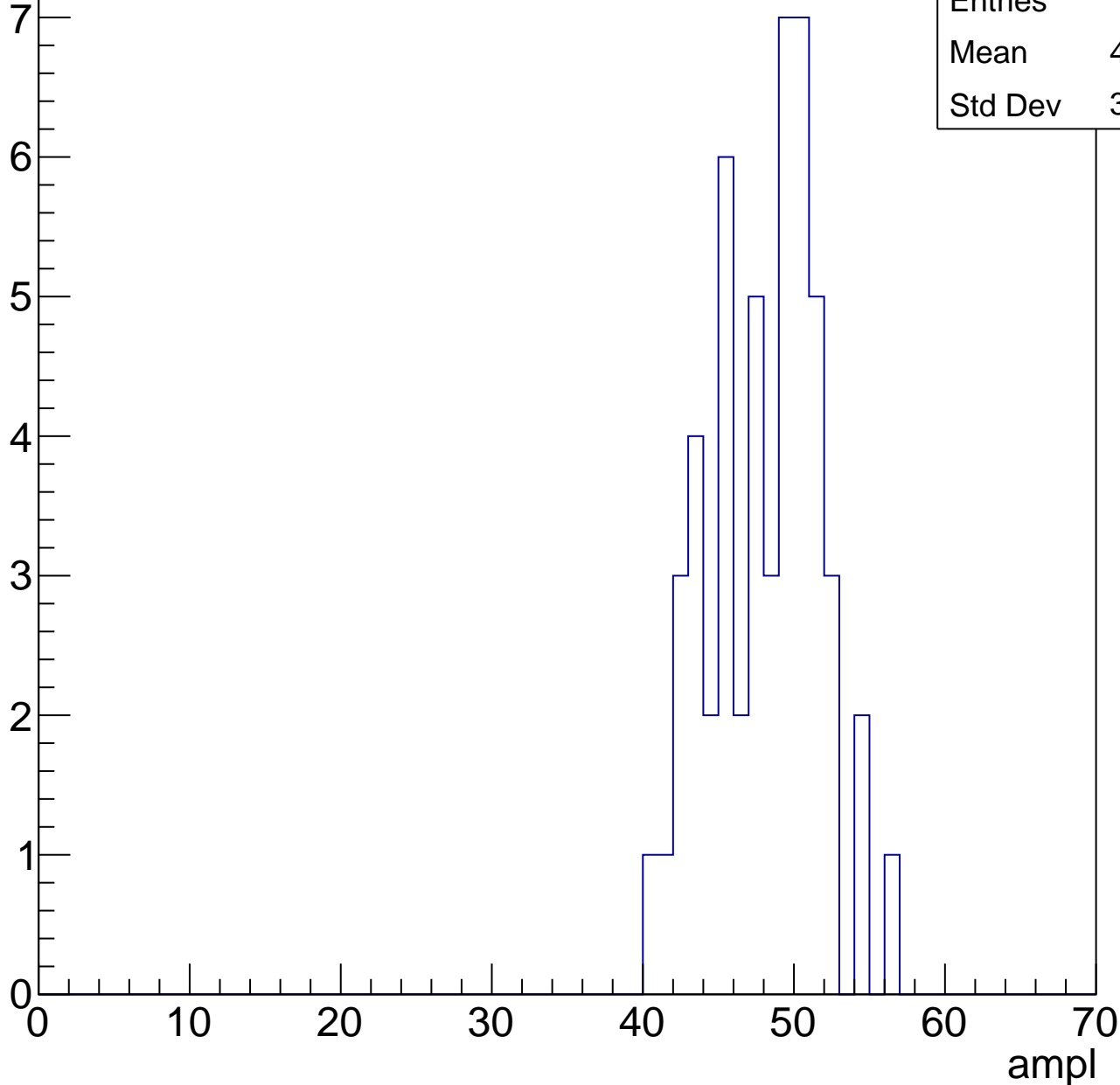


# B0L102S, U17-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	52
Mean	47.62
Std Dev	3.606



# B0L102S, U17-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

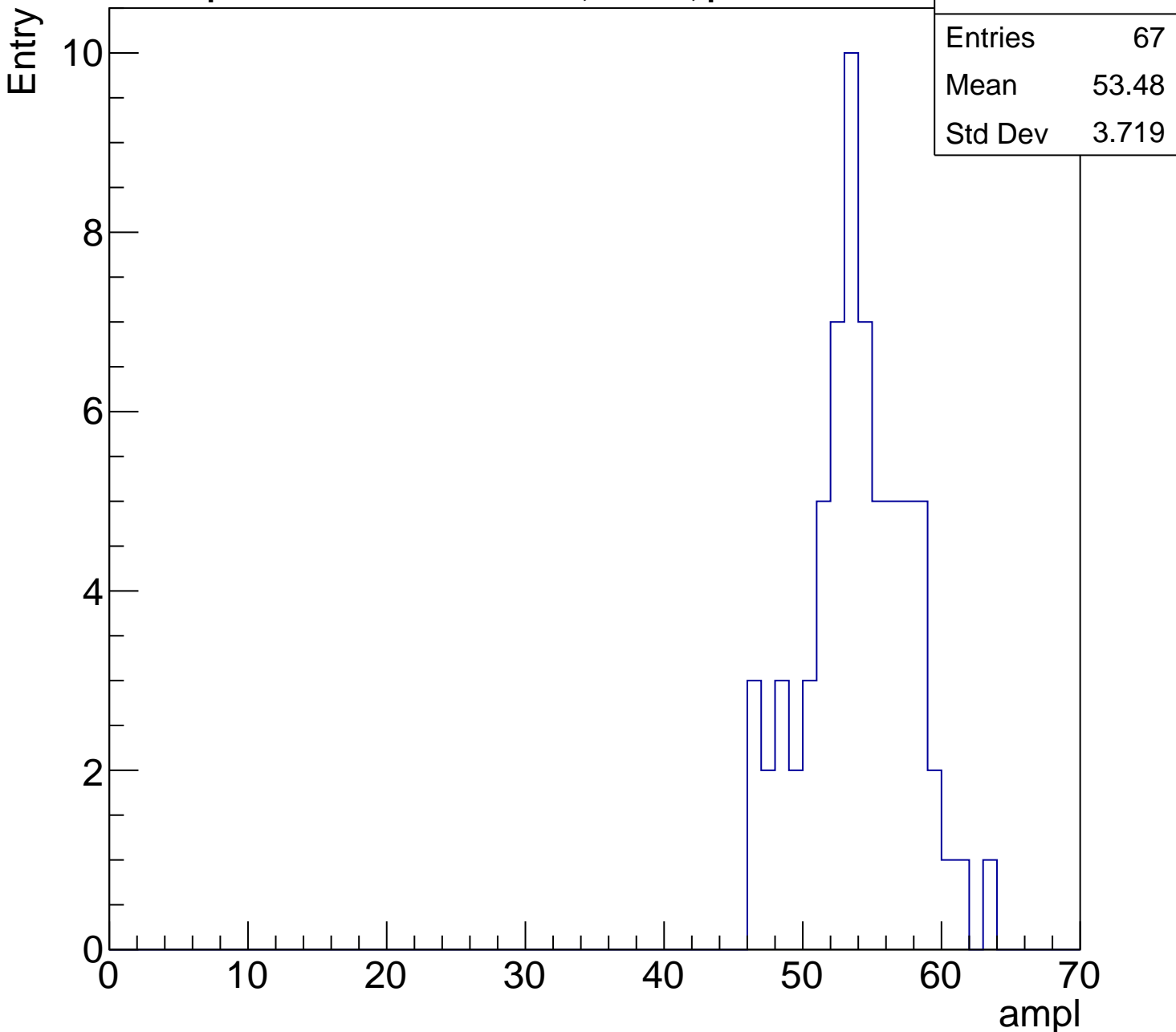
Entries	67
Mean	53.48
Std Dev	3.719

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

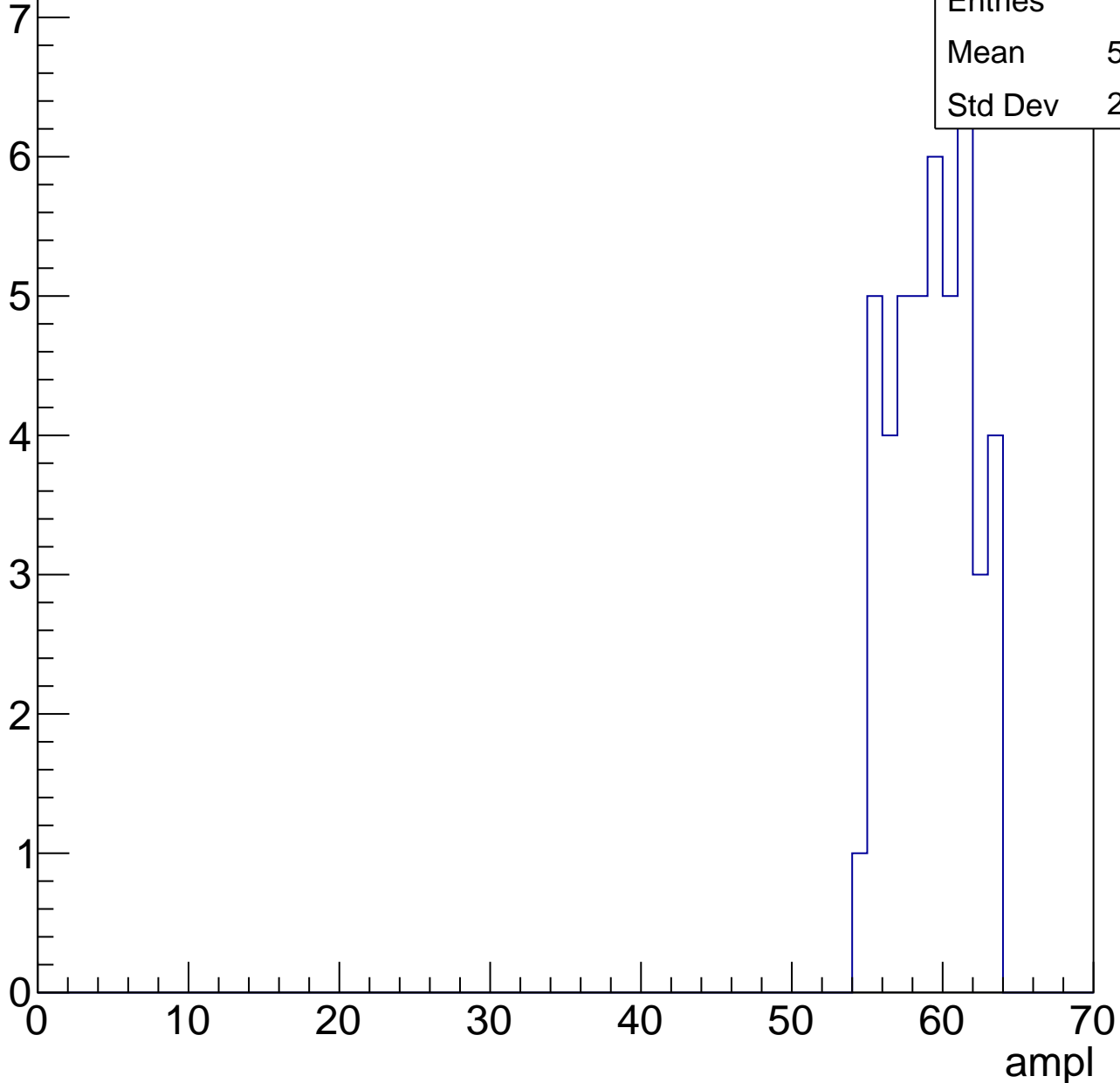


# B0L102S, U17-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	45
Mean	58.82
Std Dev	2.532

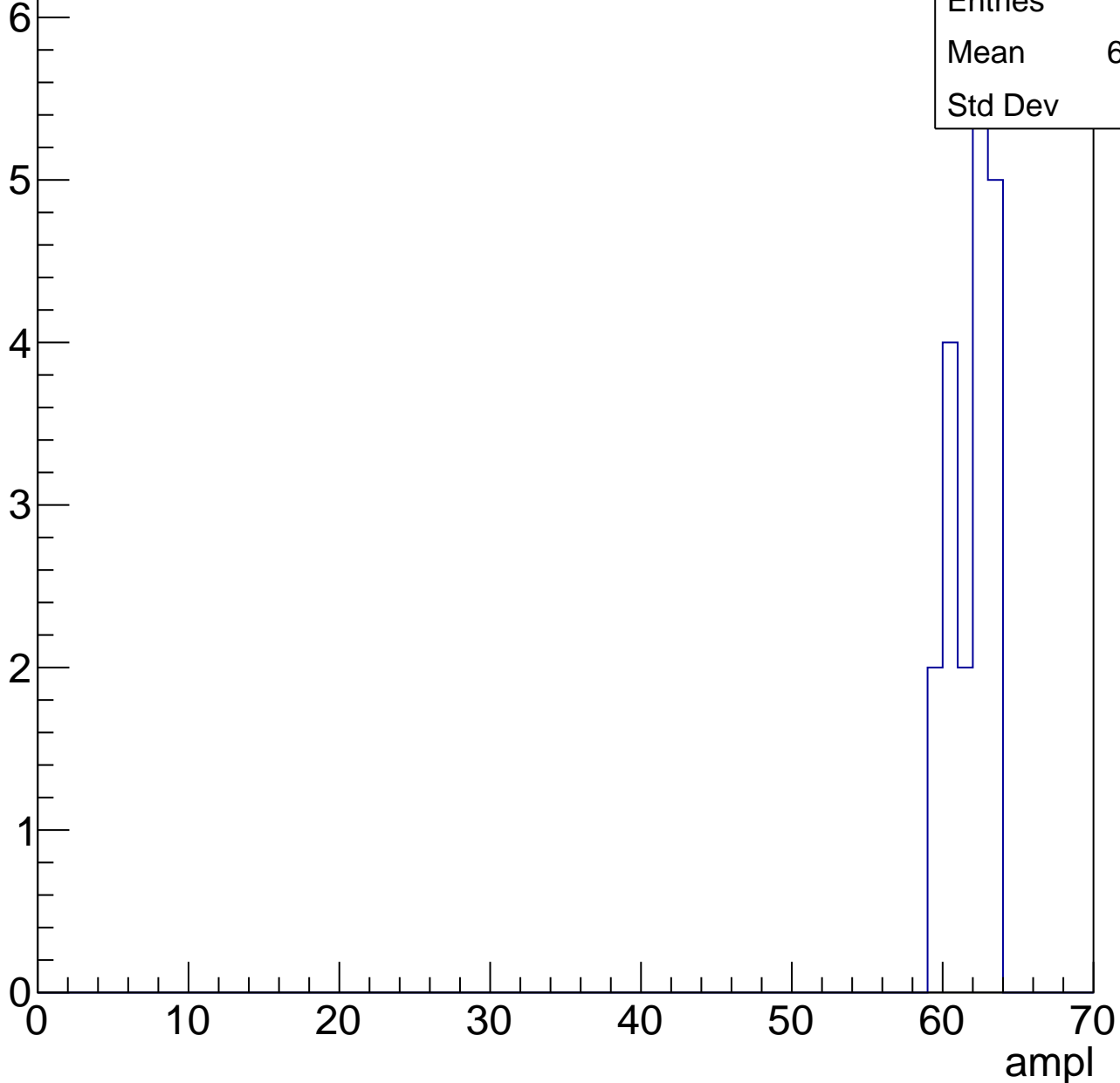


# B0L102S, U17-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	19
Mean	61.42
Std Dev	1.35

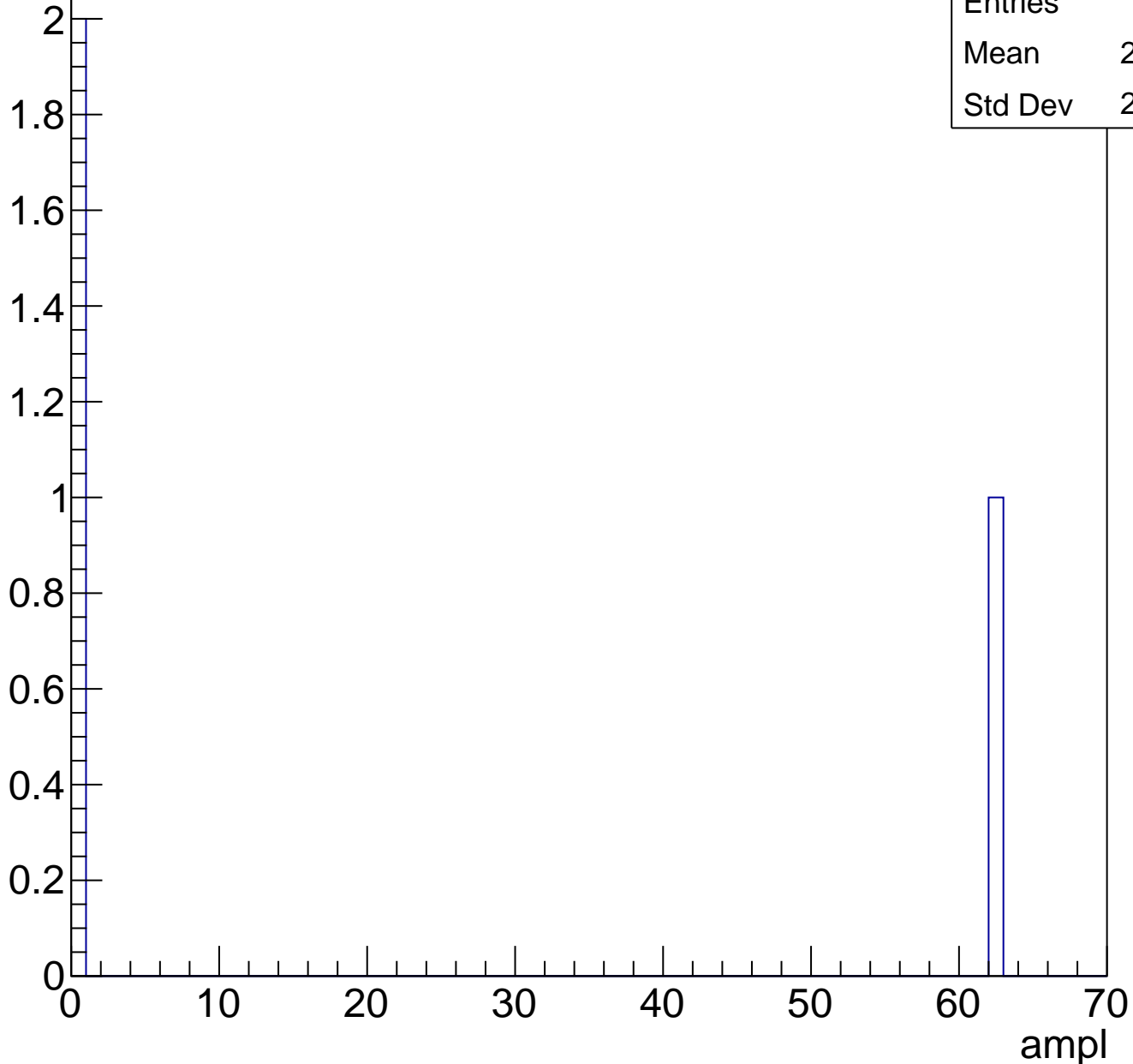




# B0L102S, U17-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L102S, U17-ch103, adc0

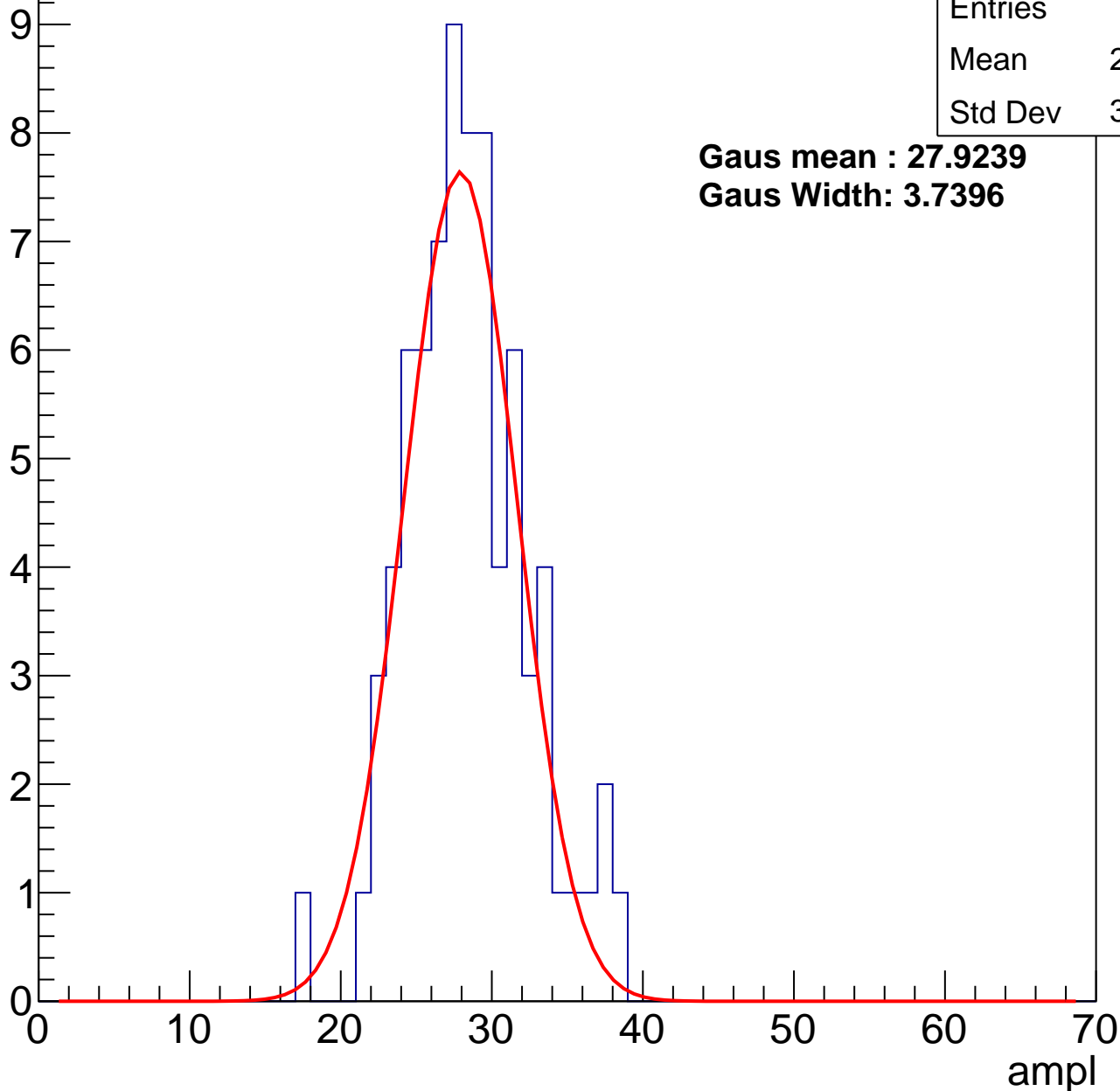
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	27.92
Std Dev	3.976

**Gaus mean : 27.9239**

**Gaus Width: 3.7396**



# B0L102S, U17-ch103, adc1

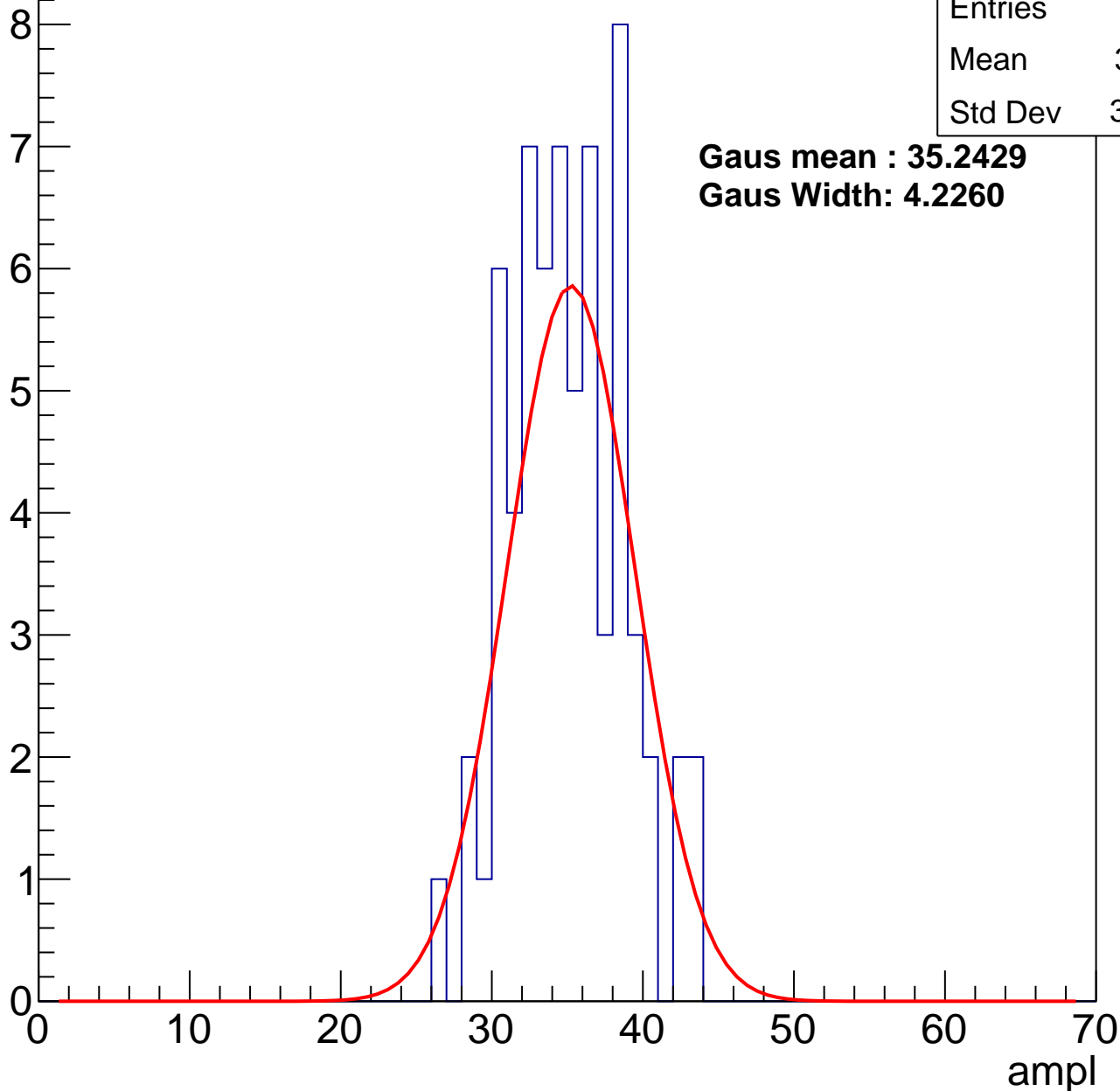
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.61
Std Dev	3.749

**Gaus mean : 35.2429**

**Gaus Width: 4.2260**



# B0L102S, U17-ch103, adc2

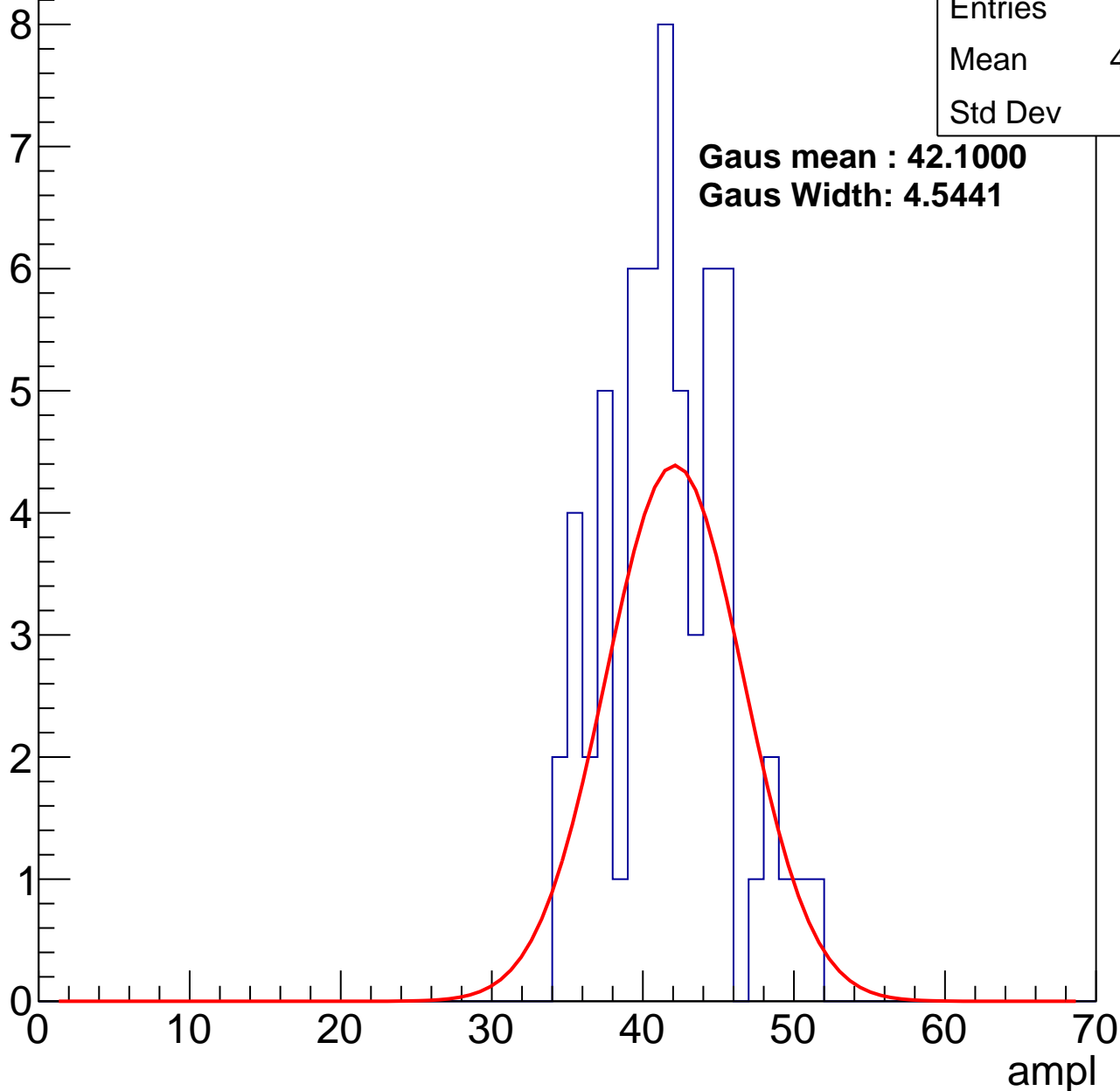
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.18
Std Dev	3.99

**Gaus mean : 42.1000**

**Gaus Width: 4.5441**

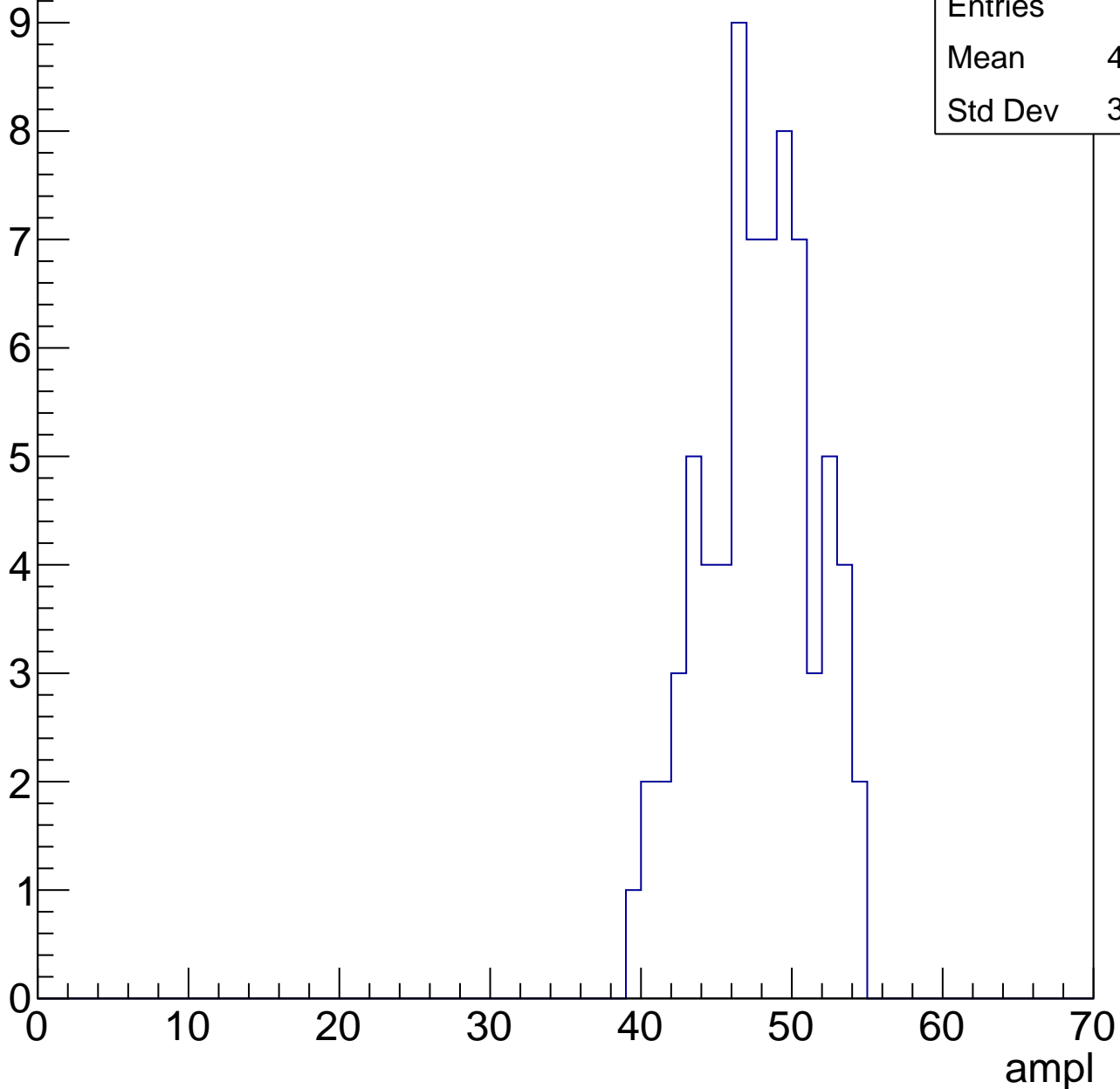


# B0L102S, U17-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	47.29
Std Dev	3.628

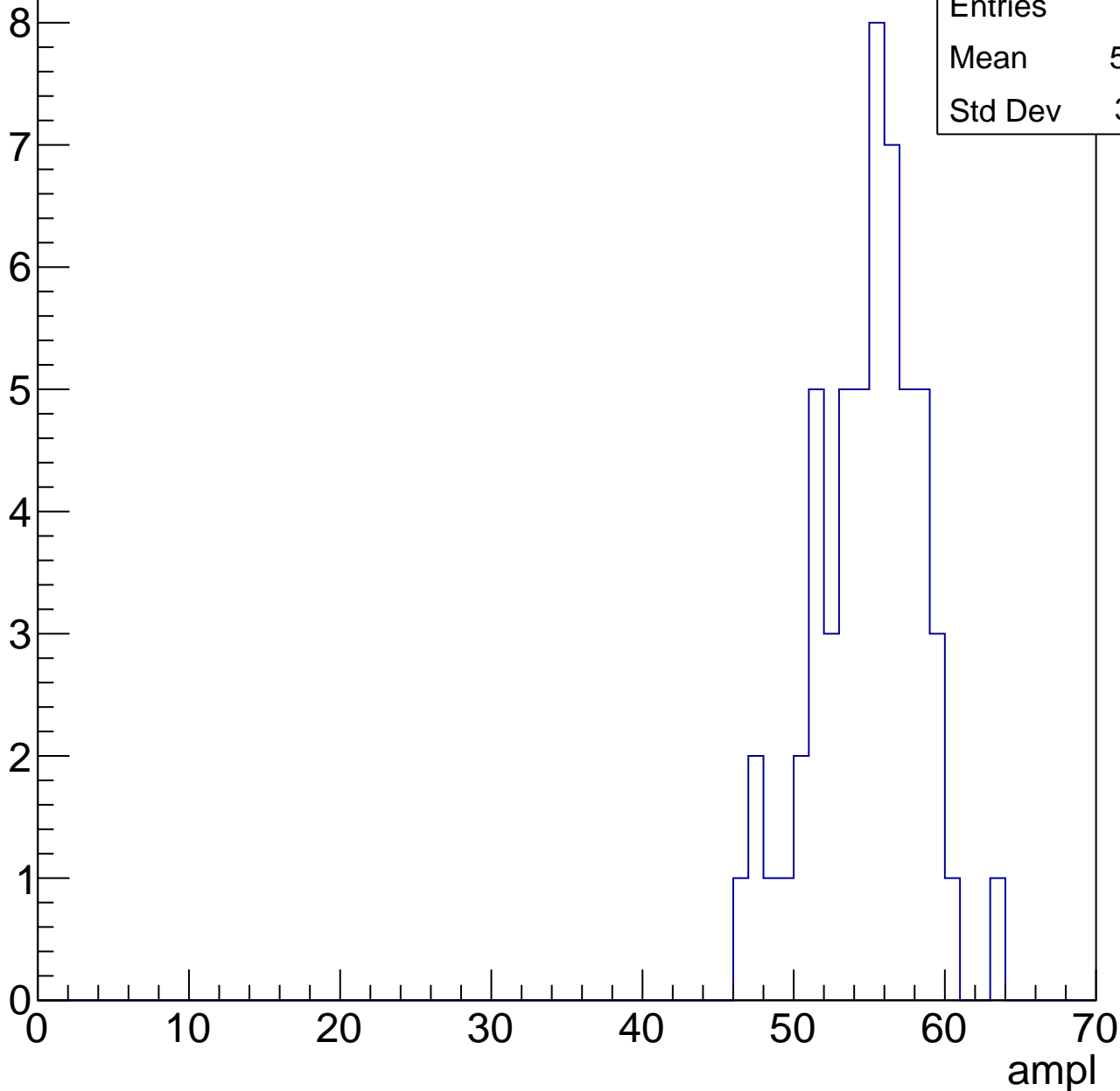


# B0L102S, U17-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	54.36
Std Dev	3.471

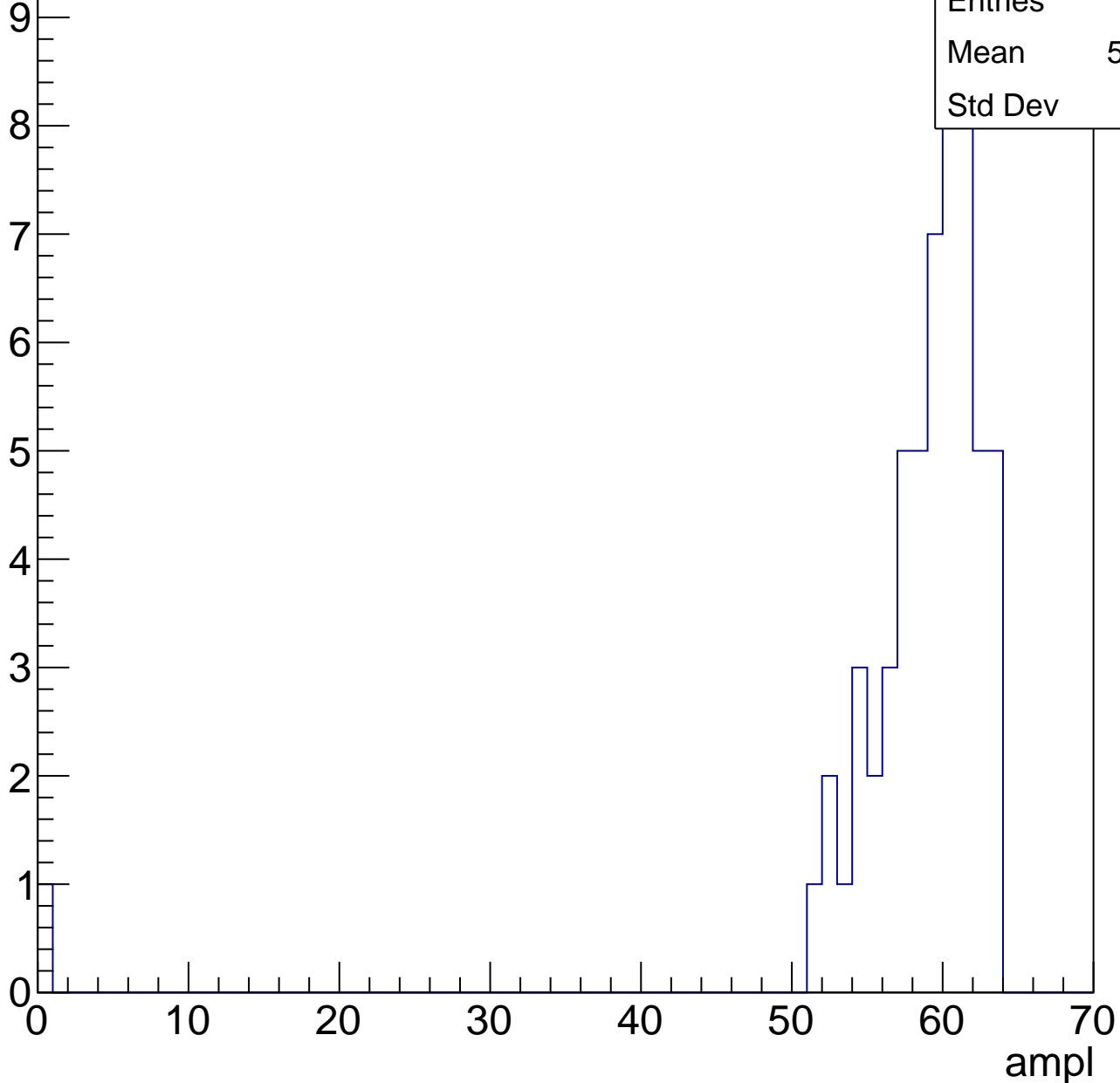


# B0L102S, U17-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	57.76
Std Dev	8.22

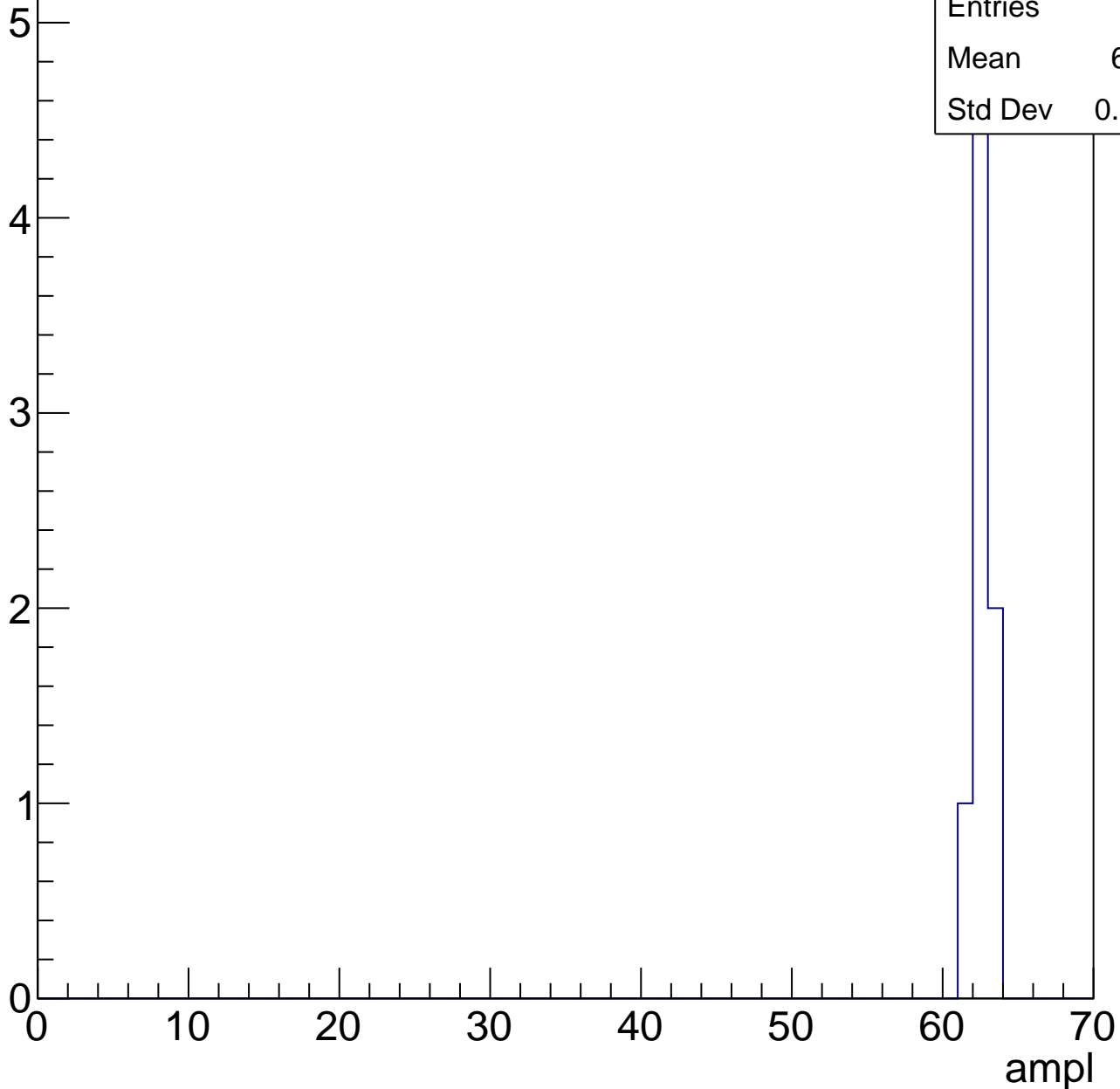


# B0L102S, U17-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	8
Mean	62.12
Std Dev	0.5995





# B0L102S, U17-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch104, adc0

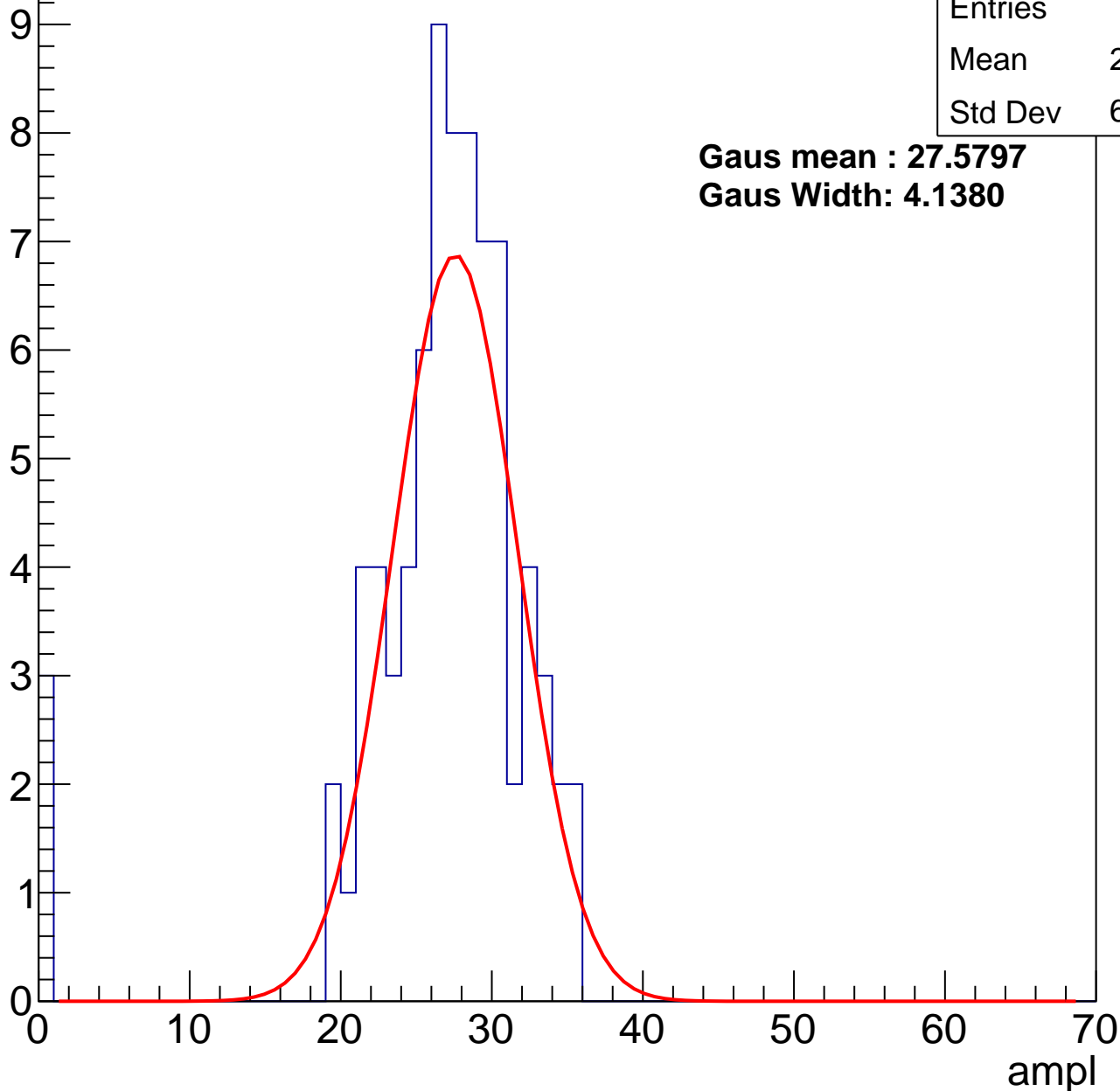
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	26.06
Std Dev	6.385

**Gaus mean : 27.5797**

**Gaus Width: 4.1380**



# B0L102S, U17-ch104, adc1

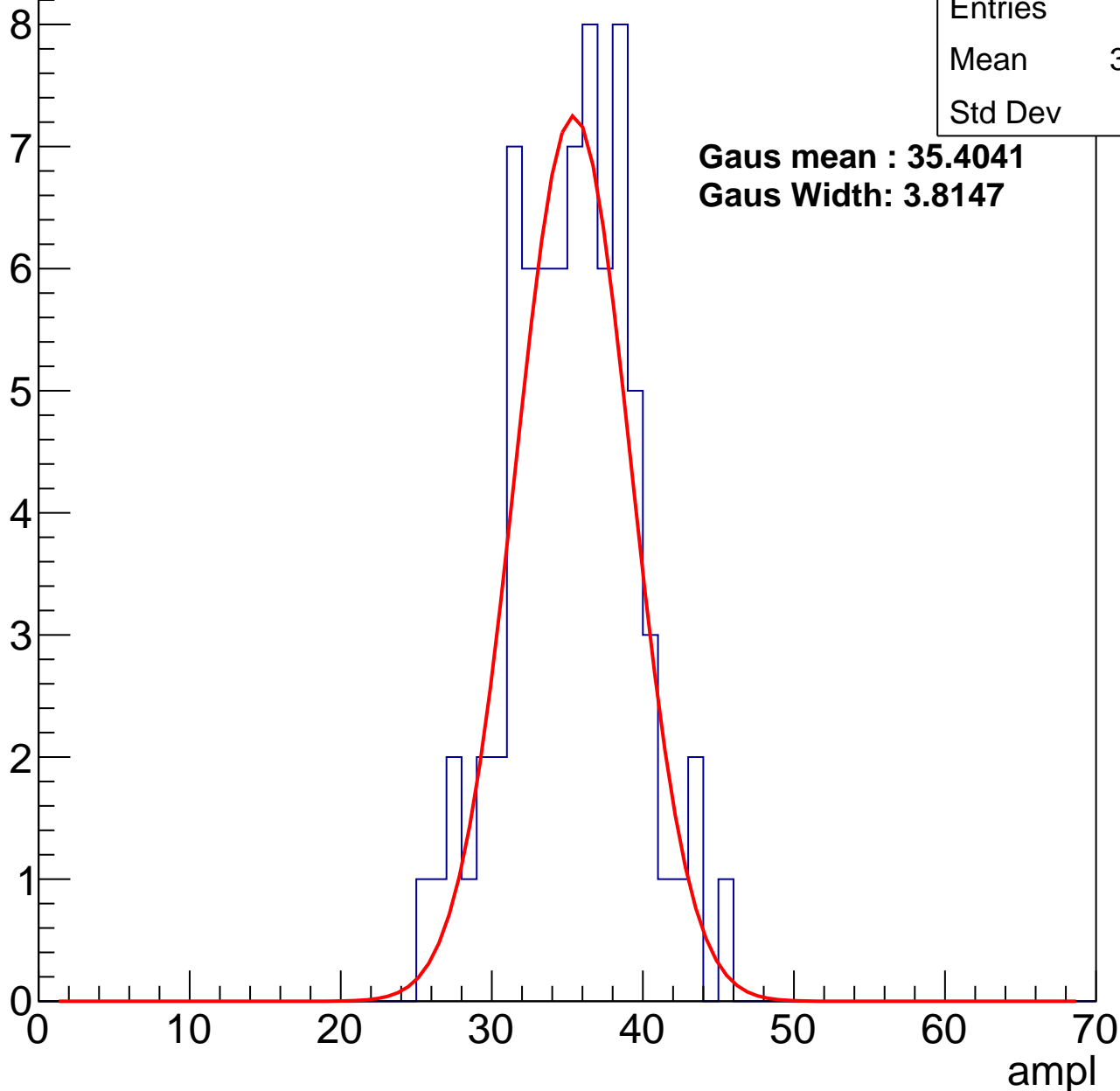
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	34.87
Std Dev	4.05

**Gaus mean : 35.4041**

**Gaus Width: 3.8147**



# B0L102S, U17-ch104, adc2

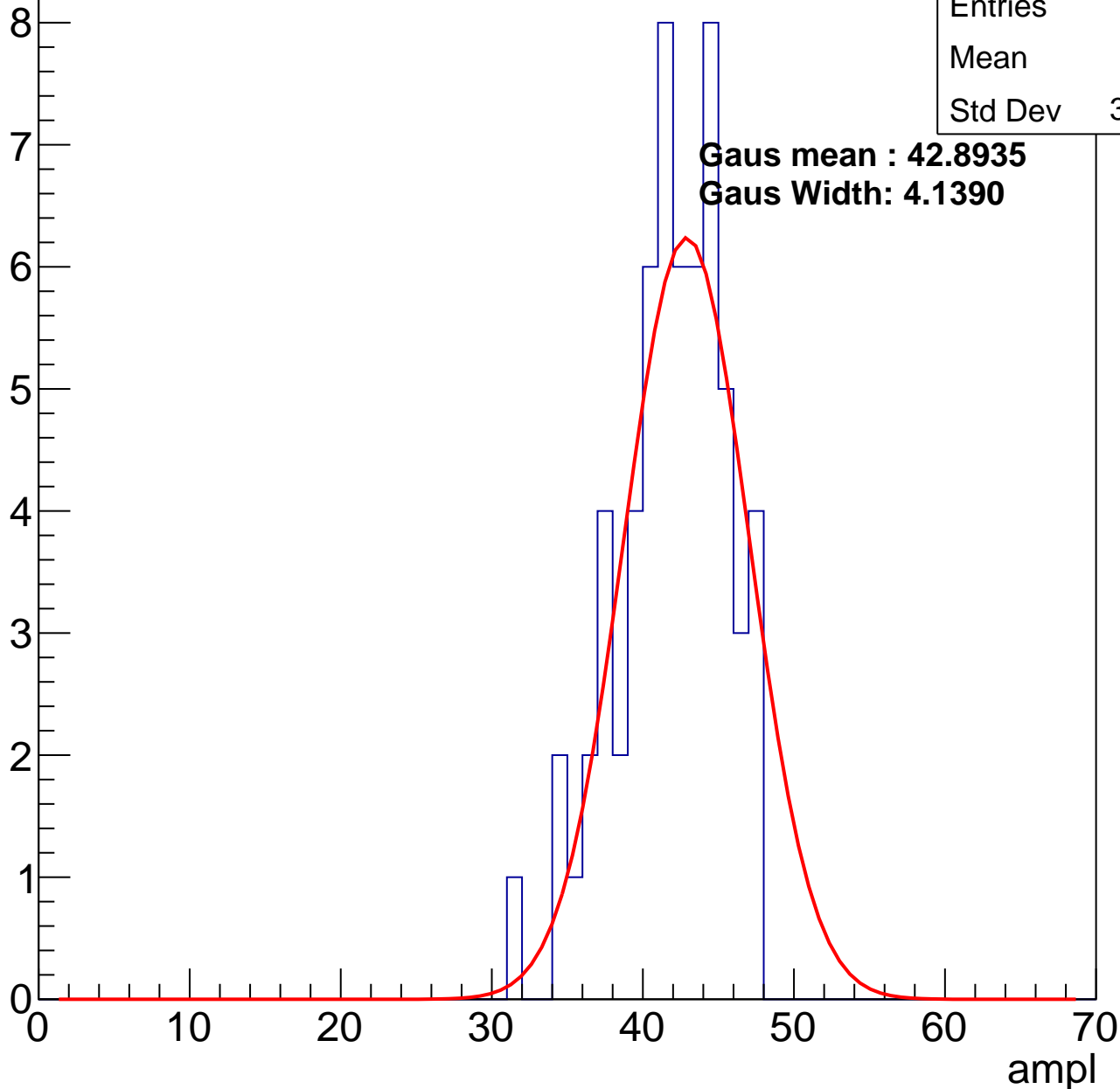
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	41.4
Std Dev	3.544

**Gaus mean : 42.8935**

**Gaus Width: 4.1390**

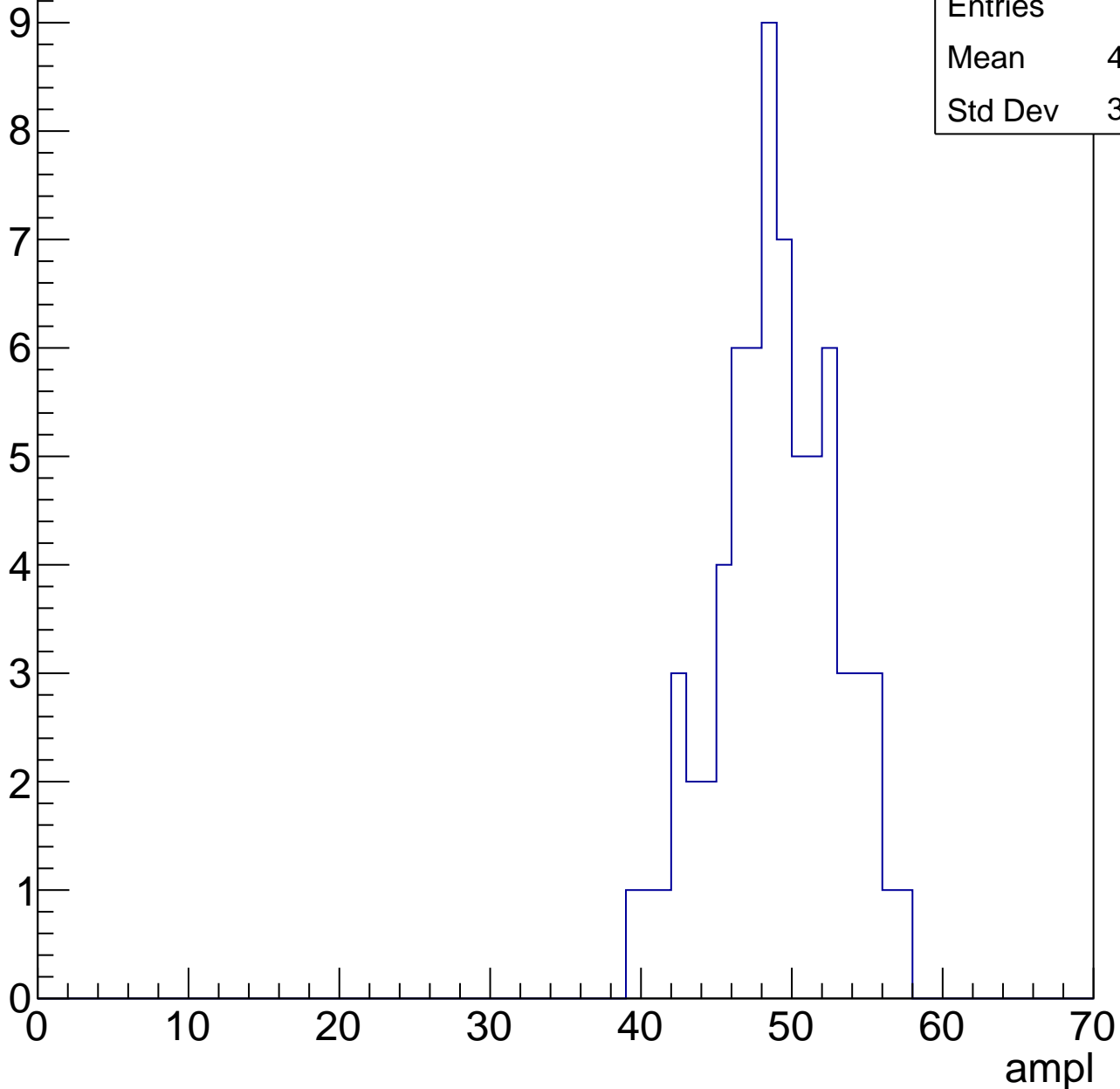


# B0L102S, U17-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	48.54
Std Dev	3.944

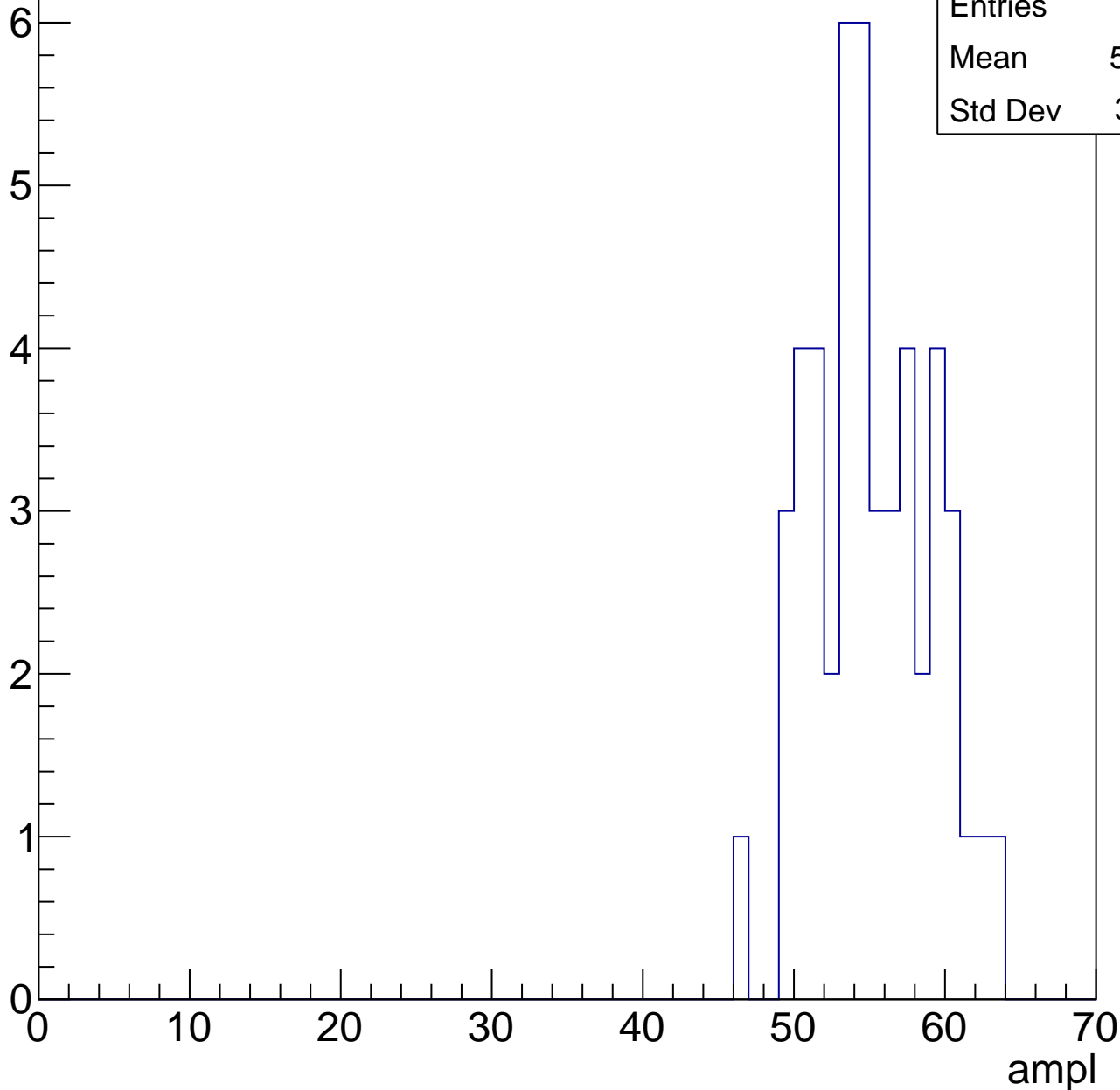


# B0L102S, U17-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	54.62
Std Dev	3.871

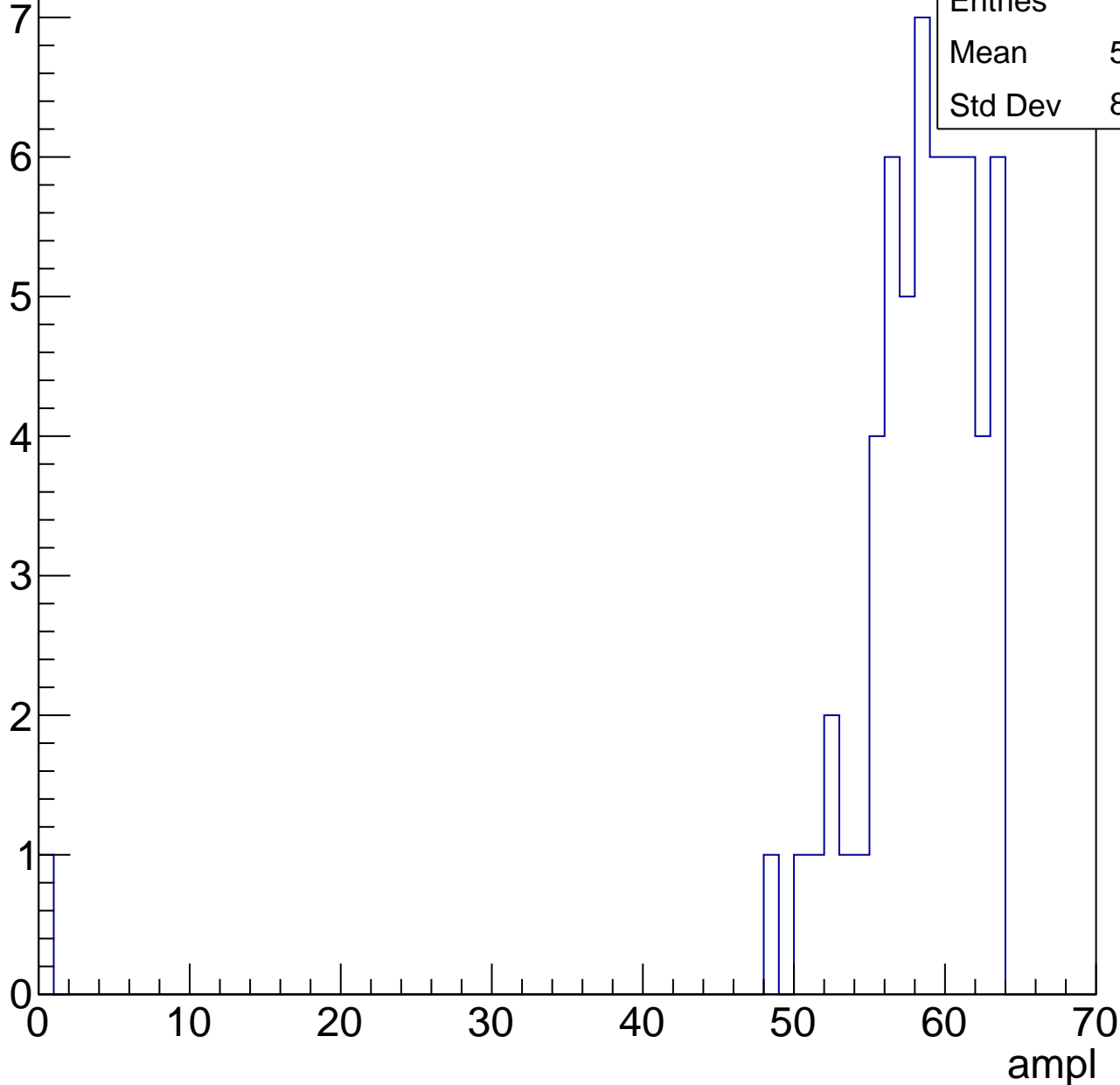


# B0L102S, U17-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	57.12
Std Dev	8.313

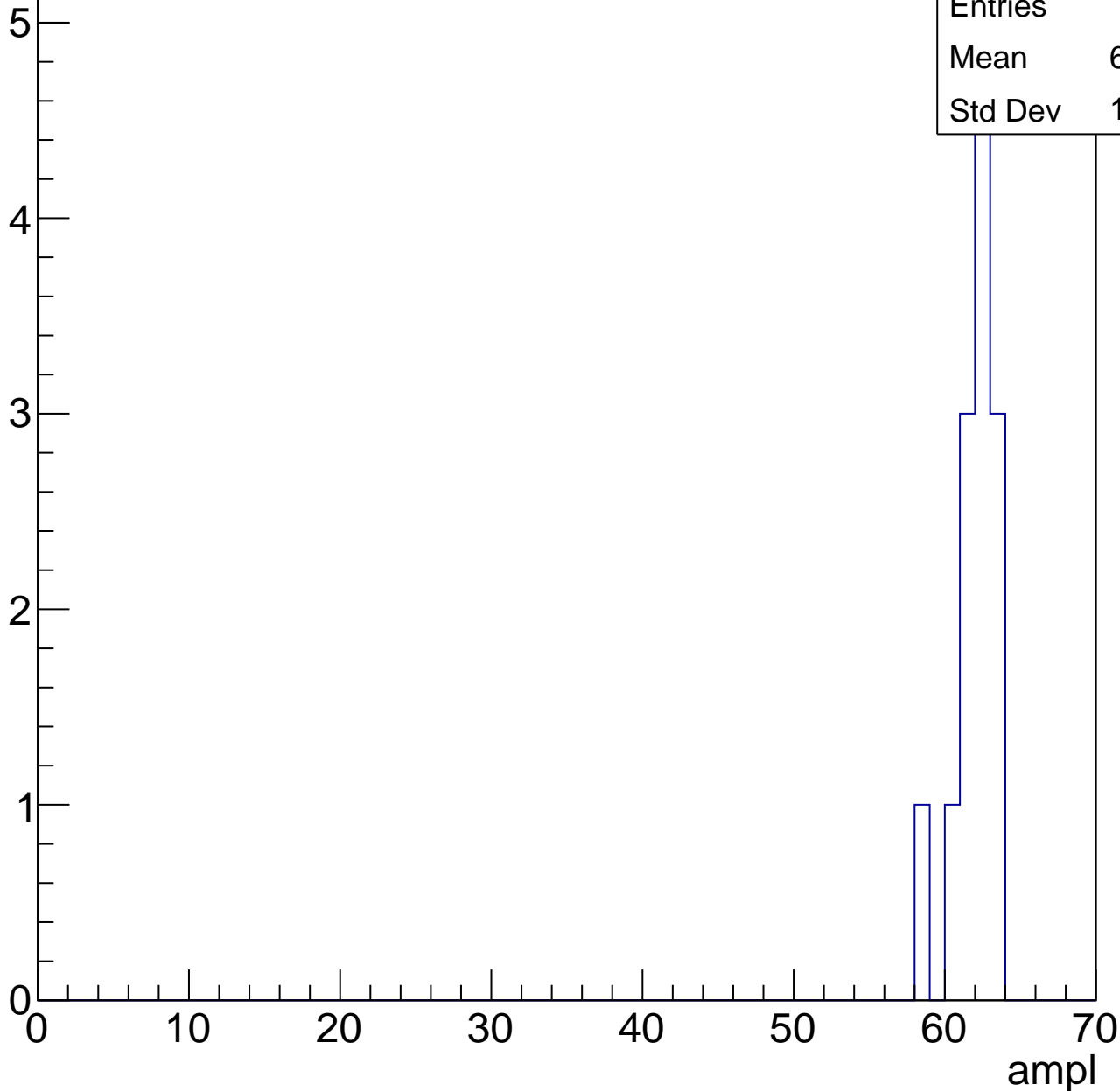


# B0L102S, U17-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	13
Mean	61.54
Std Dev	1.337





# B0L102S, U17-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	103
Mean	28.04
Std Dev	5.786

**Gaus mean : 29.4820**

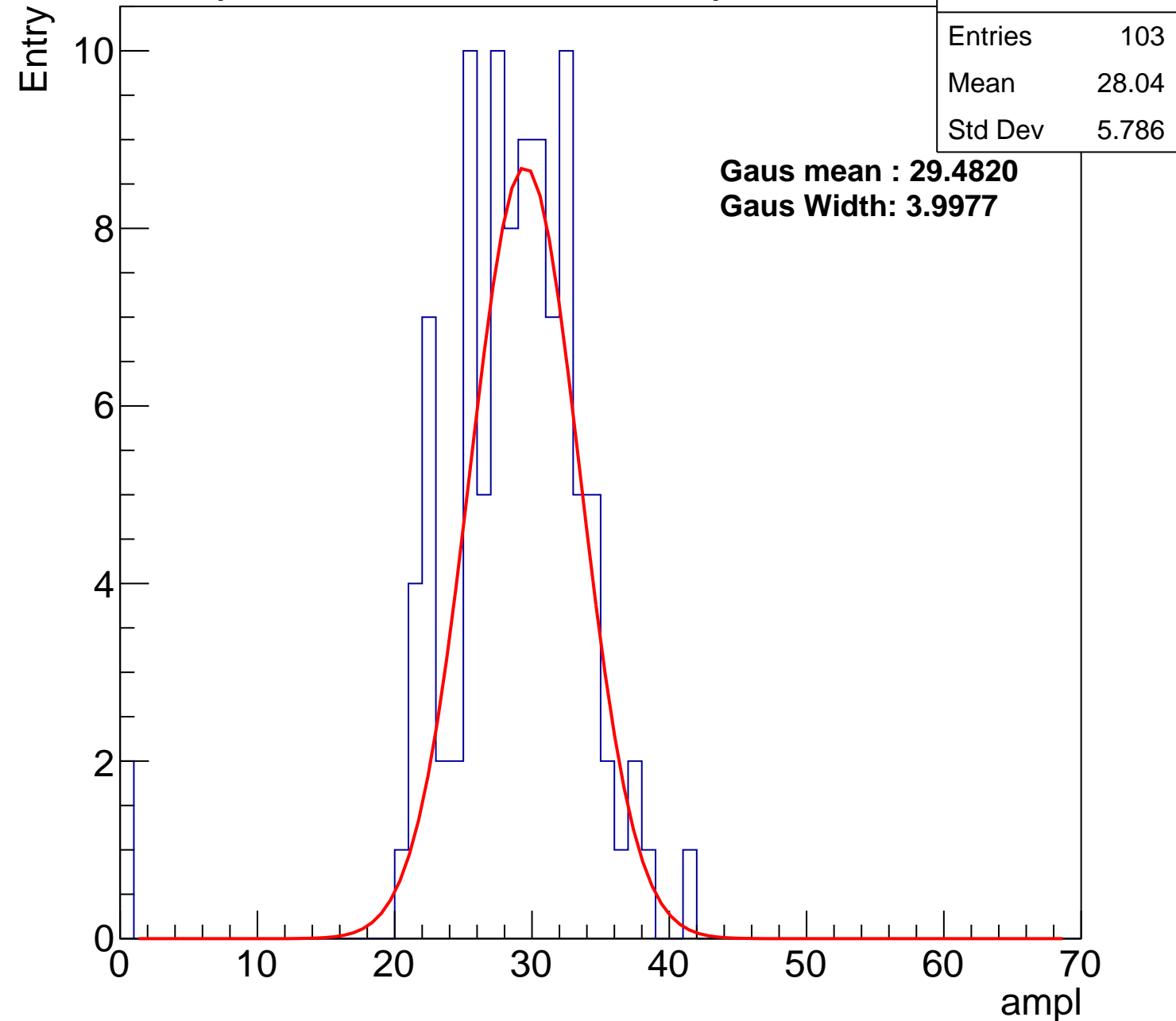
**Gaus Width: 3.9977**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch105, adc1

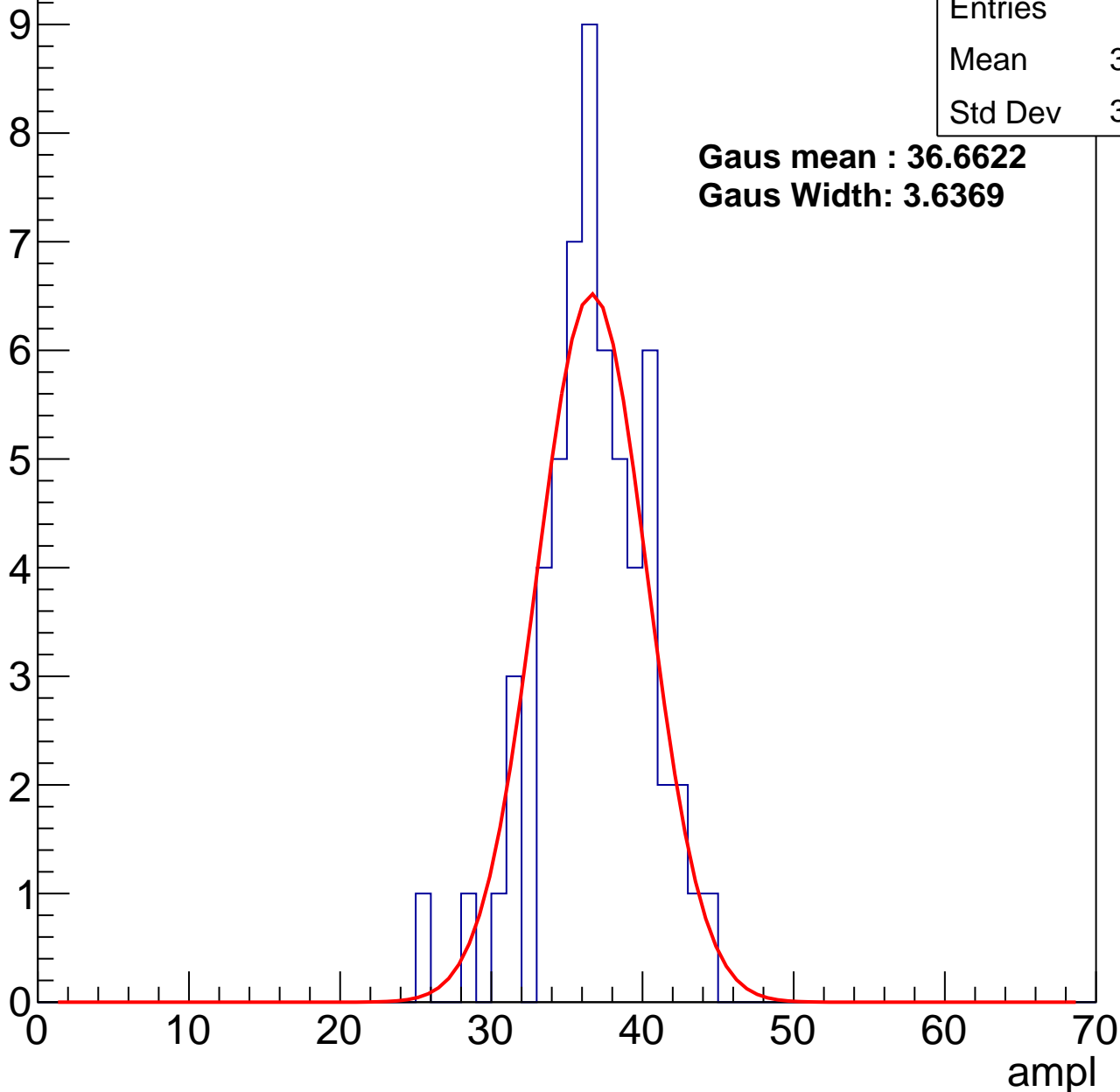
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	36.34
Std Dev	3.584

**Gaus mean : 36.6622**

**Gaus Width: 3.6369**



# B0L102S, U17-ch105, adc2

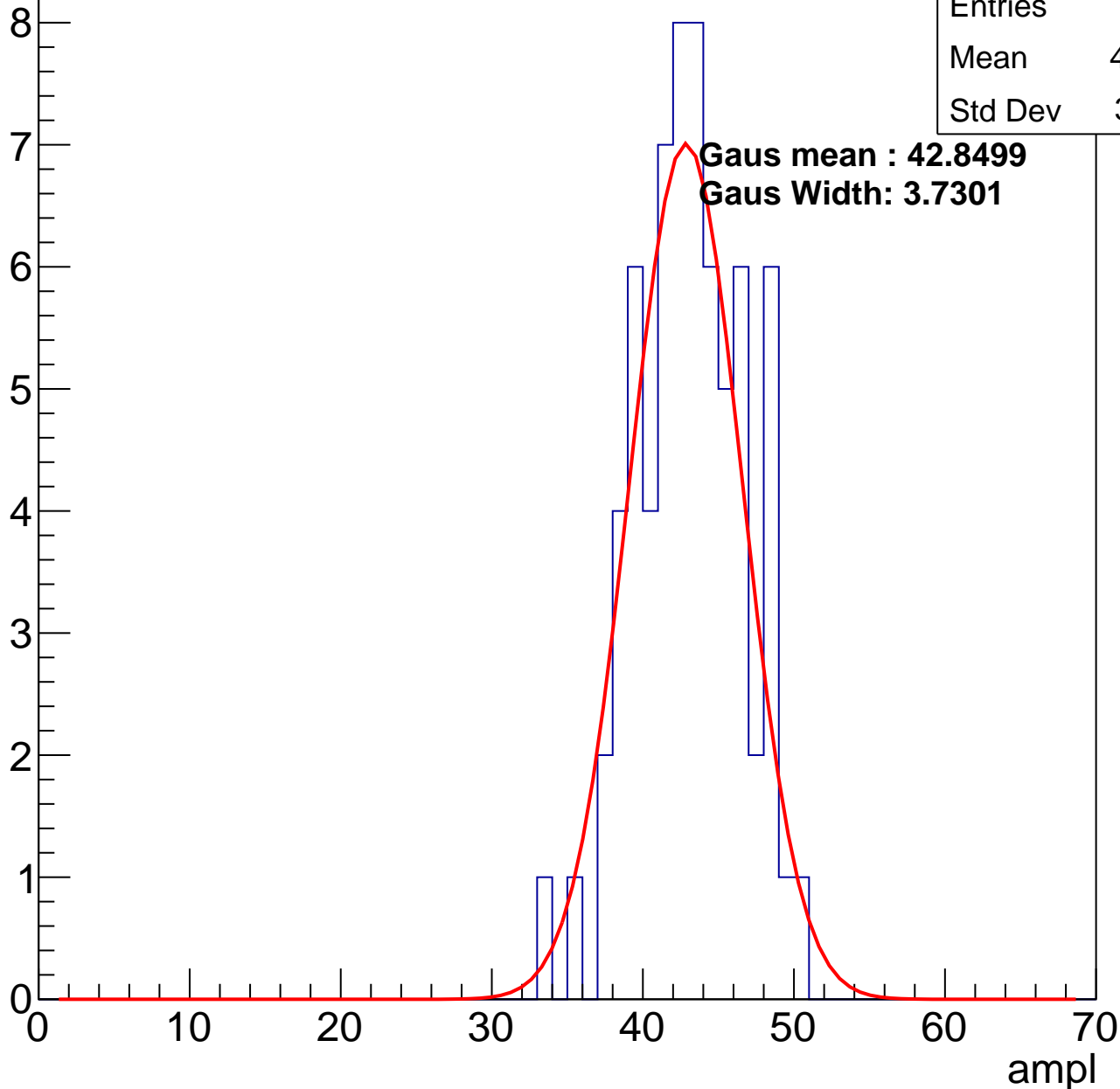
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.66
Std Dev	3.521

**Gaus mean : 42.8499**

**Gaus Width: 3.7301**

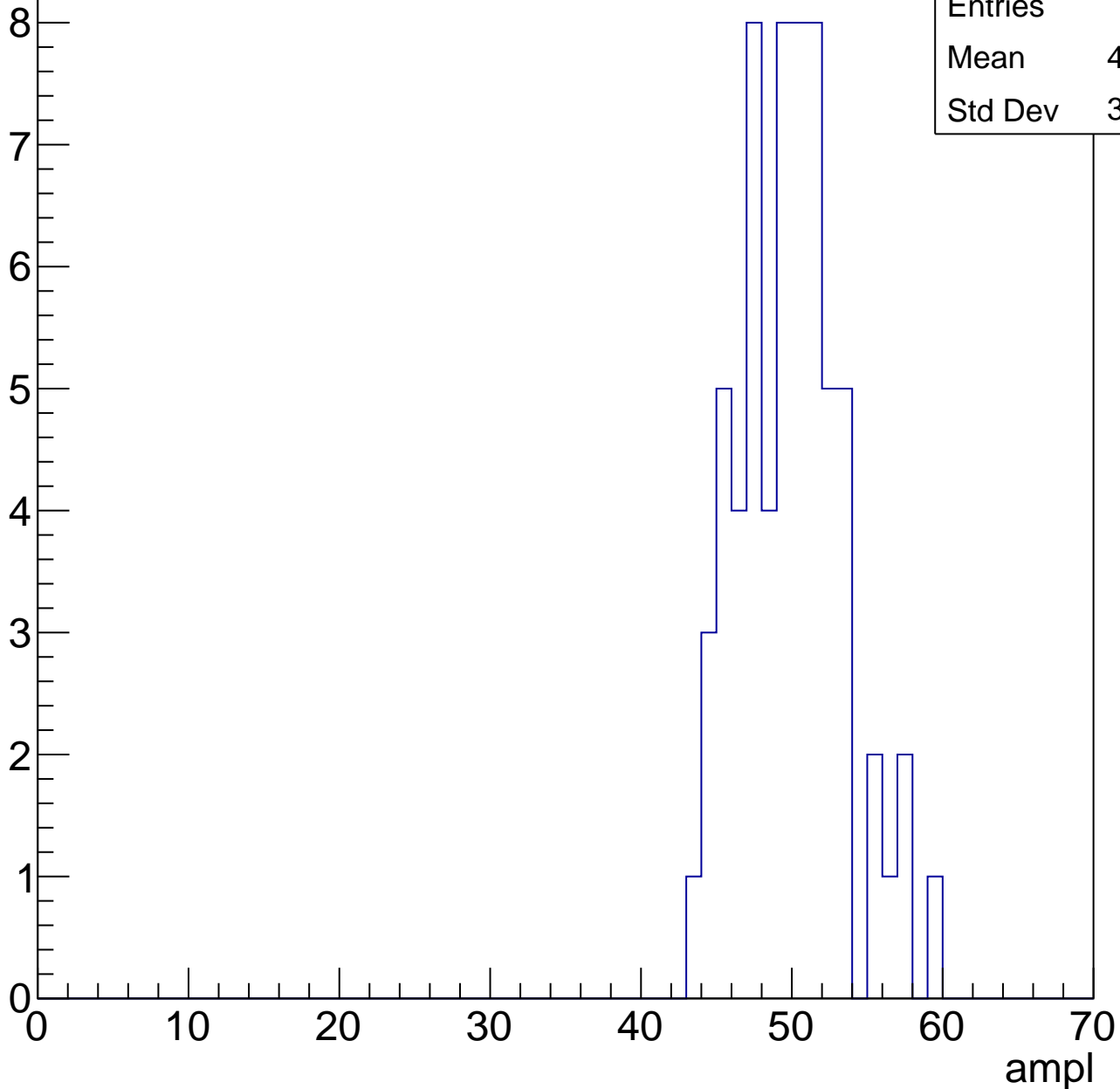


# B0L102S, U17-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.48
Std Dev	3.424

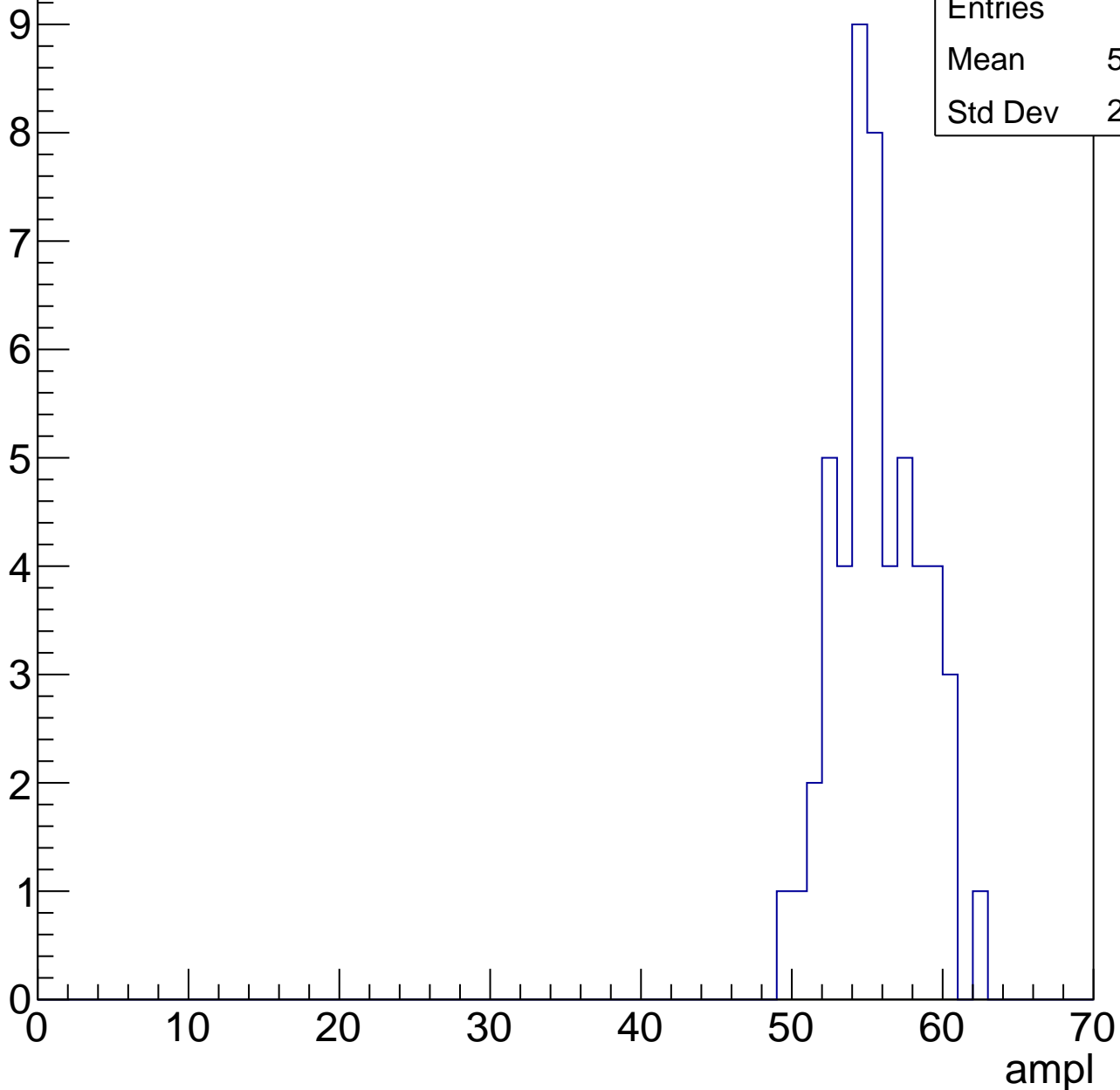


# B0L102S, U17-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	51
Mean	55.25
Std Dev	2.827

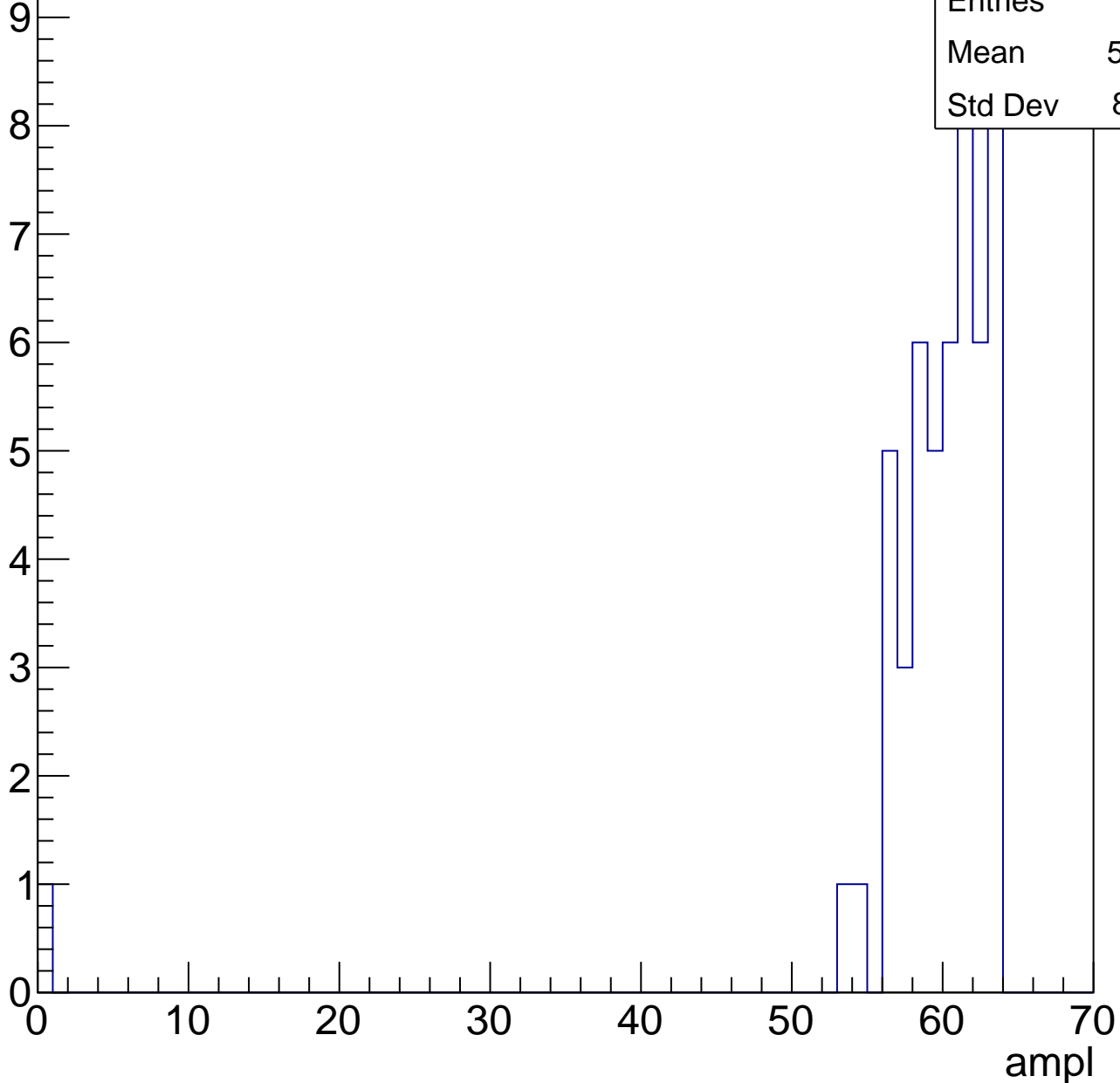


# B0L102S, U17-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

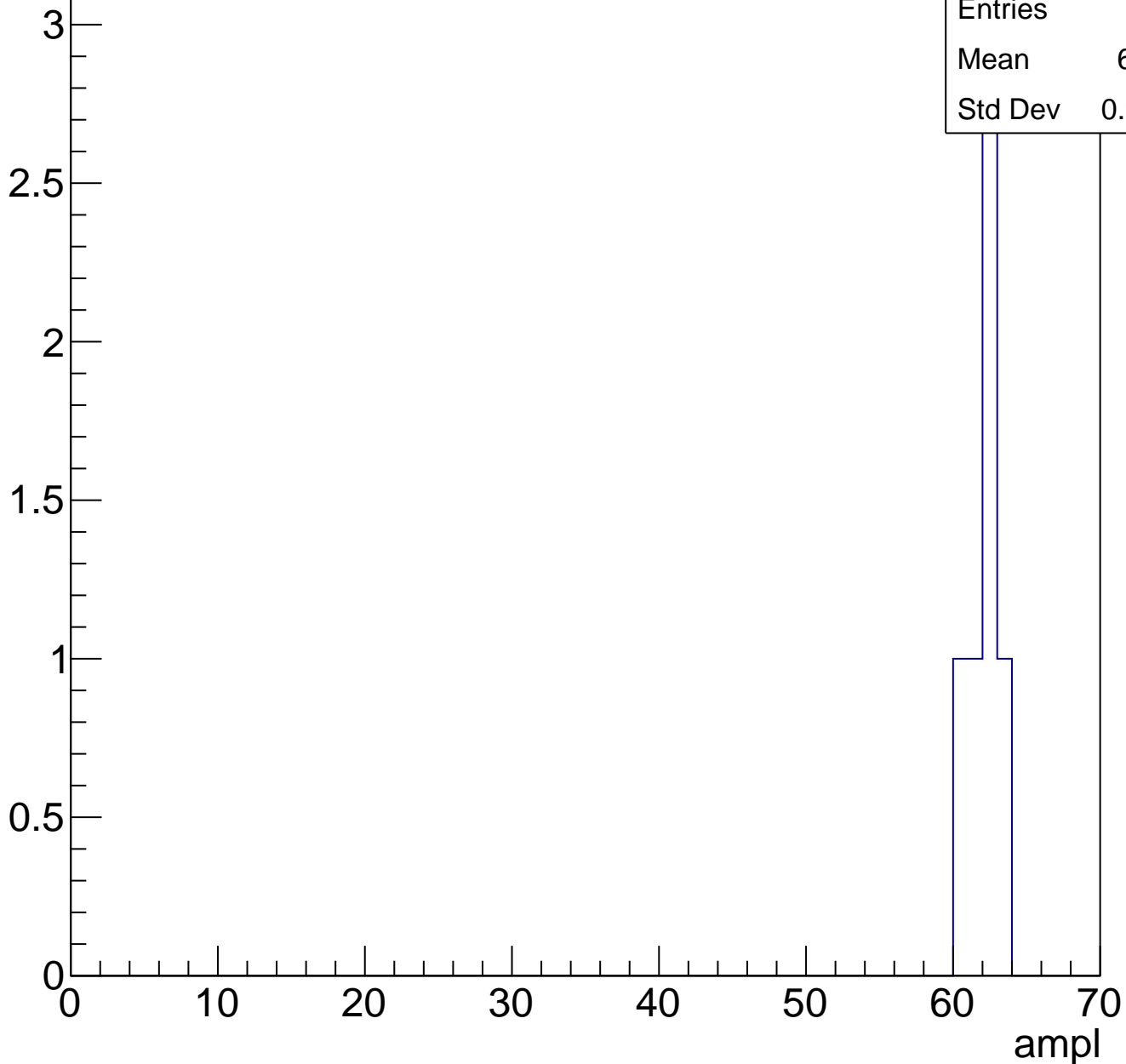
Entries	52
Mean	58.63
Std Dev	8.591



# B0L102S, U17-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch106, adc0

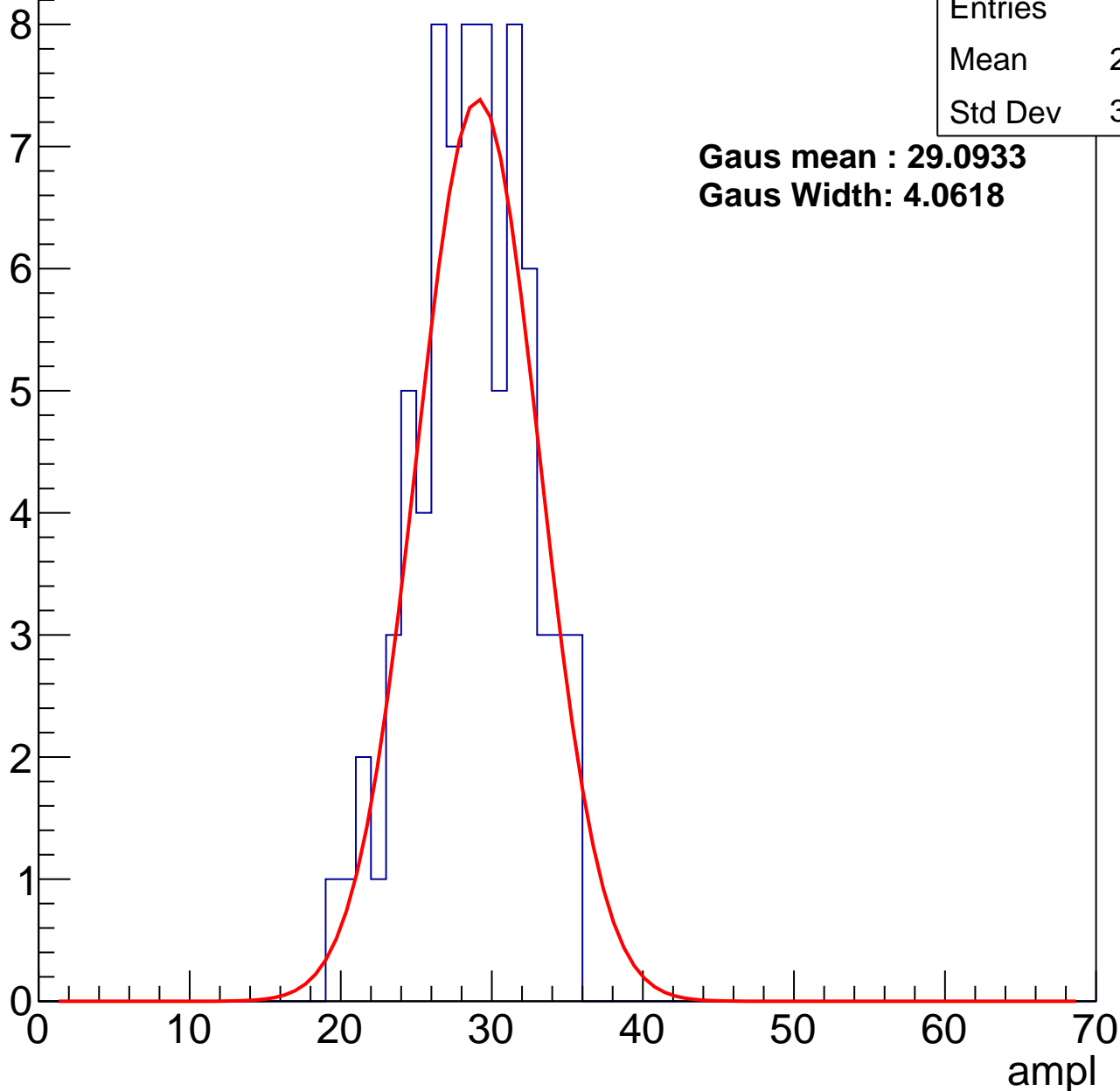
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	28.17
Std Dev	3.686

**Gaus mean : 29.0933**

**Gaus Width: 4.0618**



# B0L102S, U17-ch106, adc1

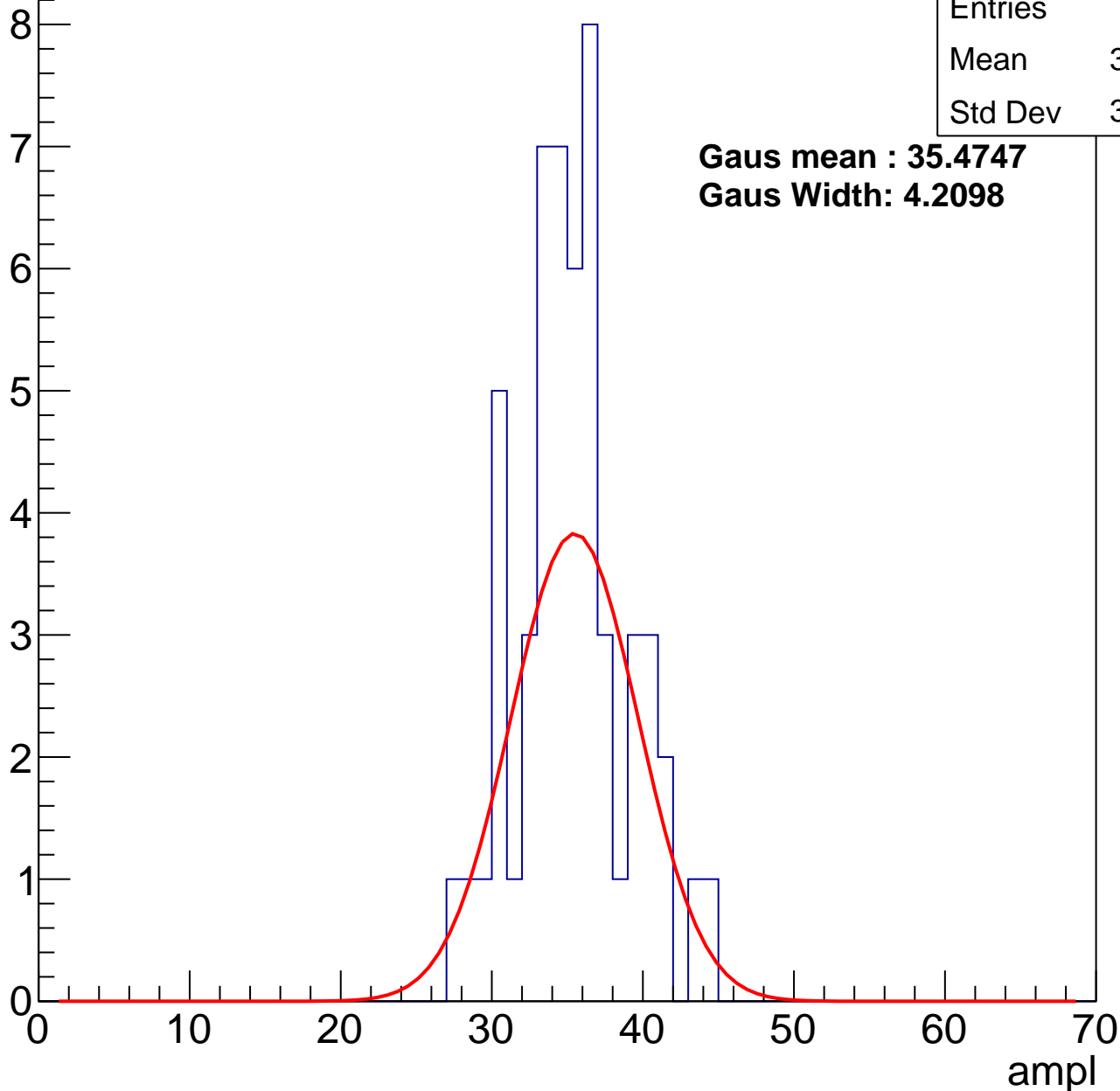
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	34.87
Std Dev	3.652

**Gaus mean : 35.4747**

**Gaus Width: 4.2098**



# B0L102S, U17-ch106, adc2

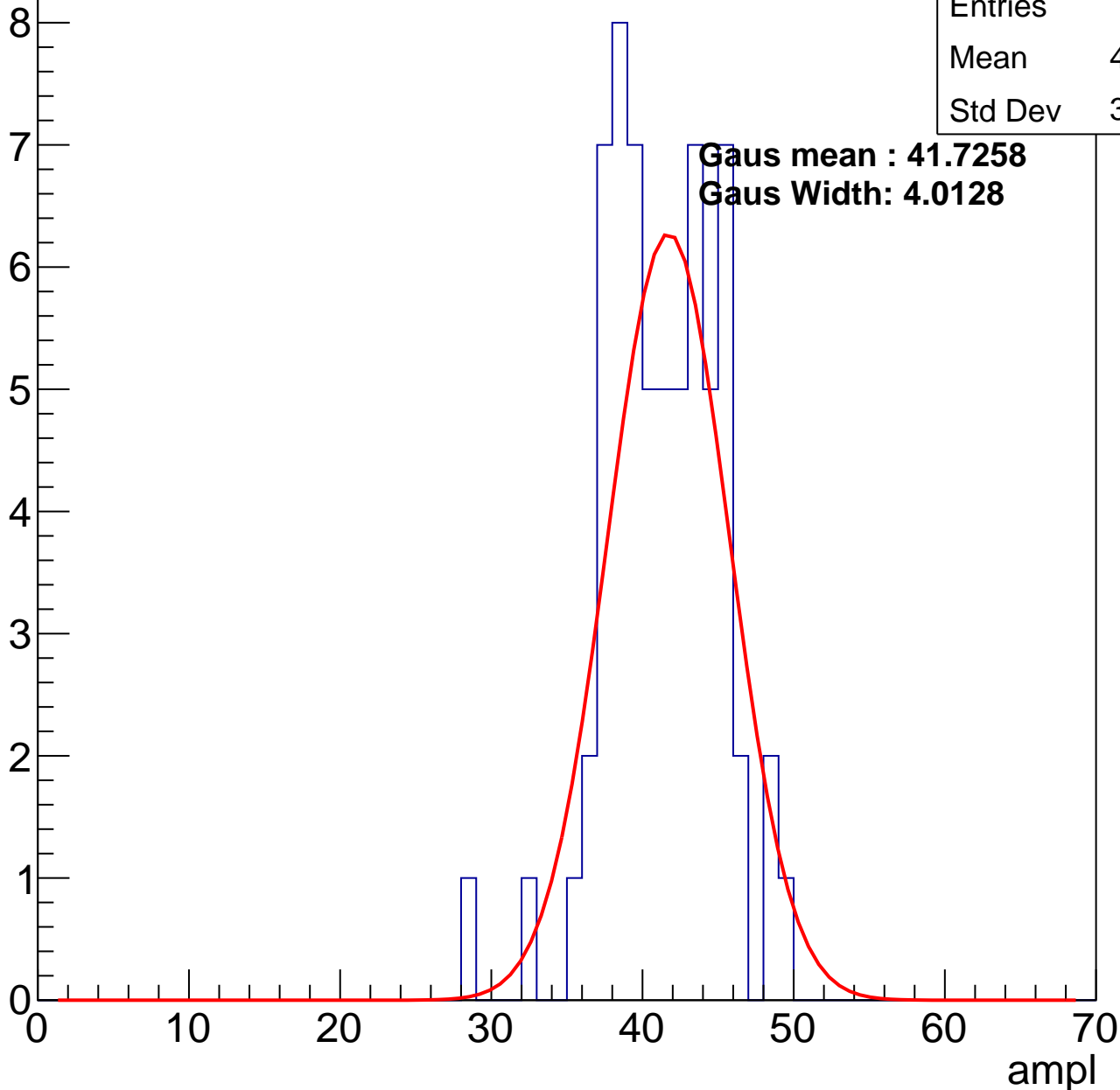
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	40.77
Std Dev	3.797

**Gaus mean : 41.7258**

**Gaus Width: 4.0128**

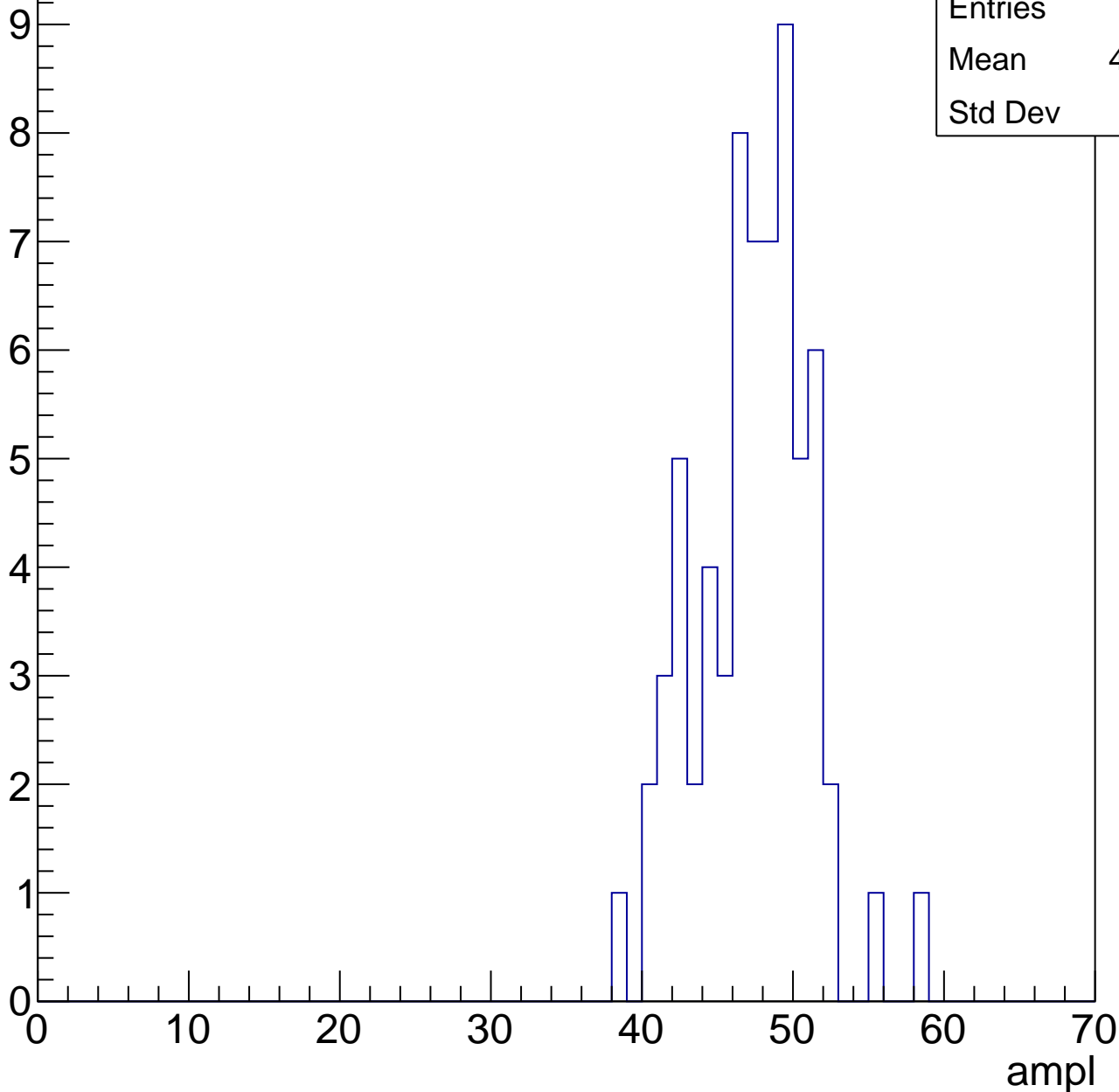


# B0L102S, U17-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	46.89
Std Dev	3.73

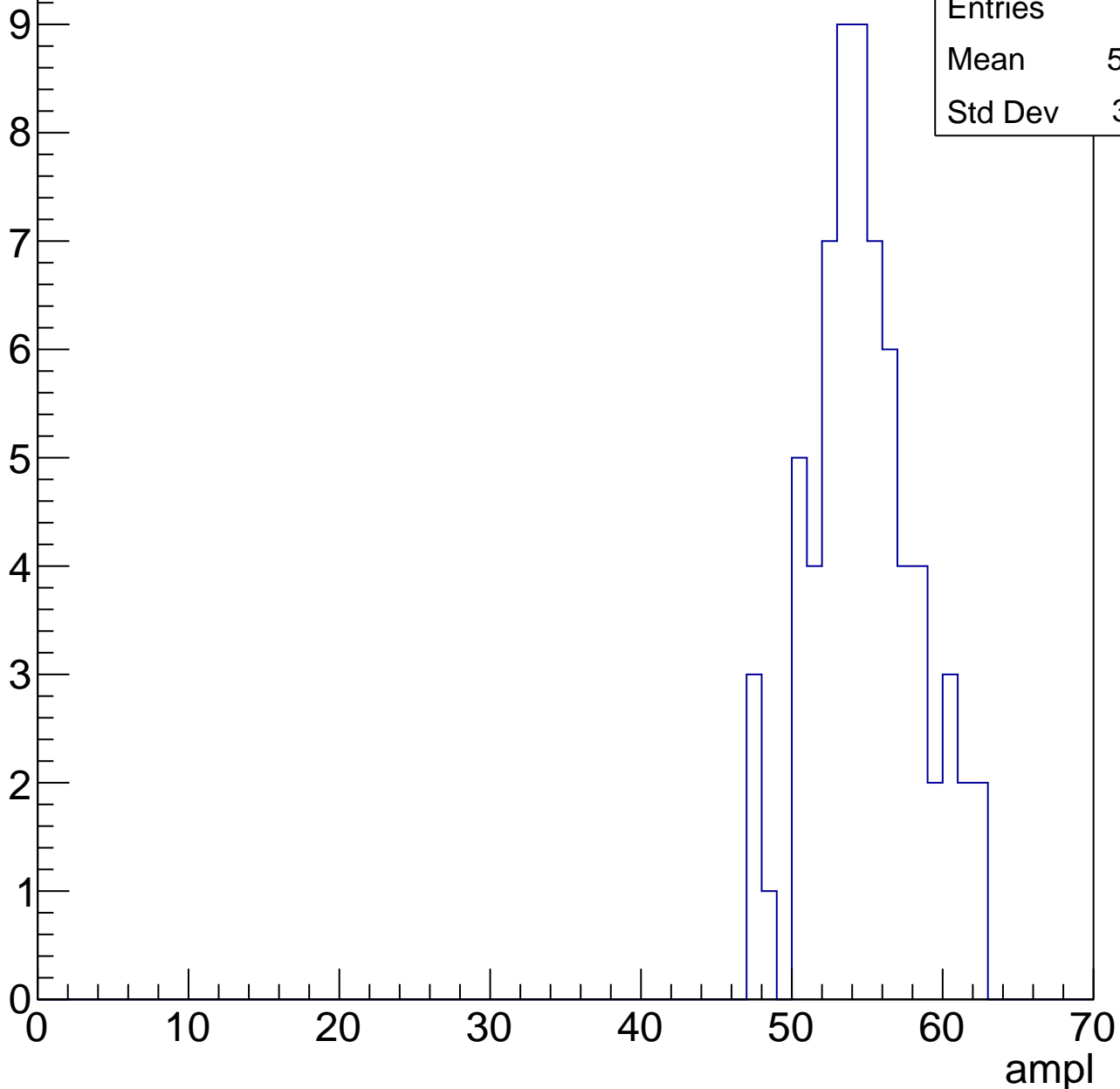


# B0L102S, U17-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	54.34
Std Dev	3.521

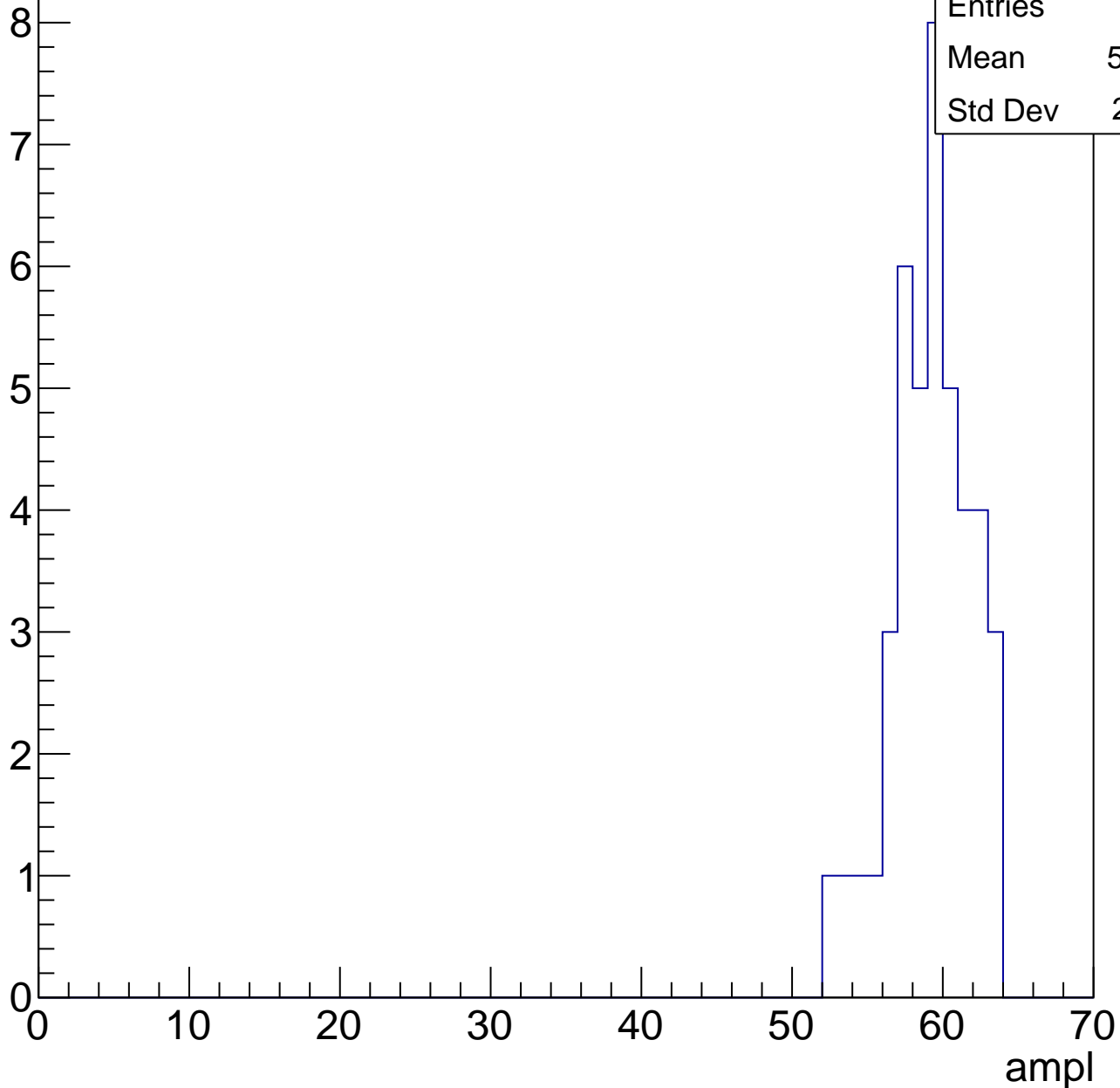


# B0L102S, U17-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	42
Mean	58.74
Std Dev	2.601

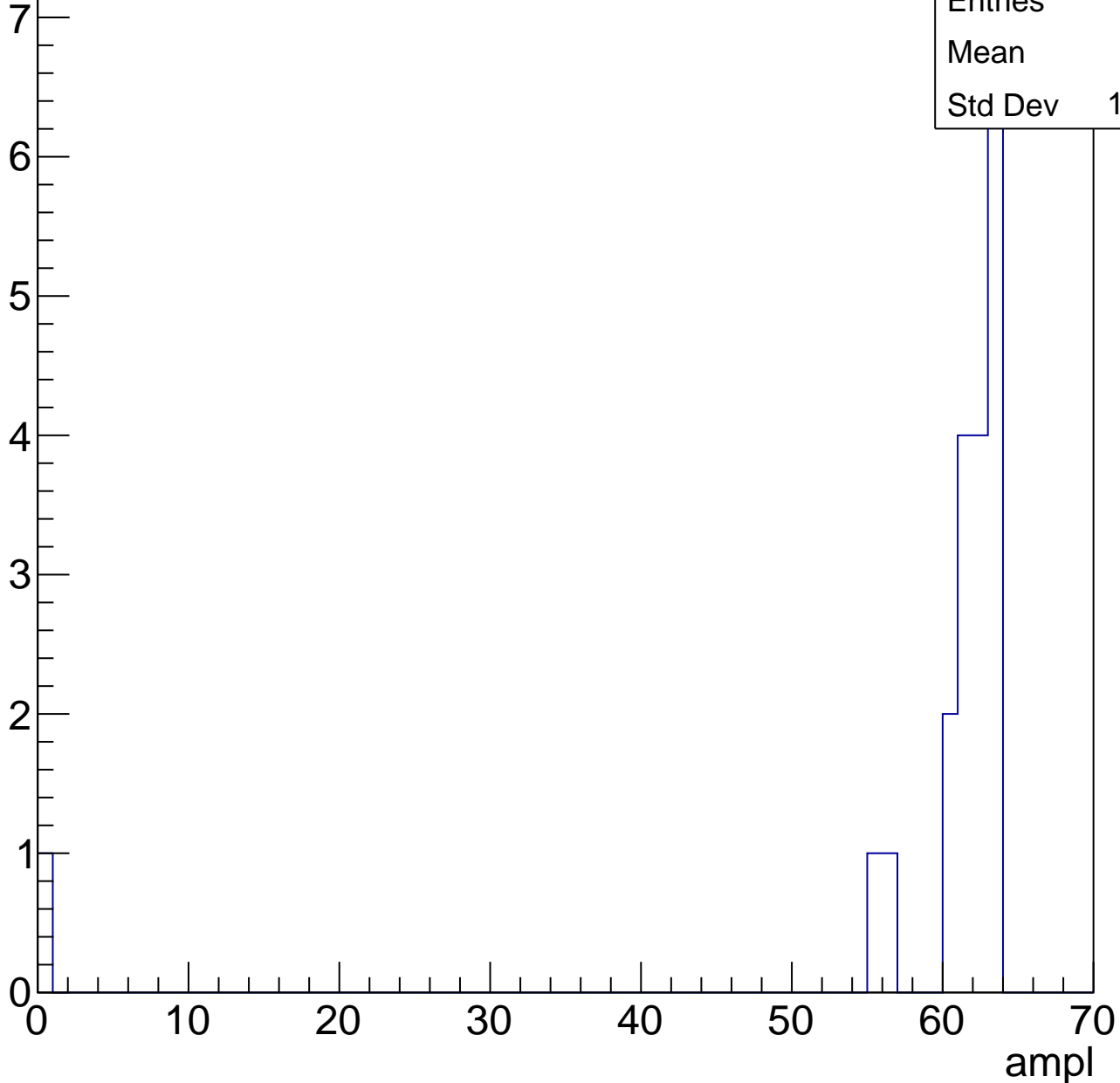


# B0L102S, U17-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	20
Mean	58.2
Std Dev	13.53

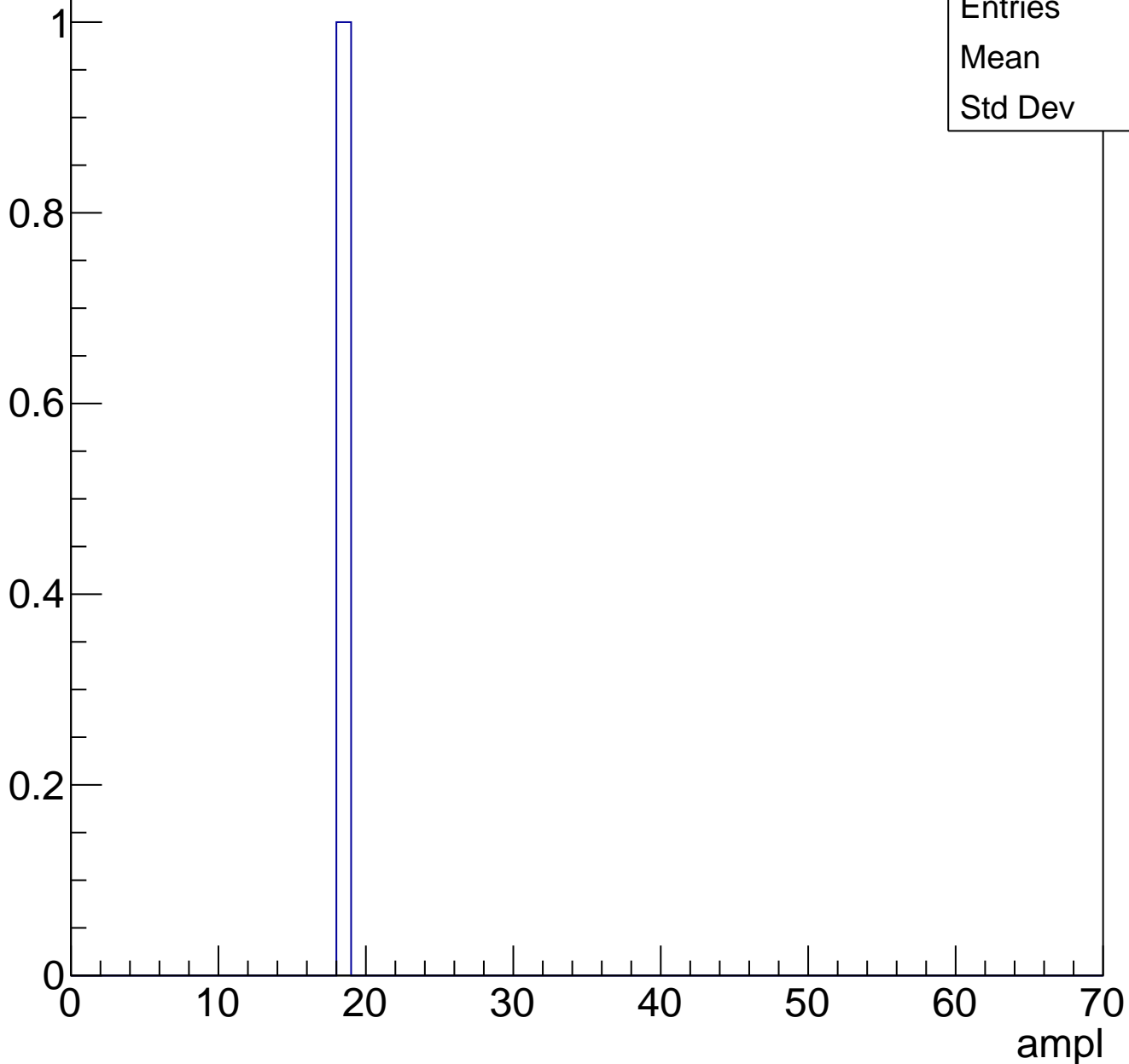




# B0L102S, U17-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch107, adc0

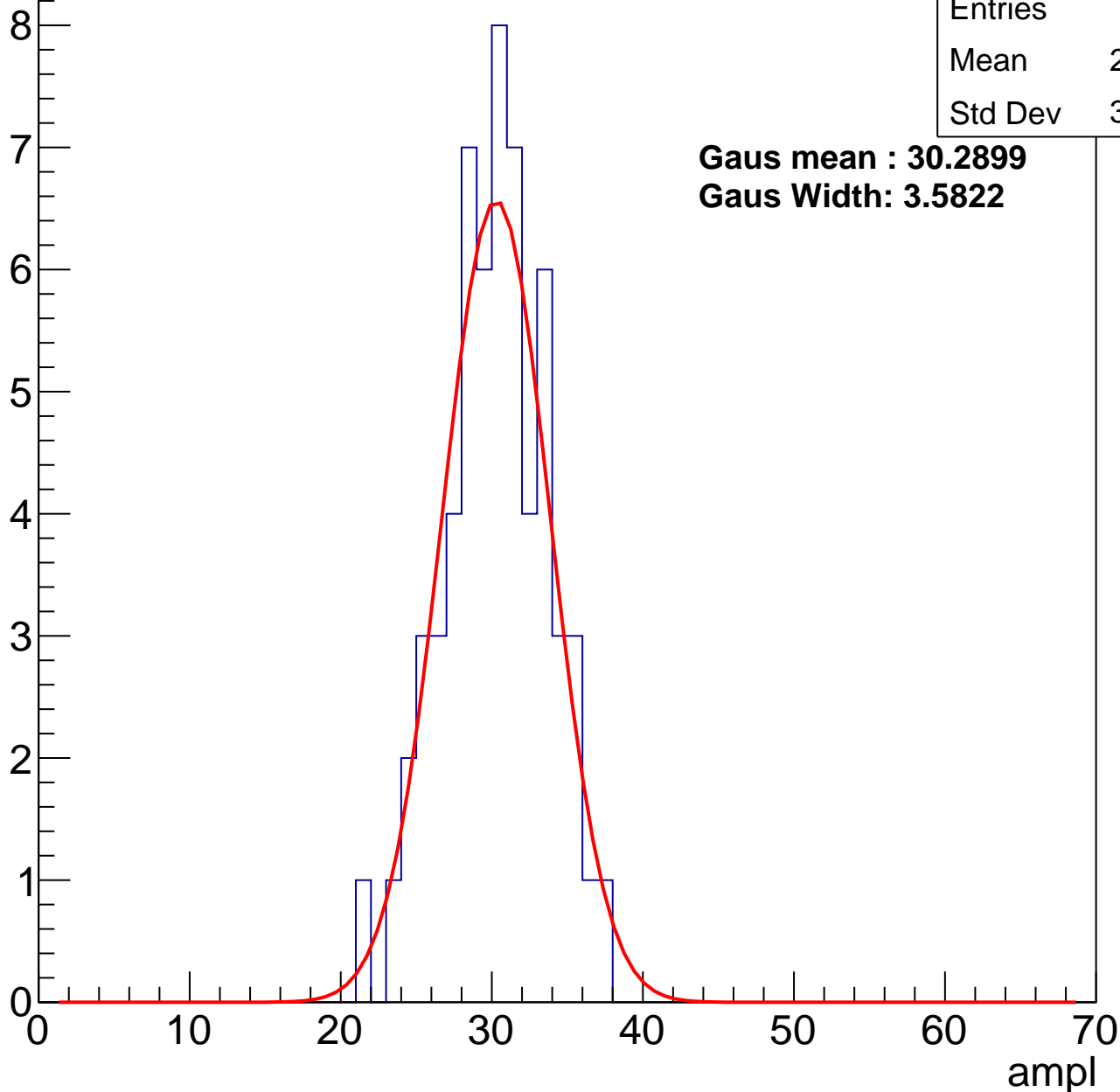
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	29.77
Std Dev	3.358

**Gaus mean : 30.2899**

**Gaus Width: 3.5822**



# B0L102S, U17-ch107, adc1

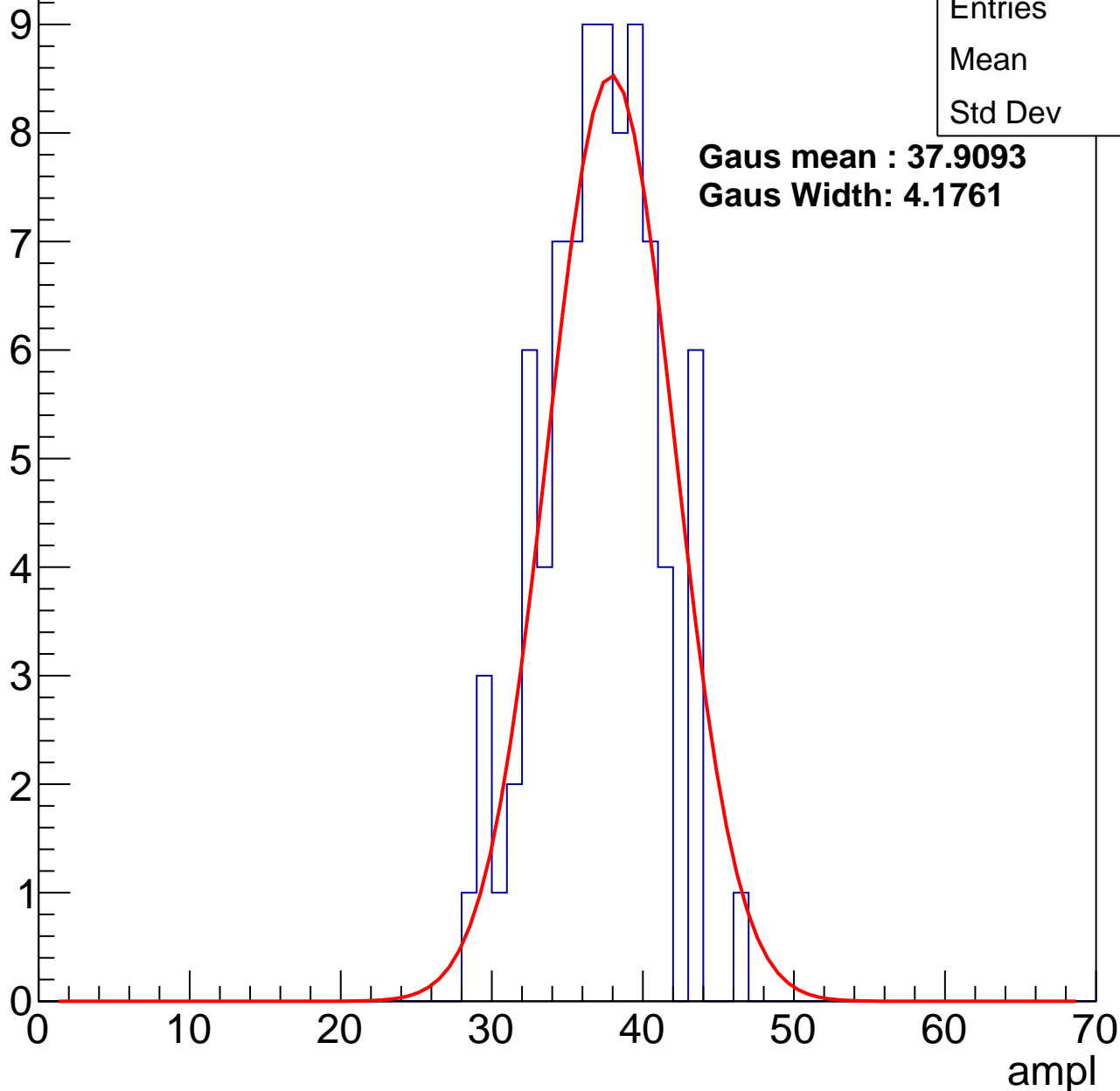
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	36.6
Std Dev	3.72

**Gaus mean : 37.9093**

**Gaus Width: 4.1761**



# B0L102S, U17-ch107, adc2

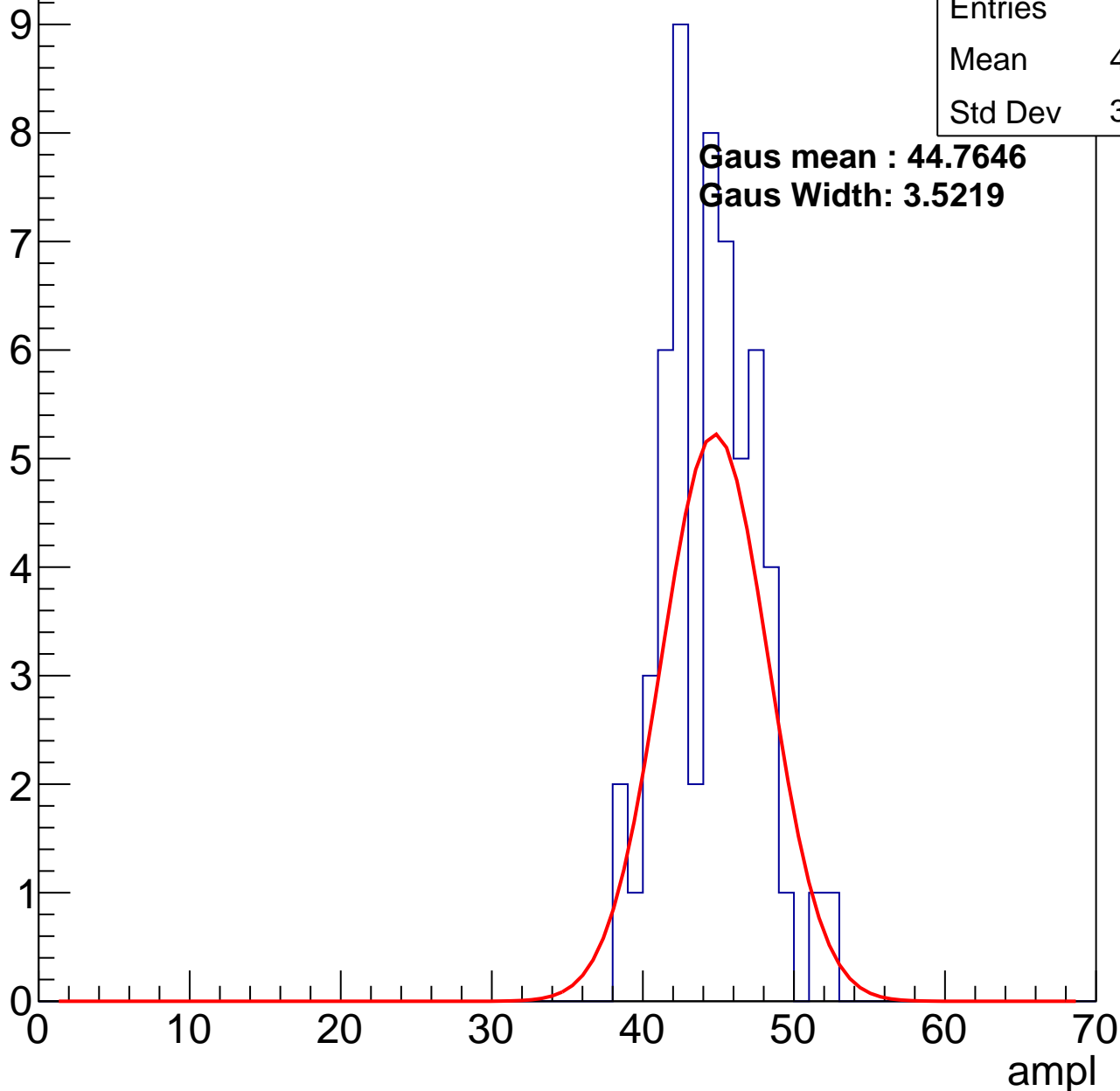
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	44.07
Std Dev	3.046

**Gaus mean : 44.7646**

**Gaus Width: 3.5219**

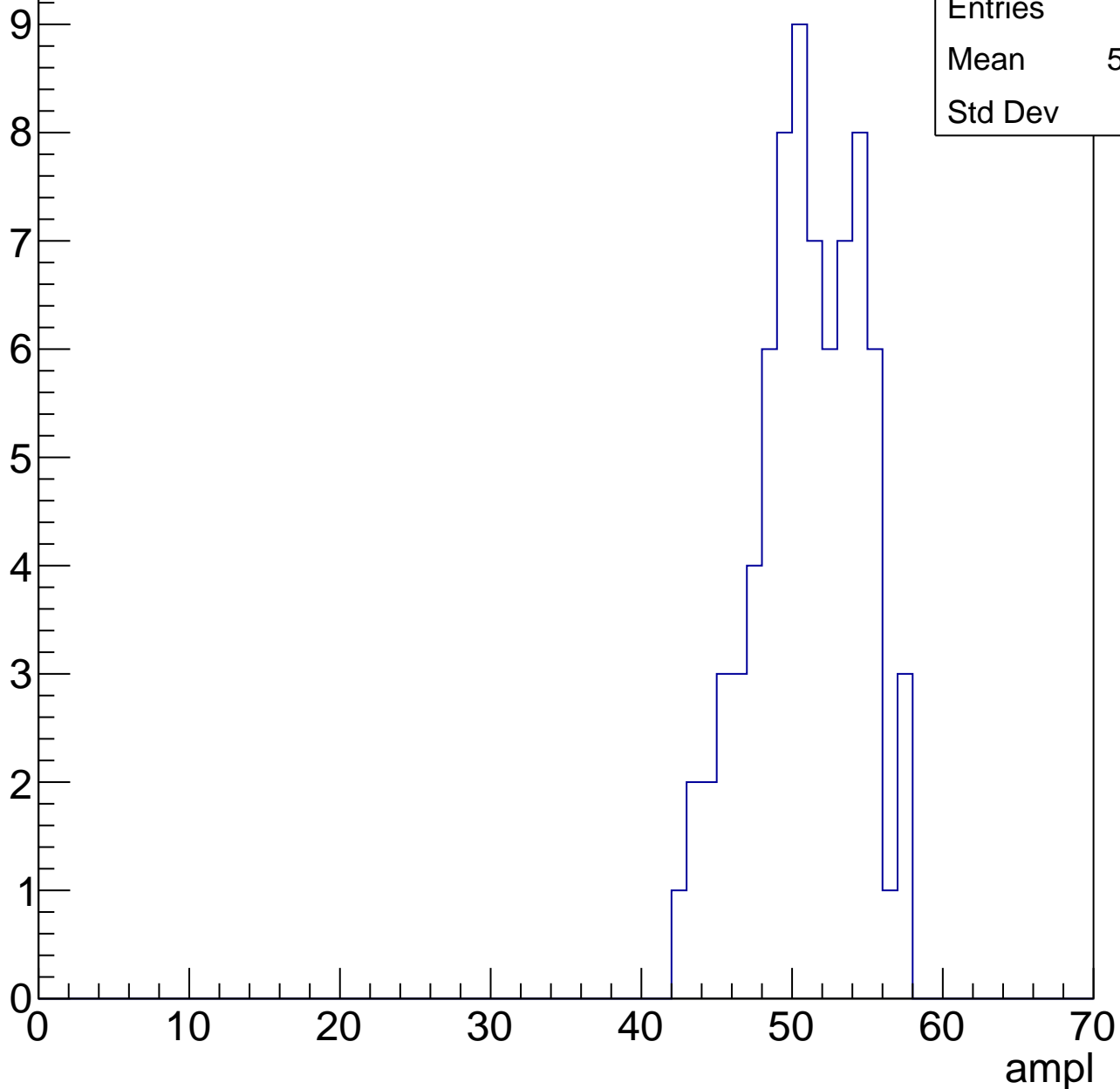


# B0L102S, U17-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	76
Mean	50.47
Std Dev	3.56

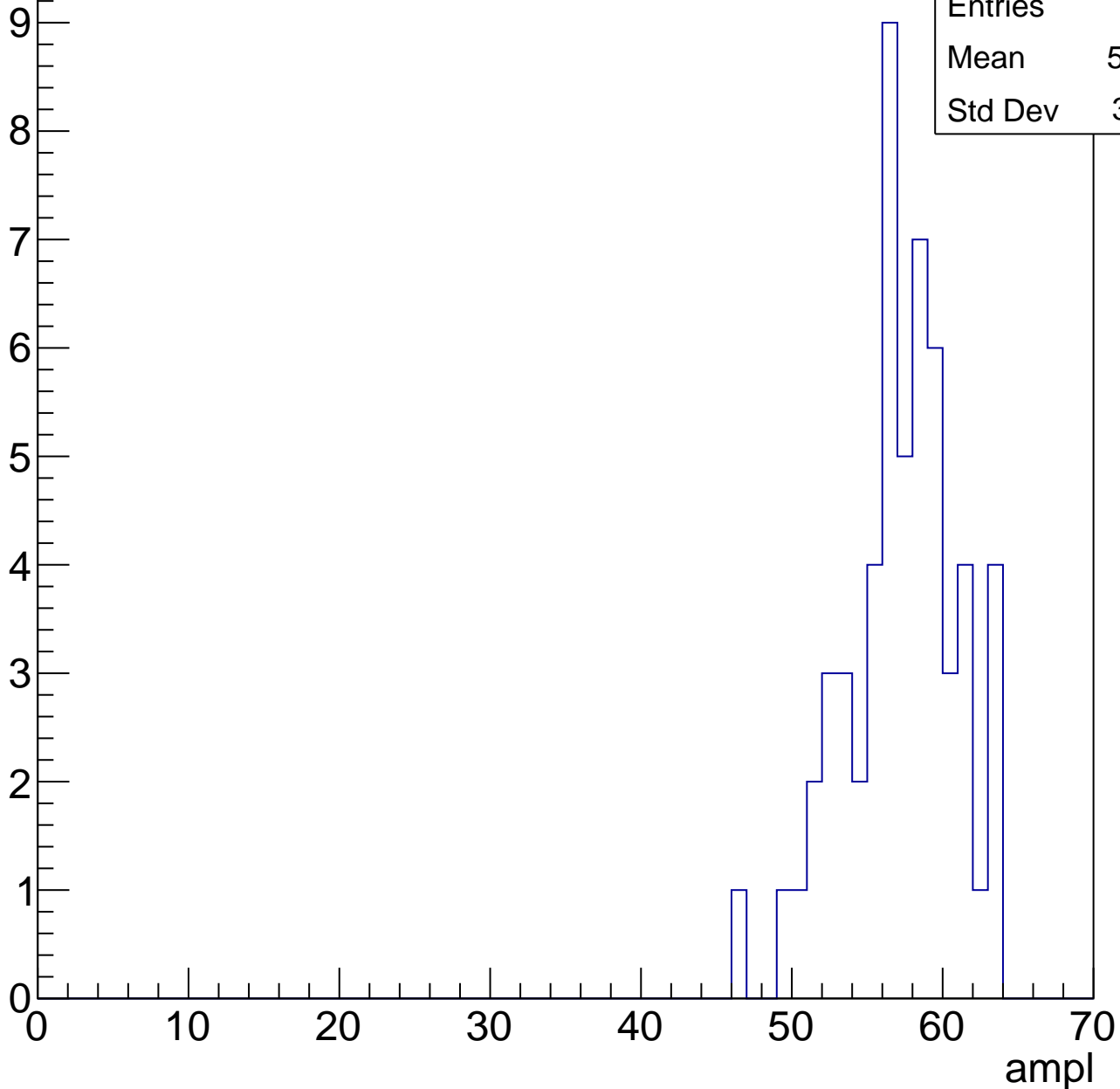


# B0L102S, U17-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	56.73
Std Dev	3.681



# B0L102S, U17-ch107, adc5

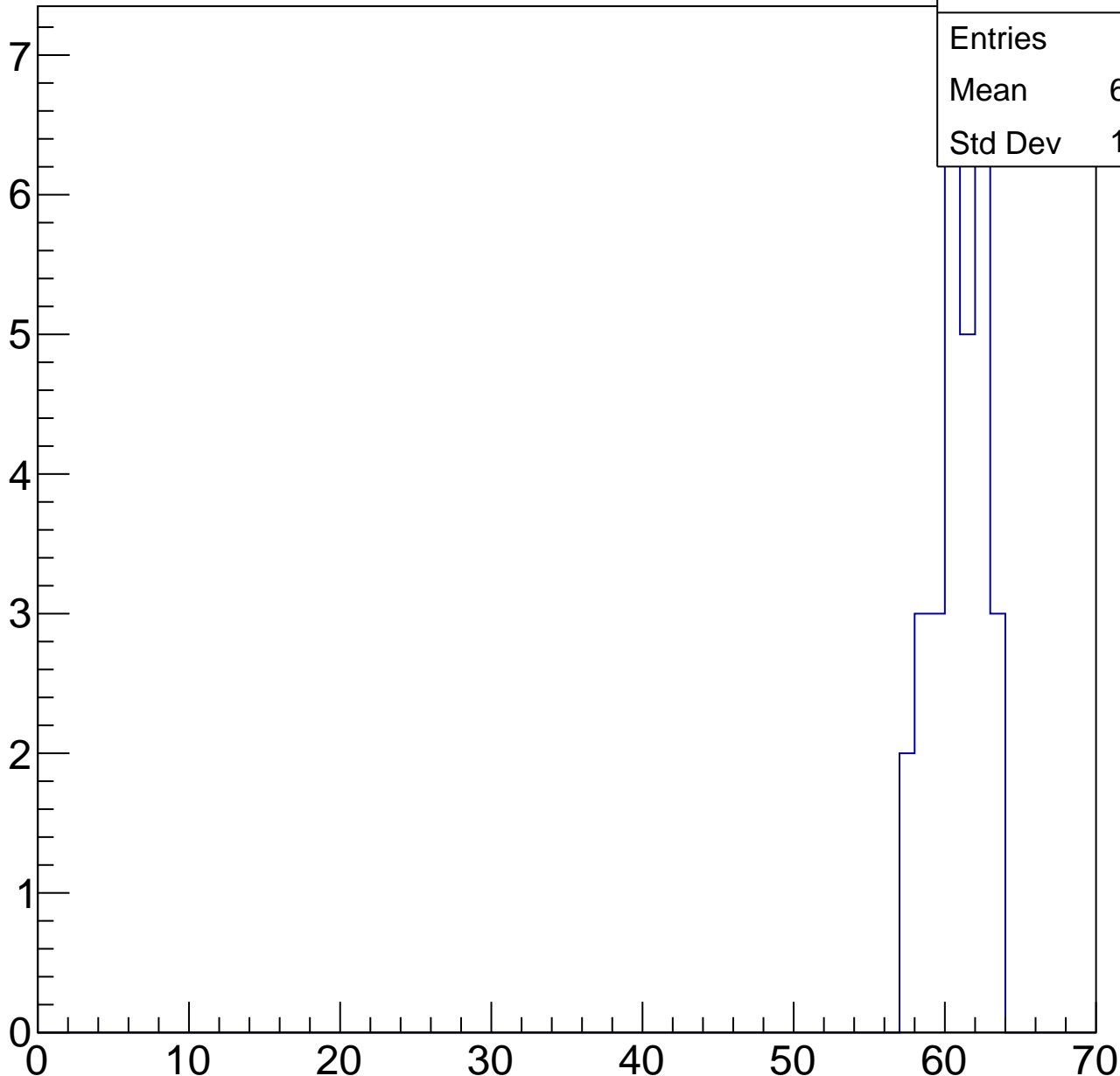
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	30
Mean	60.43
Std Dev	1.707

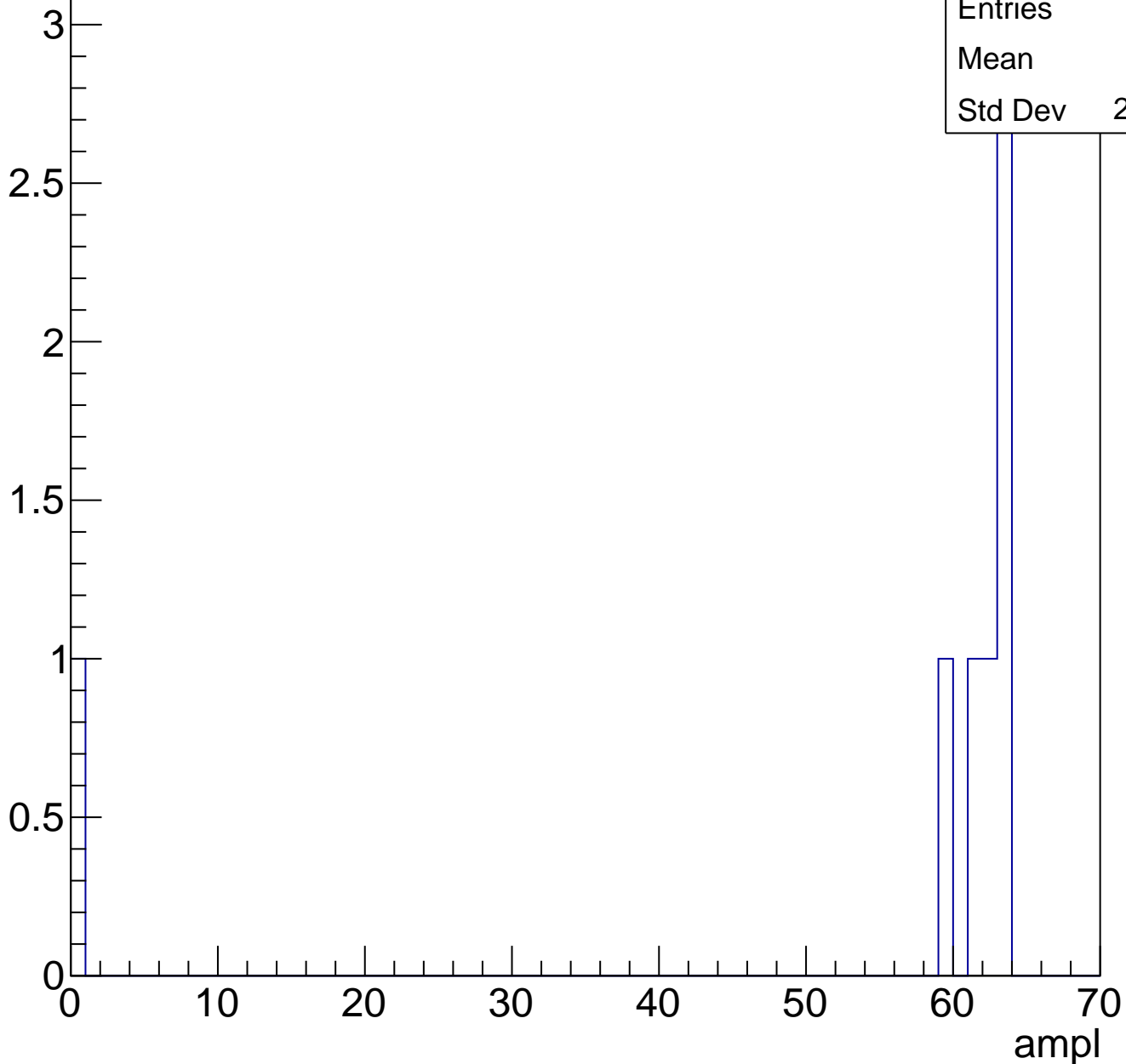
ampl



# B0L102S, U17-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

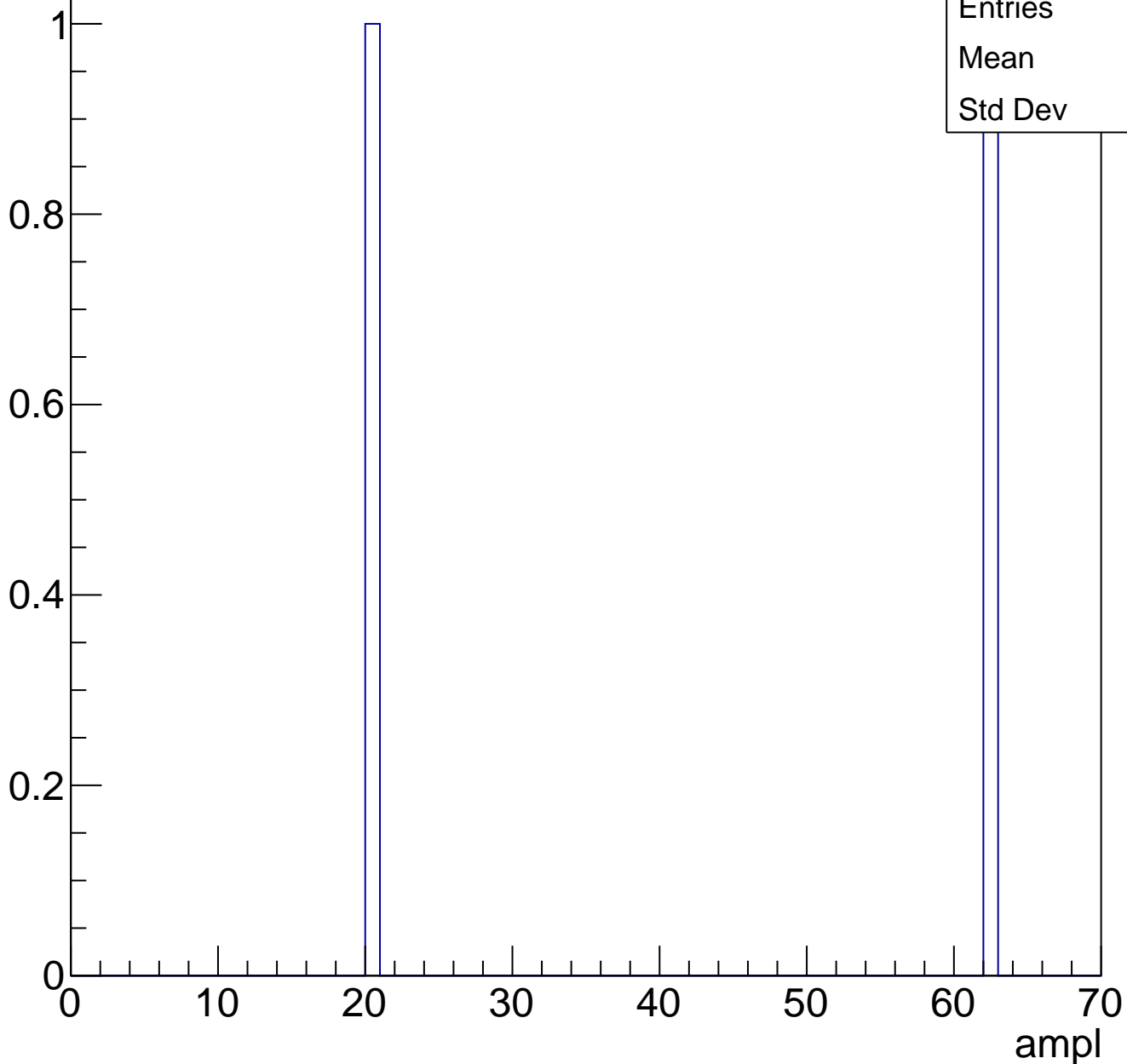




# B0L102S, U17-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	41
Std Dev	21

# B0L102S, U17-ch108, adc0

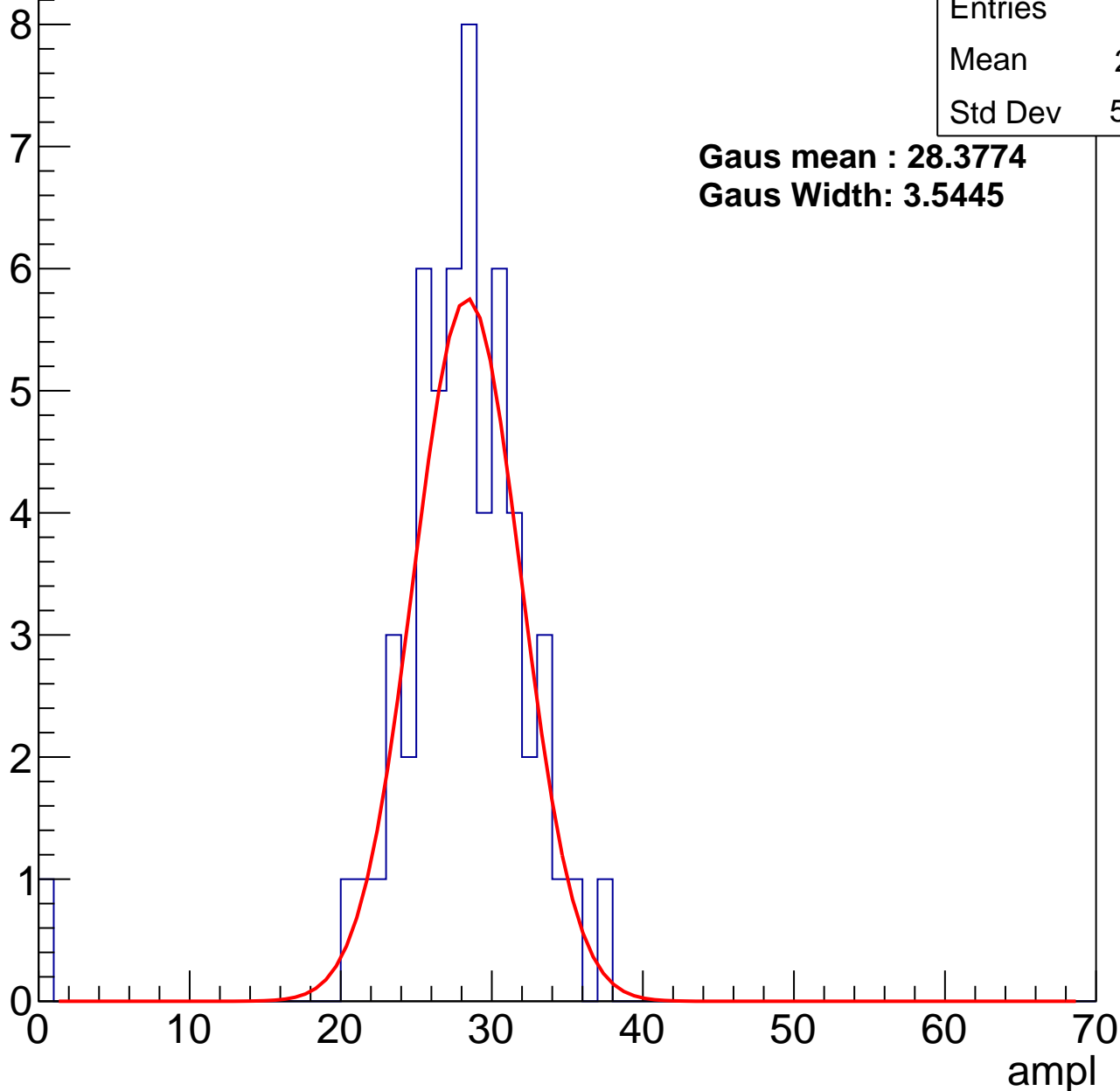
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	27.41
Std Dev	5.067

**Gaus mean : 28.3774**

**Gaus Width: 3.5445**



# B0L102S, U17-ch108, adc1

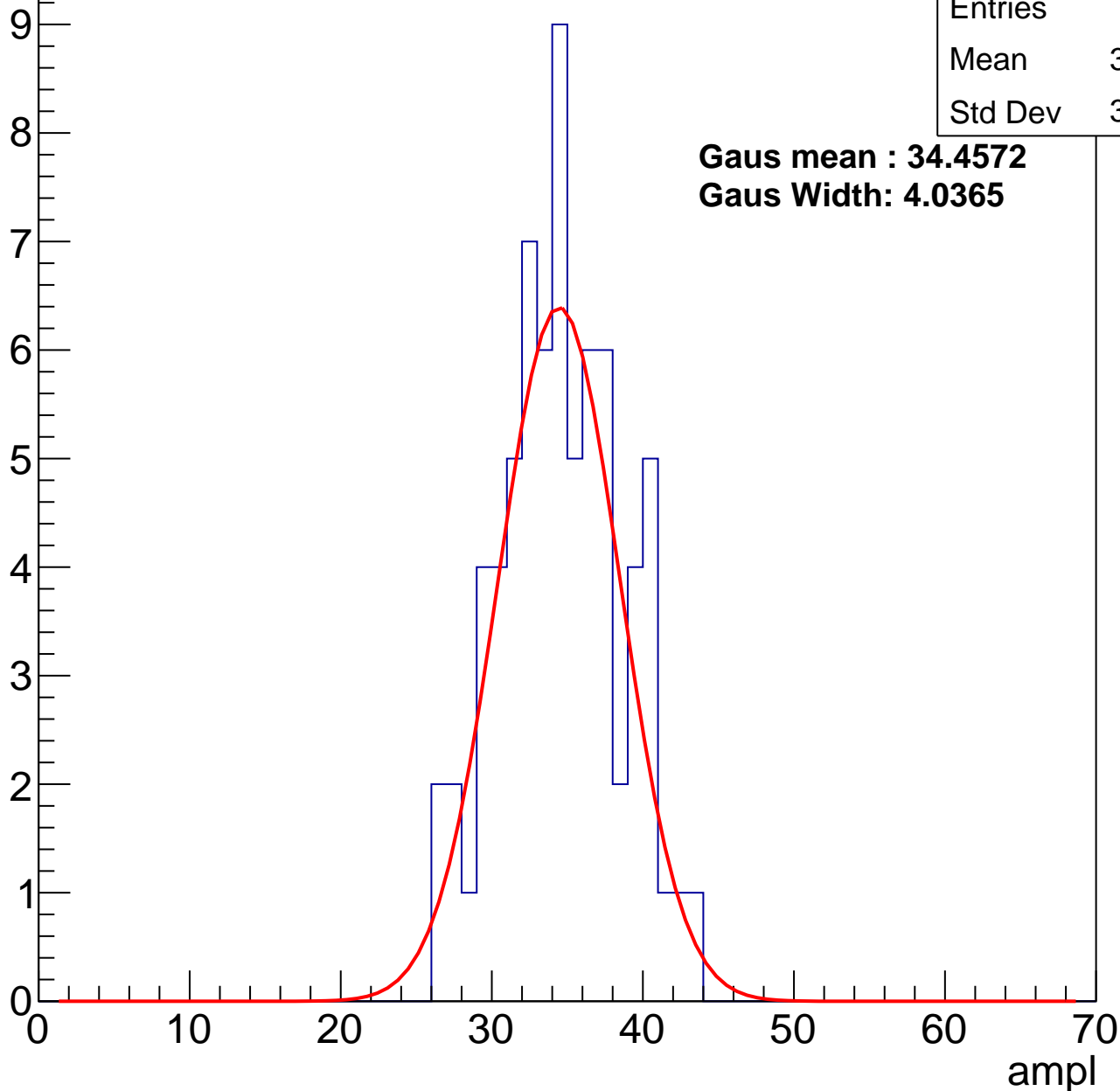
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	34.14
Std Dev	3.923

**Gaus mean : 34.4572**

**Gaus Width: 4.0365**



# B0L102S, U17-ch108, adc2

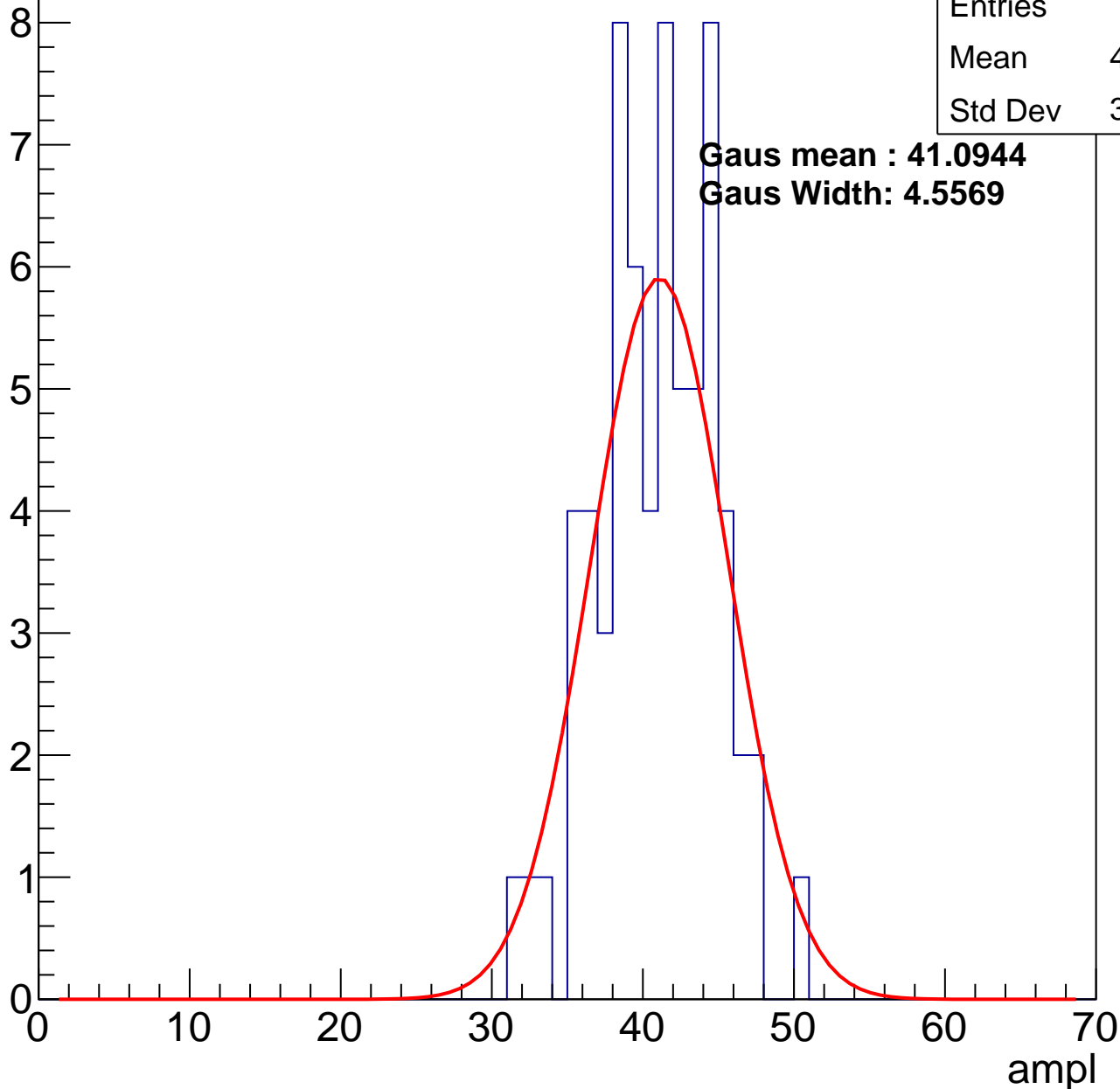
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	40.45
Std Dev	3.826

**Gaus mean : 41.0944**

**Gaus Width: 4.5569**

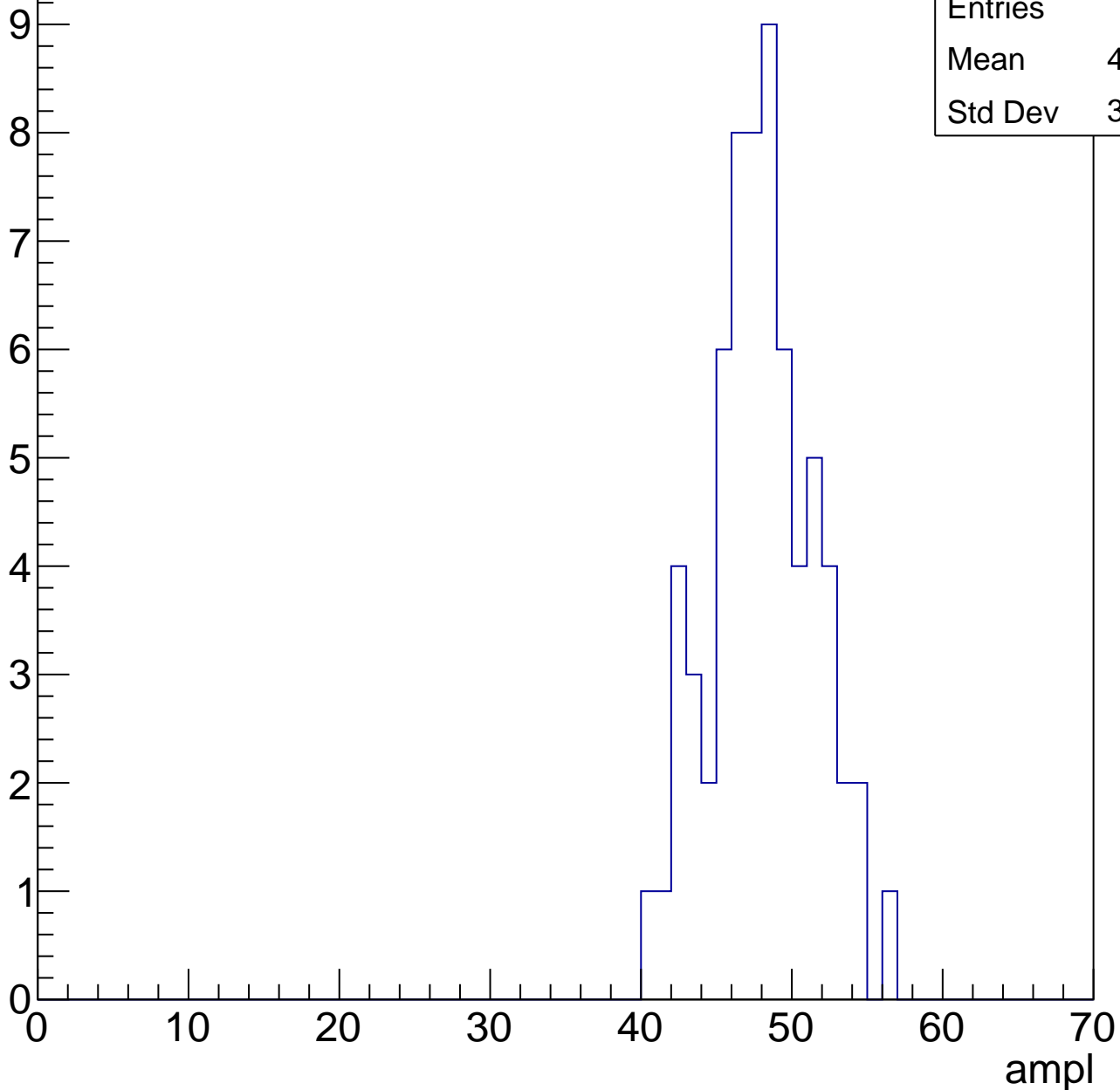


# B0L102S, U17-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.56
Std Dev	3.398

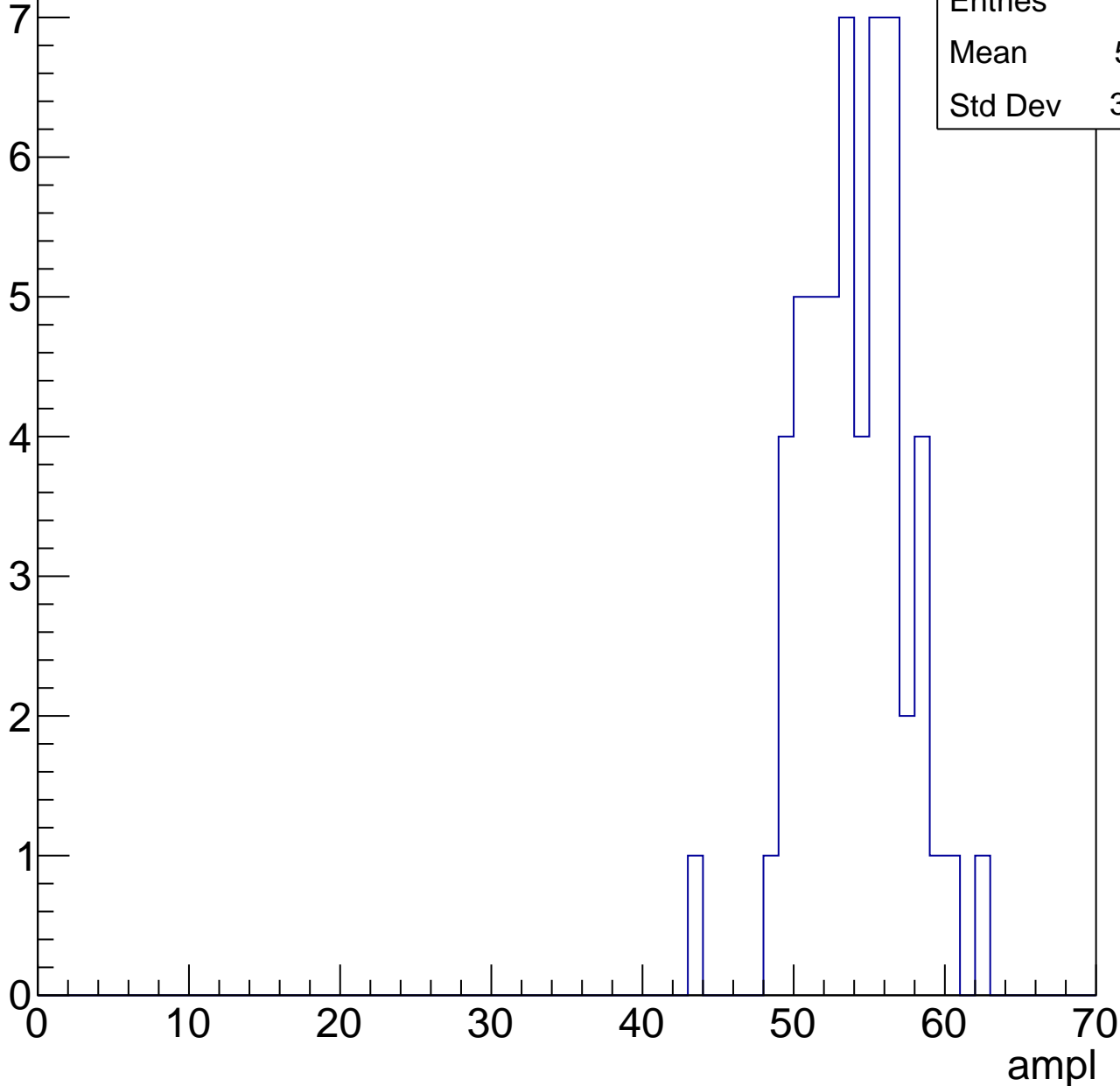


# B0L102S, U17-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	53.51
Std Dev	3.405

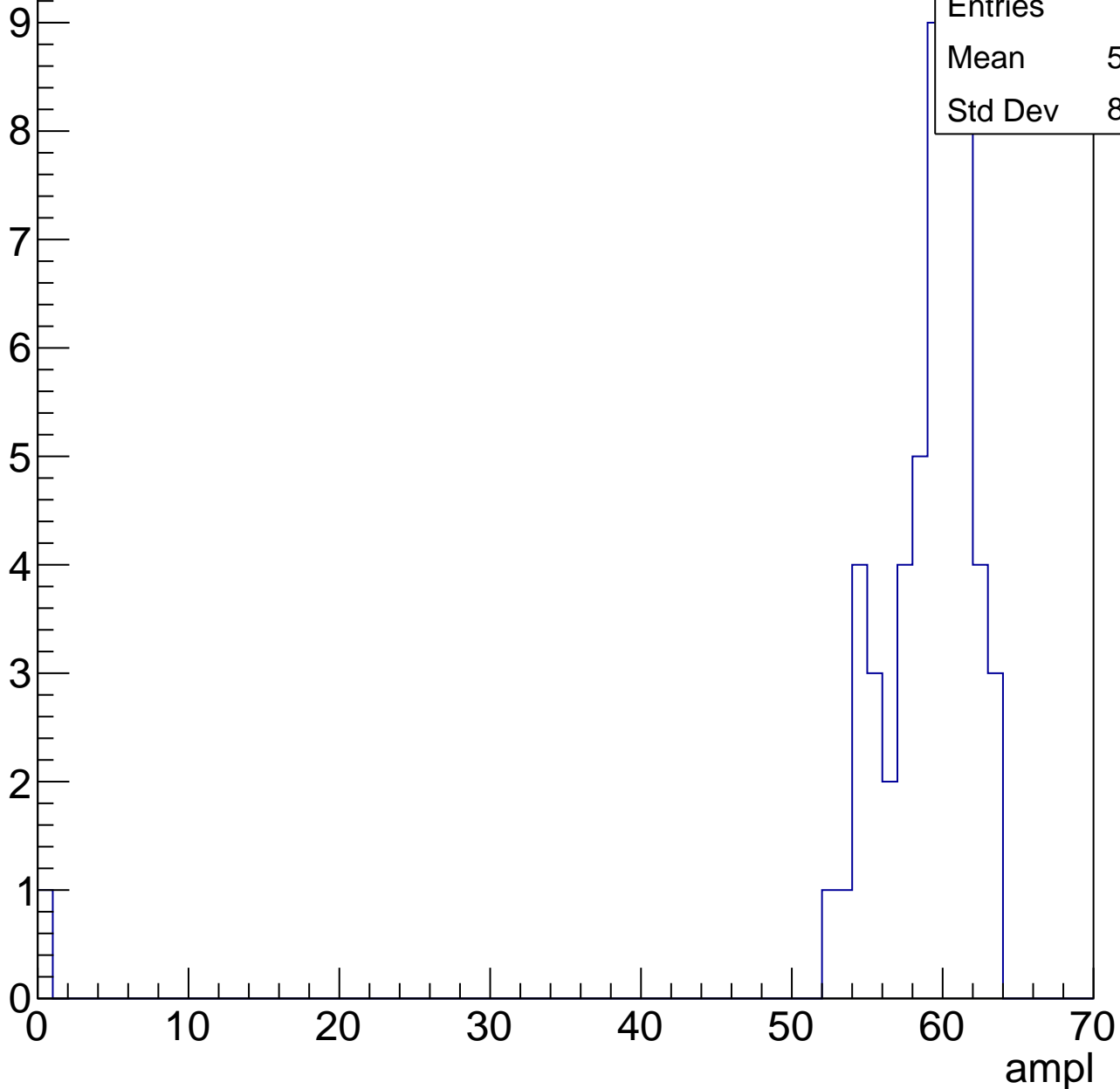


# B0L102S, U17-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	57.58
Std Dev	8.435

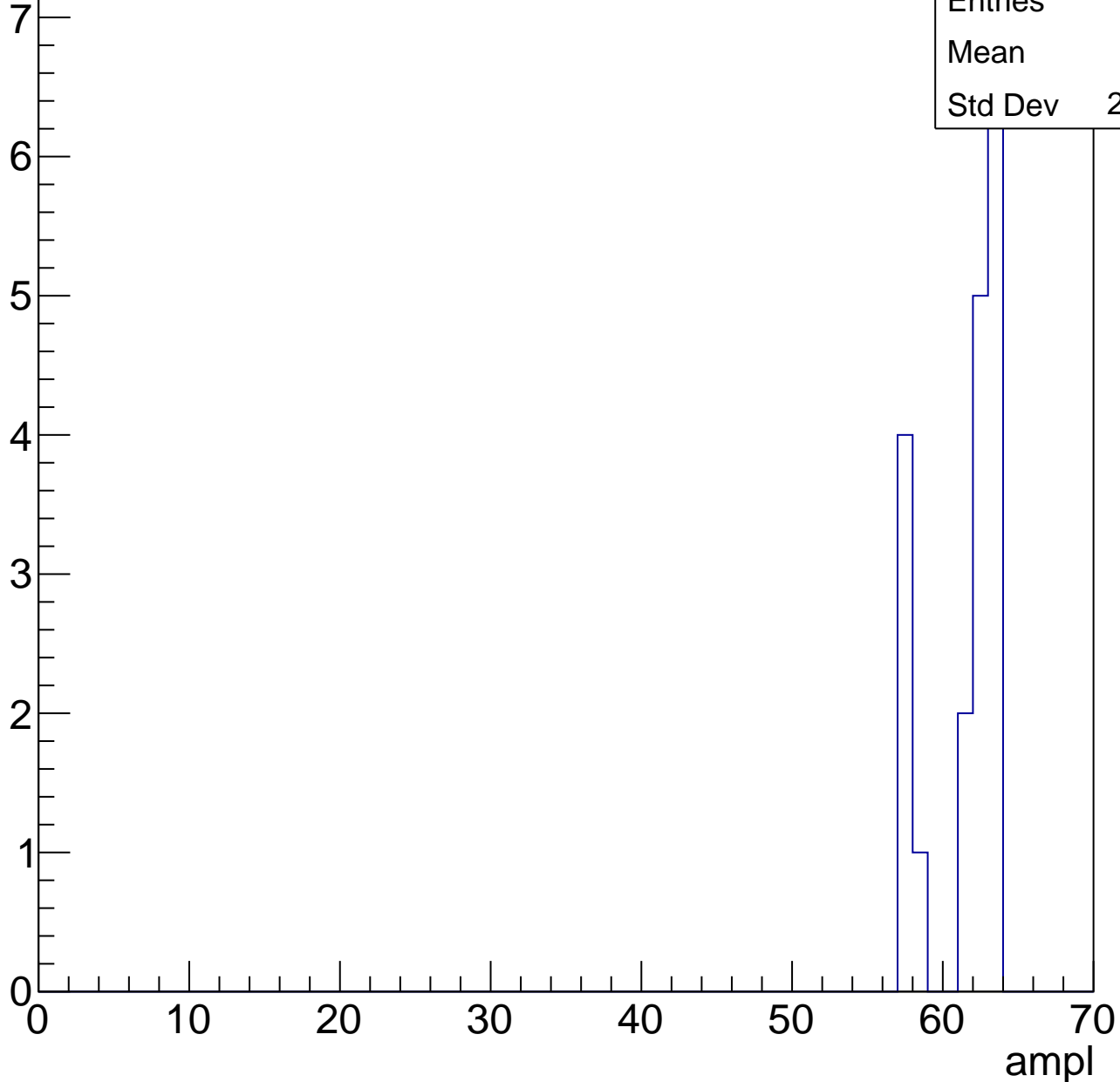


# B0L102S, U17-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	19
Mean	61
Std Dev	2.362





# B0L102S, U17-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch109, adc0

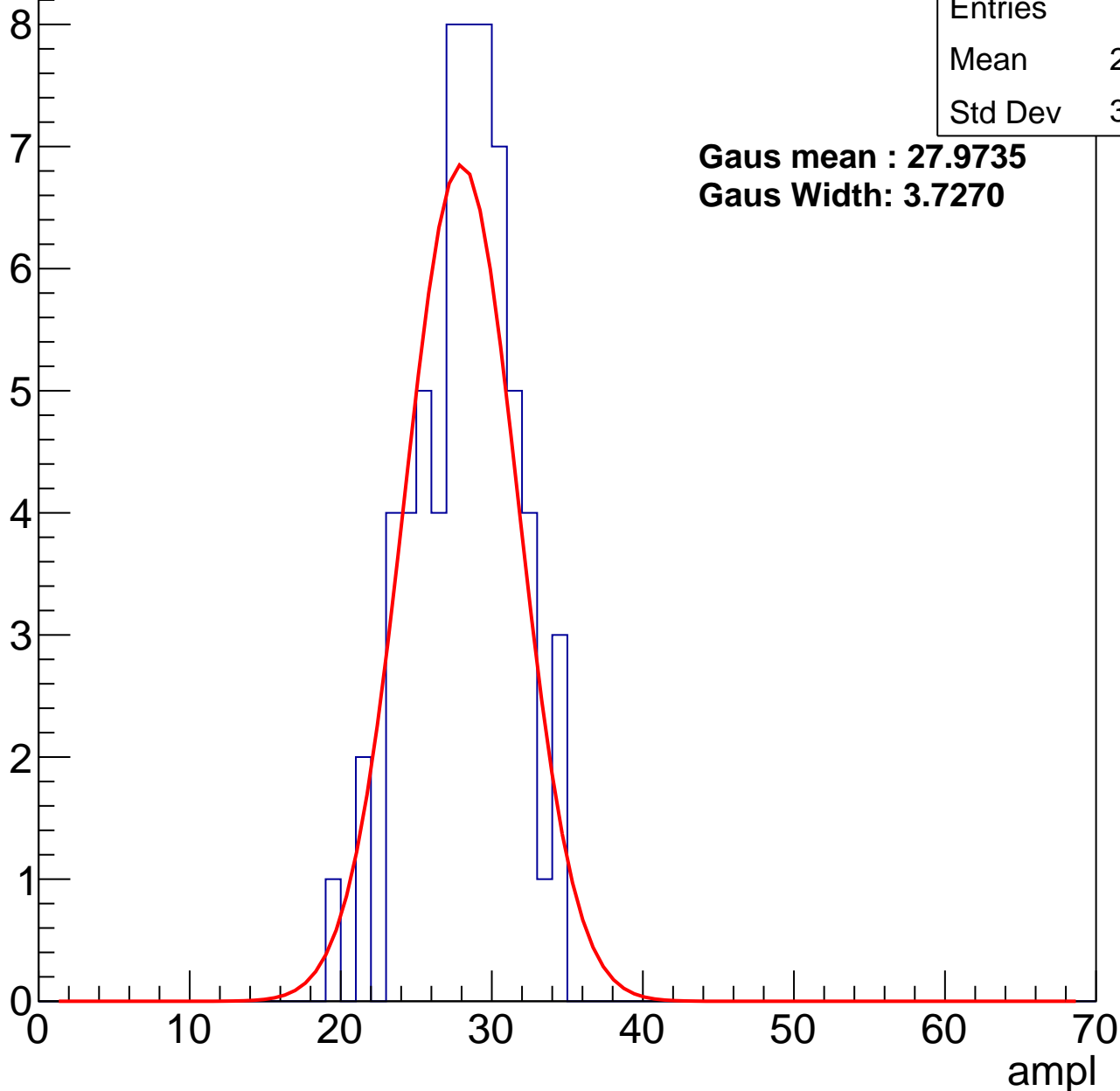
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	27.78
Std Dev	3.276

**Gaus mean : 27.9735**

**Gaus Width: 3.7270**



# B0L102S, U17-ch109, adc1

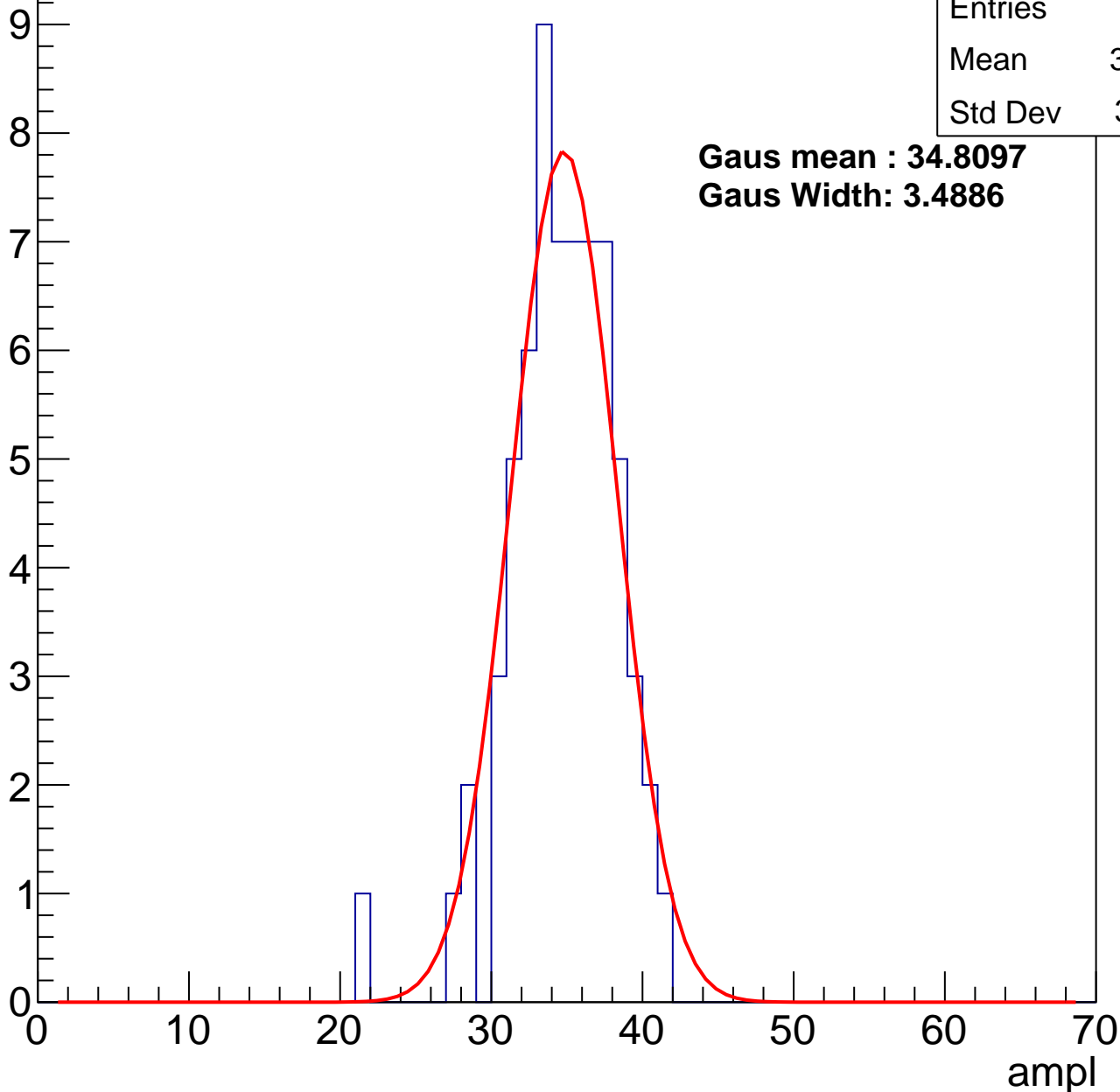
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.24
Std Dev	3.451

**Gaus mean : 34.8097**

**Gaus Width: 3.4886**



# B0L102S, U17-ch109, adc2

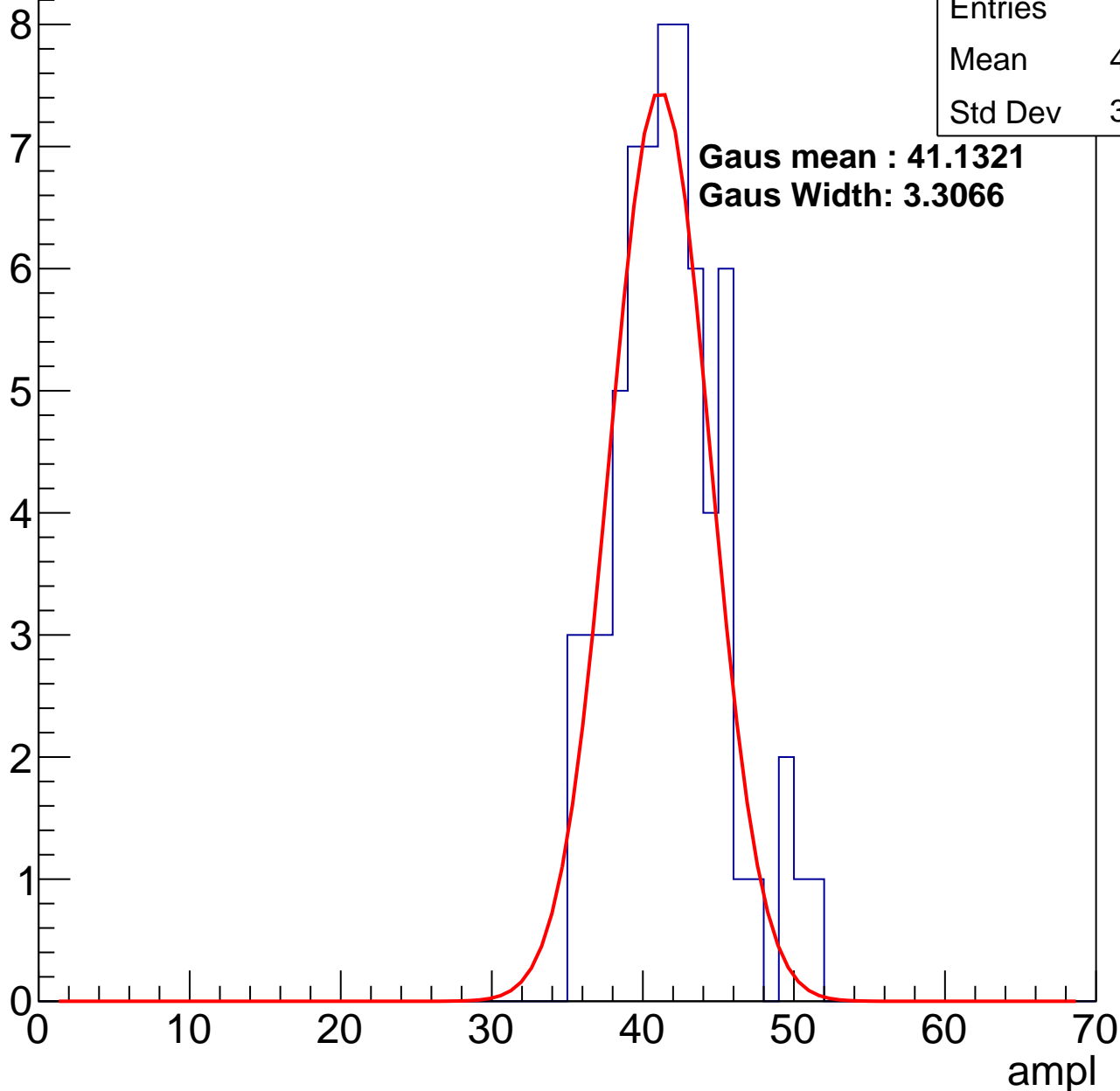
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.32
Std Dev	3.564

**Gaus mean : 41.1321**

**Gaus Width: 3.3066**

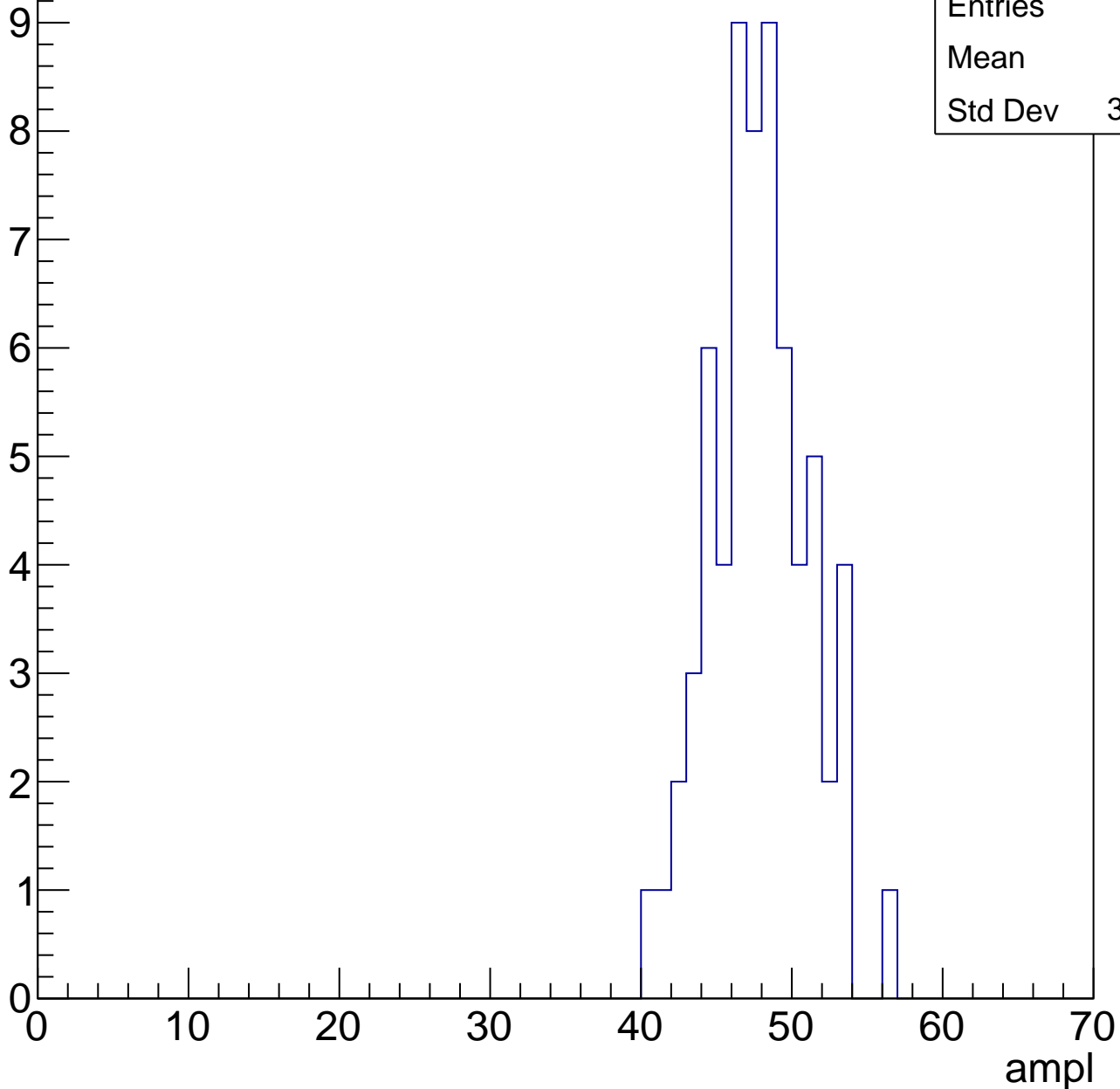


# B0L102S, U17-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	47.4
Std Dev	3.224

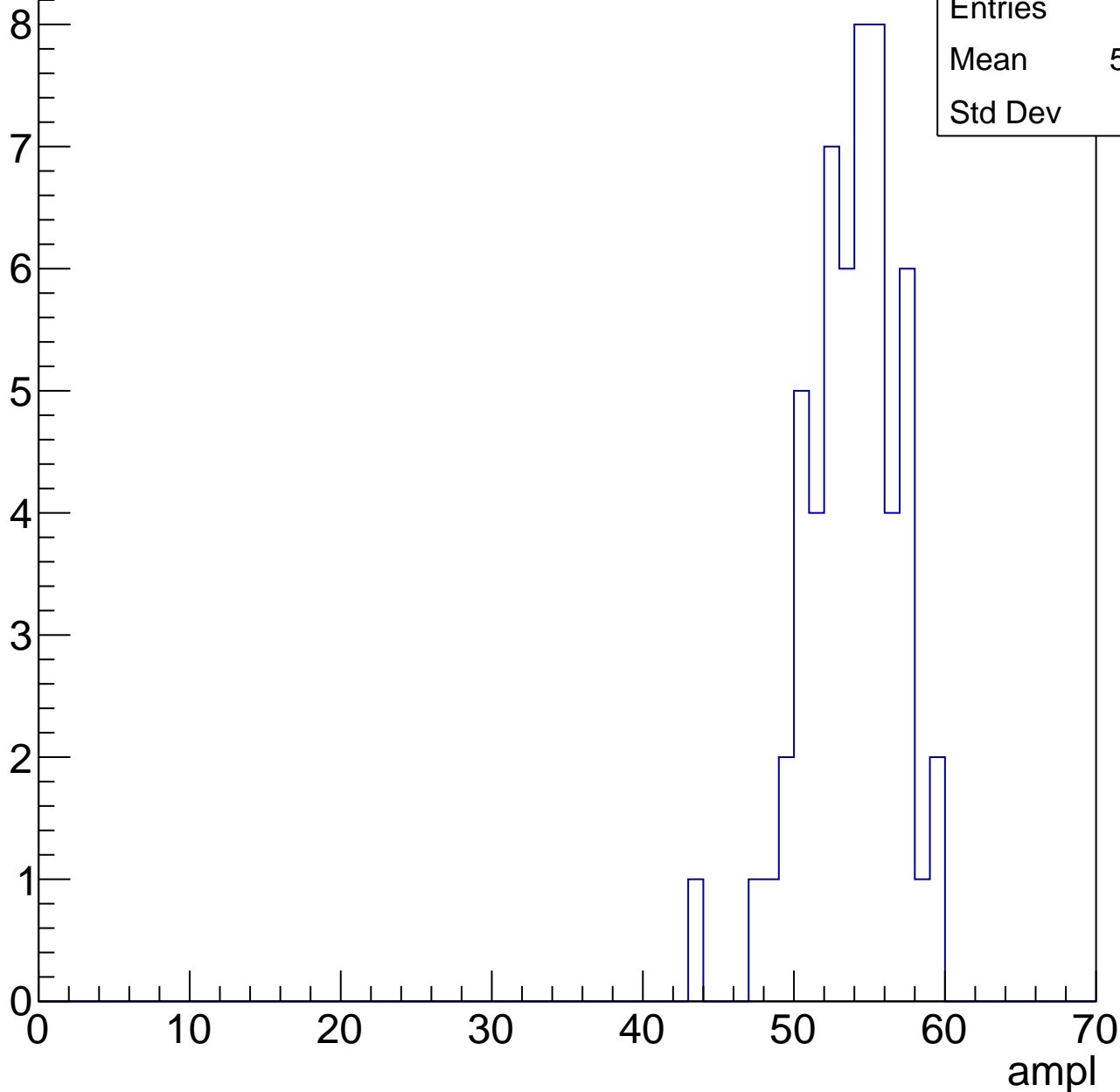


# B0L102S, U17-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	53.32
Std Dev	3.06



# B0L102S, U17-ch109, adc5

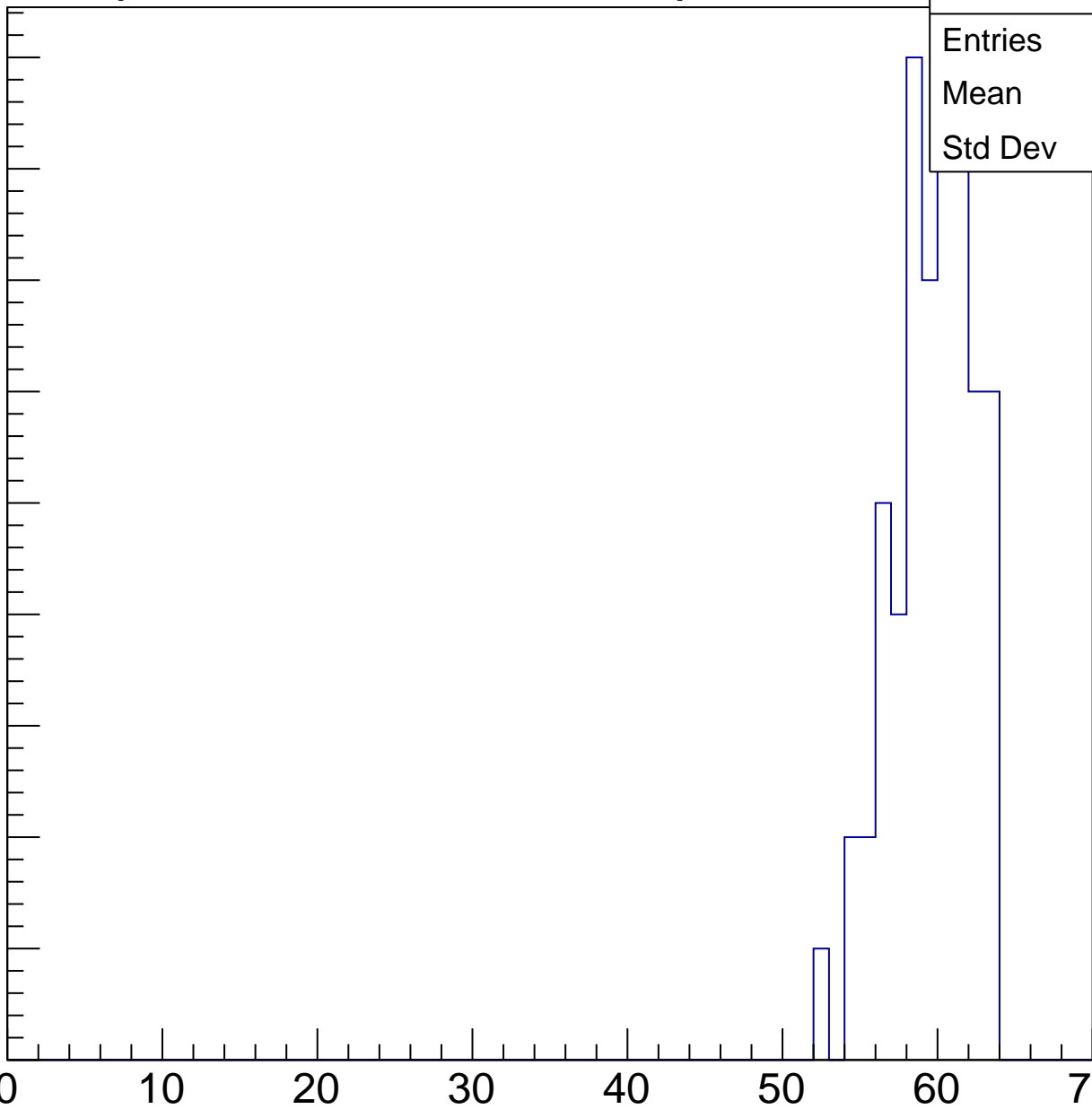
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	59.16
Std Dev	2.592

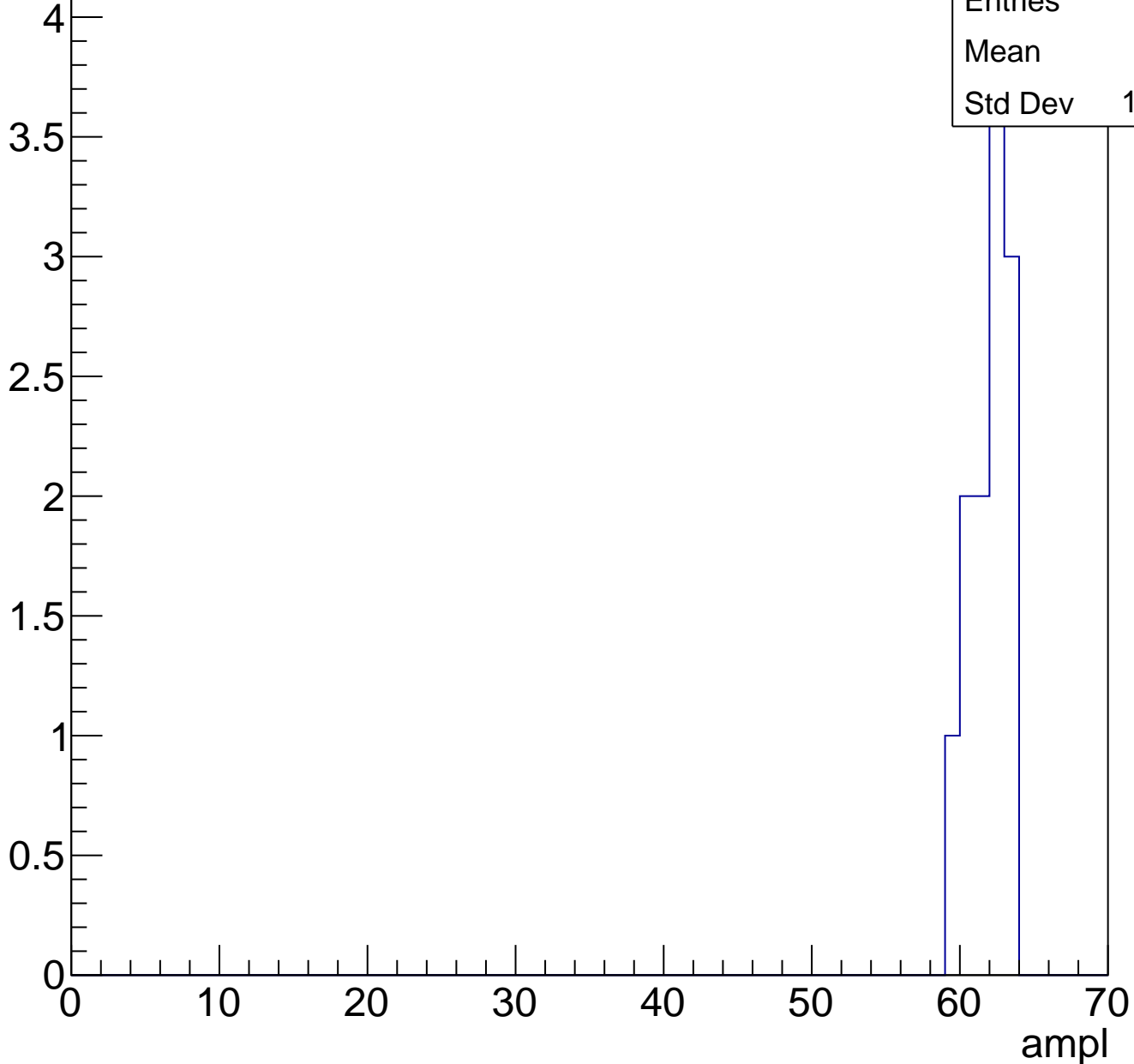
ampl



# B0L102S, U17-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch110, adc0

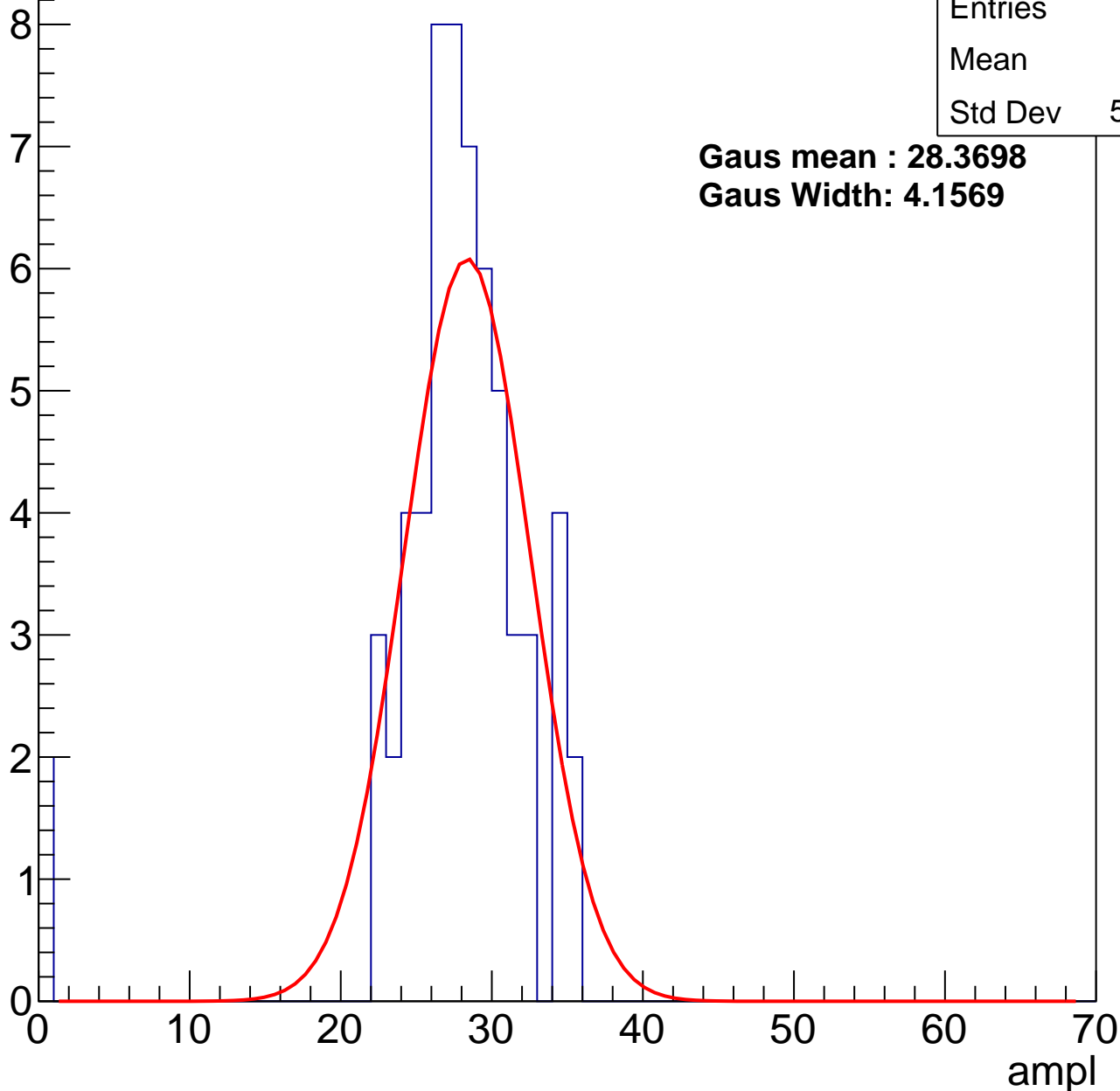
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	27
Std Dev	5.934

**Gaus mean : 28.3698**

**Gaus Width: 4.1569**



# B0L102S, U17-ch110, adc1

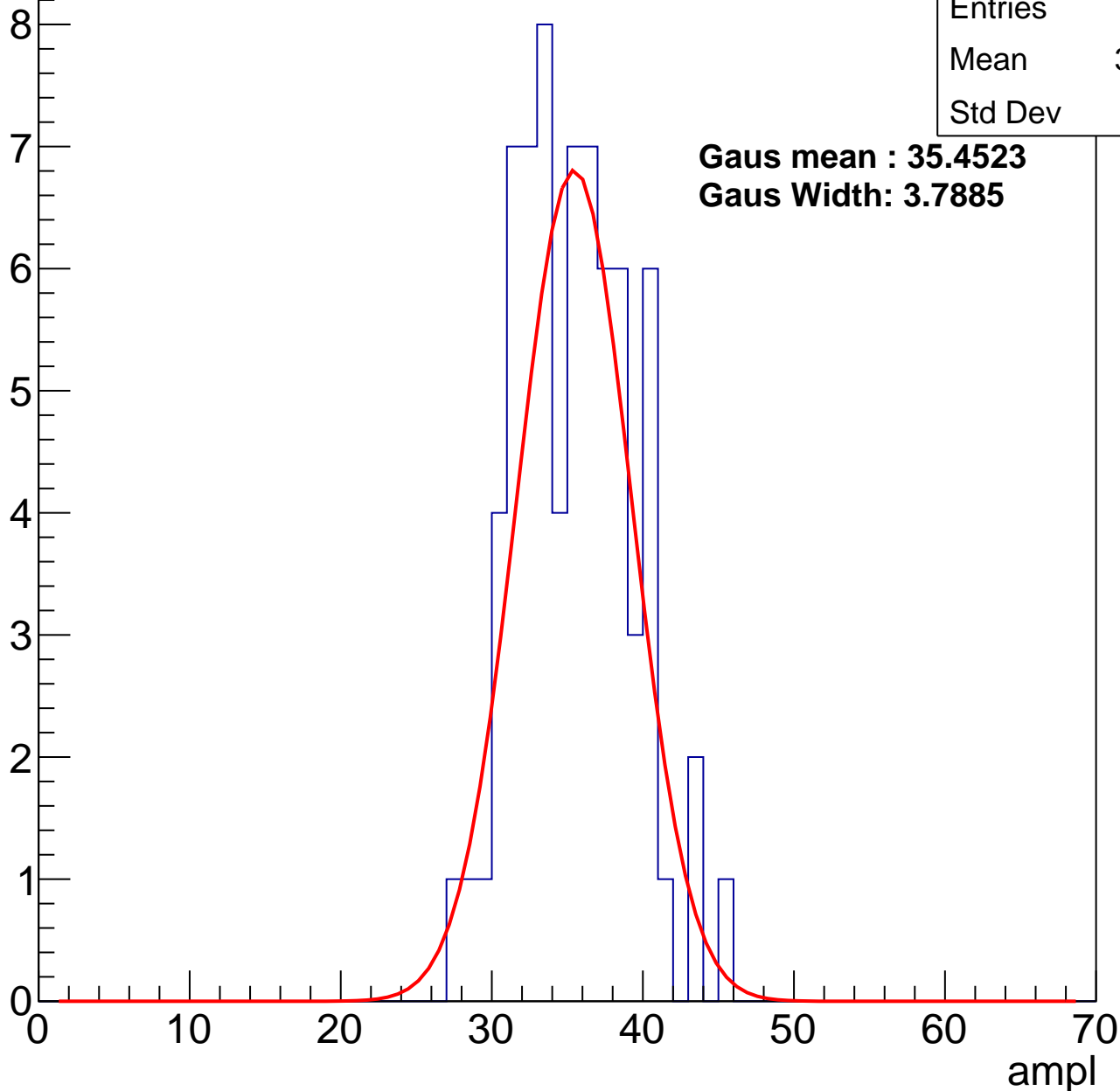
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	35.01
Std Dev	3.74

**Gaus mean : 35.4523**

**Gaus Width: 3.7885**



# B0L102S, U17-ch110, adc2

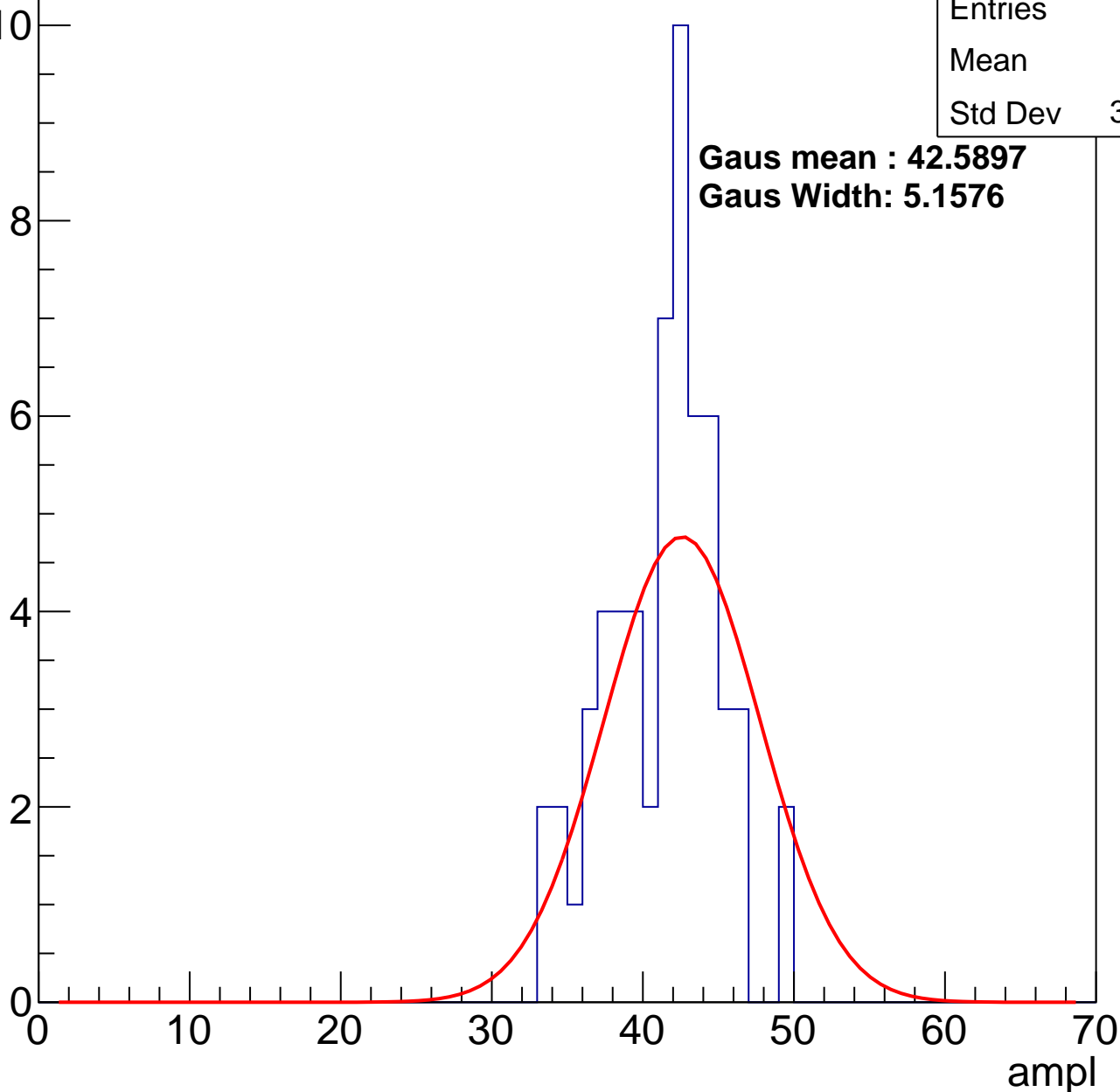
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	59
Mean	40.9
Std Dev	3.667

**Gaus mean : 42.5897**

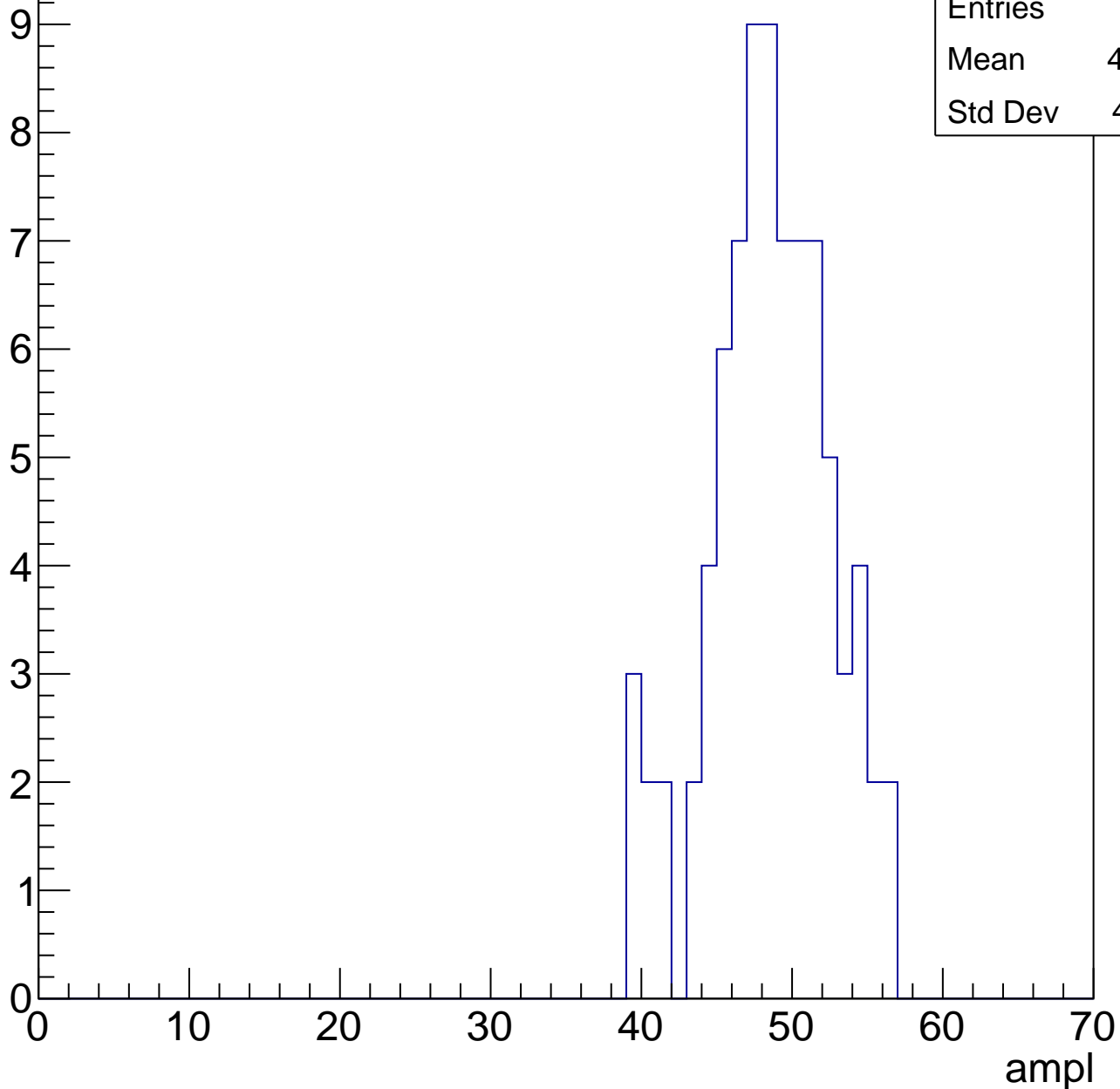
**Gaus Width: 5.1576**



# B0L102S, U17-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

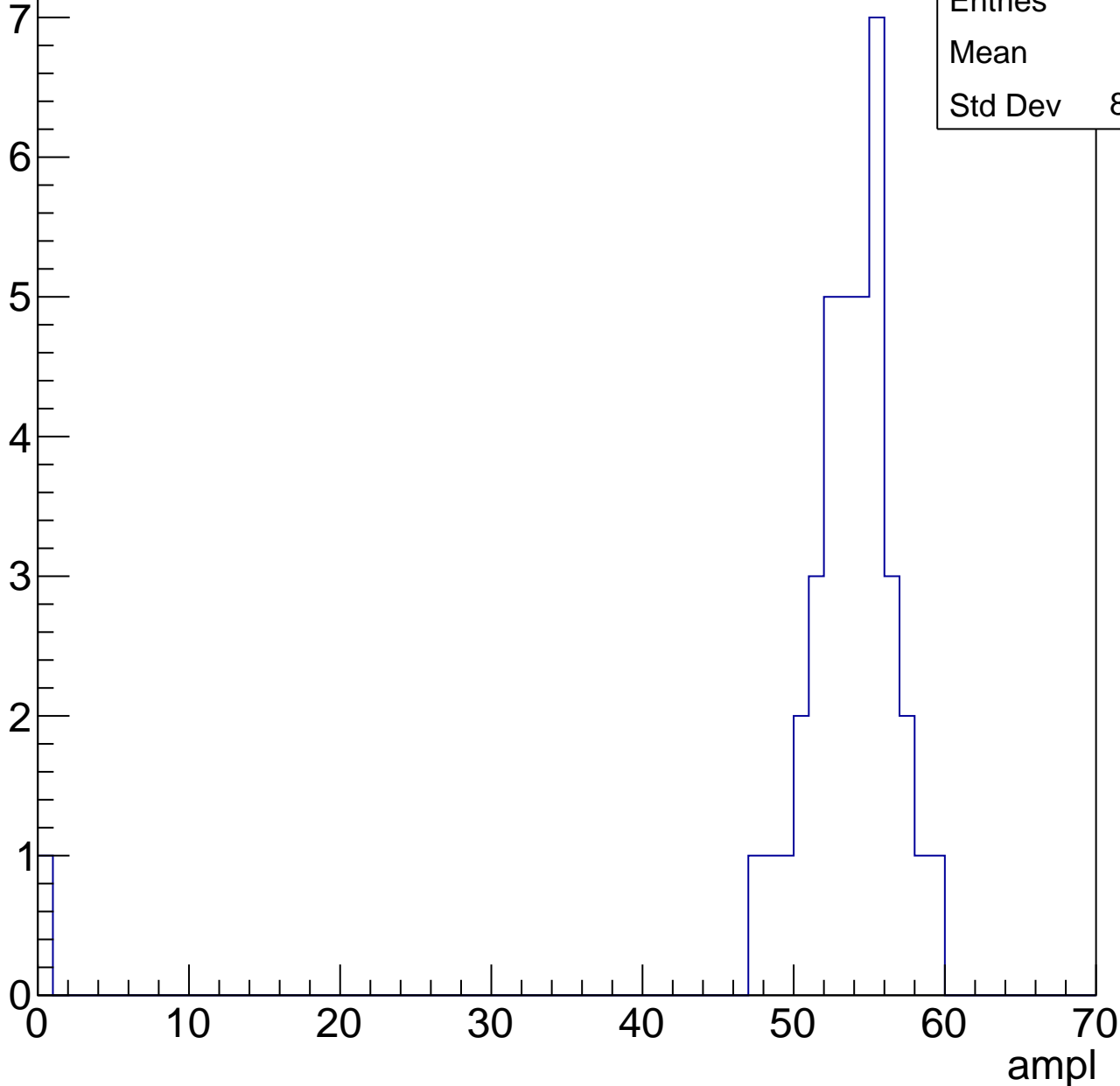


# B0L102S, U17-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	52
Std Dev	8.938

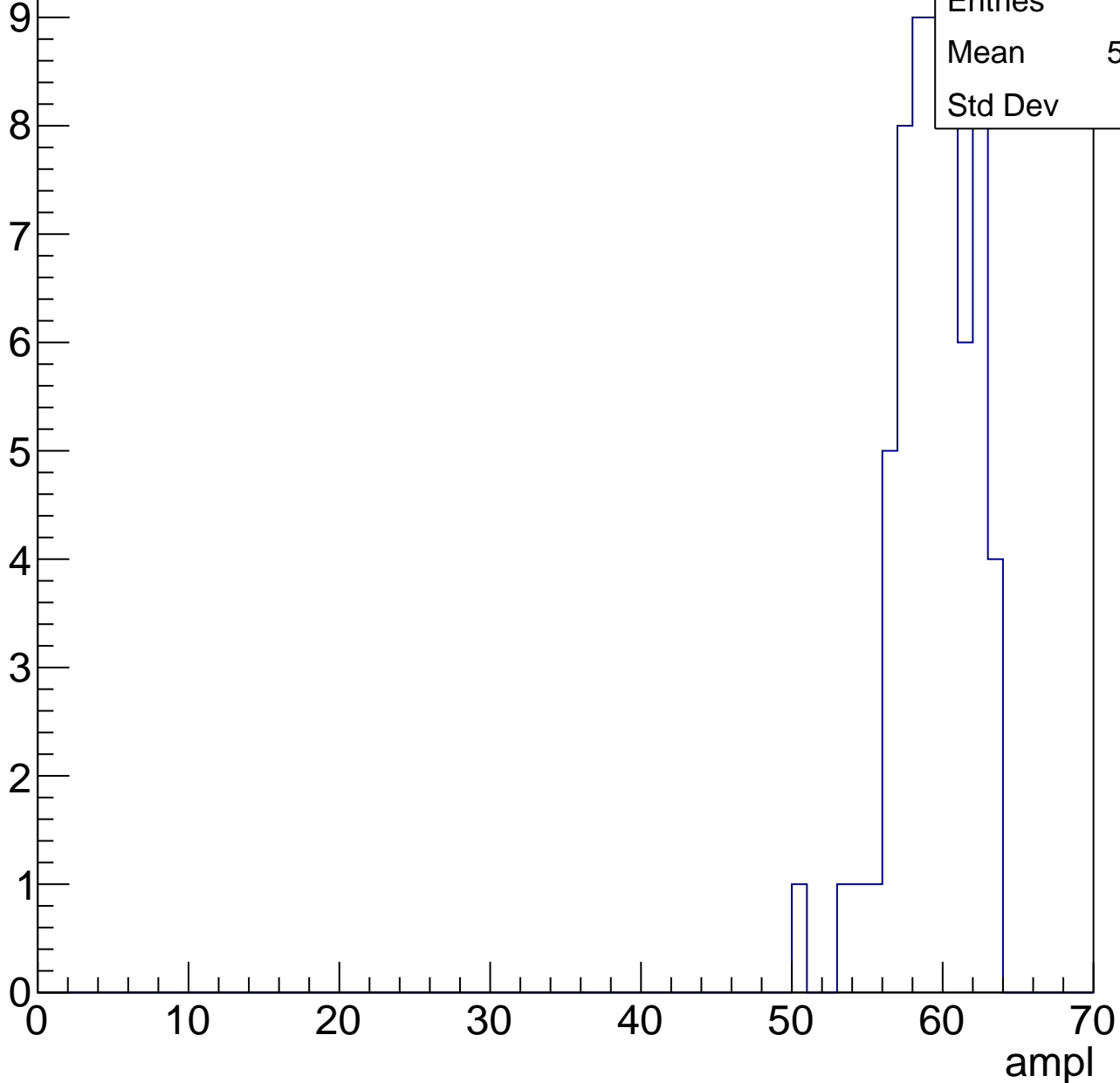


# B0L102S, U17-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	58.95
Std Dev	2.58

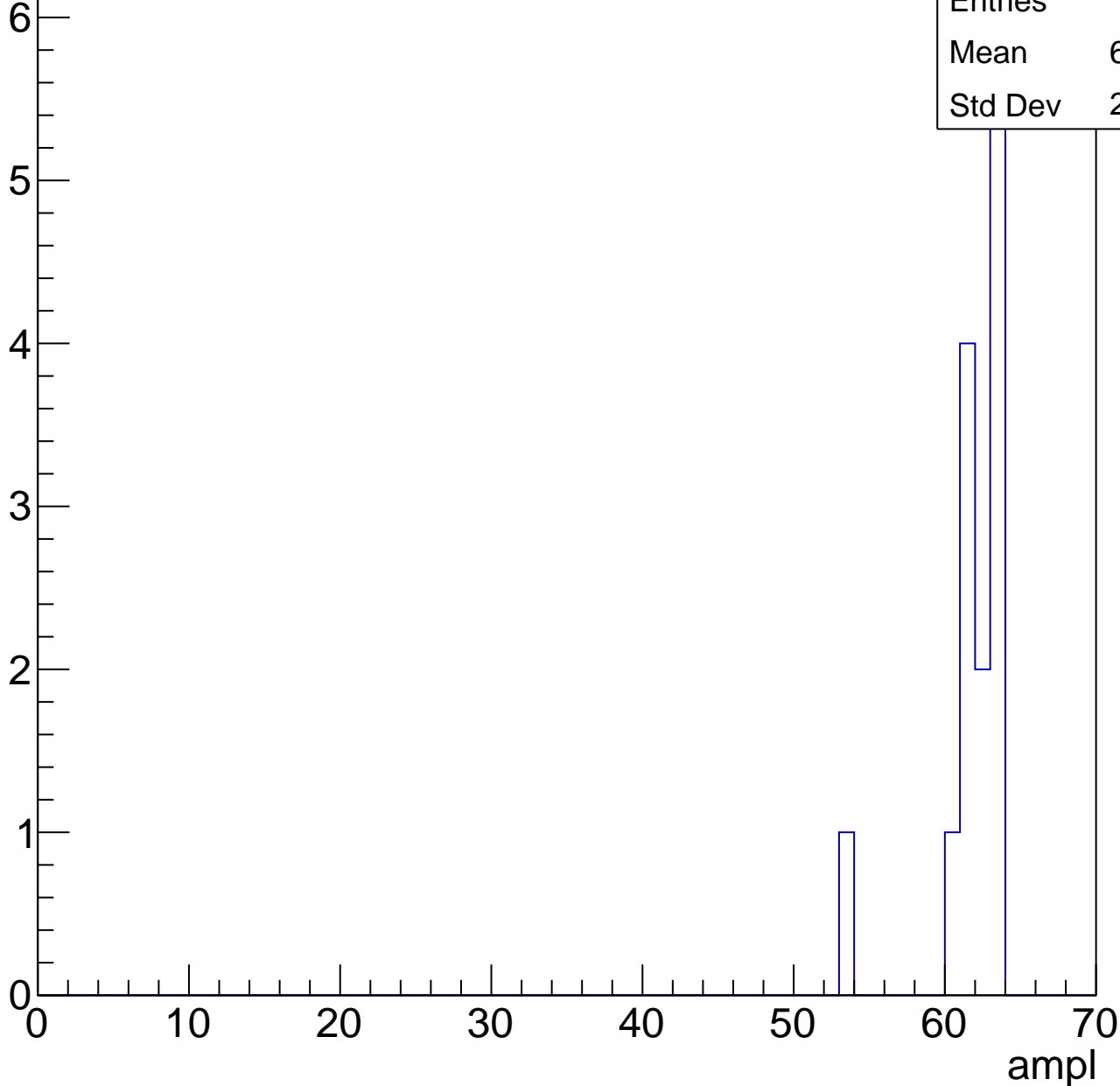


# B0L102S, U17-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.36
Std Dev	2.524





# B0L102S, U17-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L102S, U17-ch111, adc0

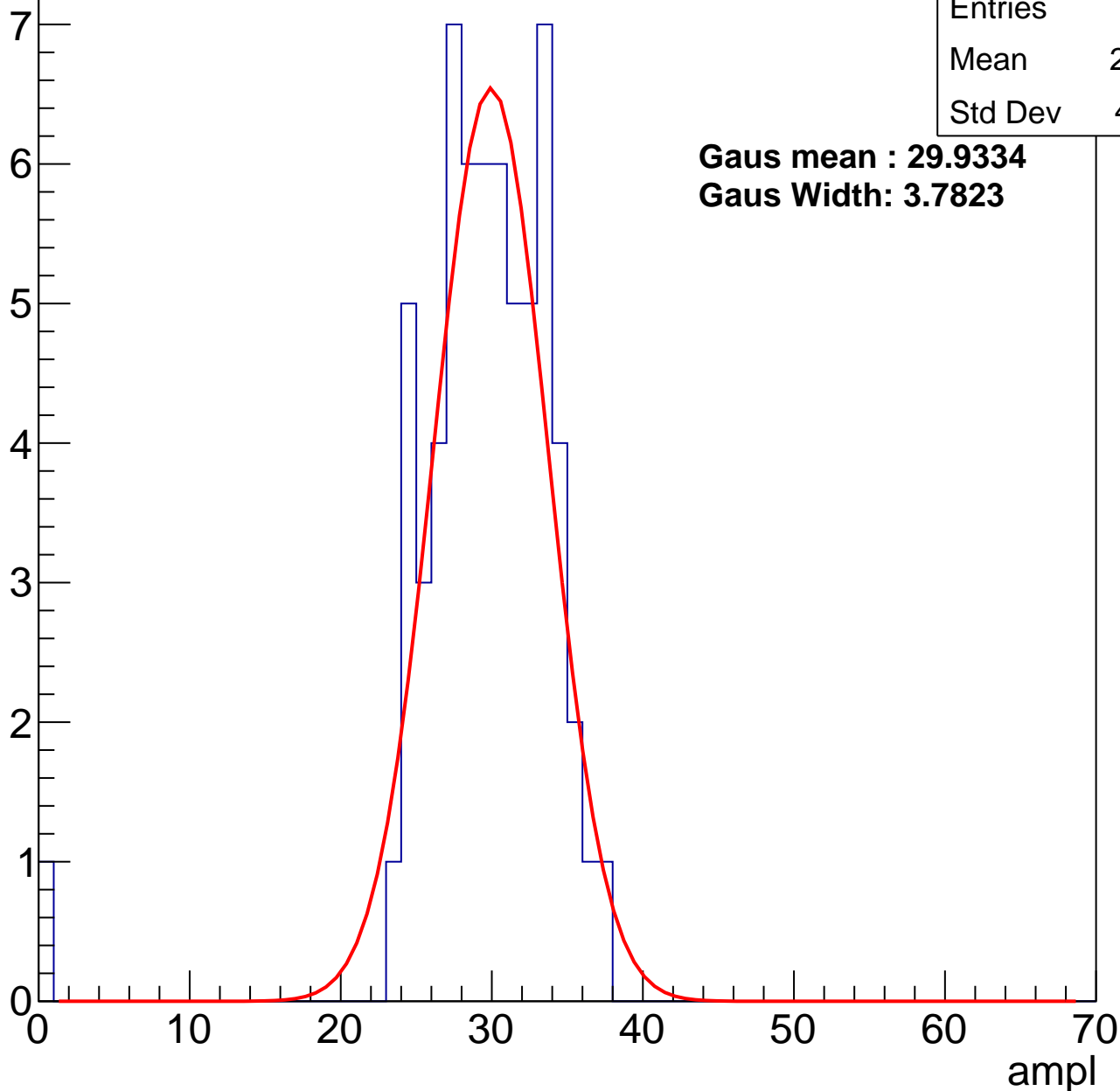
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.03
Std Dev	4.981

**Gaus mean : 29.9334**

**Gaus Width: 3.7823**



# B0L102S, U17-ch111, adc1

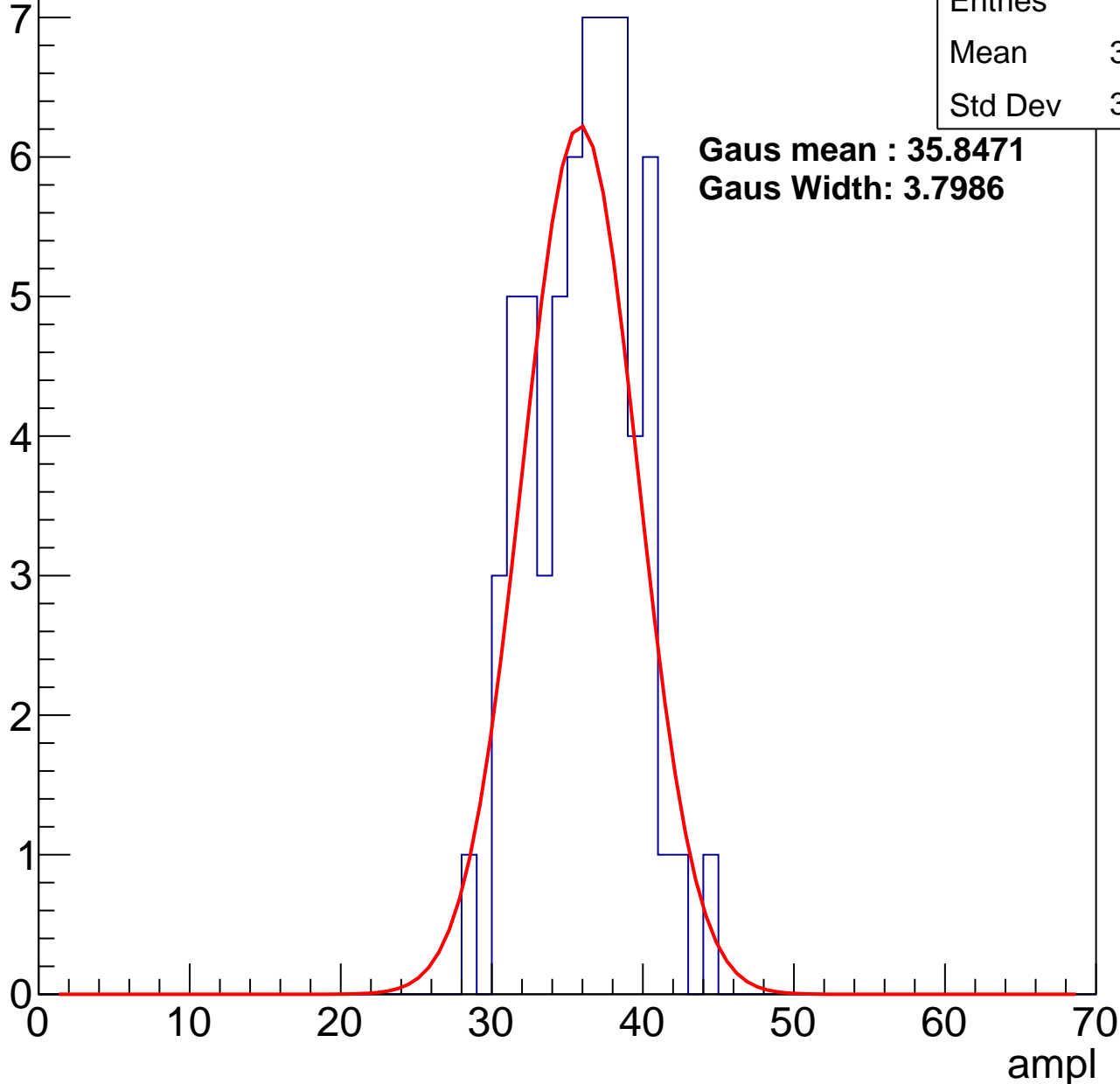
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	35.68
Std Dev	3.397

**Gaus mean : 35.8471**

**Gaus Width: 3.7986**



# B0L102S, U17-ch111, adc2

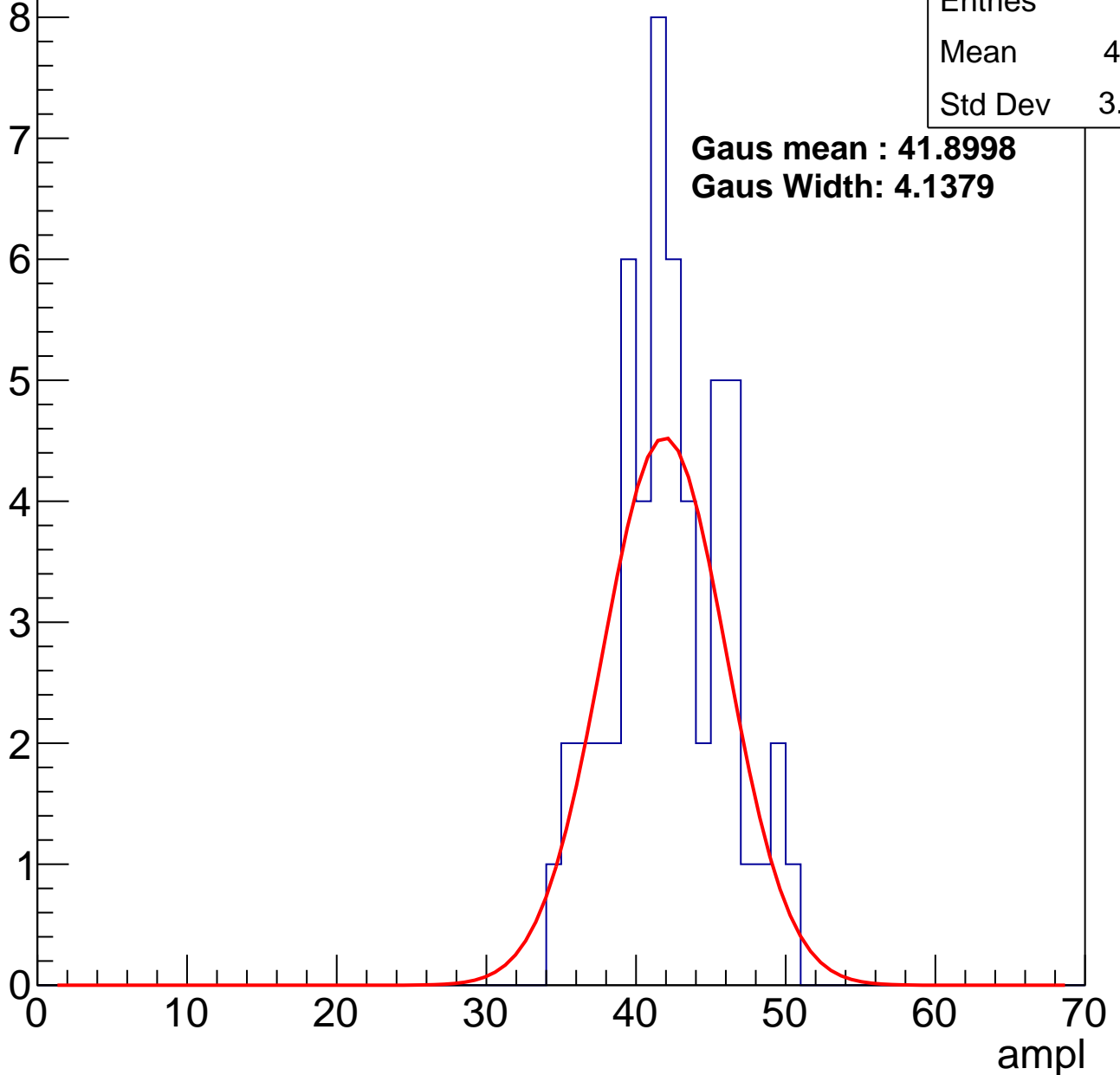
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	41.81
Std Dev	3.732

**Gaus mean : 41.8998**

**Gaus Width: 4.1379**

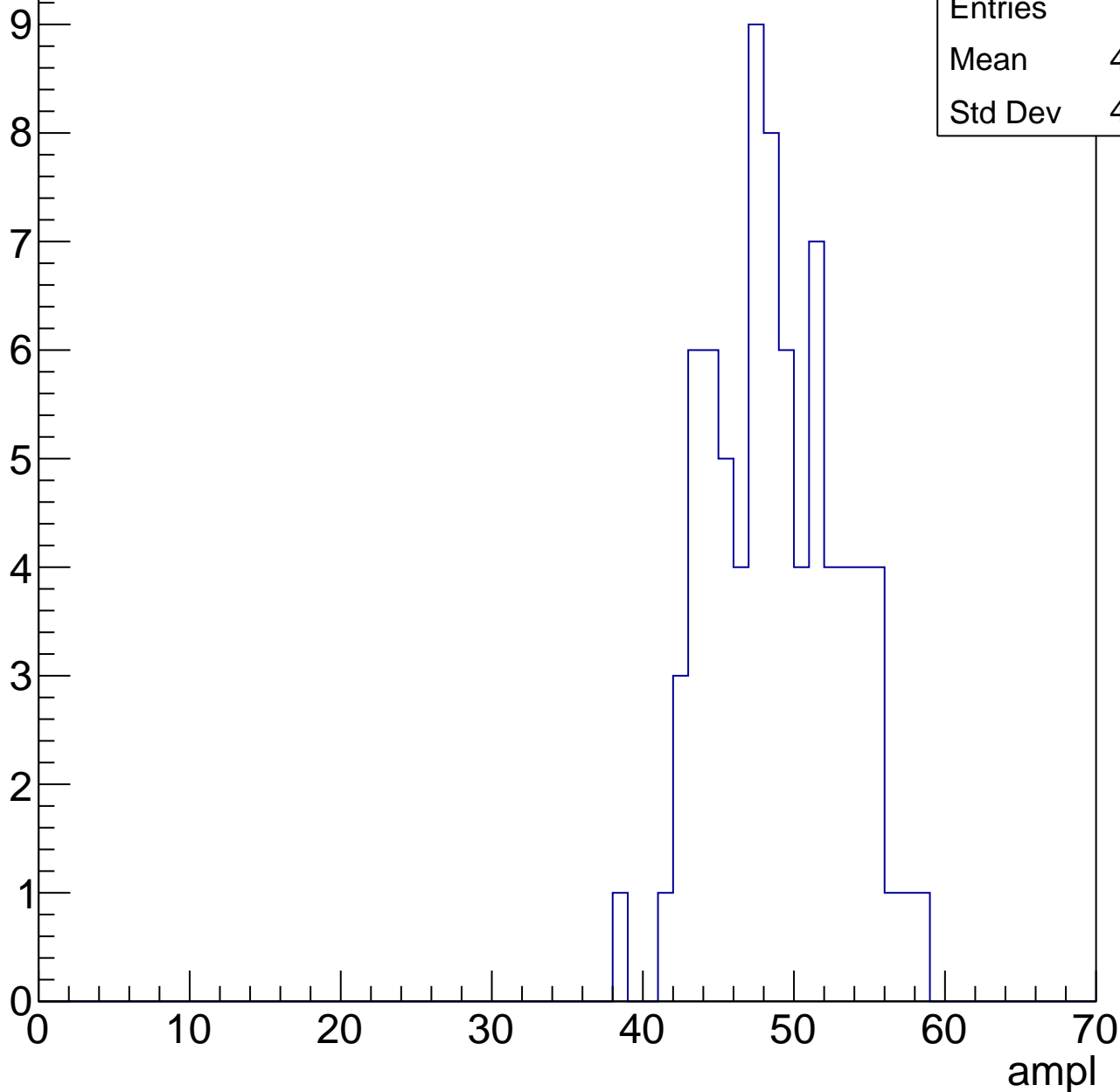


# B0L102S, U17-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	48.37
Std Dev	4.204

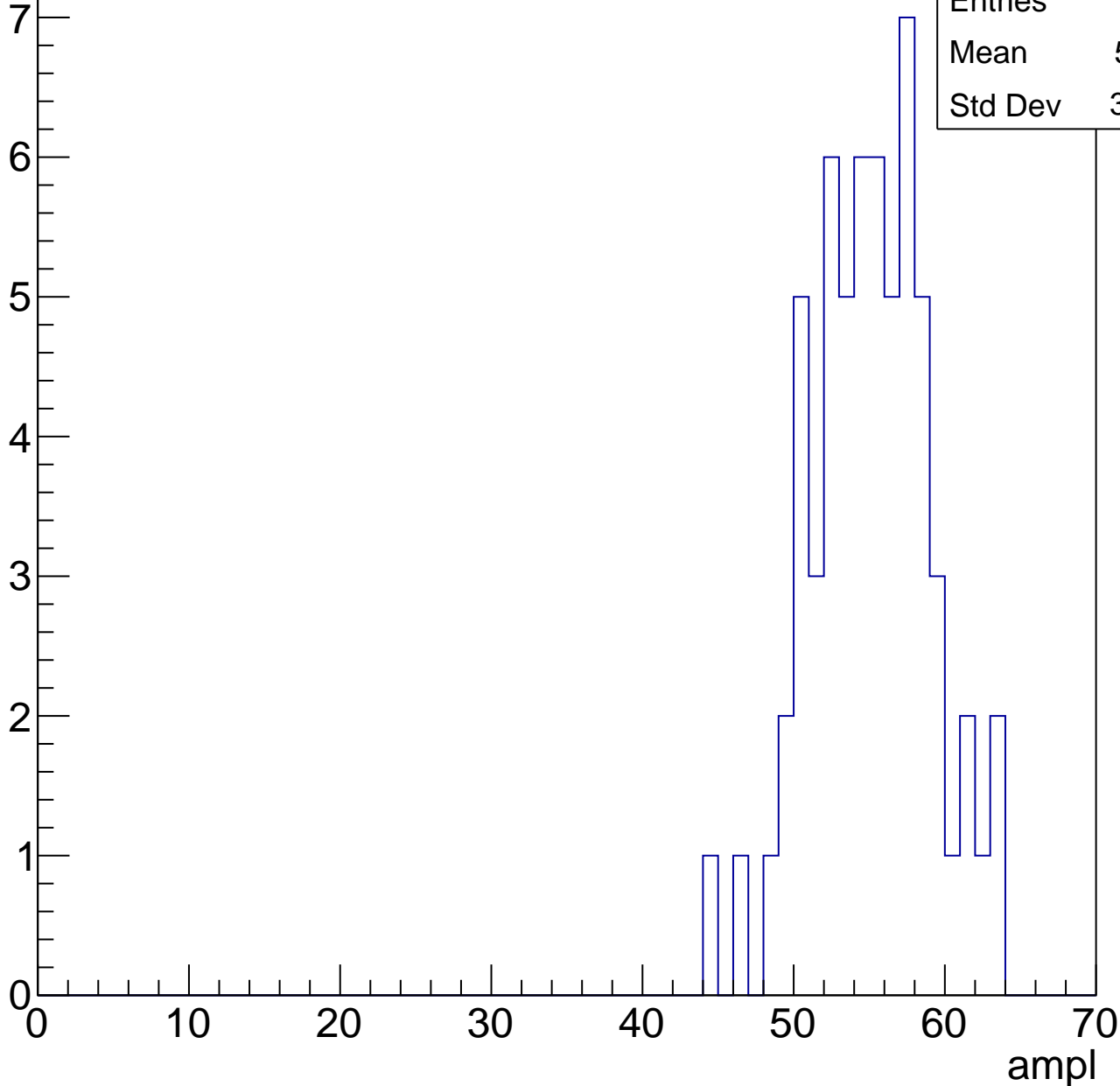


# B0L102S, U17-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	54.61
Std Dev	3.949

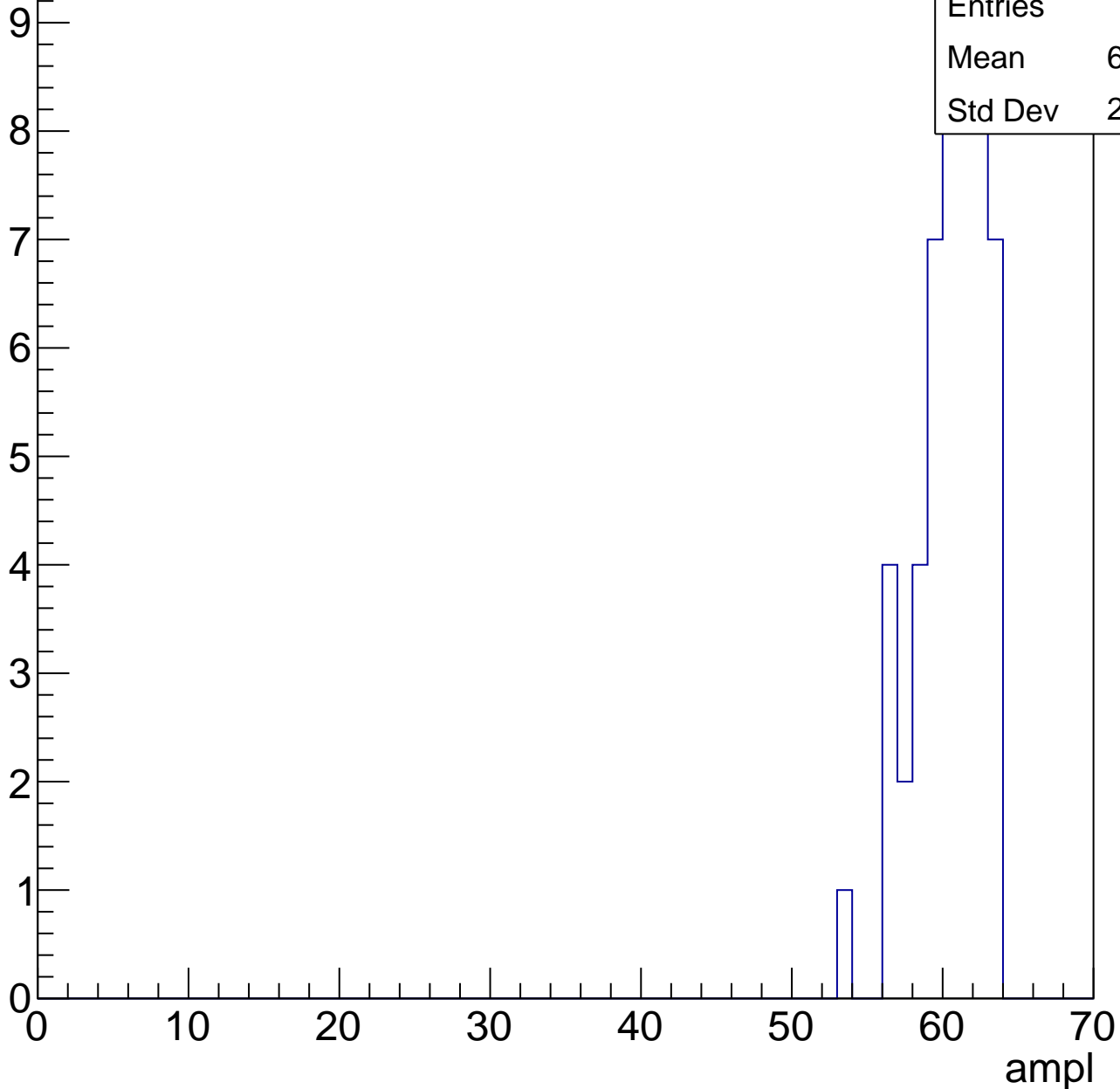


# B0L102S, U17-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

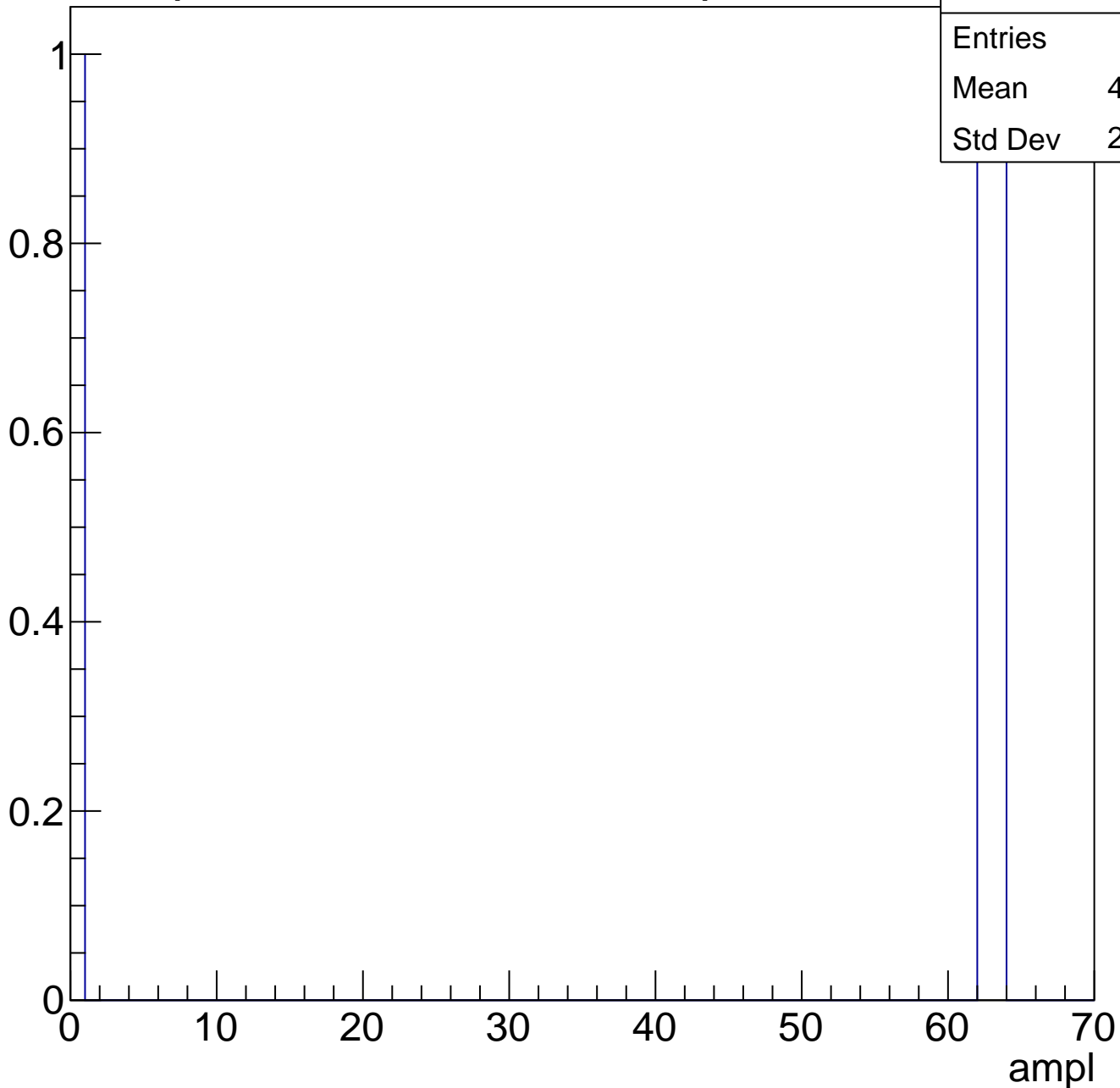
Entries	50
Mean	60.02
Std Dev	2.267



# B0L102S, U17-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch112, adc0

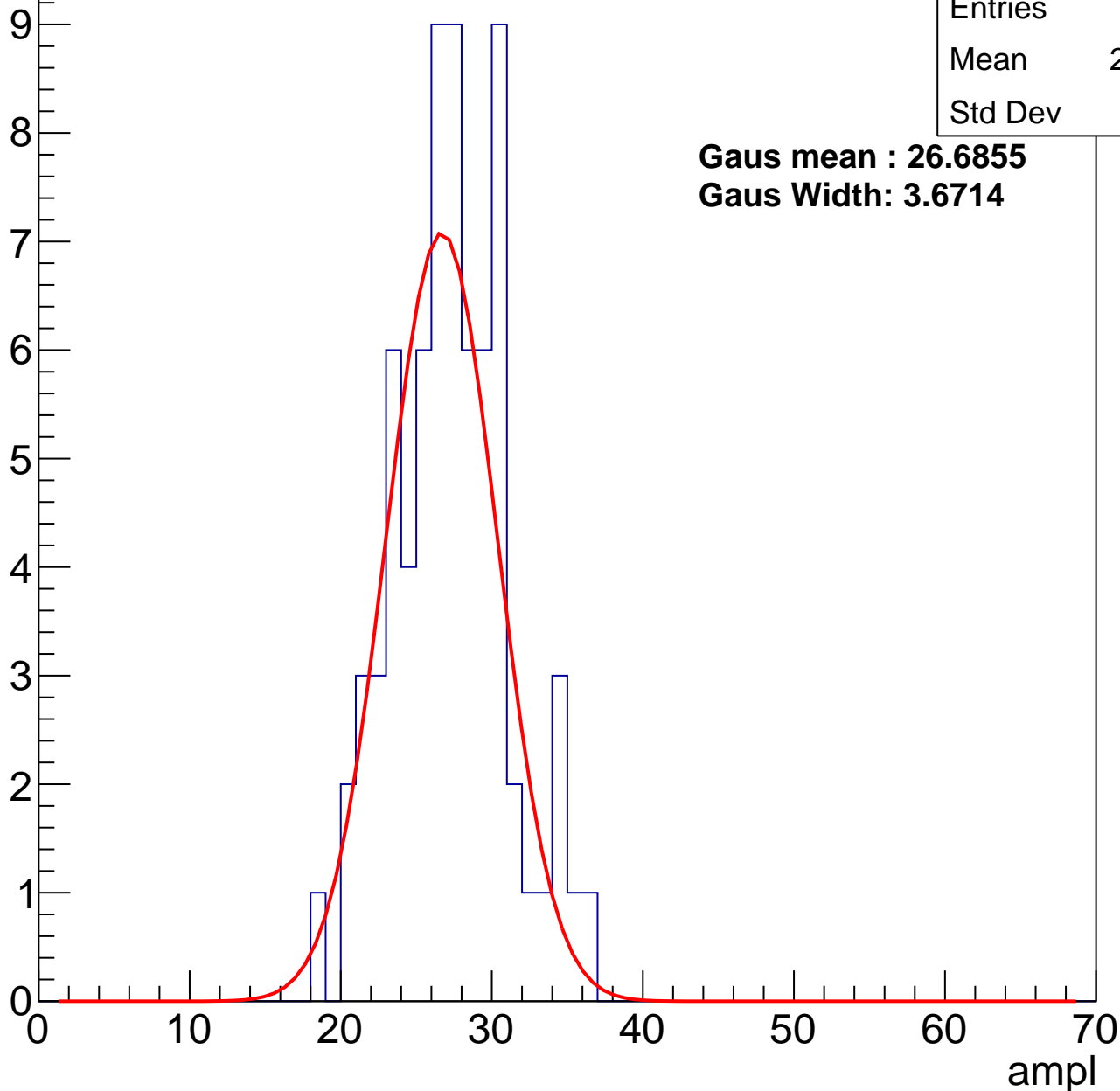
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	26.85
Std Dev	3.74

**Gaus mean : 26.6855**

**Gaus Width: 3.6714**



# B0L102S, U17-ch112, adc1

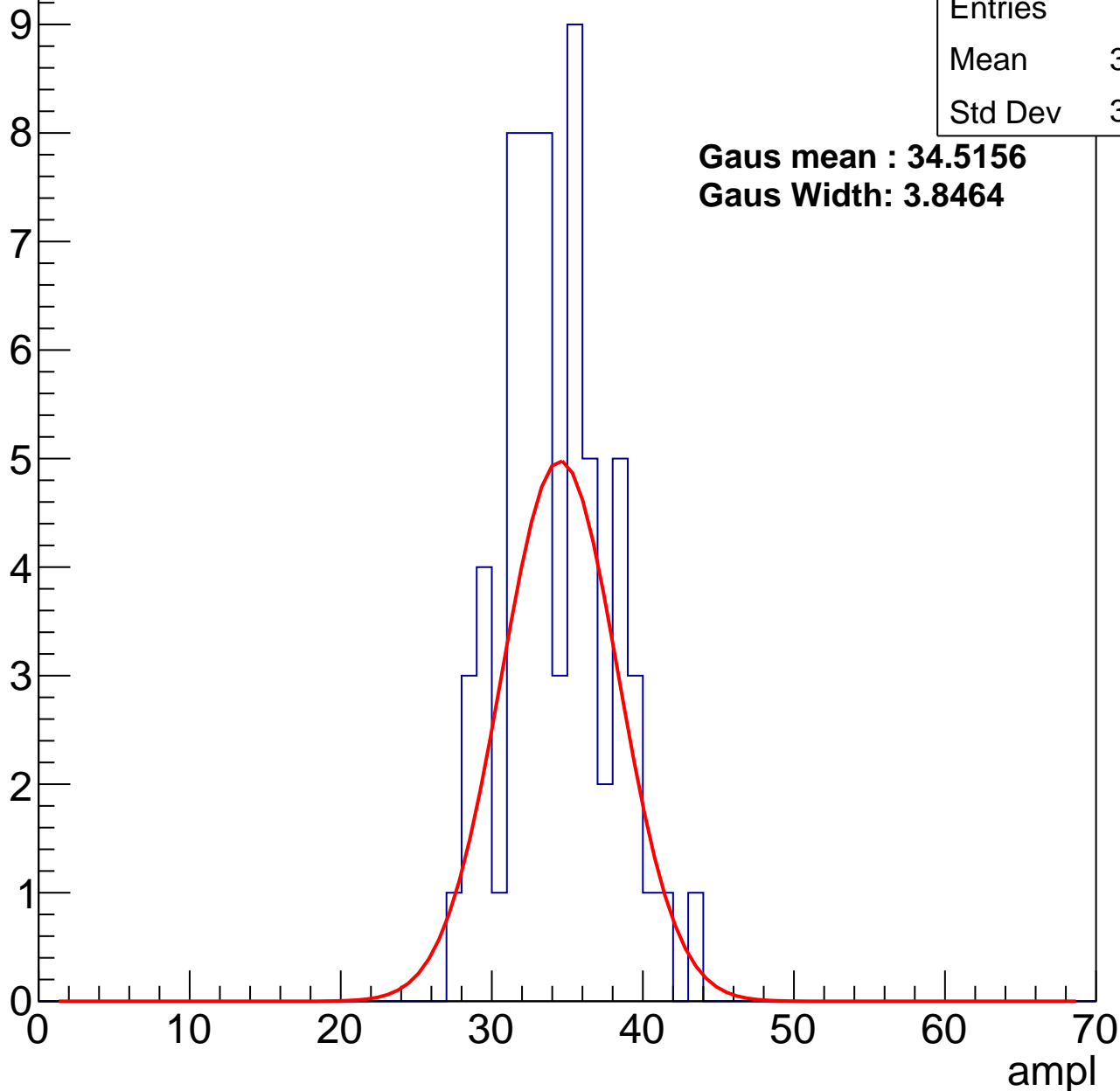
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	33.76
Std Dev	3.449

**Gaus mean : 34.5156**

**Gaus Width: 3.8464**



# B0L102S, U17-ch112, adc2

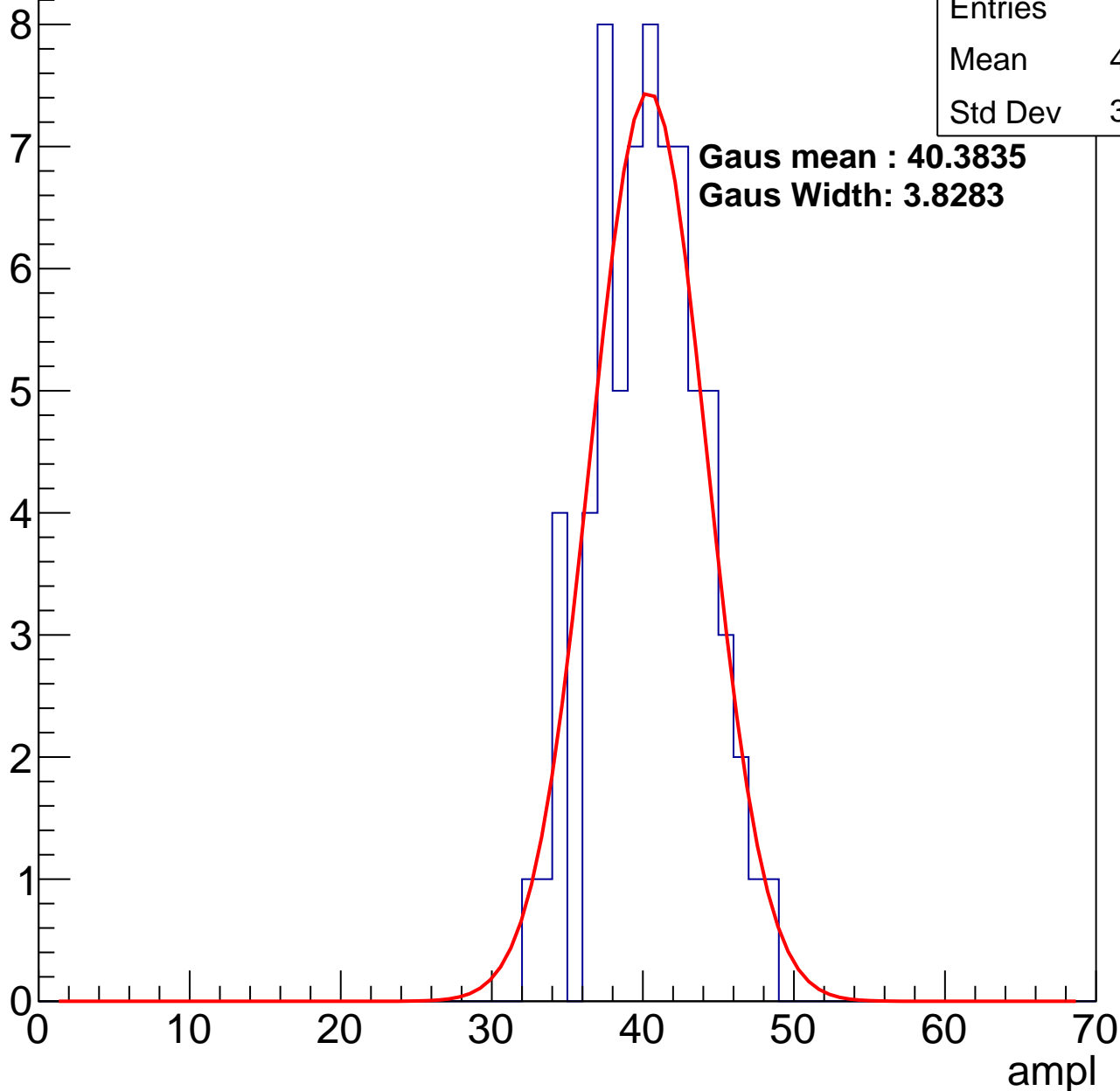
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	40.03
Std Dev	3.489

**Gaus mean : 40.3835**

**Gaus Width: 3.8283**

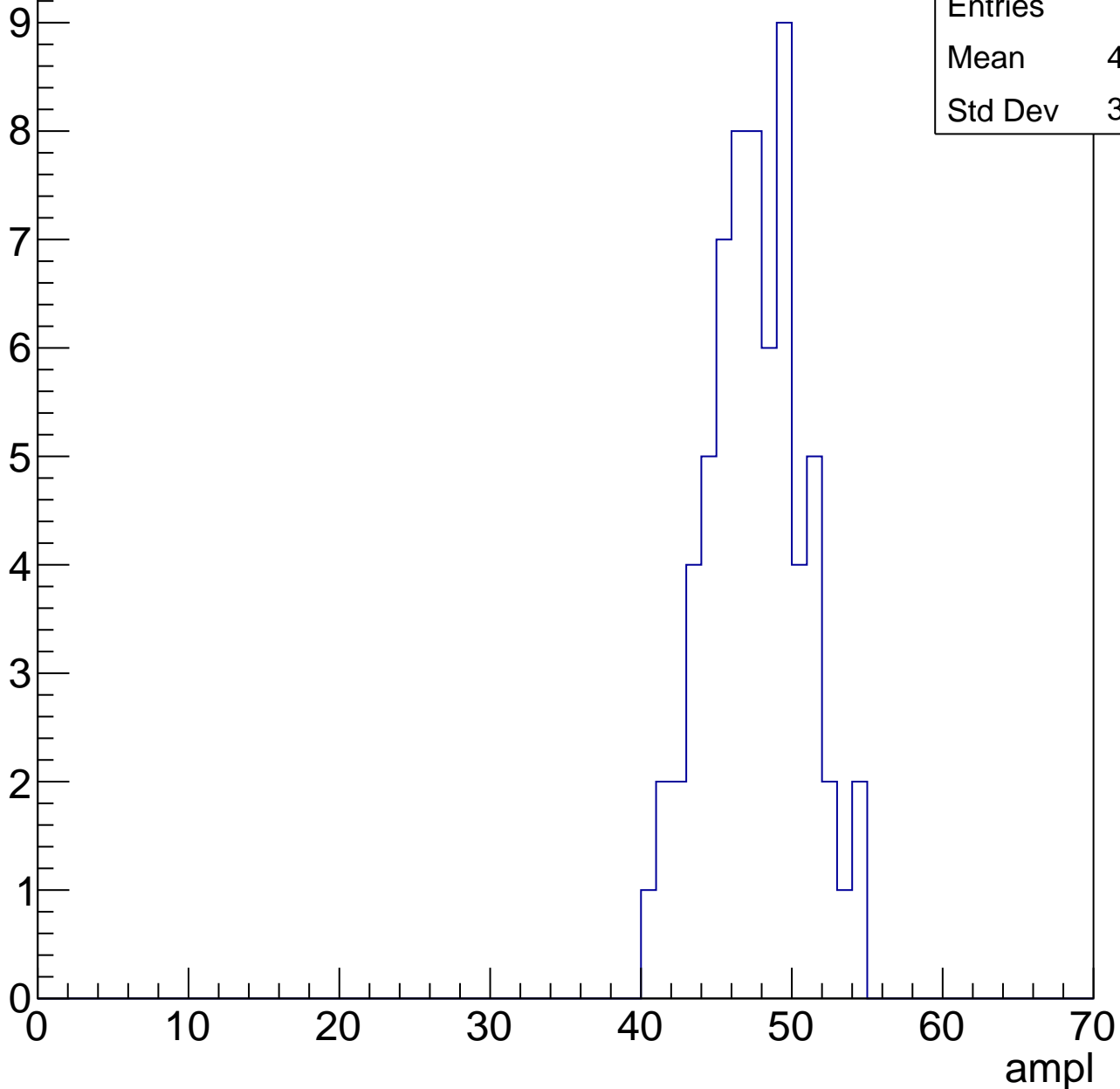


# B0L102S, U17-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	47.06
Std Dev	3.157

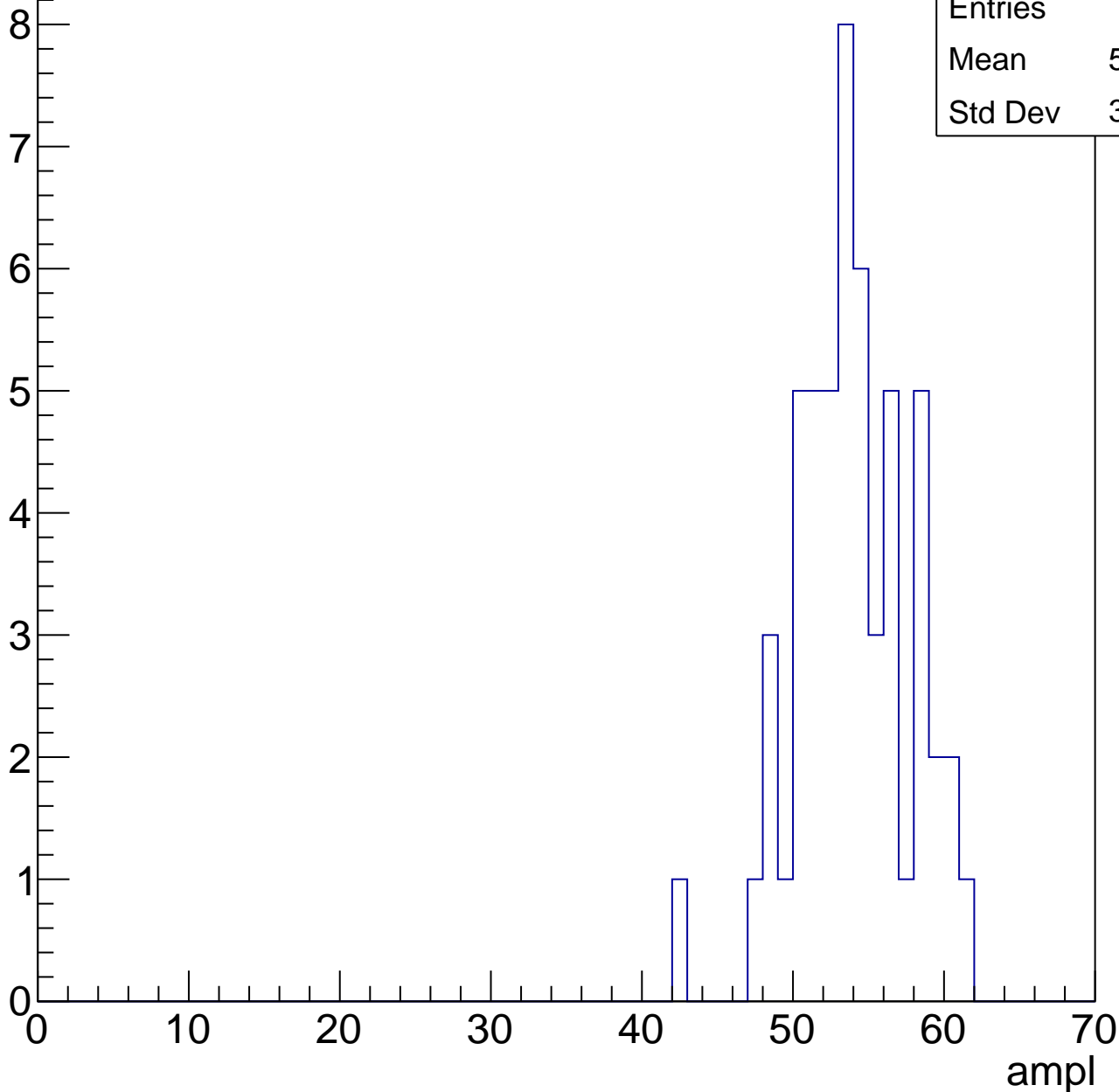


# B0L102S, U17-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	53.44
Std Dev	3.715

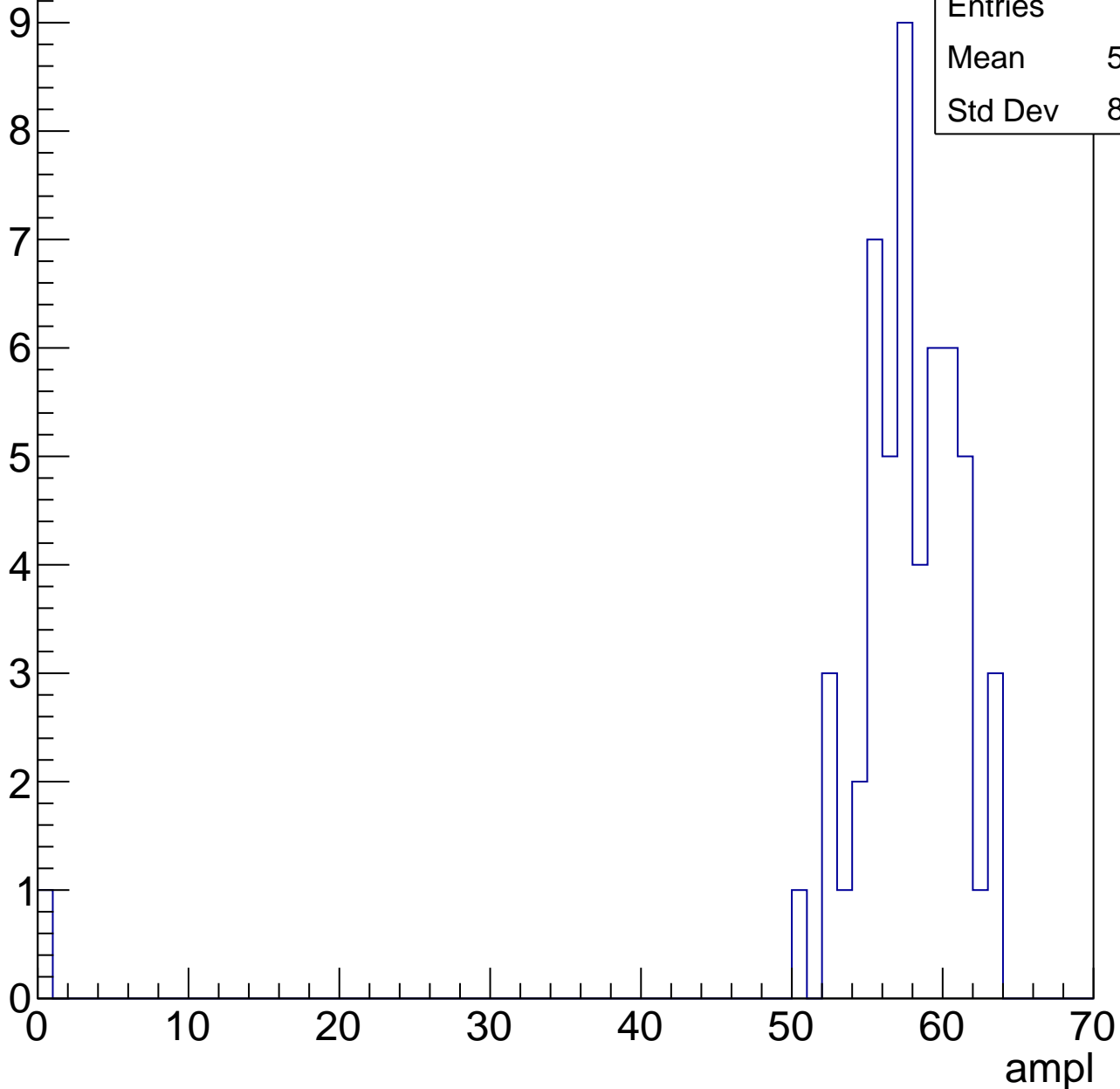


# B0L102S, U17-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	56.43
Std Dev	8.297

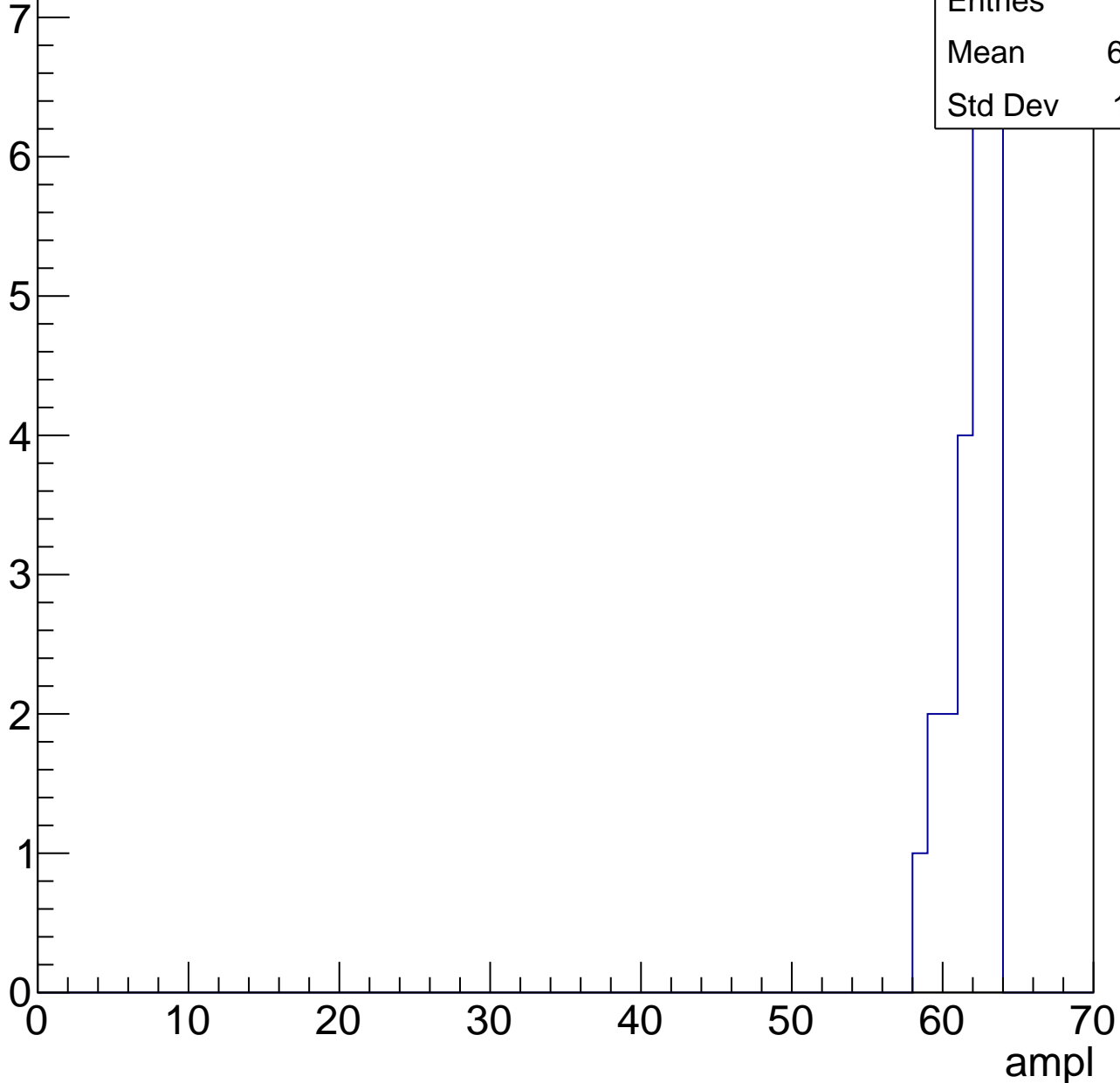


# B0L102S, U17-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	23
Mean	61.52
Std Dev	1.441

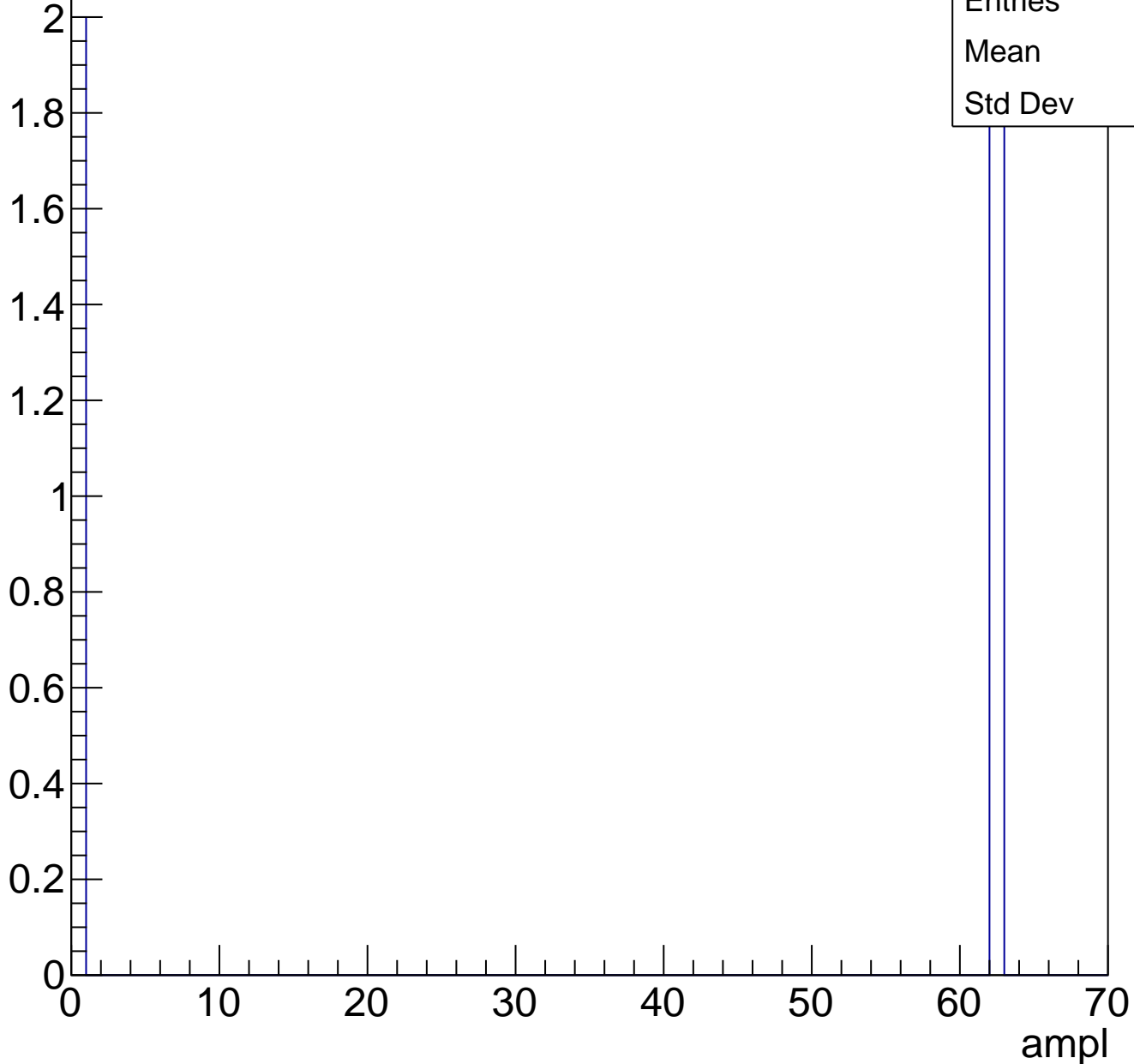




# B0L102S, U17-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch113, adc0

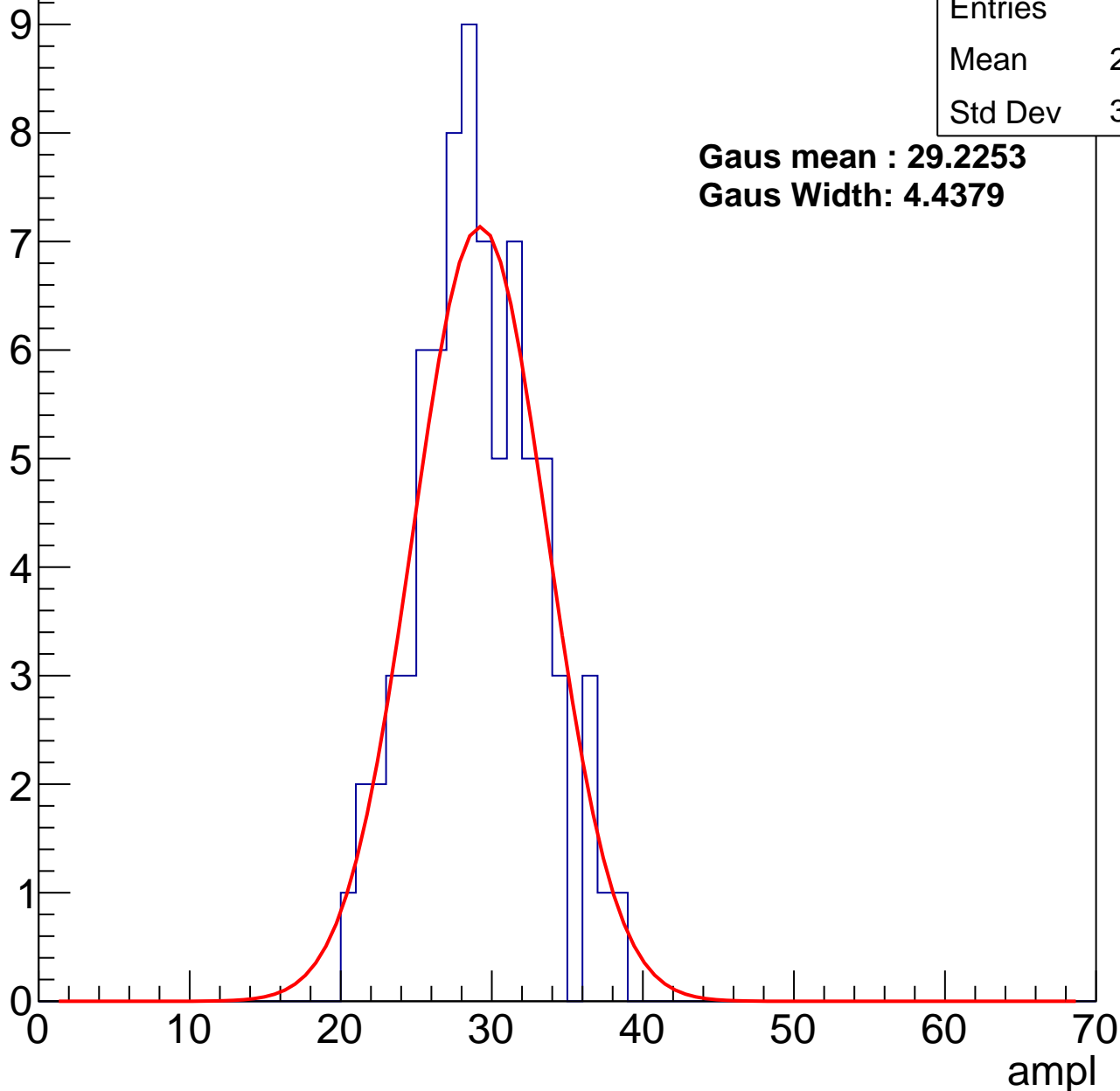
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.58
Std Dev	3.936

**Gaus mean : 29.2253**

**Gaus Width: 4.4379**



# B0L102S, U17-ch113, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	71
Mean	35.42
Std Dev	3.921

**Gaus mean : 35.9619**

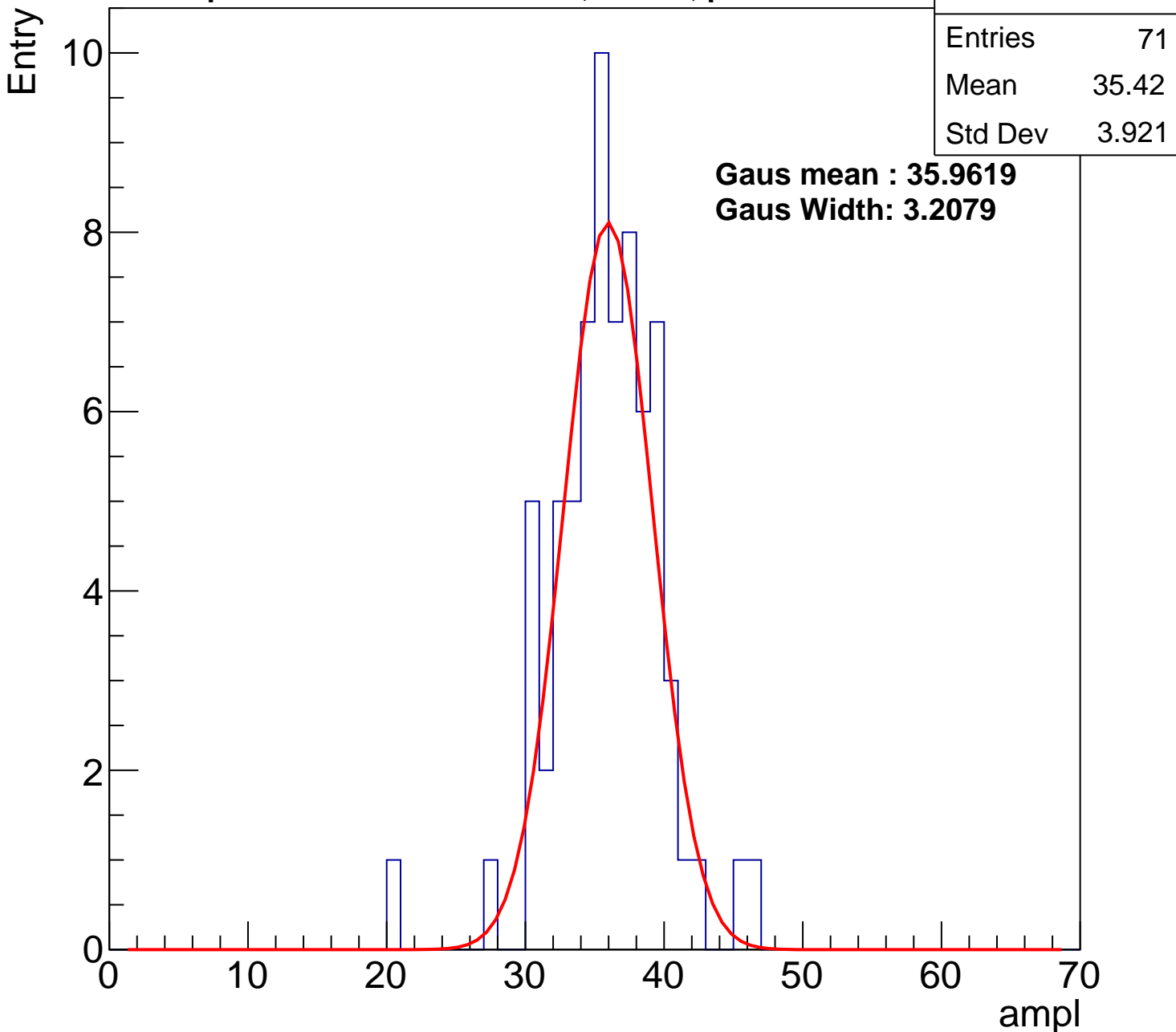
**Gaus Width: 3.2079**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L102S, U17-ch113, adc2

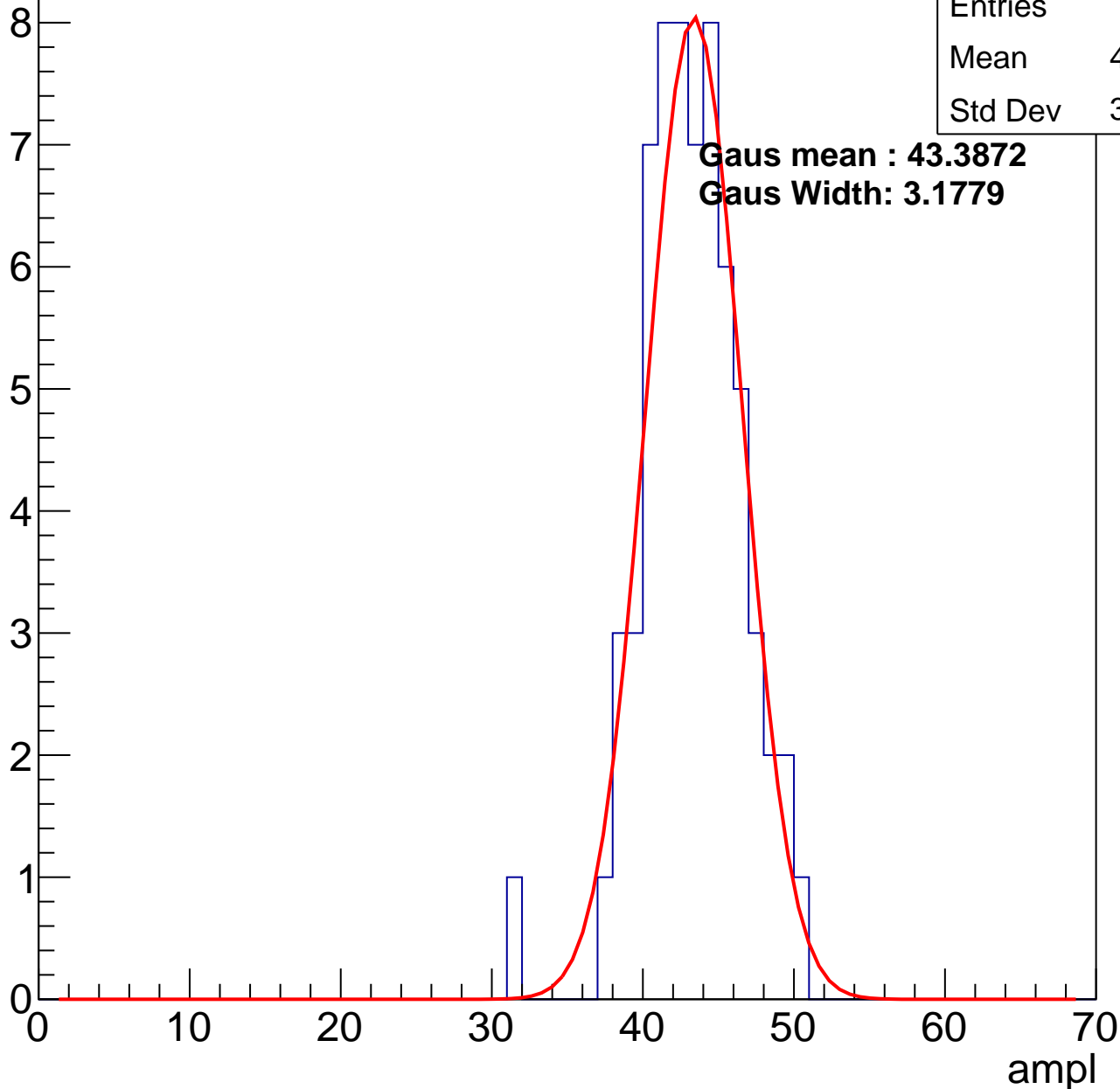
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.78
Std Dev	3.279

**Gaus mean : 43.3872**

**Gaus Width: 3.1779**

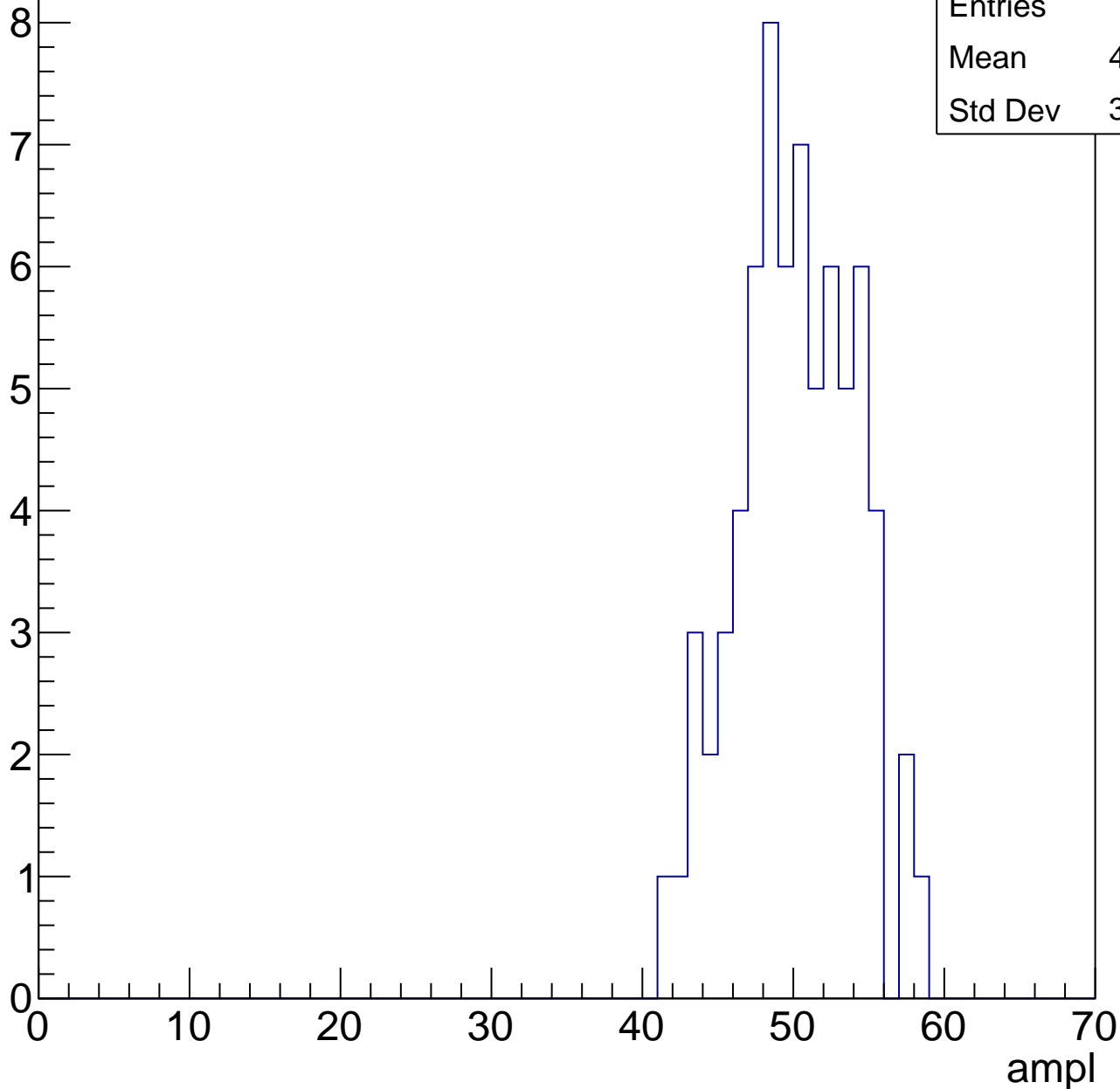


# B0L102S, U17-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	49.67
Std Dev	3.816

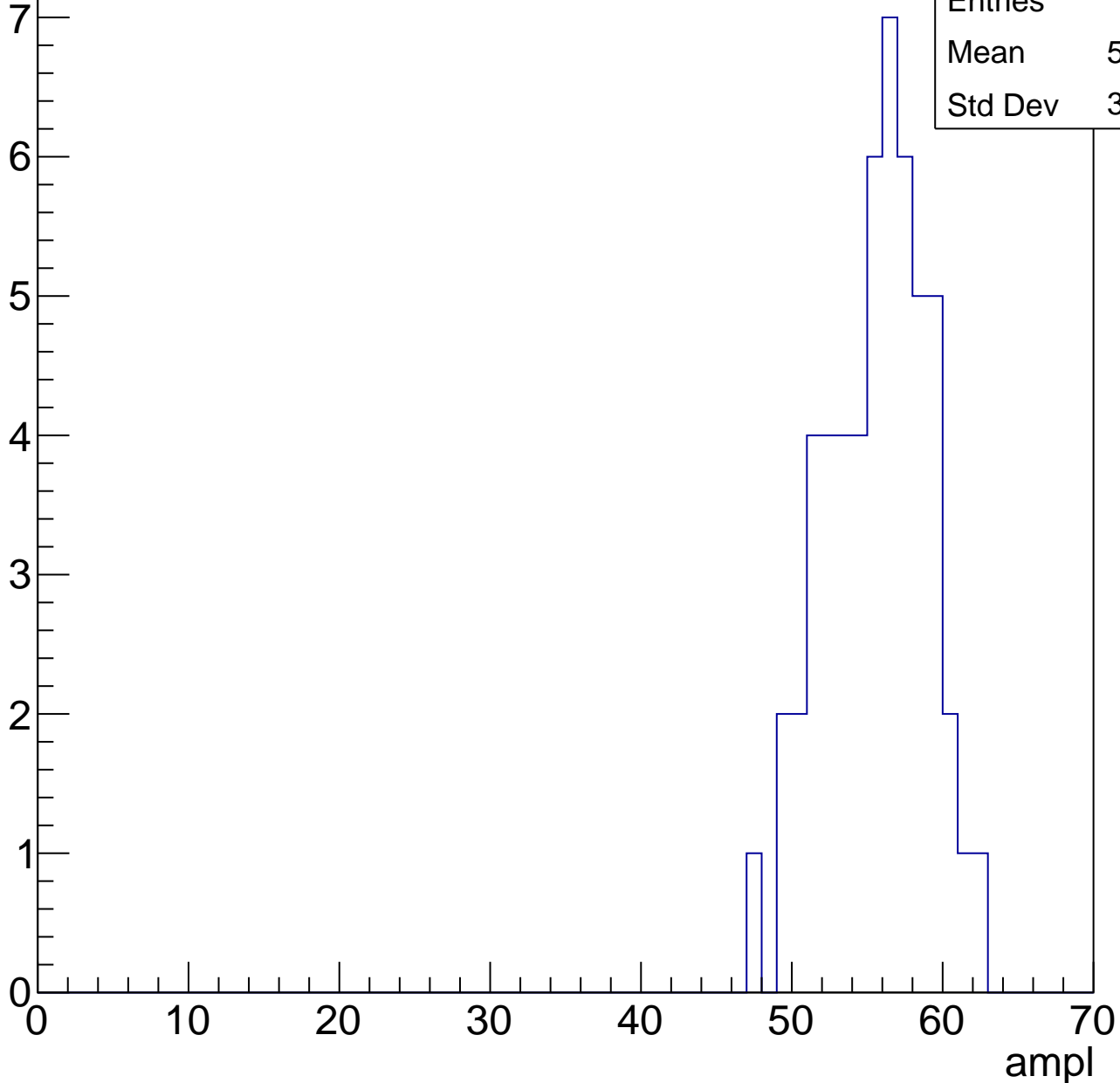


# B0L102S, U17-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	54
Mean	55.13
Std Dev	3.322

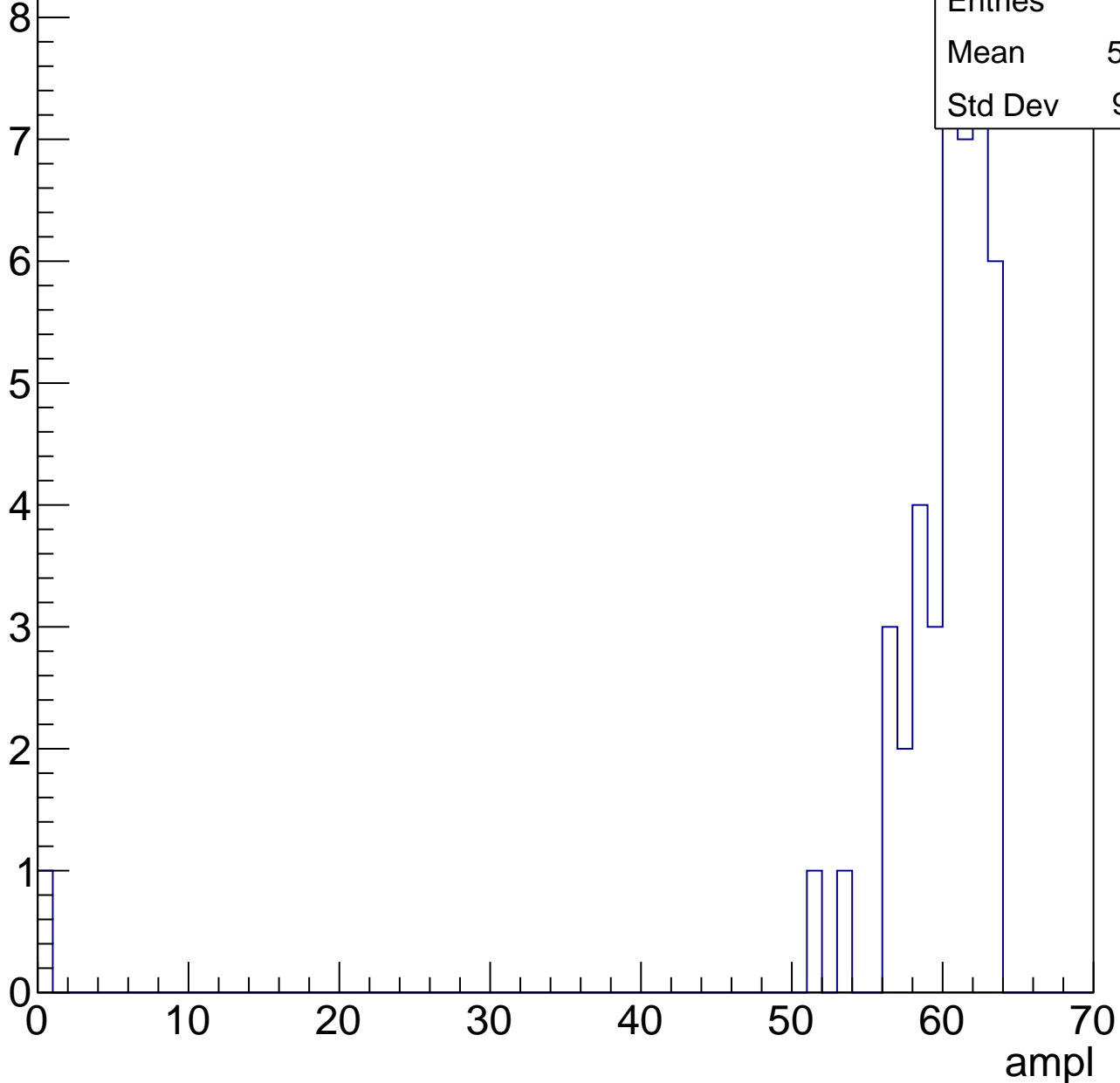


# B0L102S, U17-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

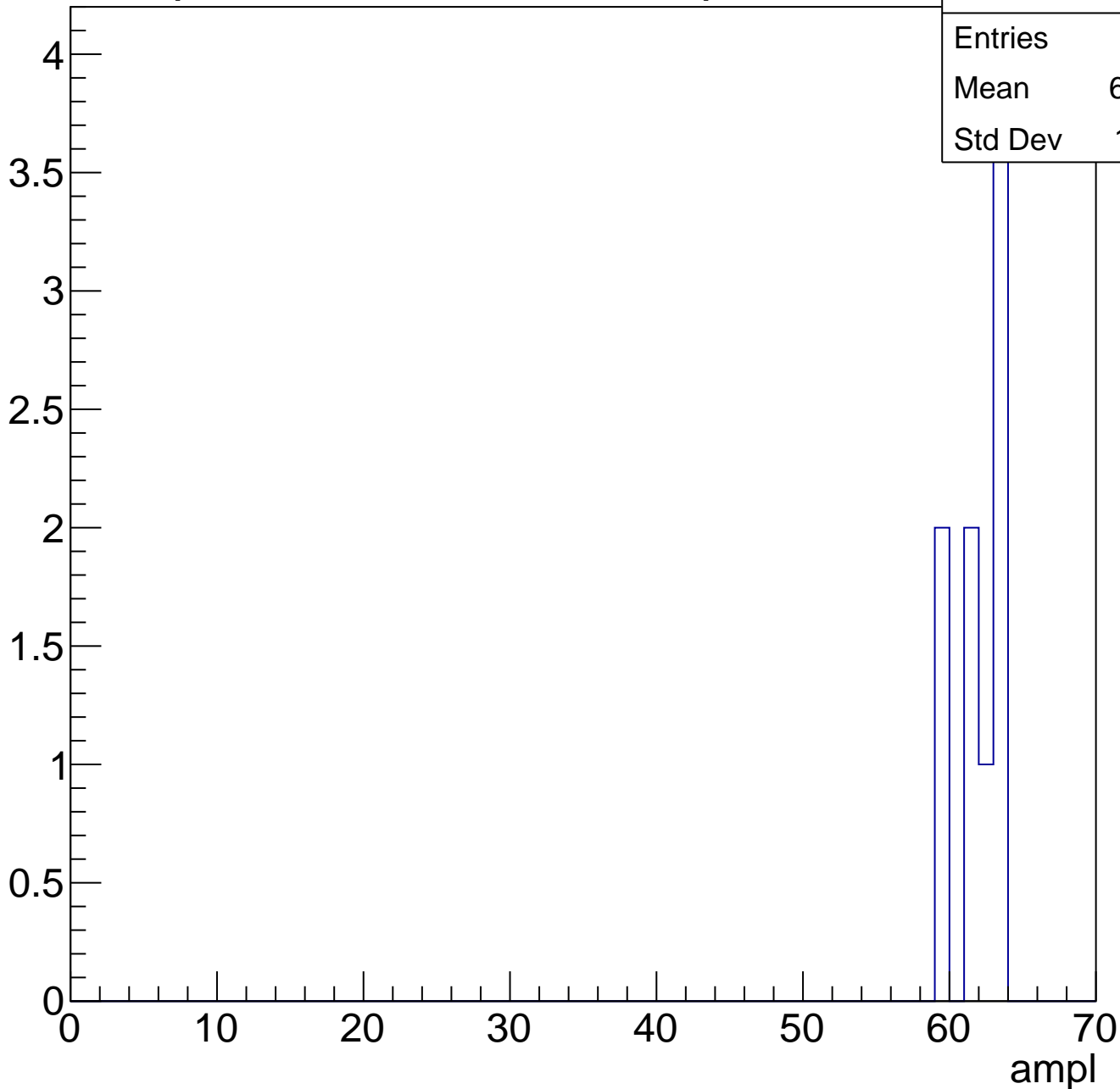
Entries	44
Mean	58.55
Std Dev	9.311



# B0L102S, U17-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch114, adc0

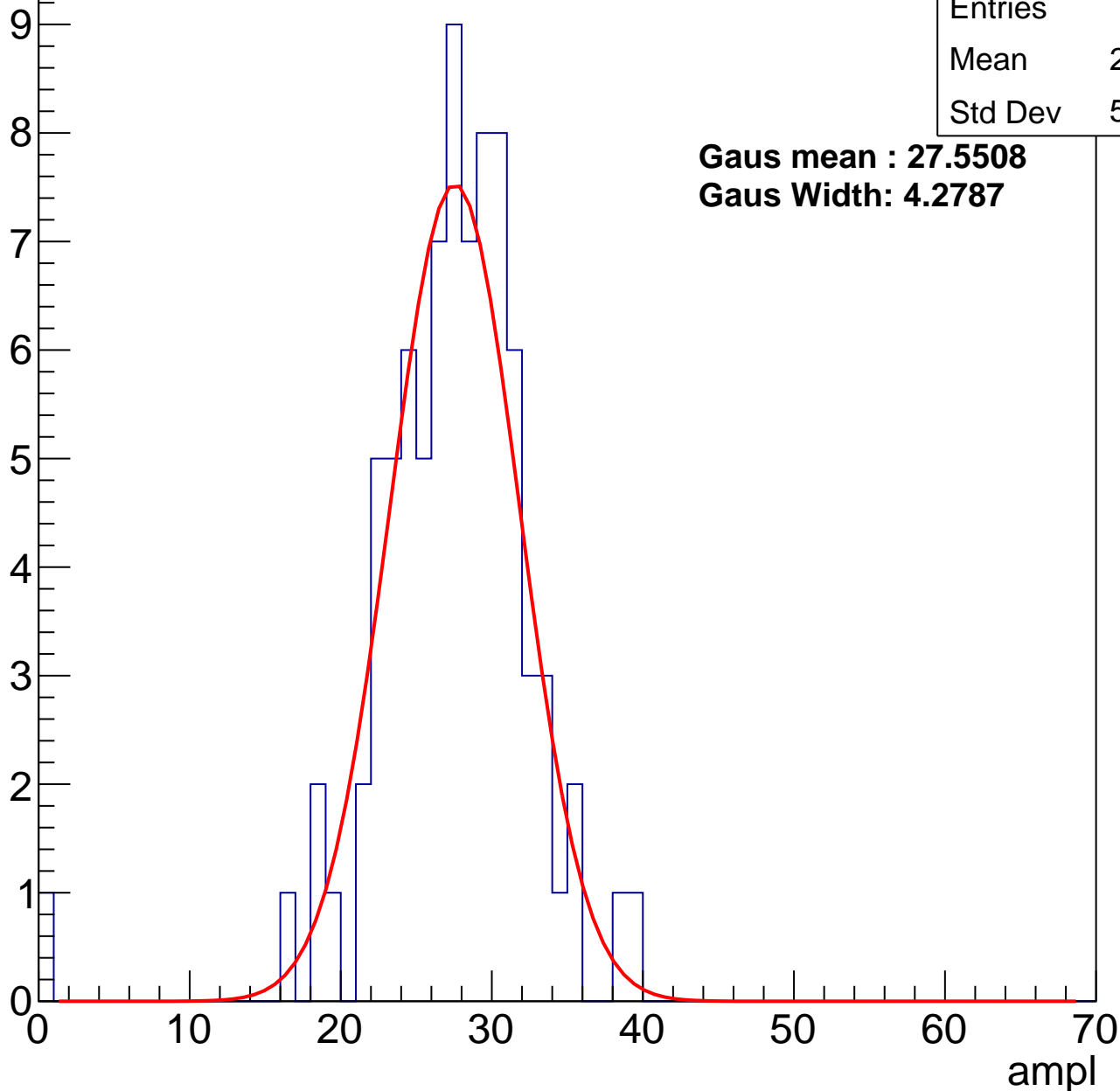
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	26.93
Std Dev	5.193

**Gaus mean : 27.5508**

**Gaus Width: 4.2787**



# B0L102S, U17-ch114, adc1

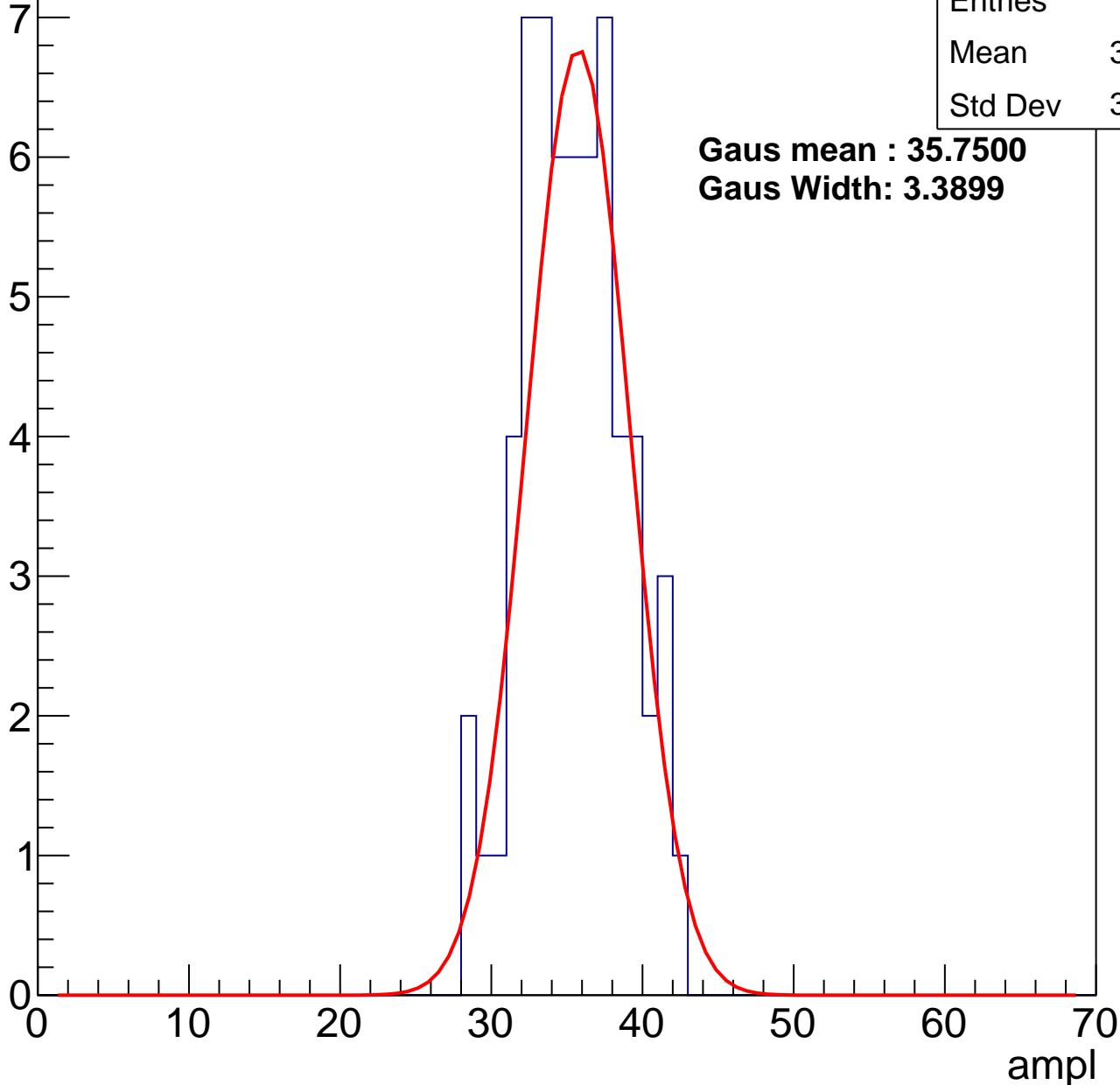
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	61
Mean	35.02
Std Dev	3.292

**Gaus mean : 35.7500**

**Gaus Width: 3.3899**



# B0L102S, U17-ch114, adc2

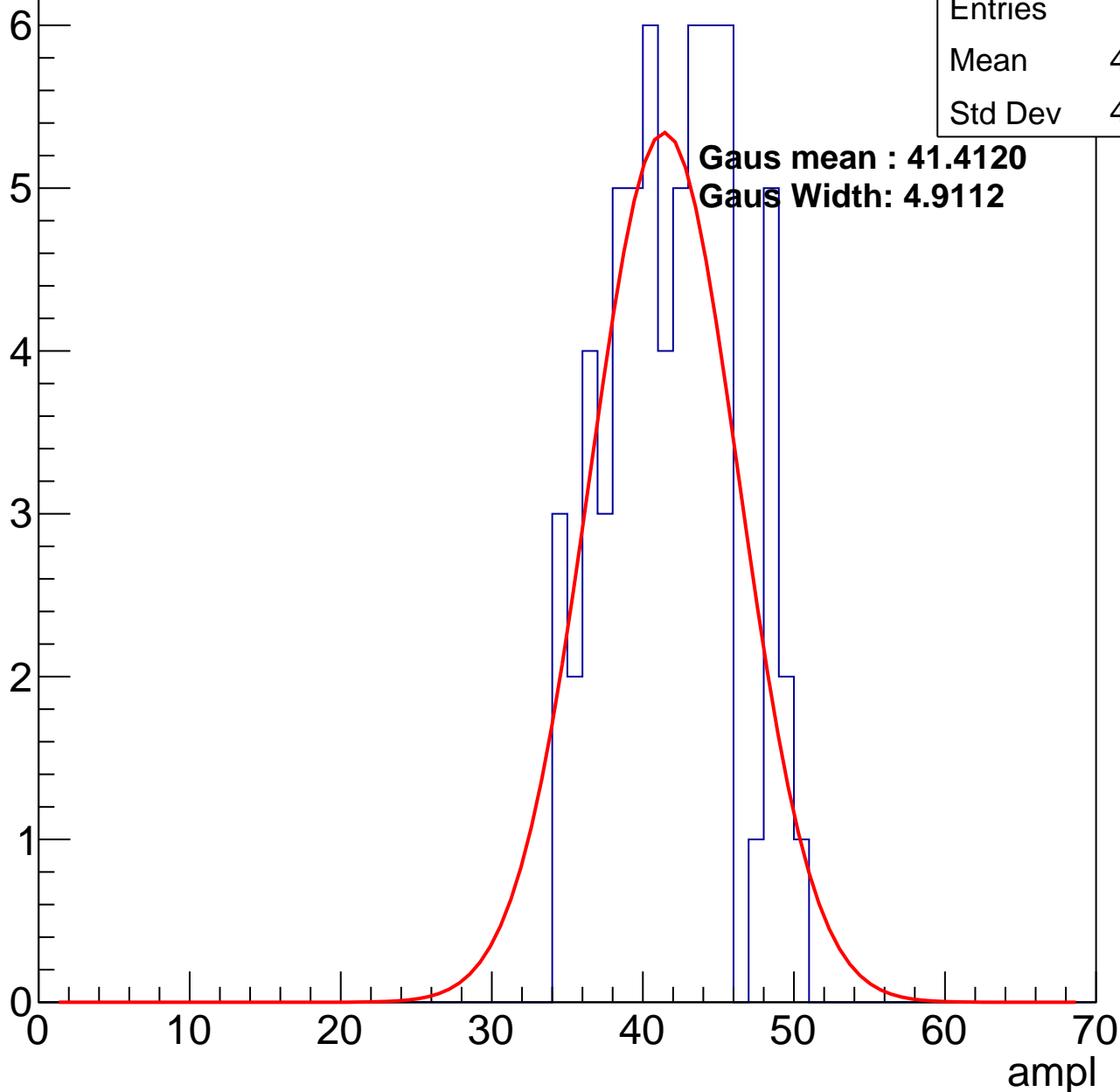
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.45
Std Dev	4.134

**Gaus mean : 41.4120**

**Gaus Width: 4.9112**

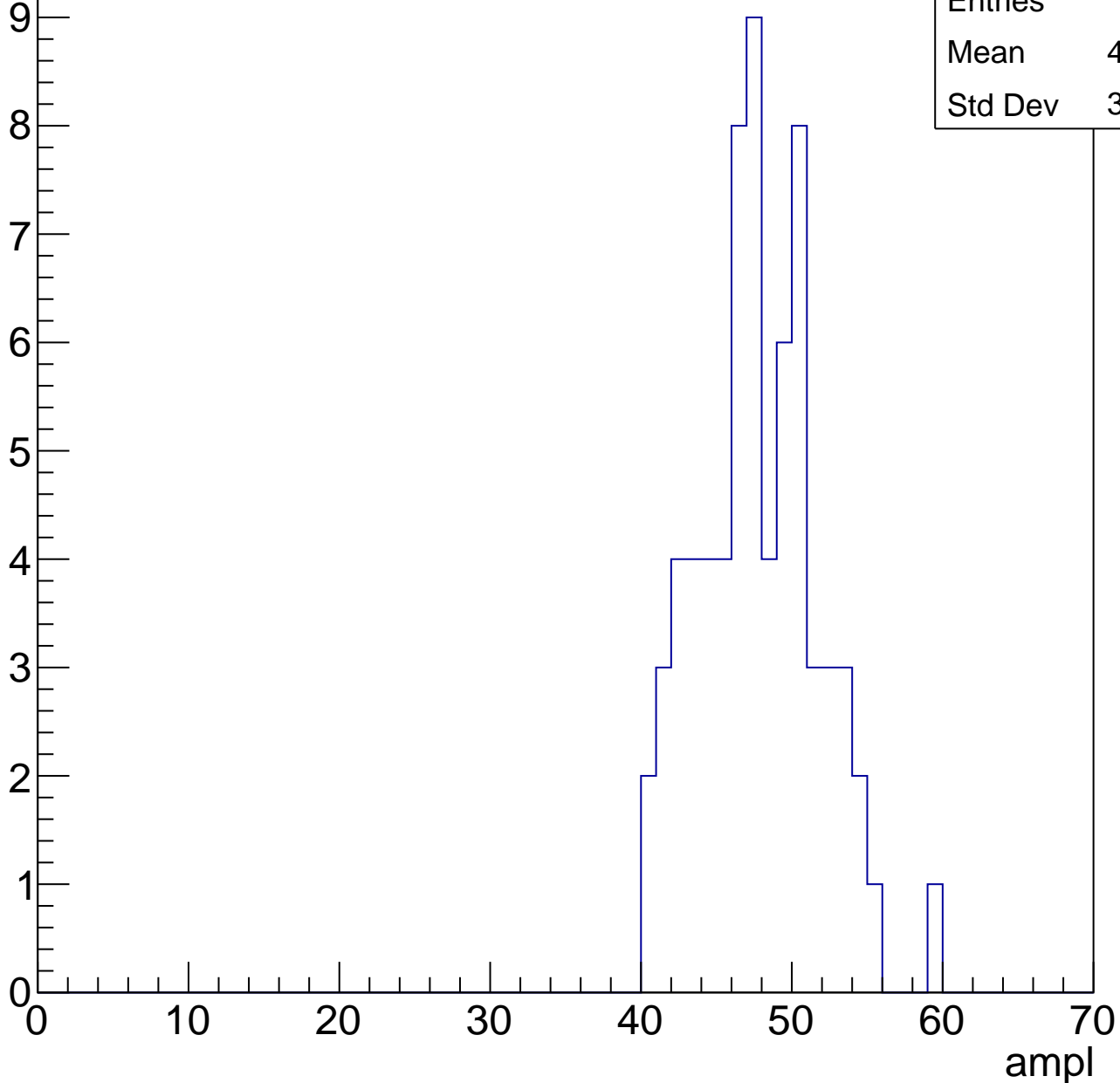


# B0L102S, U17-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	47.33
Std Dev	3.918

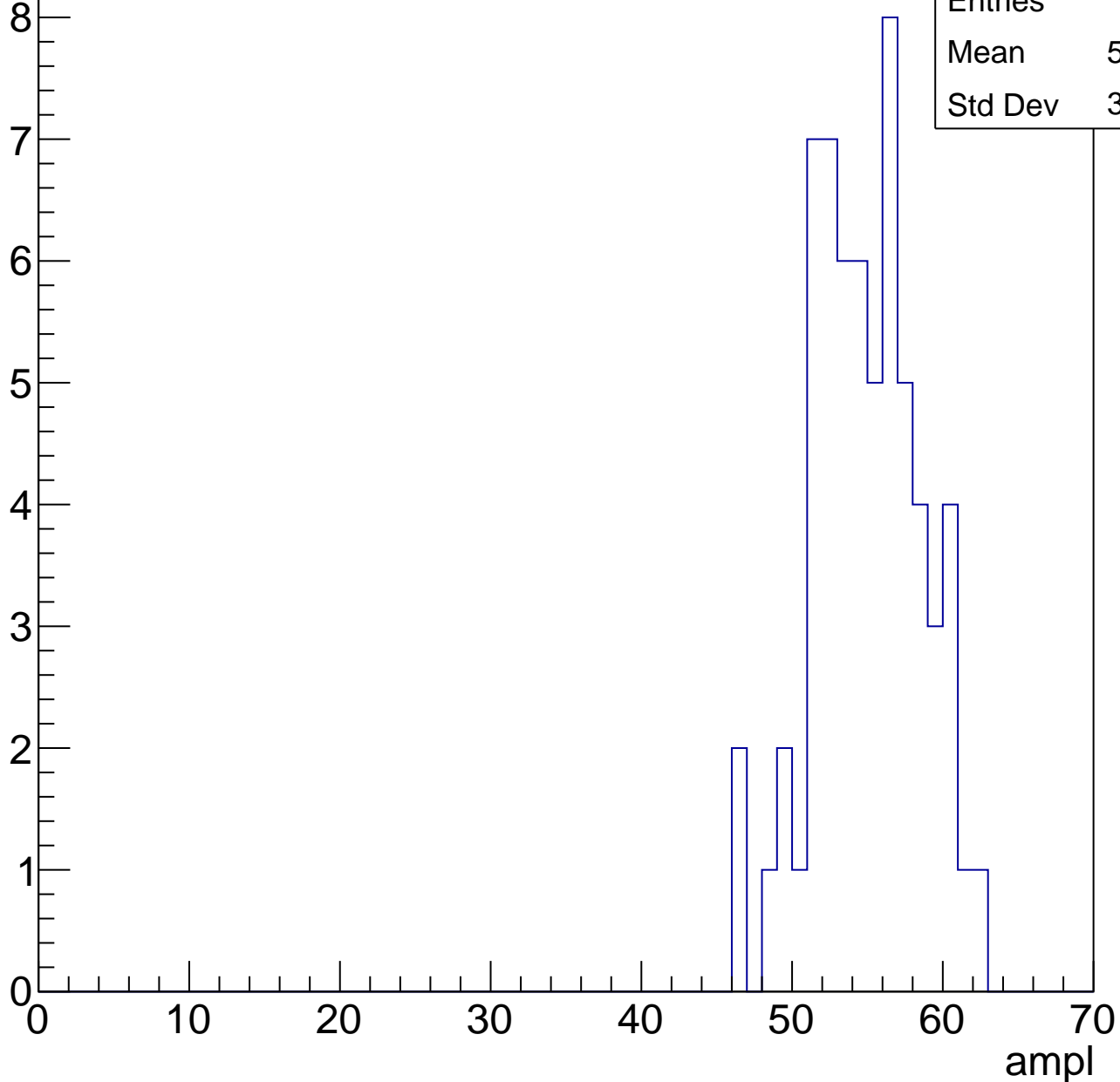


# B0L102S, U17-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

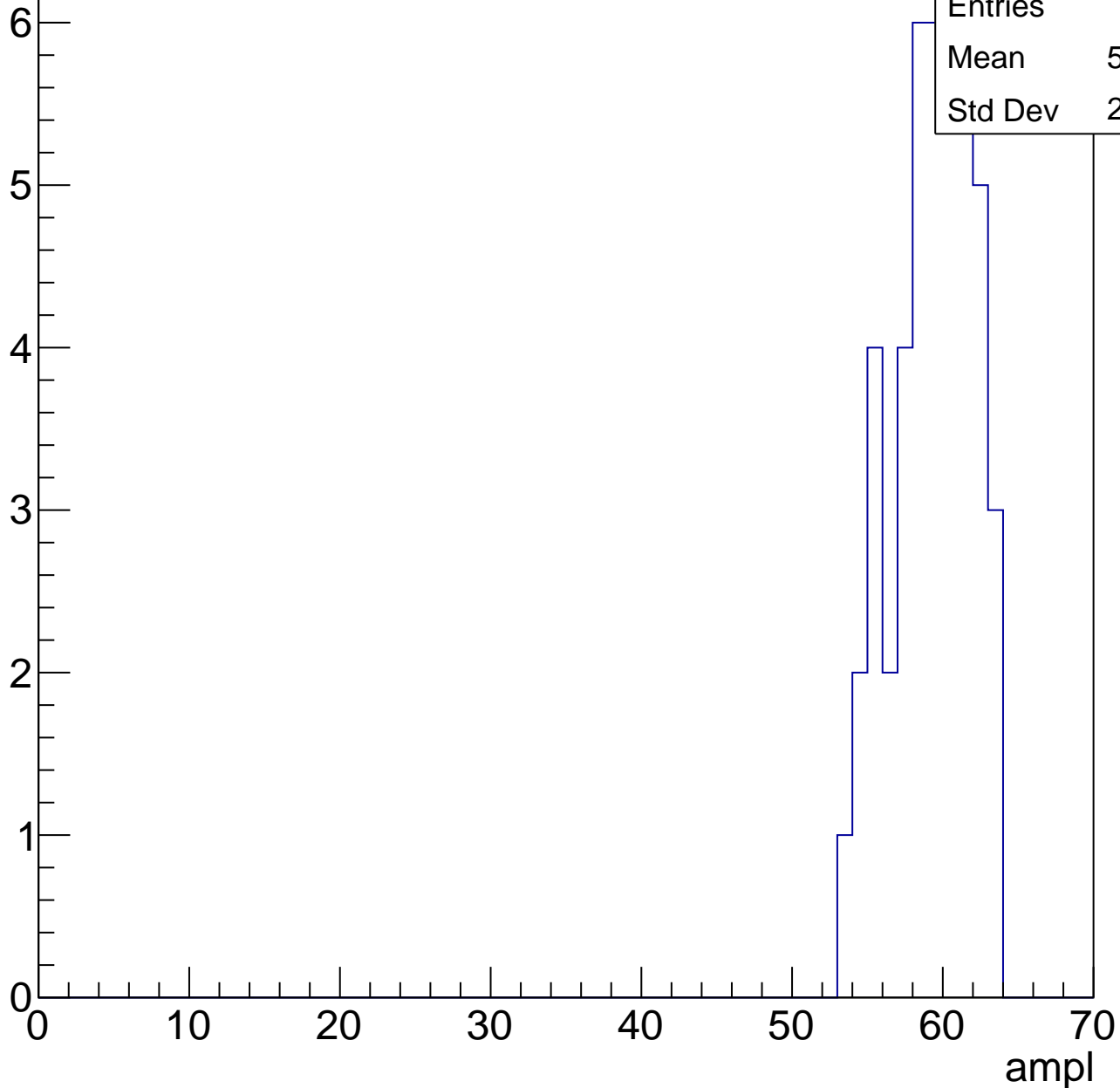
Entries	63
Mean	54.46
Std Dev	3.545



# B0L102S, U17-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

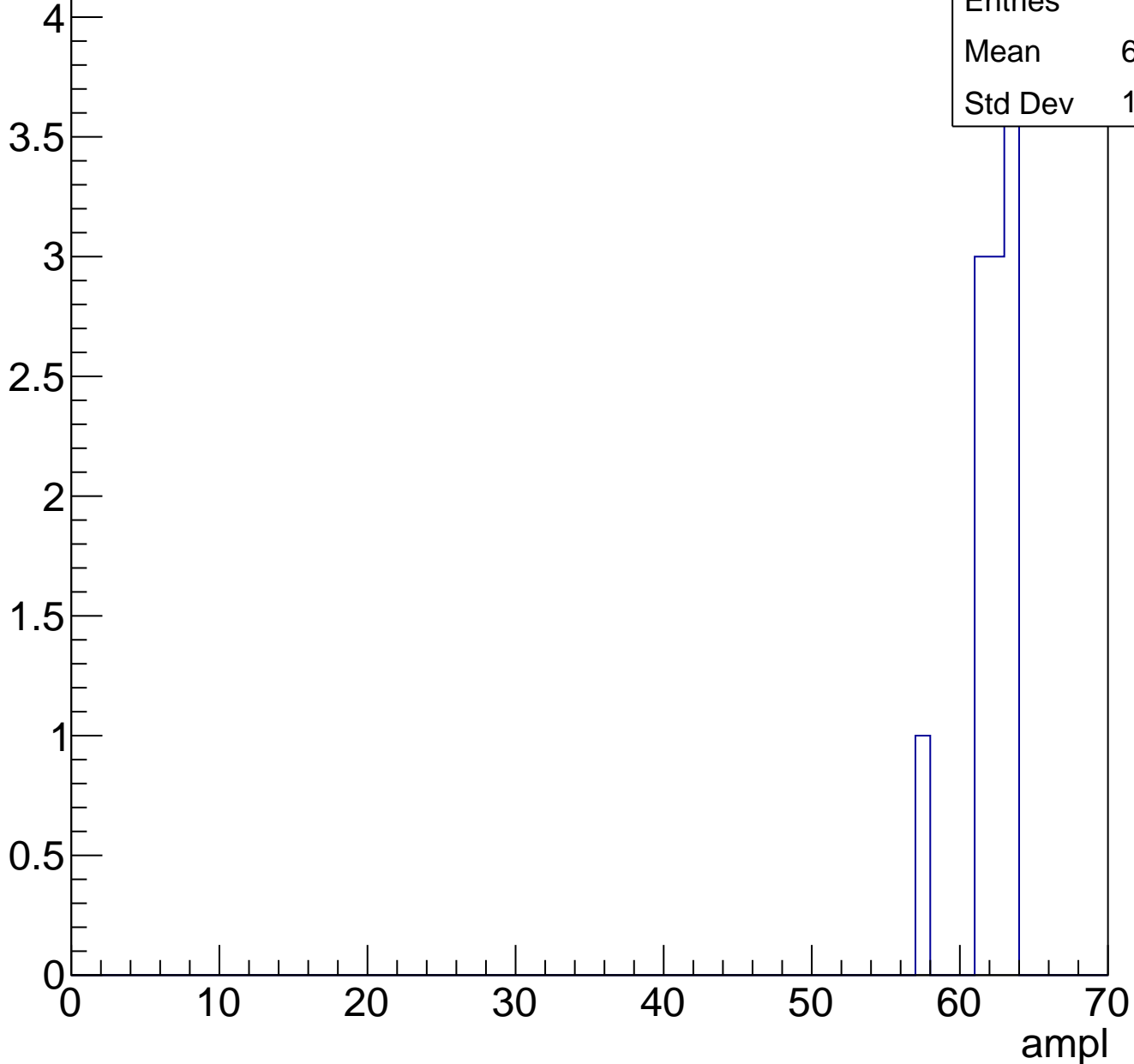
Entry



# B0L102S, U17-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

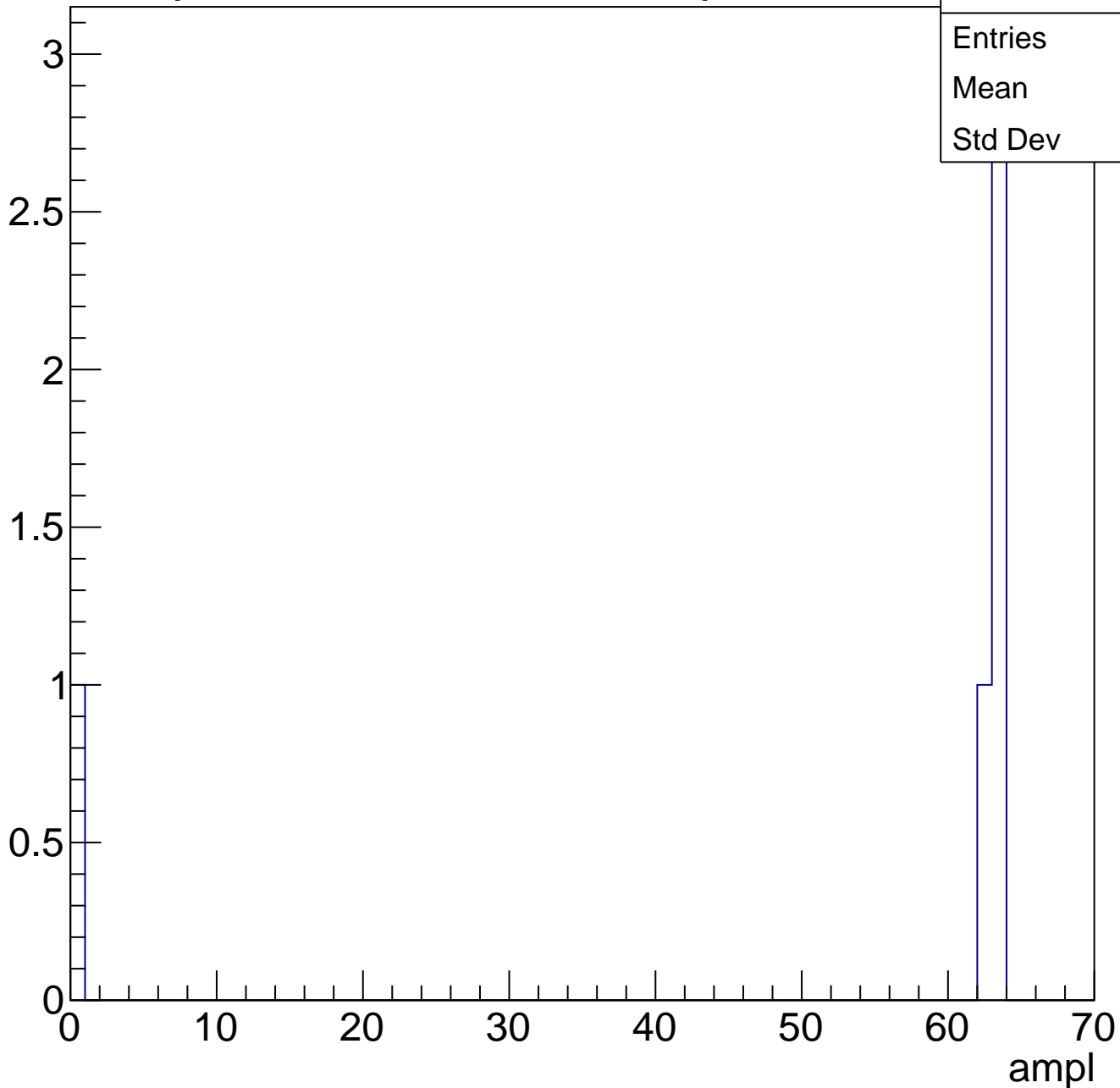




# B0L102S, U17-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch115, adc0

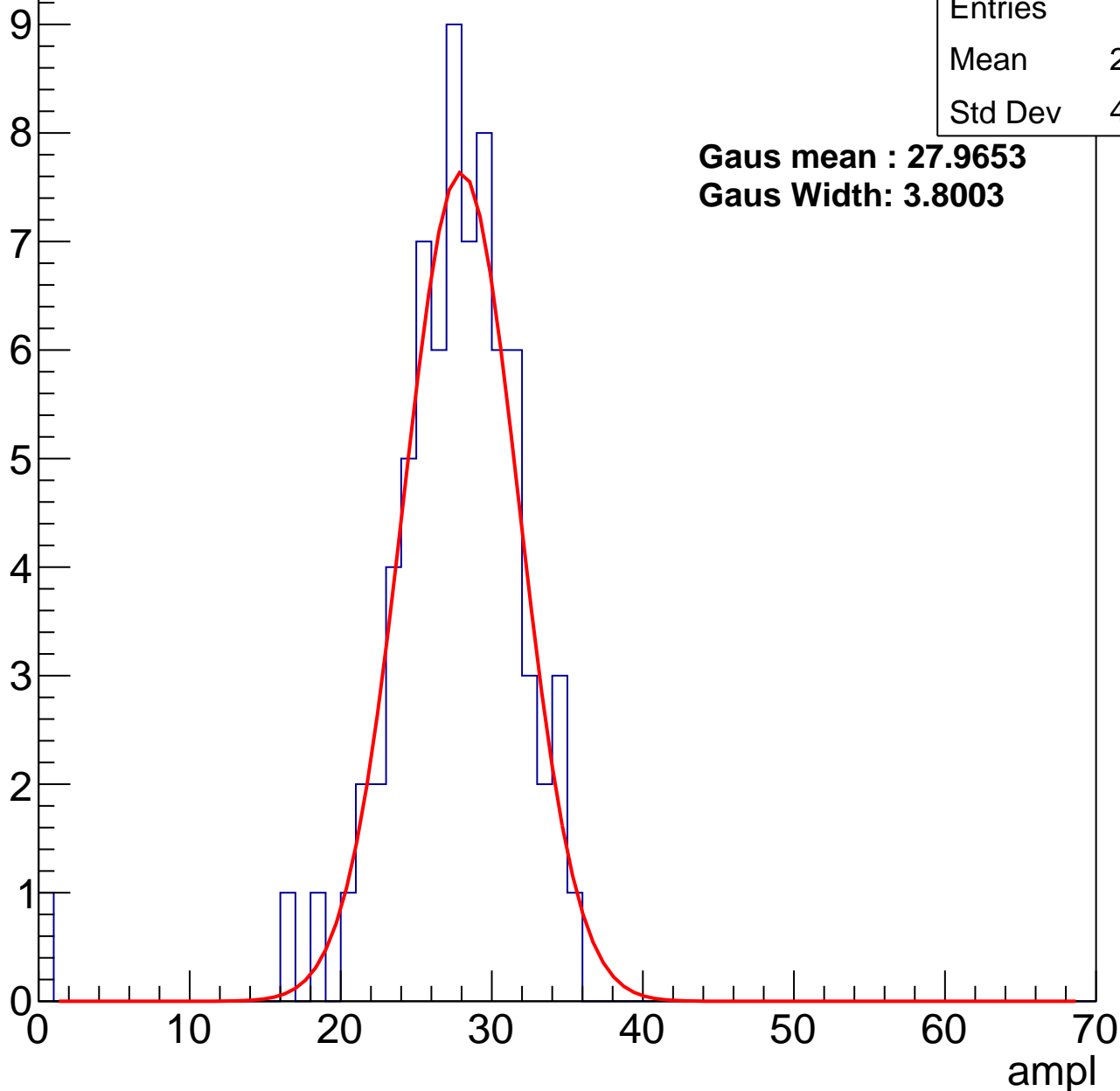
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	26.92
Std Dev	4.887

**Gaus mean : 27.9653**

**Gaus Width: 3.8003**



# B0L102S, U17-ch115, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	80
Mean	34.61
Std Dev	3.91

**Gaus mean : 35.4480**

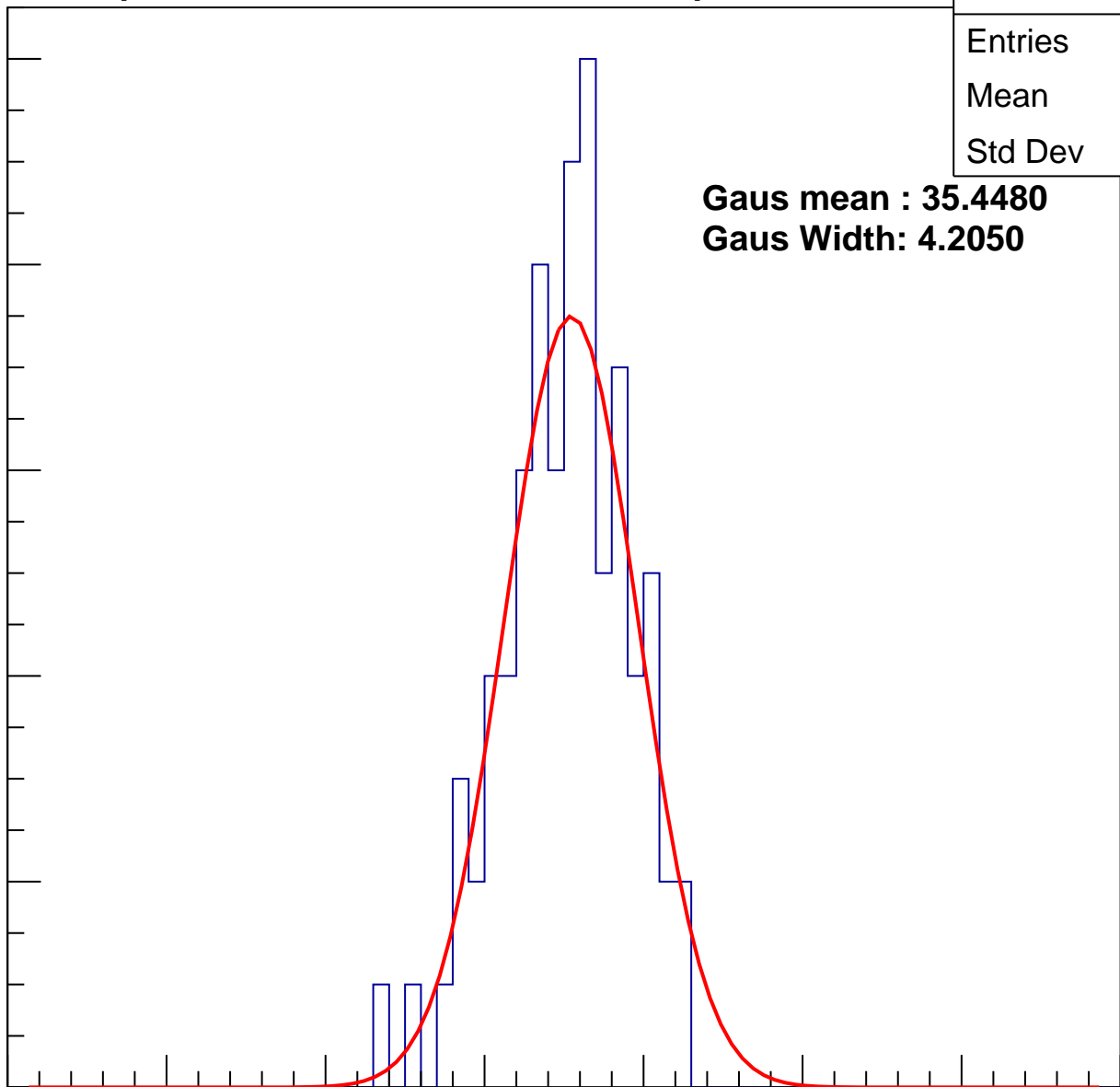
**Gaus Width: 4.2050**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch115, adc2

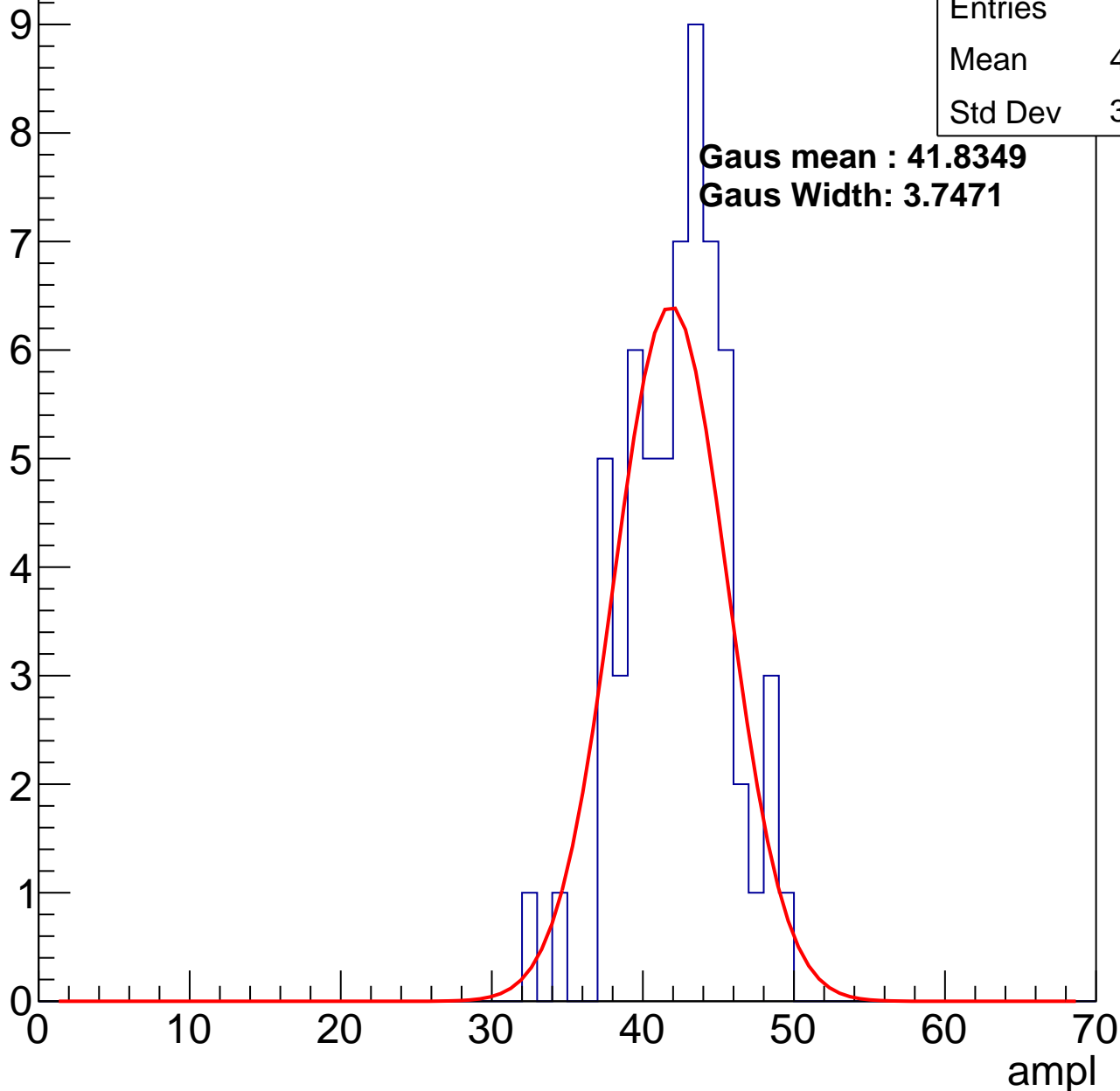
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	41.85
Std Dev	3.407

**Gaus mean : 41.8349**

**Gaus Width: 3.7471**

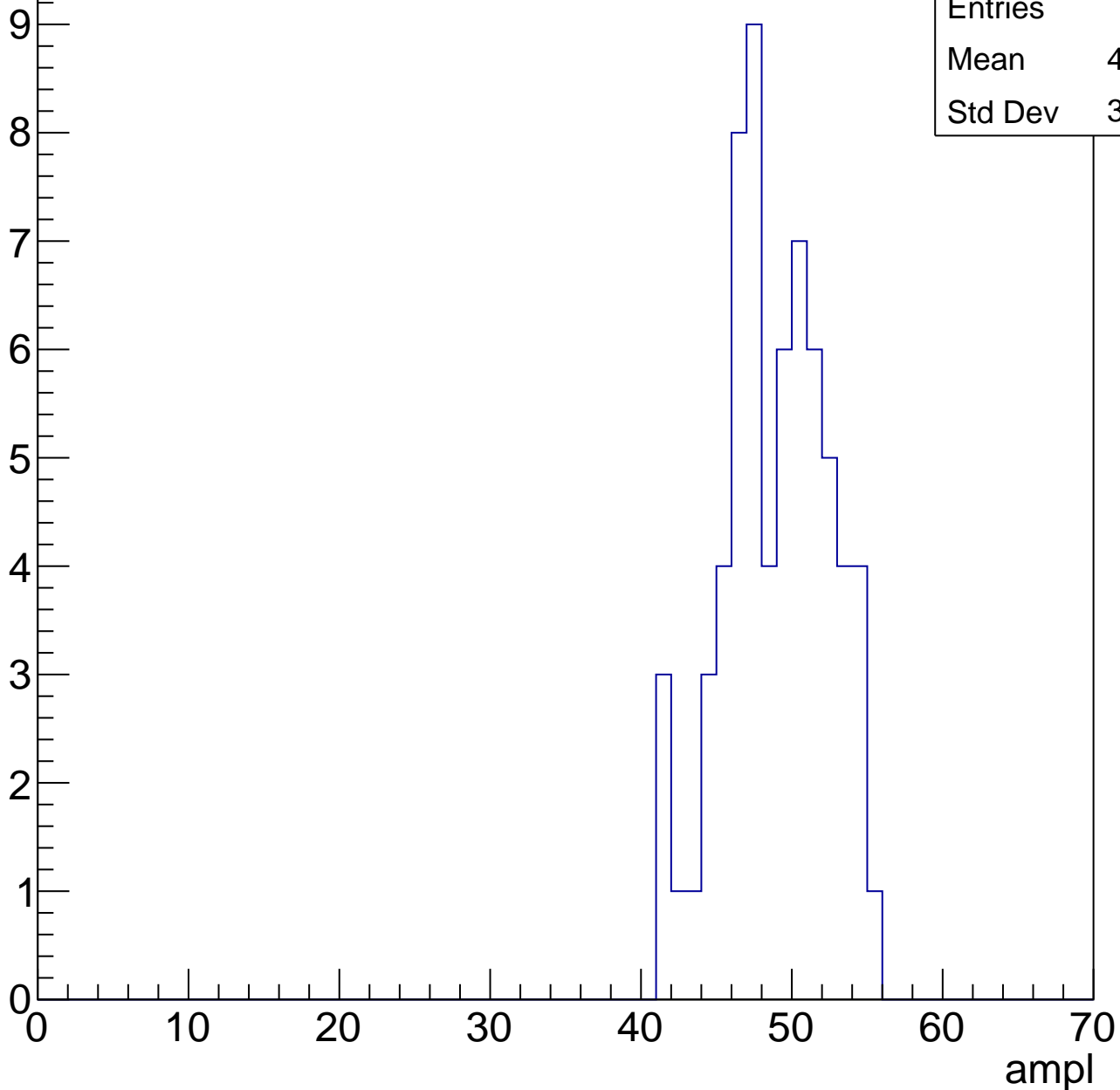


# B0L102S, U17-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.42
Std Dev	3.442

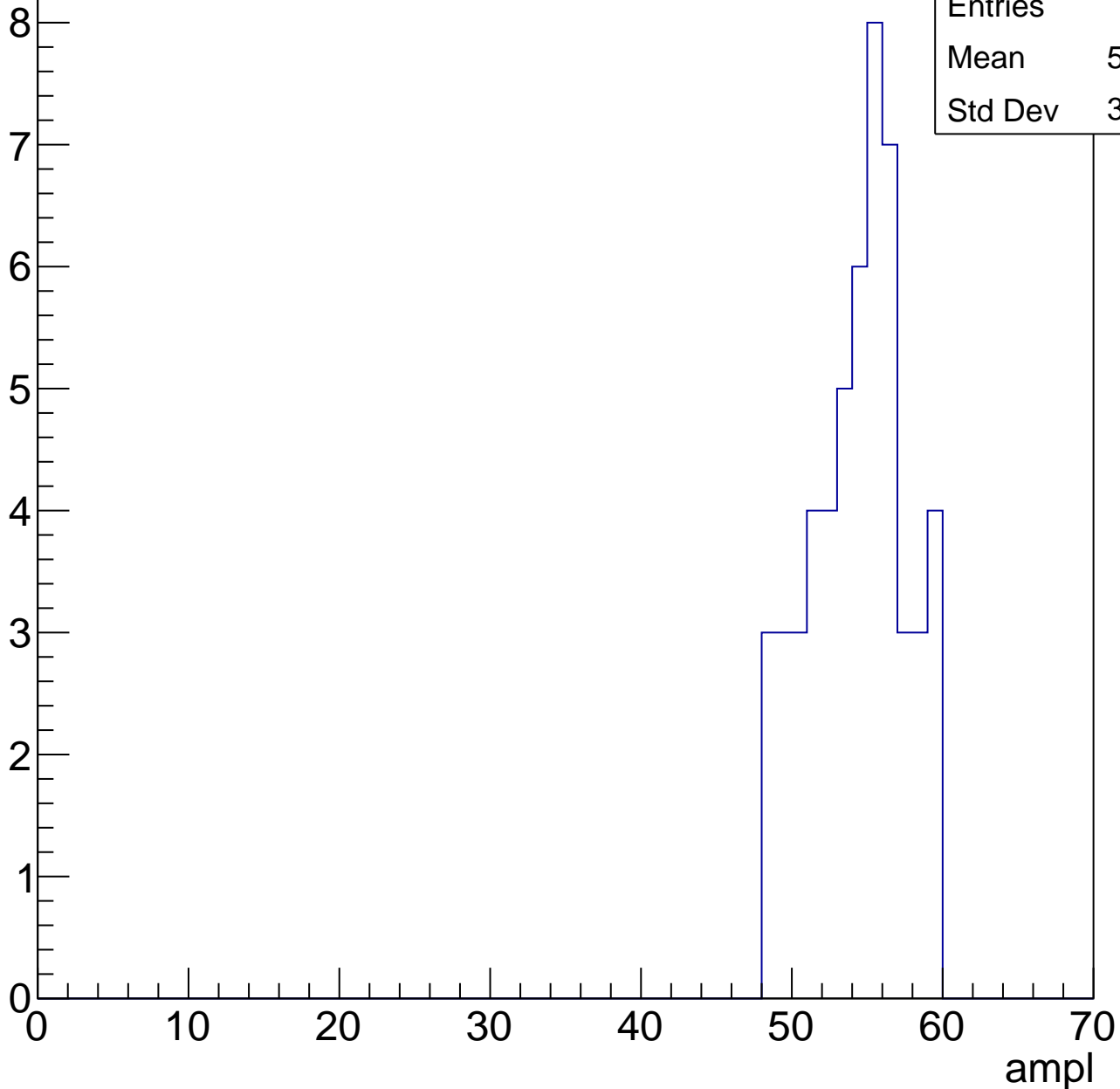


# B0L102S, U17-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	53.87
Std Dev	3.066

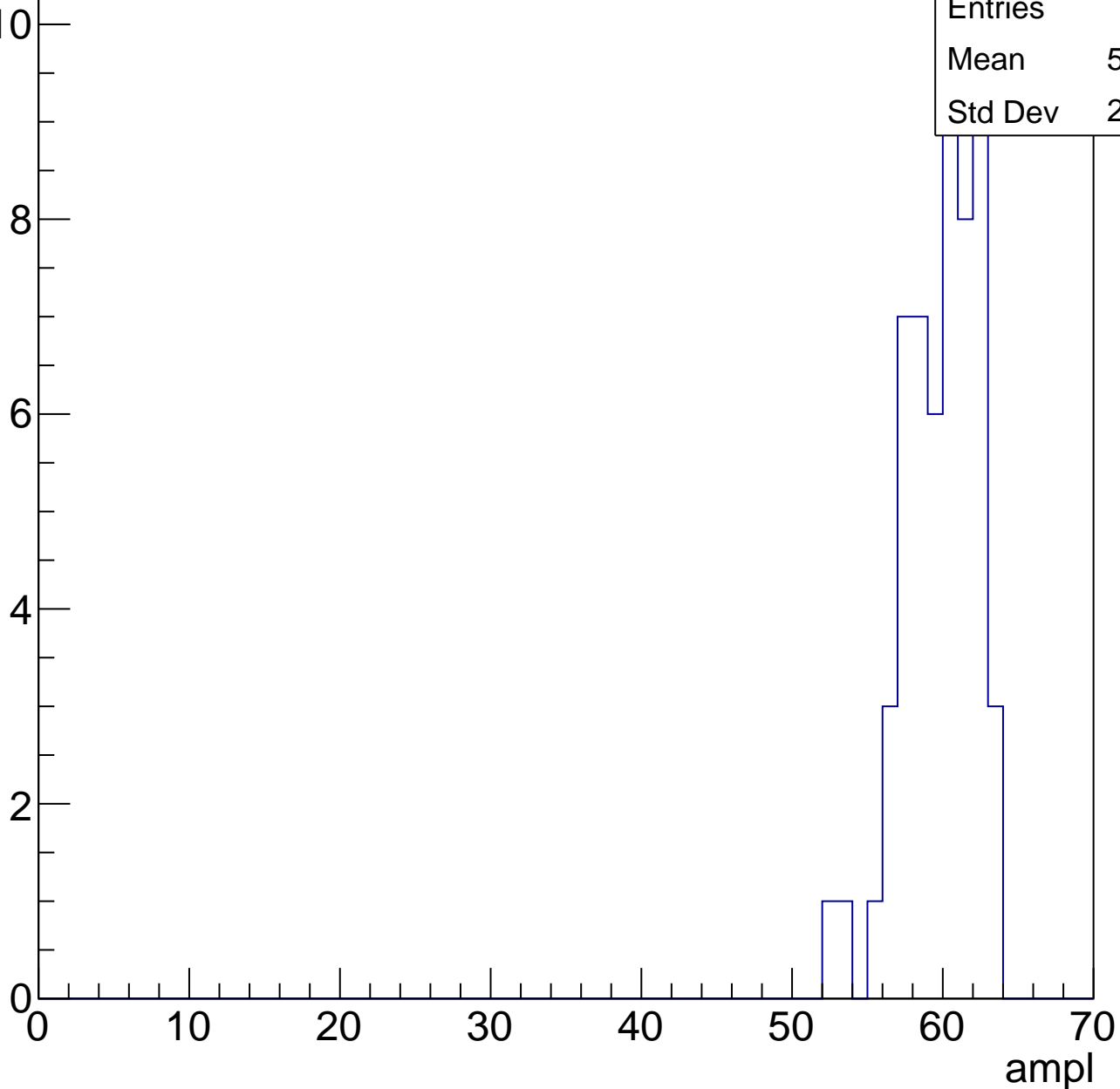


# B0L102S, U17-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	59.37
Std Dev	2.425

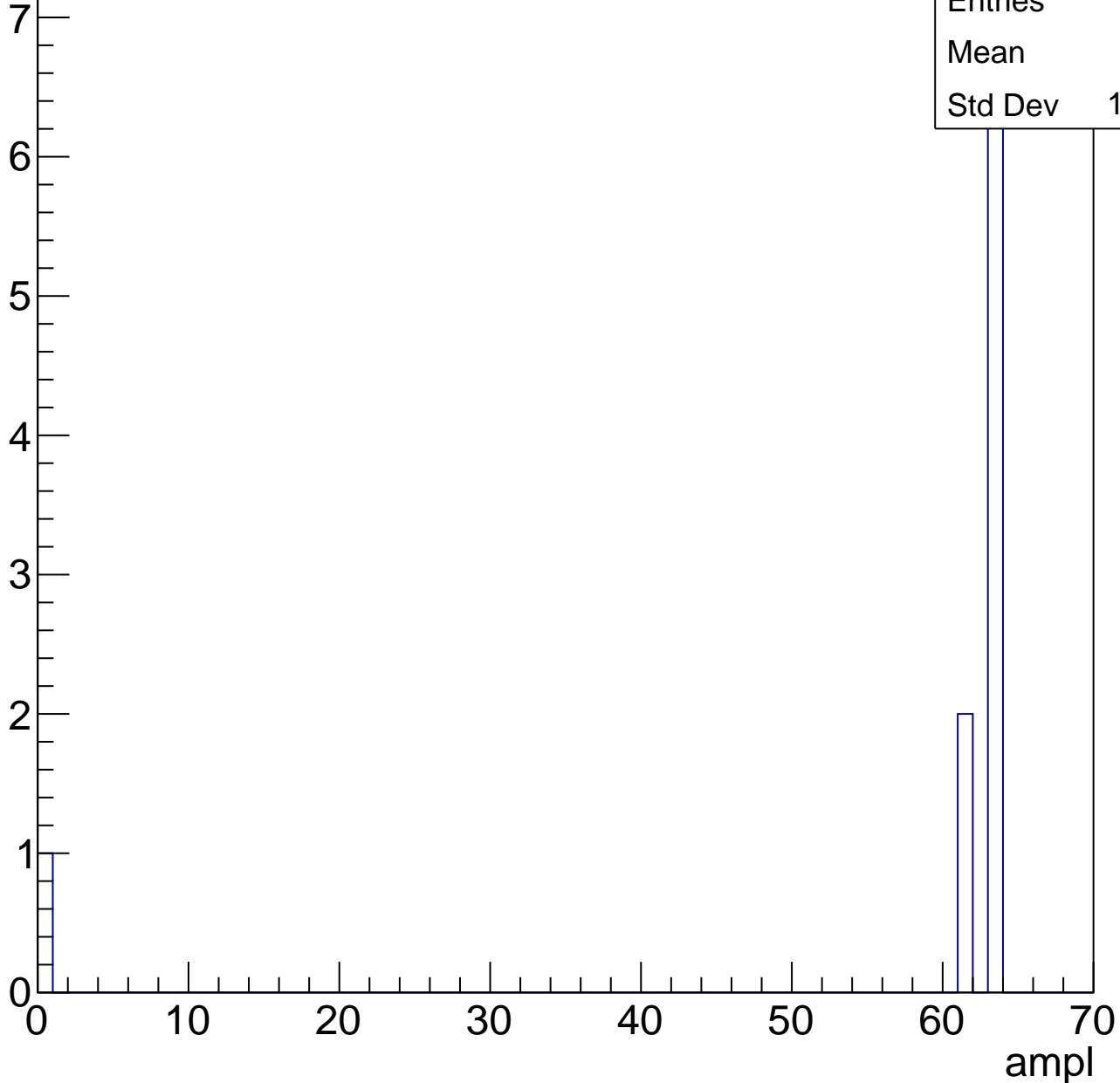


# B0L102S, U17-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	10
Mean	56.3
Std Dev	18.78





# B0L102S, U17-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch116, adc0

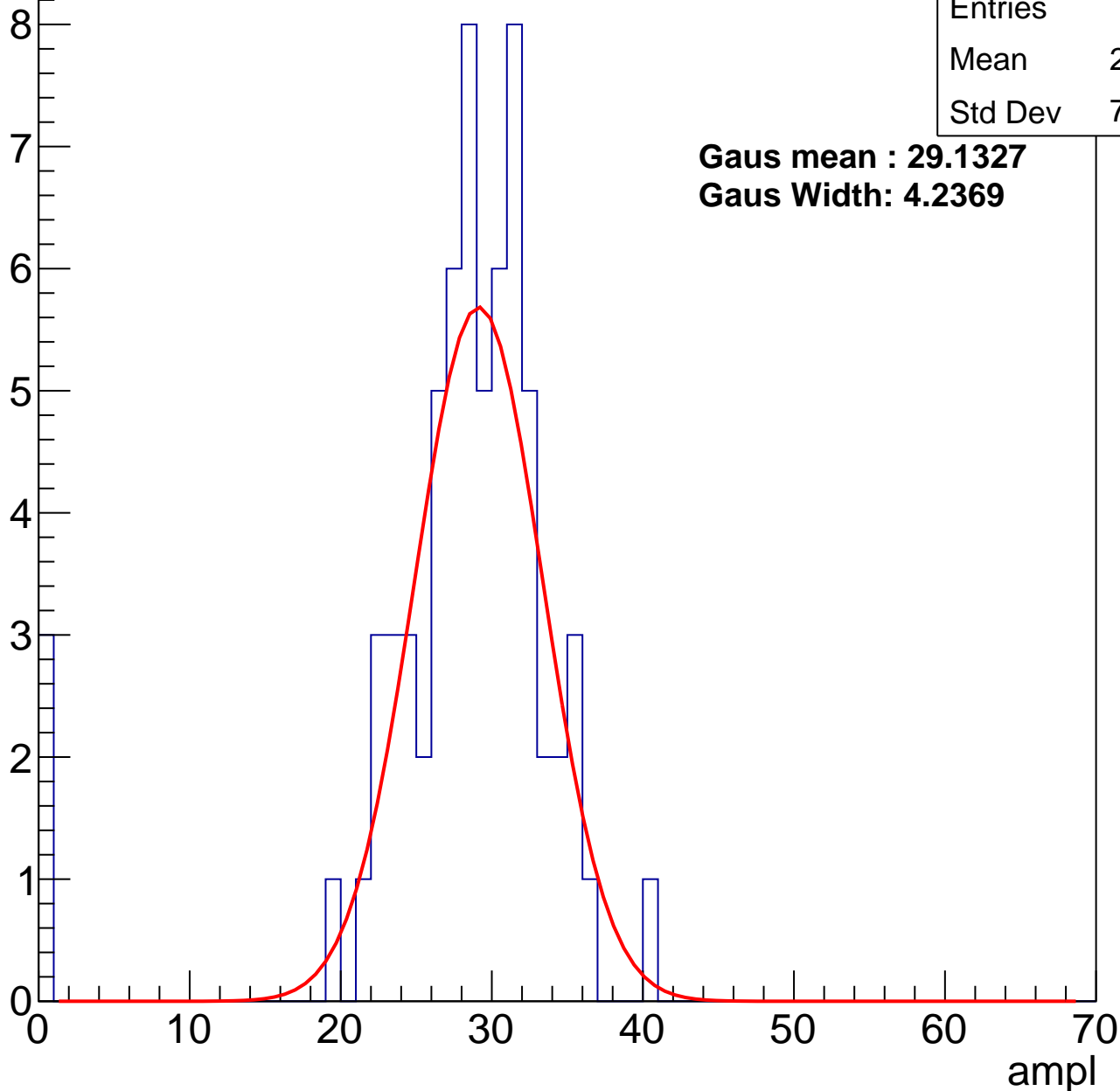
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	68
Mean	27.37
Std Dev	7.058

**Gaus mean : 29.1327**

**Gaus Width: 4.2369**



# B0L102S, U17-ch116, adc1

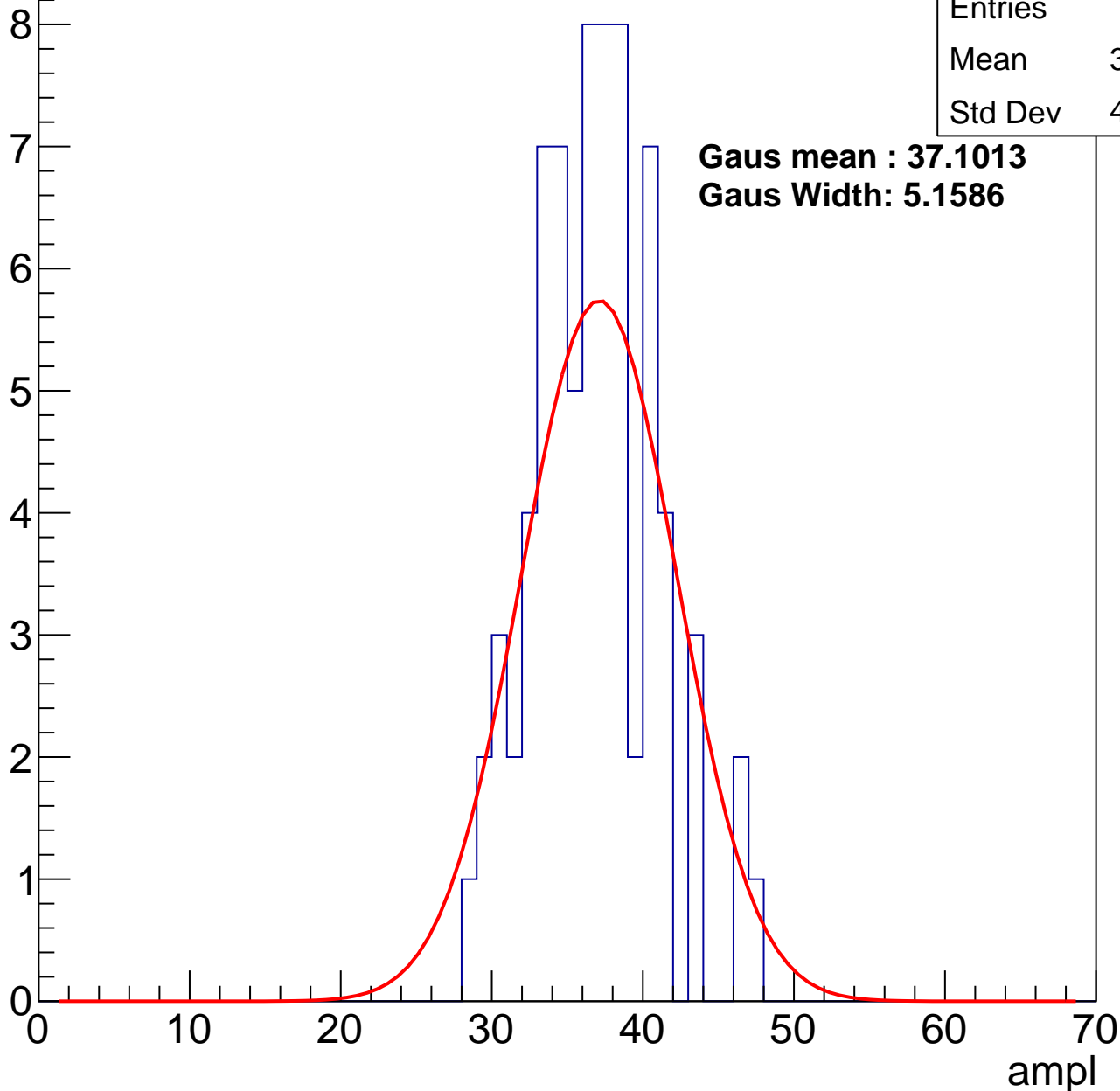
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	36.32
Std Dev	4.044

**Gaus mean : 37.1013**

**Gaus Width: 5.1586**



# B0L102S, U17-ch116, adc2

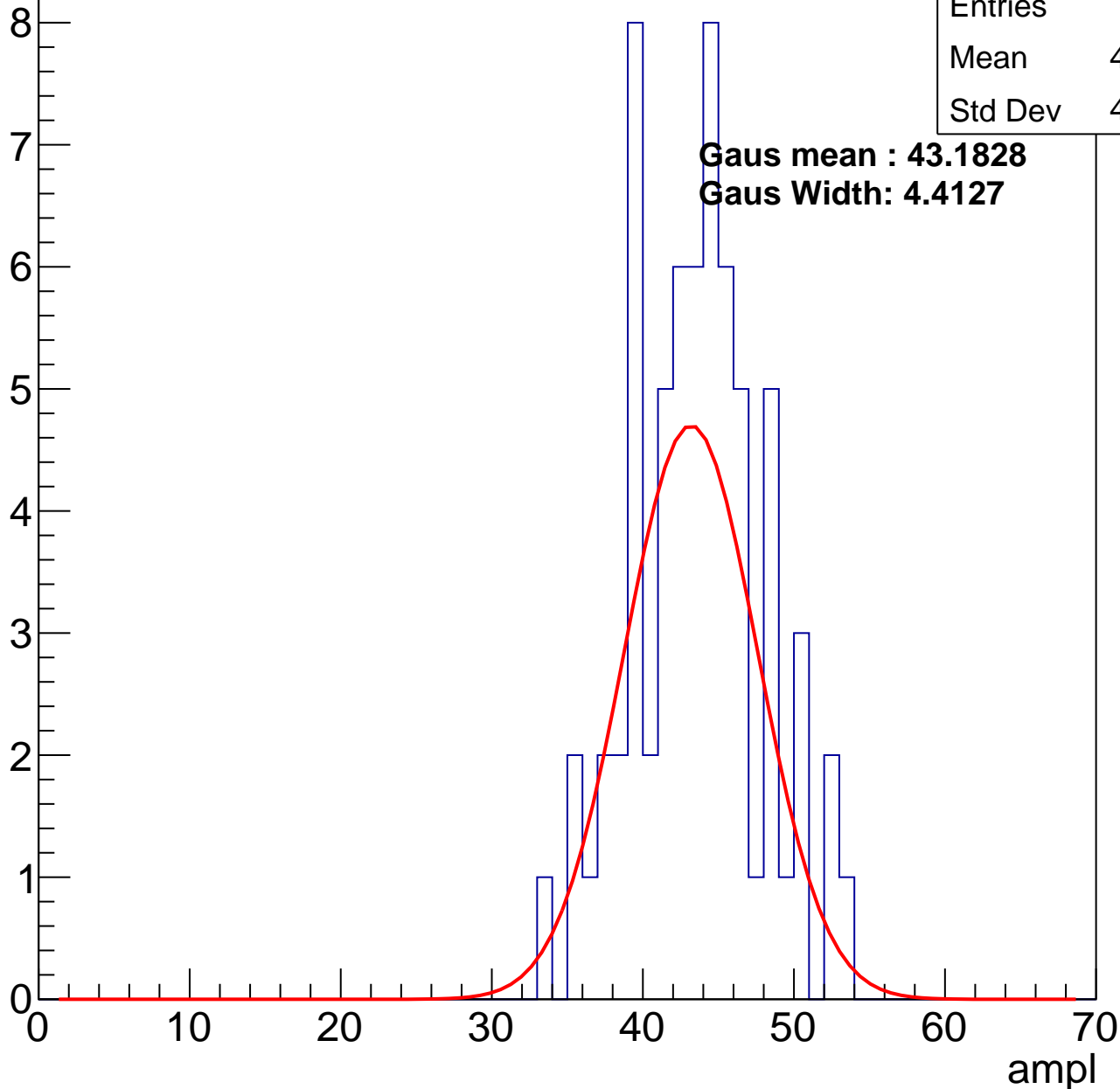
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.15
Std Dev	4.289

**Gaus mean : 43.1828**

**Gaus Width: 4.4127**

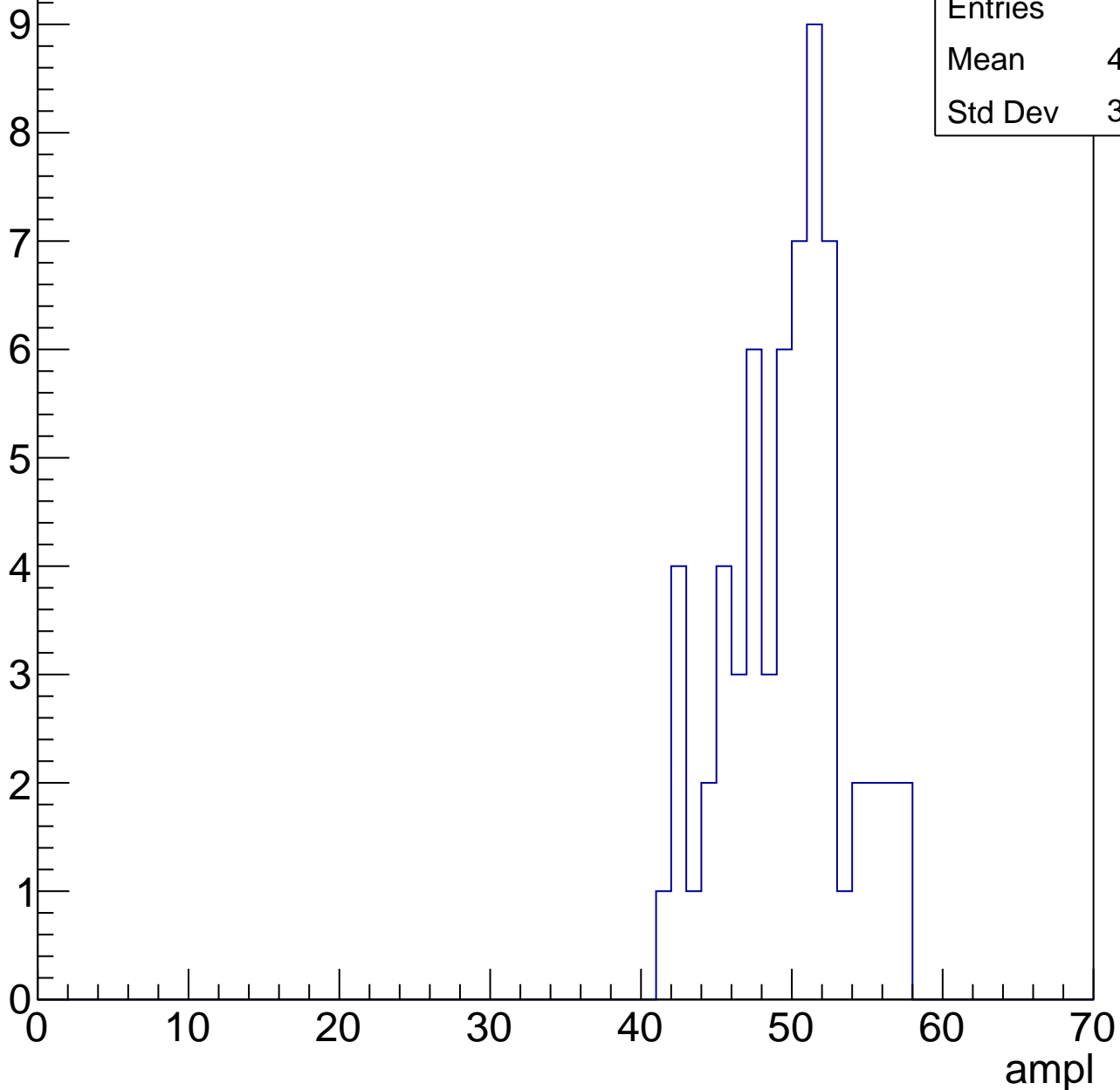


# B0L102S, U17-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	49.16
Std Dev	3.878

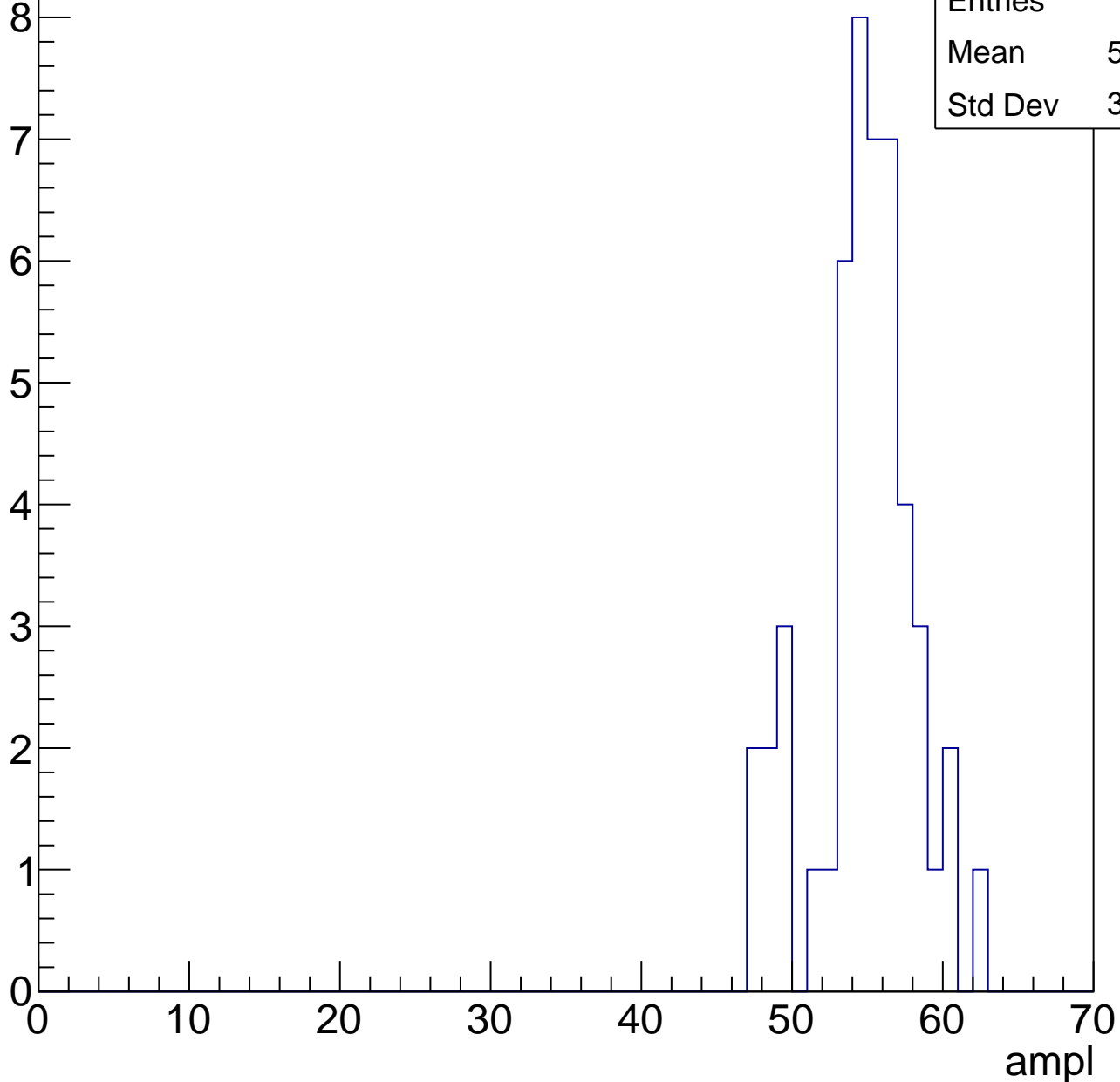


# B0L102S, U17-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	48
Mean	54.38
Std Dev	3.346

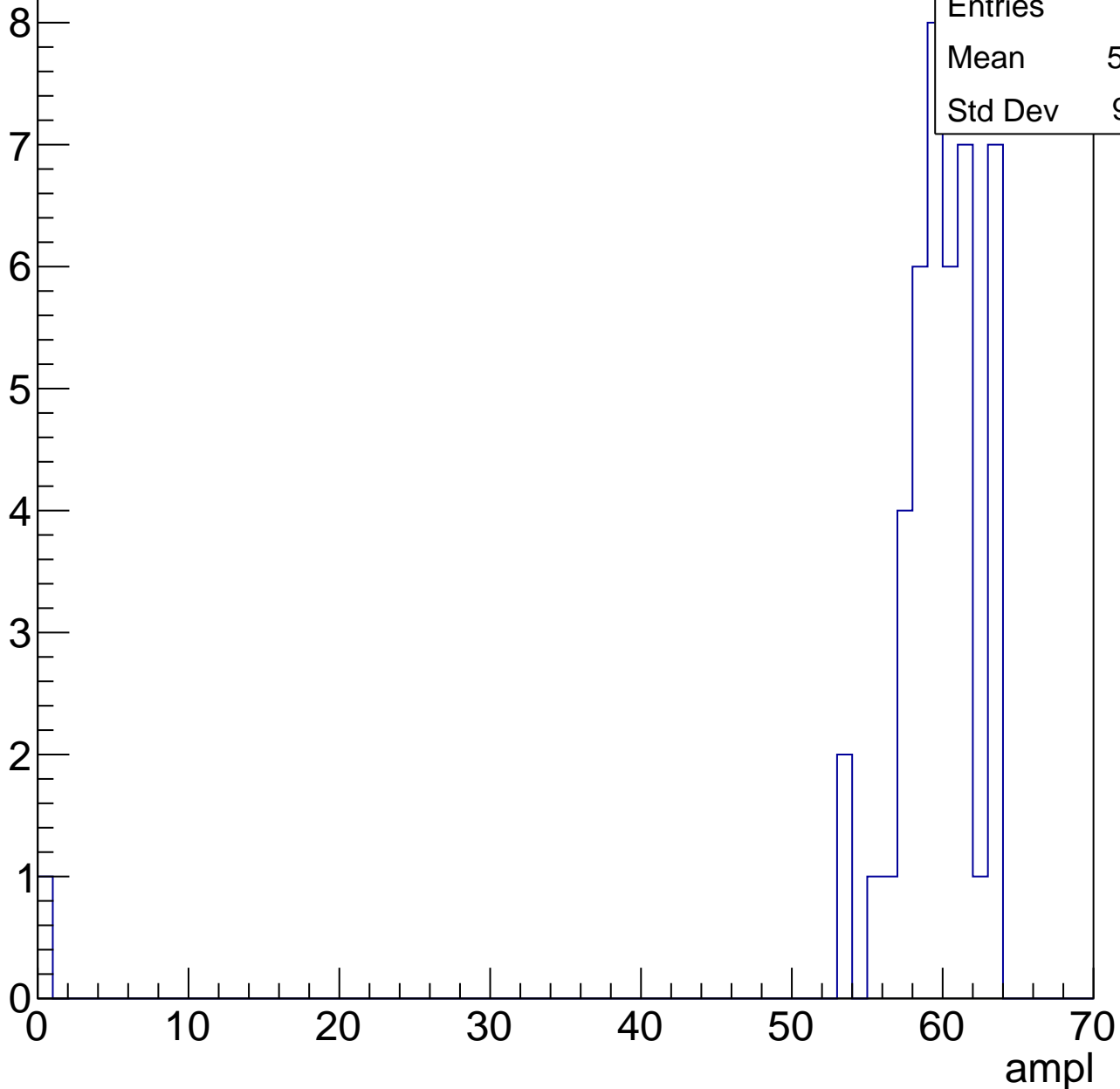


# B0L102S, U17-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.07
Std Dev	9.191

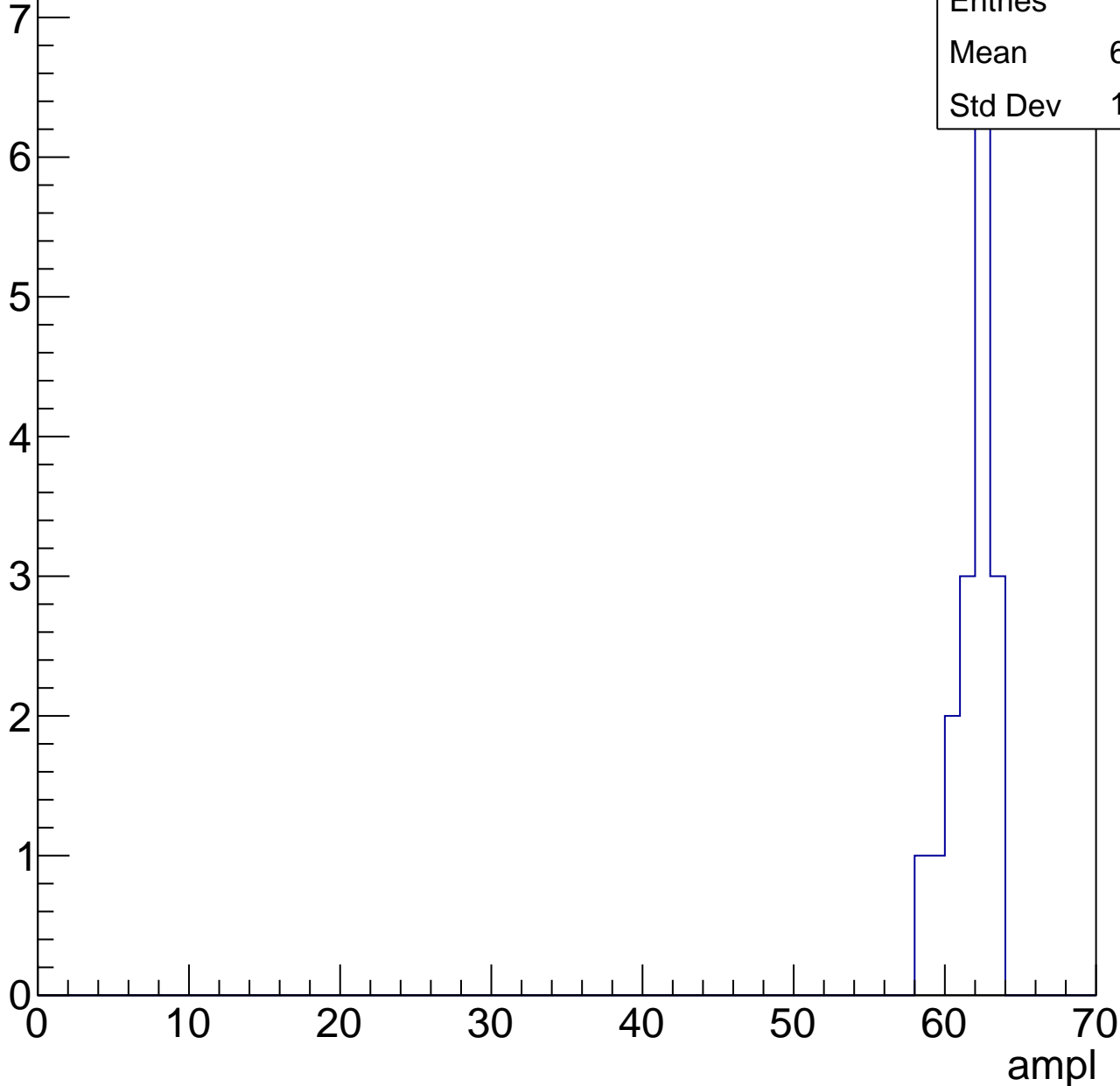


# B0L102S, U17-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	17
Mean	61.35
Std Dev	1.369

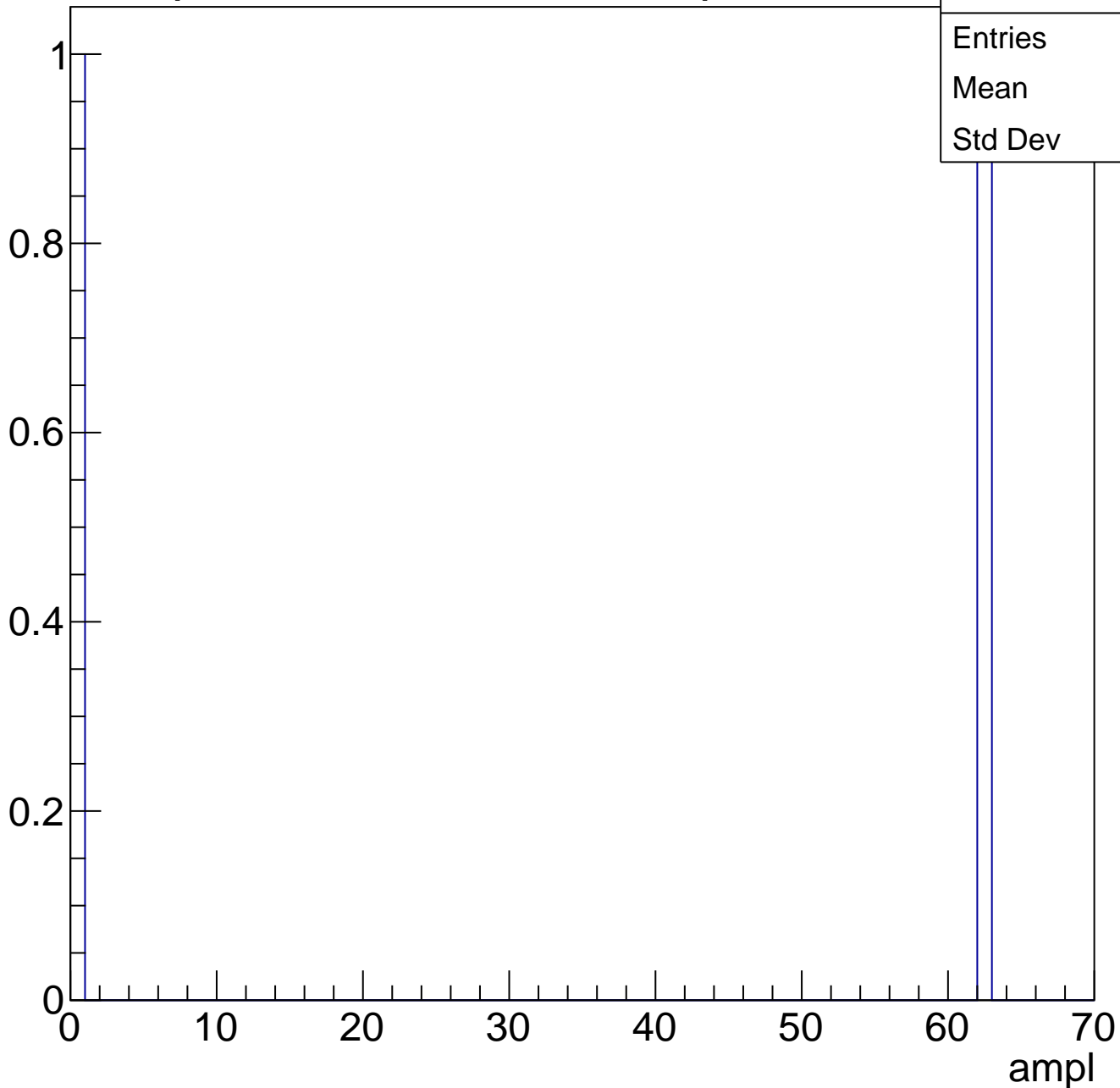




# B0L102S, U17-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch117, adc0

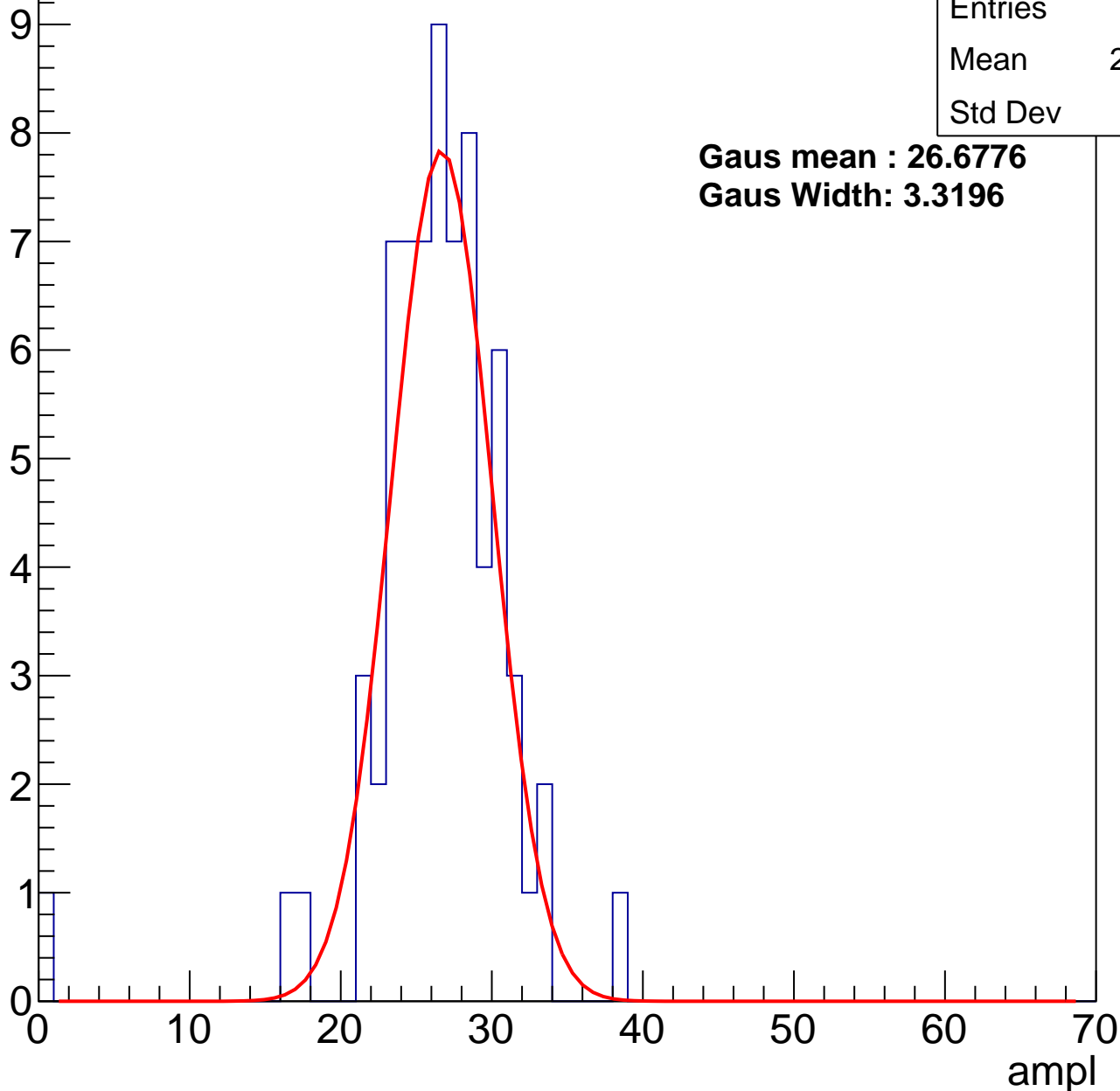
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	25.94
Std Dev	4.76

**Gaus mean : 26.6776**

**Gaus Width: 3.3196**



# B0L102S, U17-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	77
Mean	34.64
Std Dev	3.993

**Gaus mean : 34.2528**

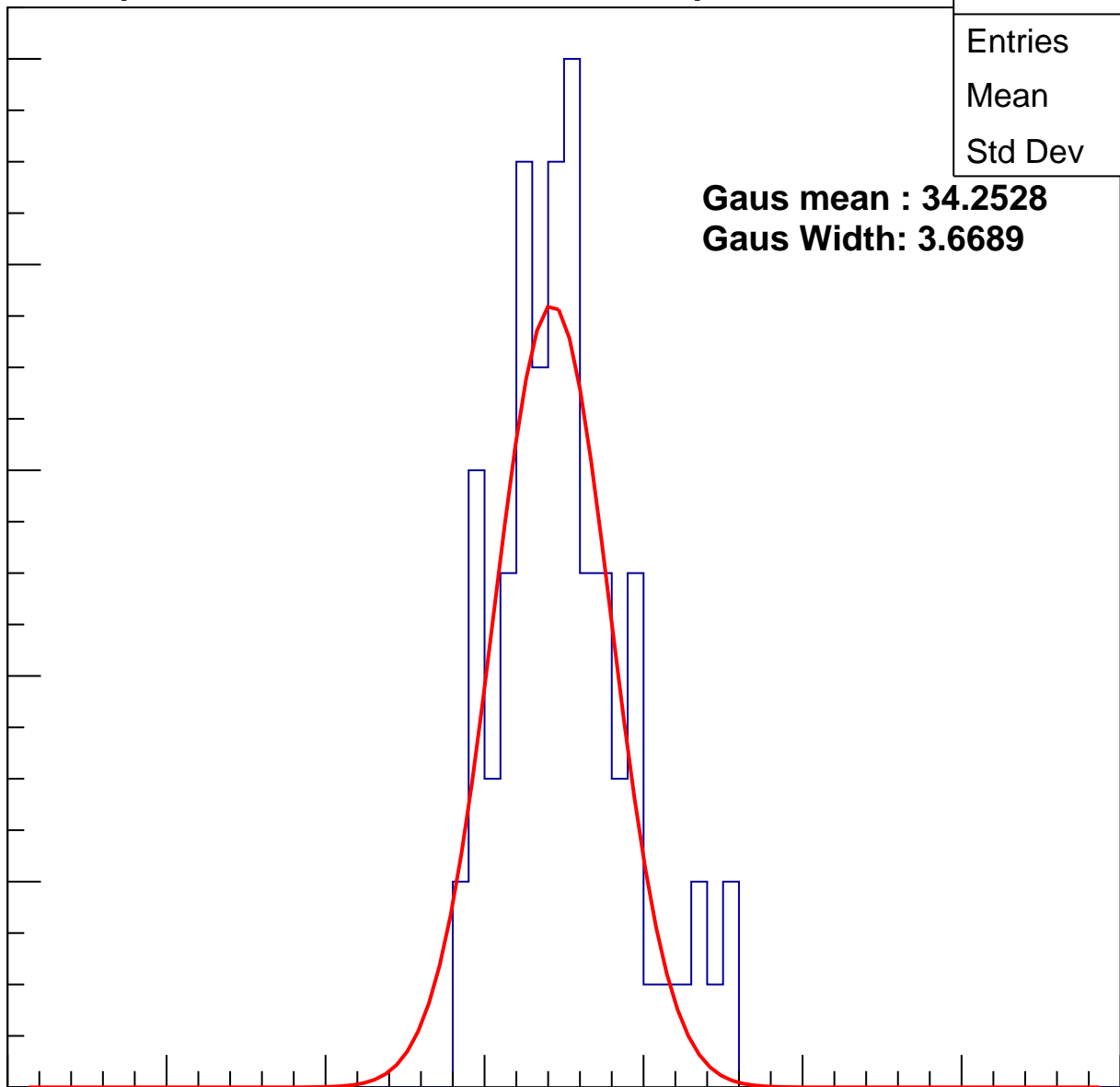
**Gaus Width: 3.6689**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U17-ch117, adc2

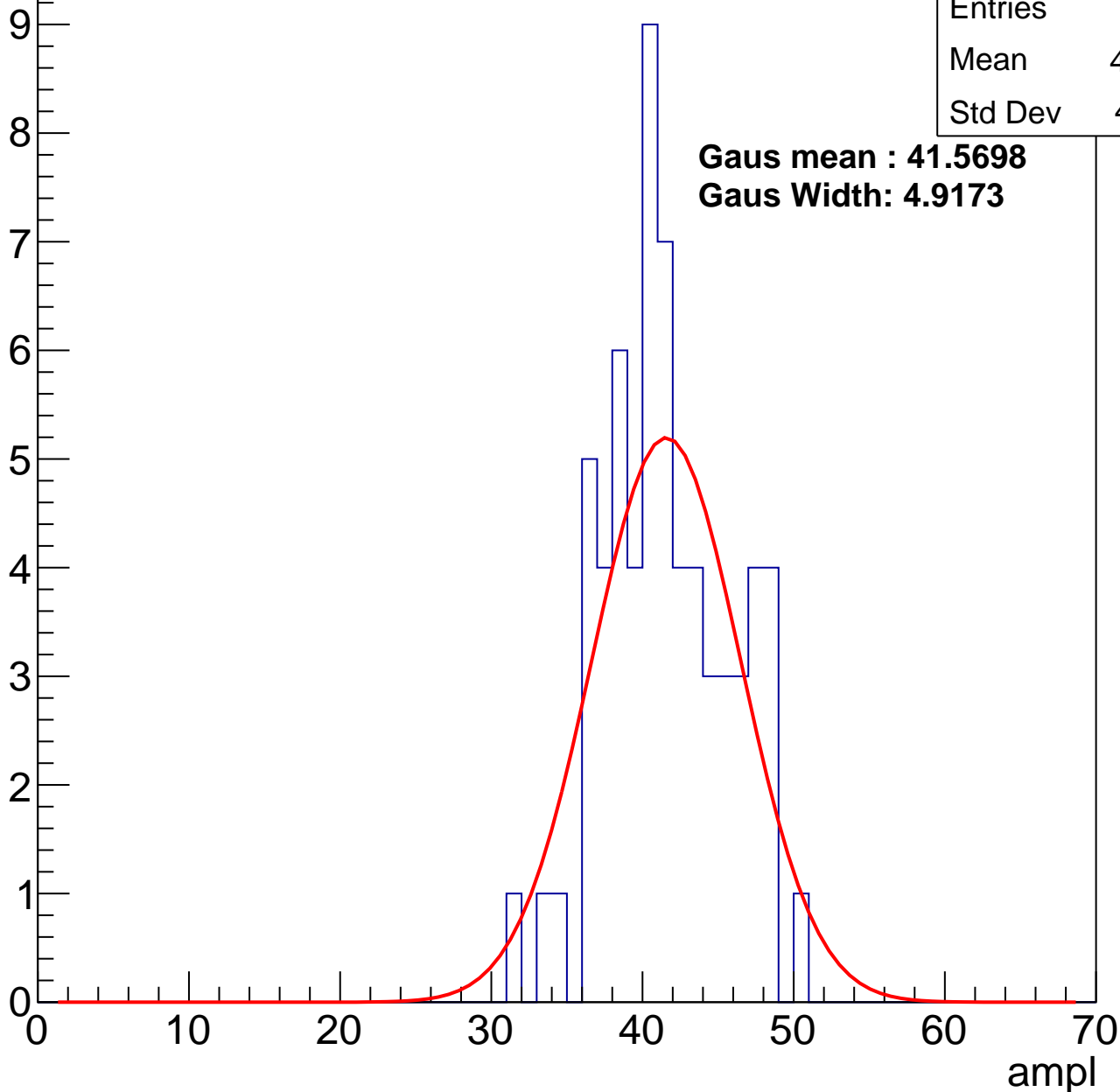
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.12
Std Dev	4.091

**Gaus mean : 41.5698**

**Gaus Width: 4.9173**

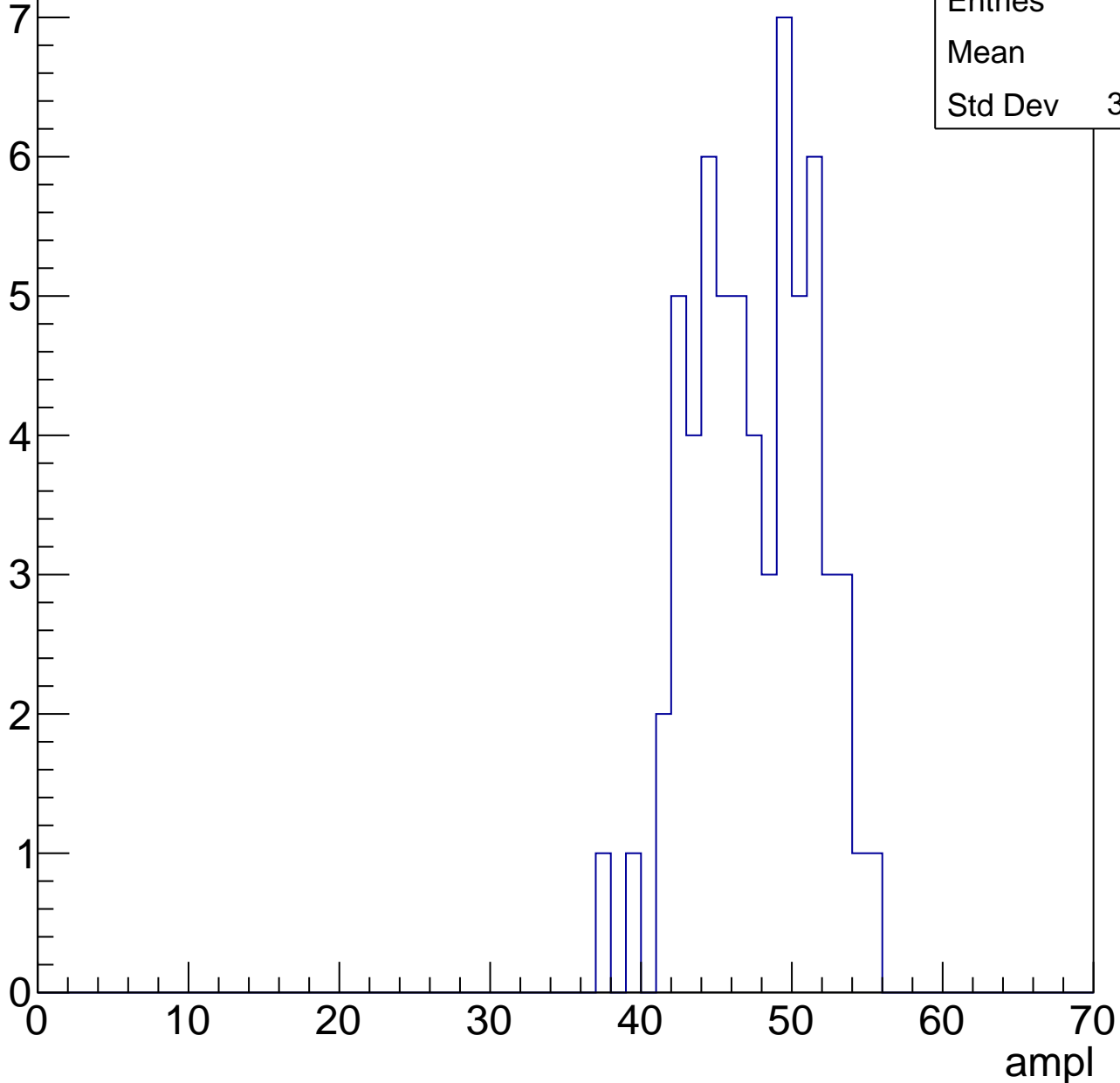


# B0L102S, U17-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	47
Std Dev	3.959

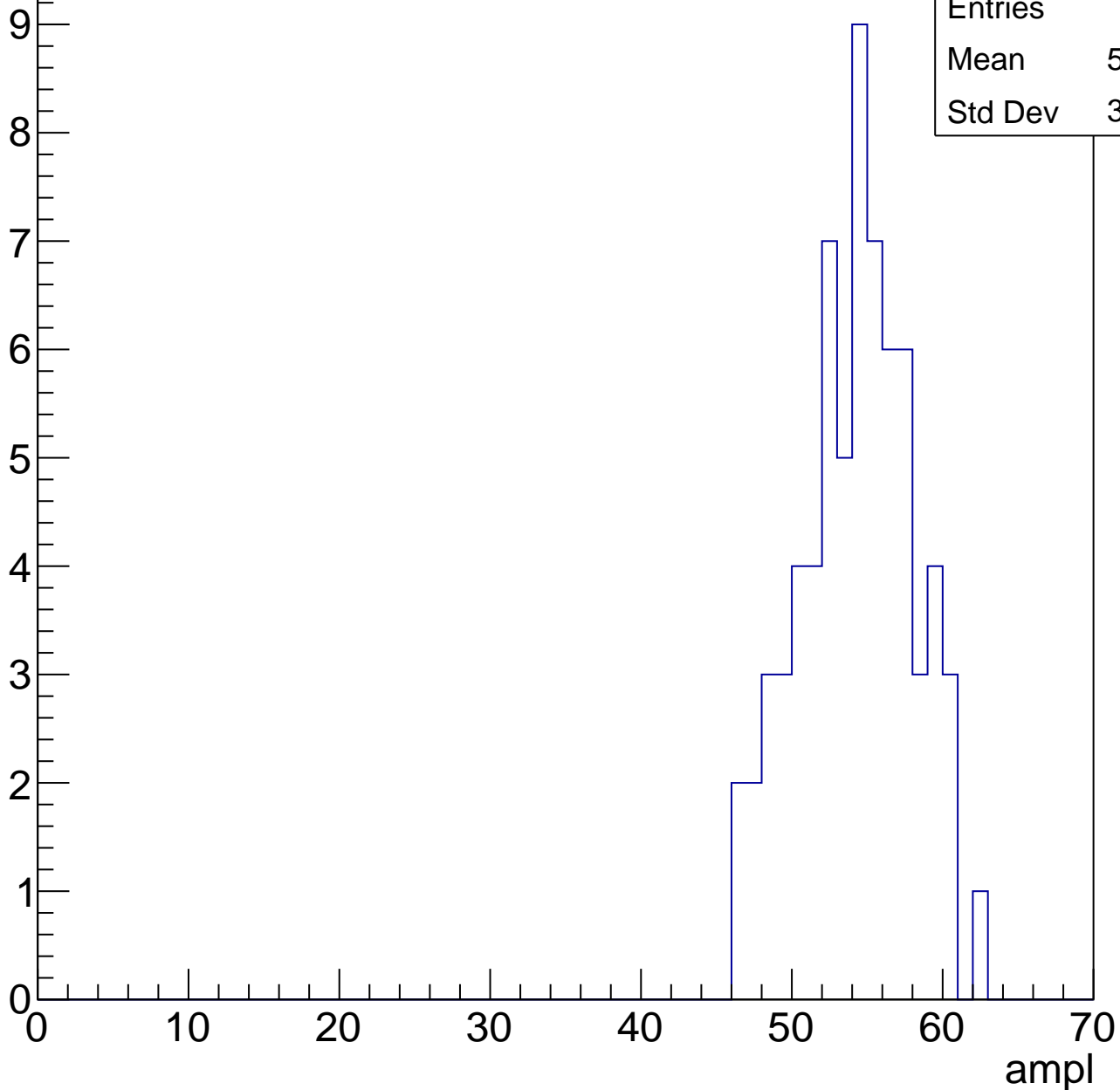


# B0L102S, U17-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	53.78
Std Dev	3.698

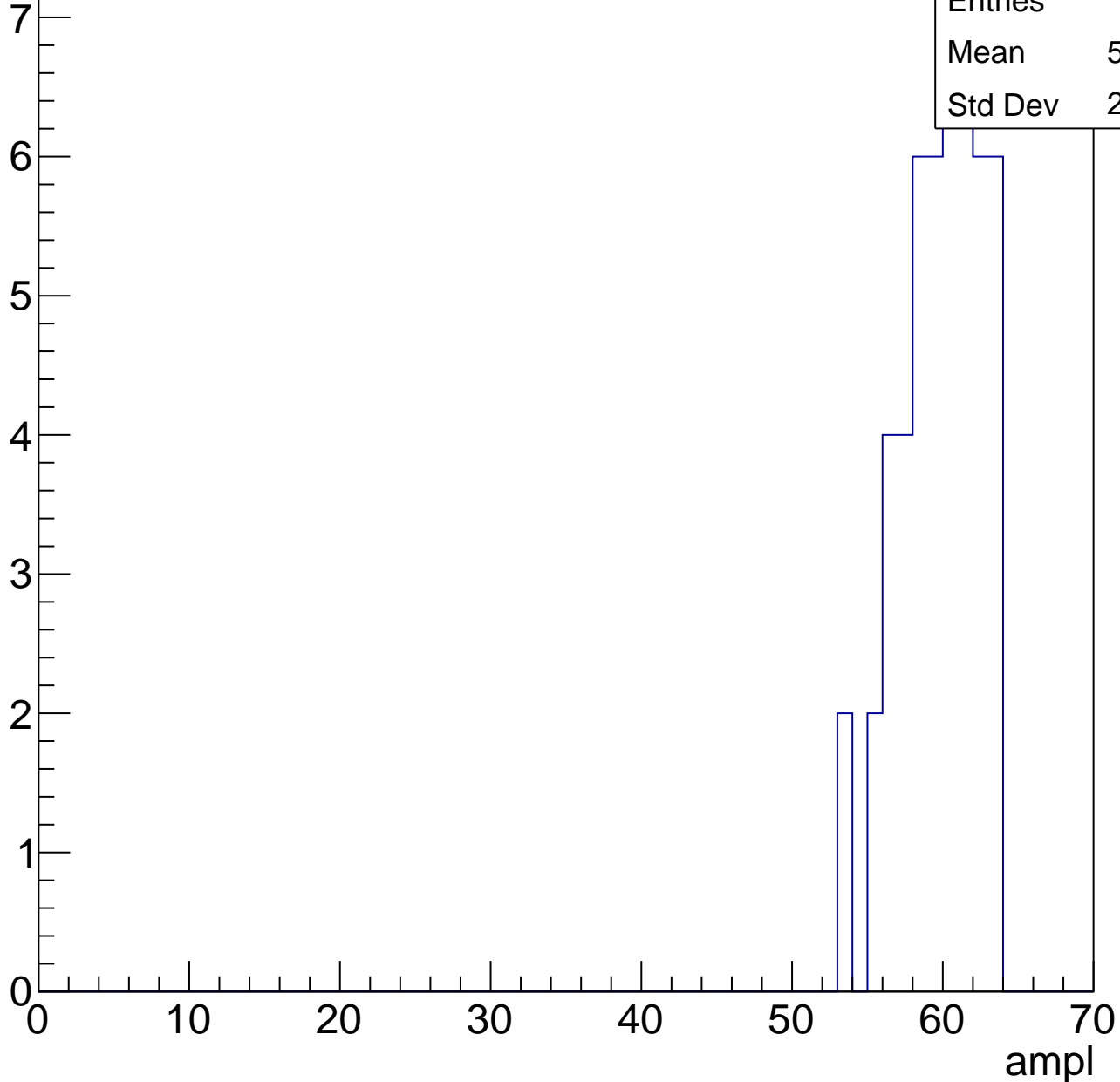


# B0L102S, U17-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

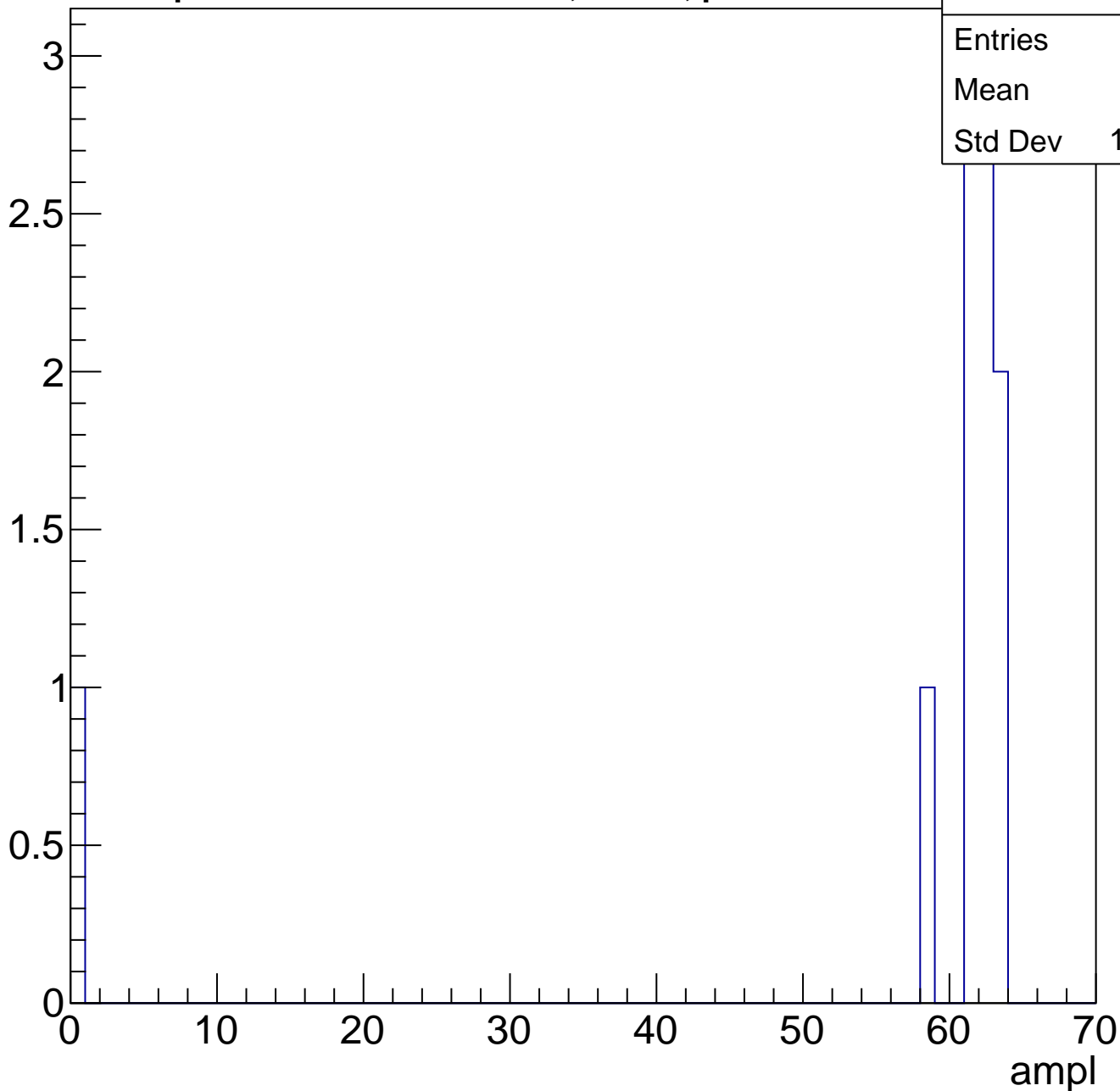
Entries	50
Mean	59.34
Std Dev	2.612



# B0L102S, U17-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch118, adc0

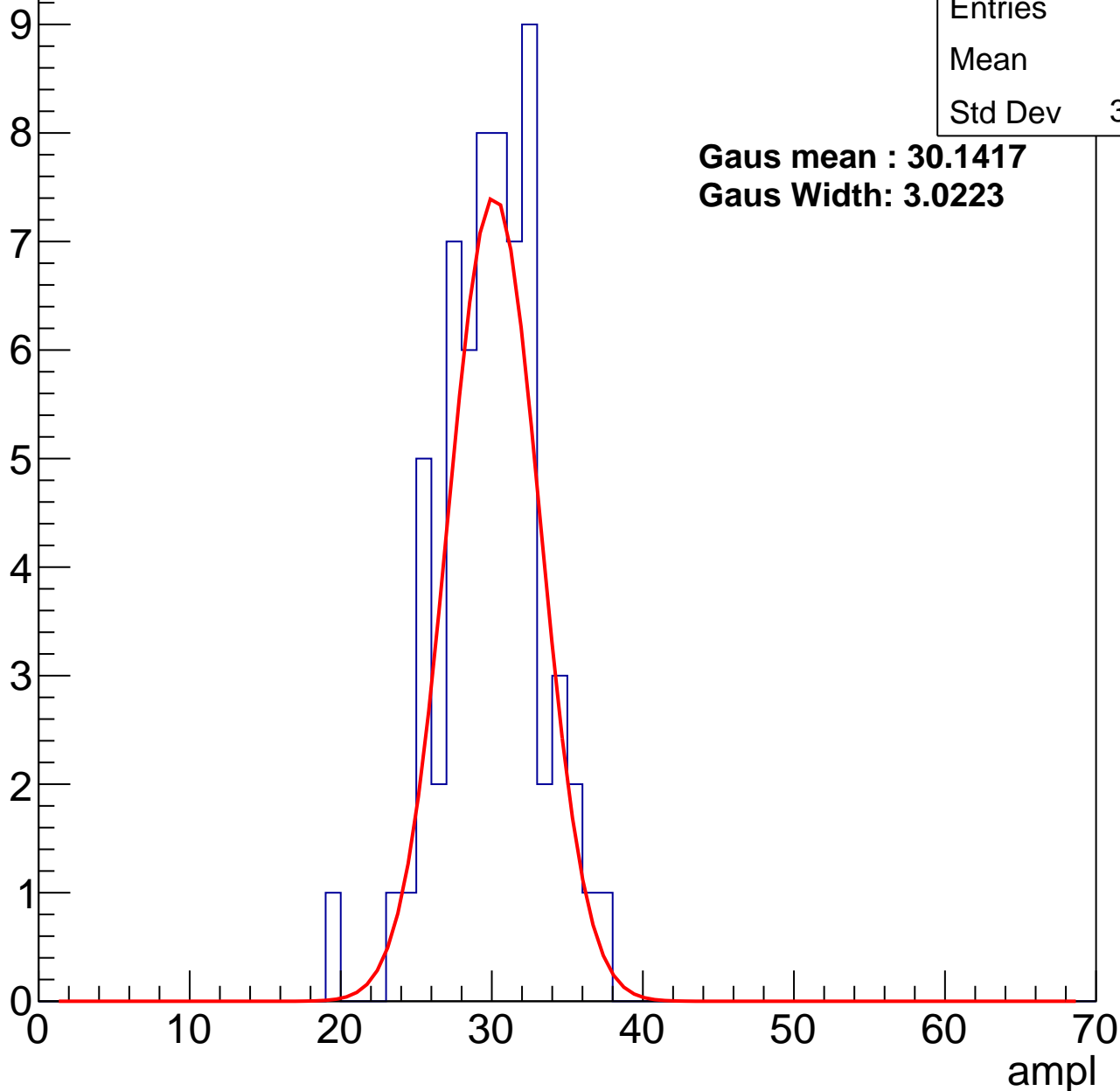
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	29.5
Std Dev	3.274

**Gaus mean : 30.1417**

**Gaus Width: 3.0223**



# B0L102S, U17-ch118, adc1

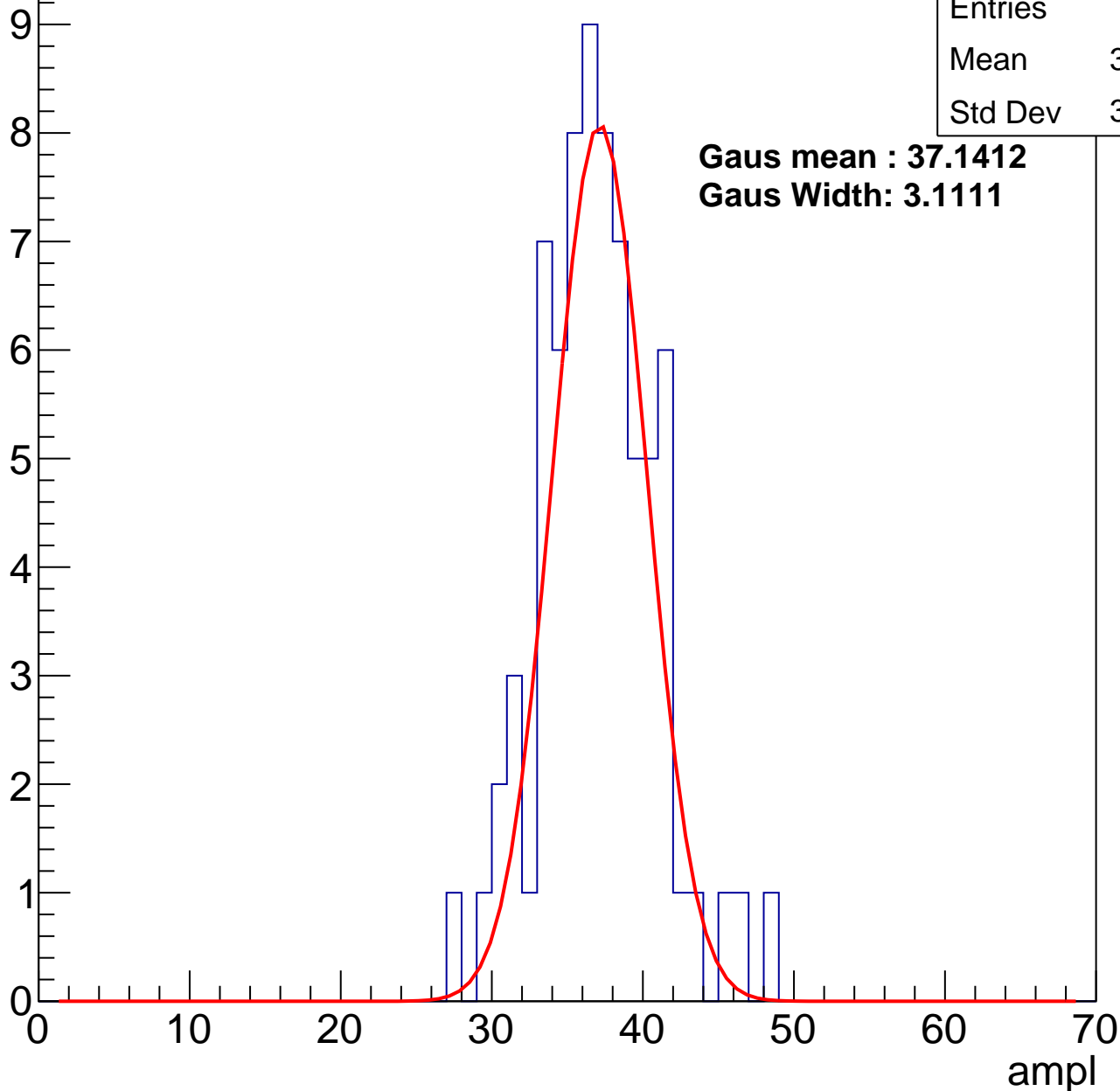
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	74
Mean	36.58
Std Dev	3.827

**Gaus mean : 37.1412**

**Gaus Width: 3.1111**



# B0L102S, U17-ch118, adc2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	72
Mean	43.57
Std Dev	3.593

**Gaus mean : 43.7468**

**Gaus Width: 3.5878**

Entry

10

8

6

4

2

0

0

10

20

30

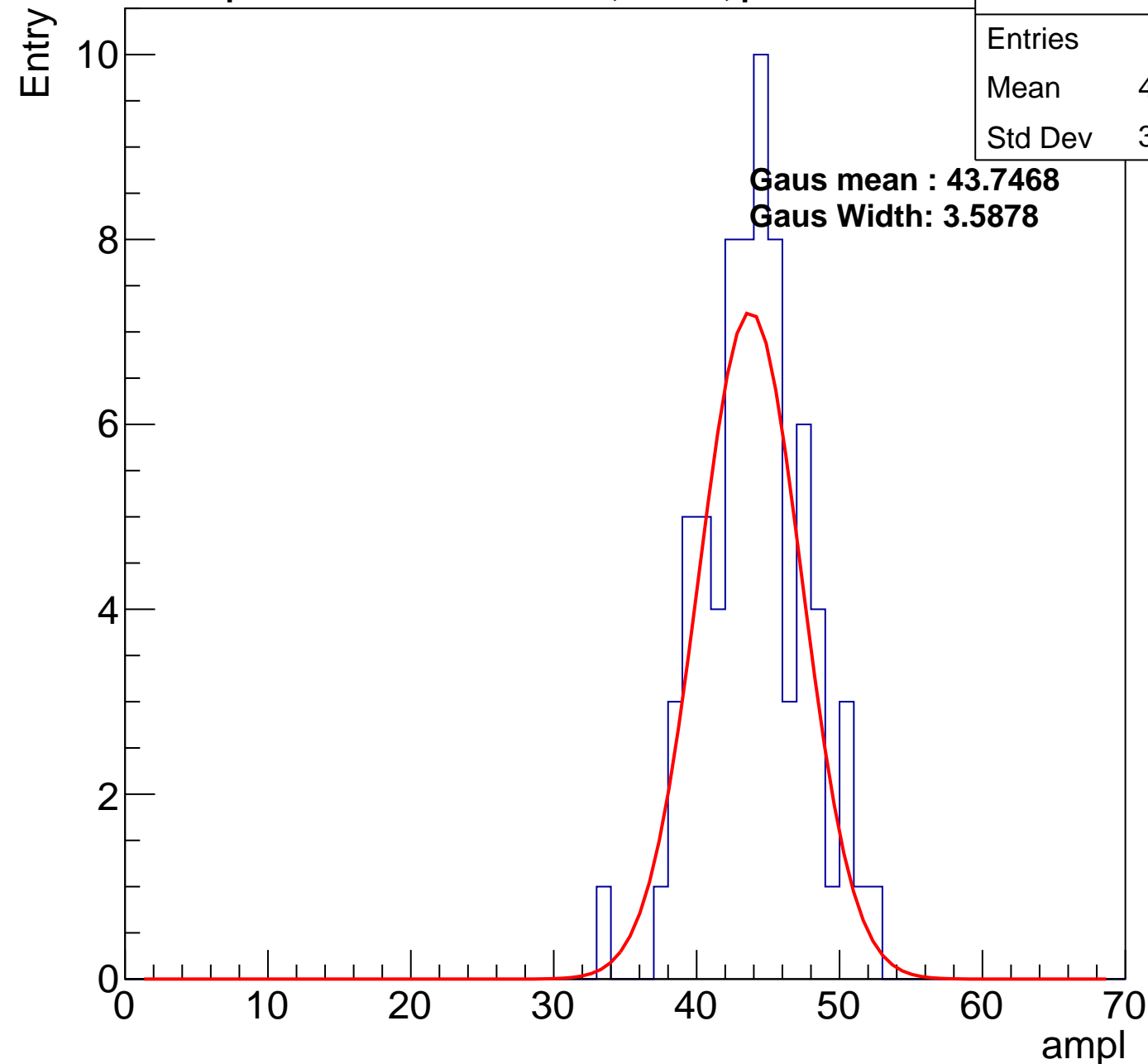
40

50

60

70

ampl

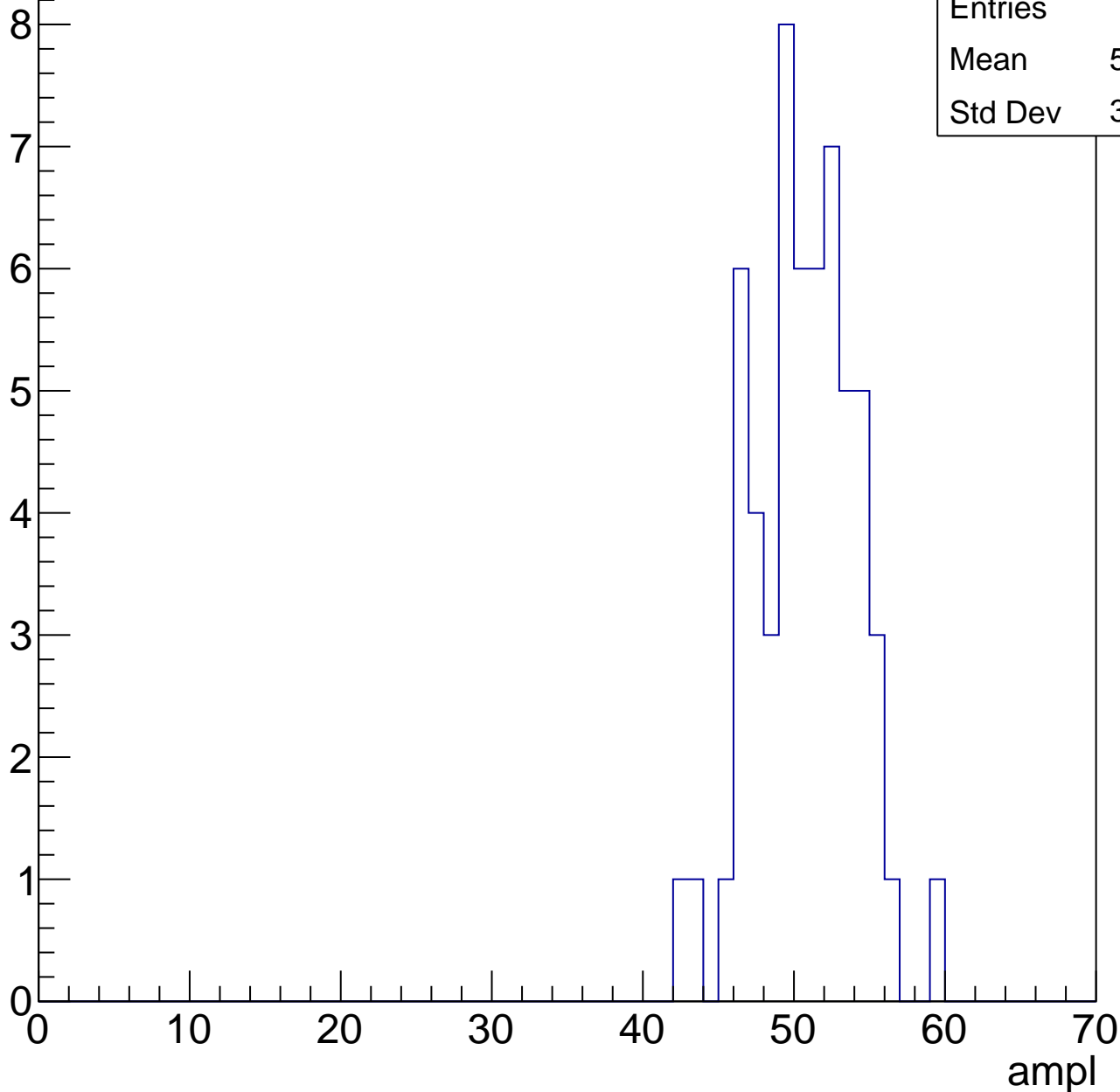


# B0L102S, U17-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	50.26
Std Dev	3.314

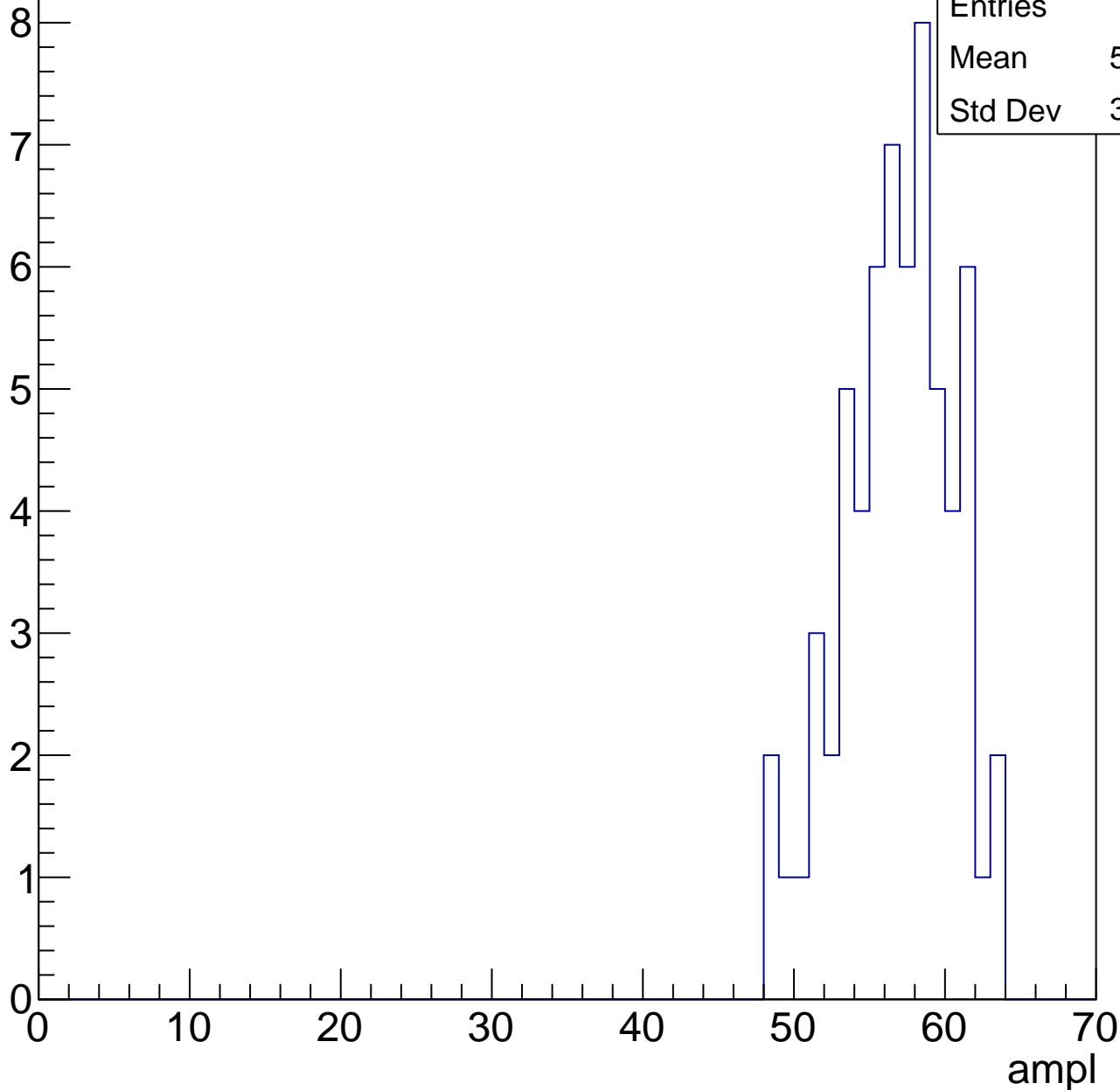


# B0L102S, U17-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	56.35
Std Dev	3.586

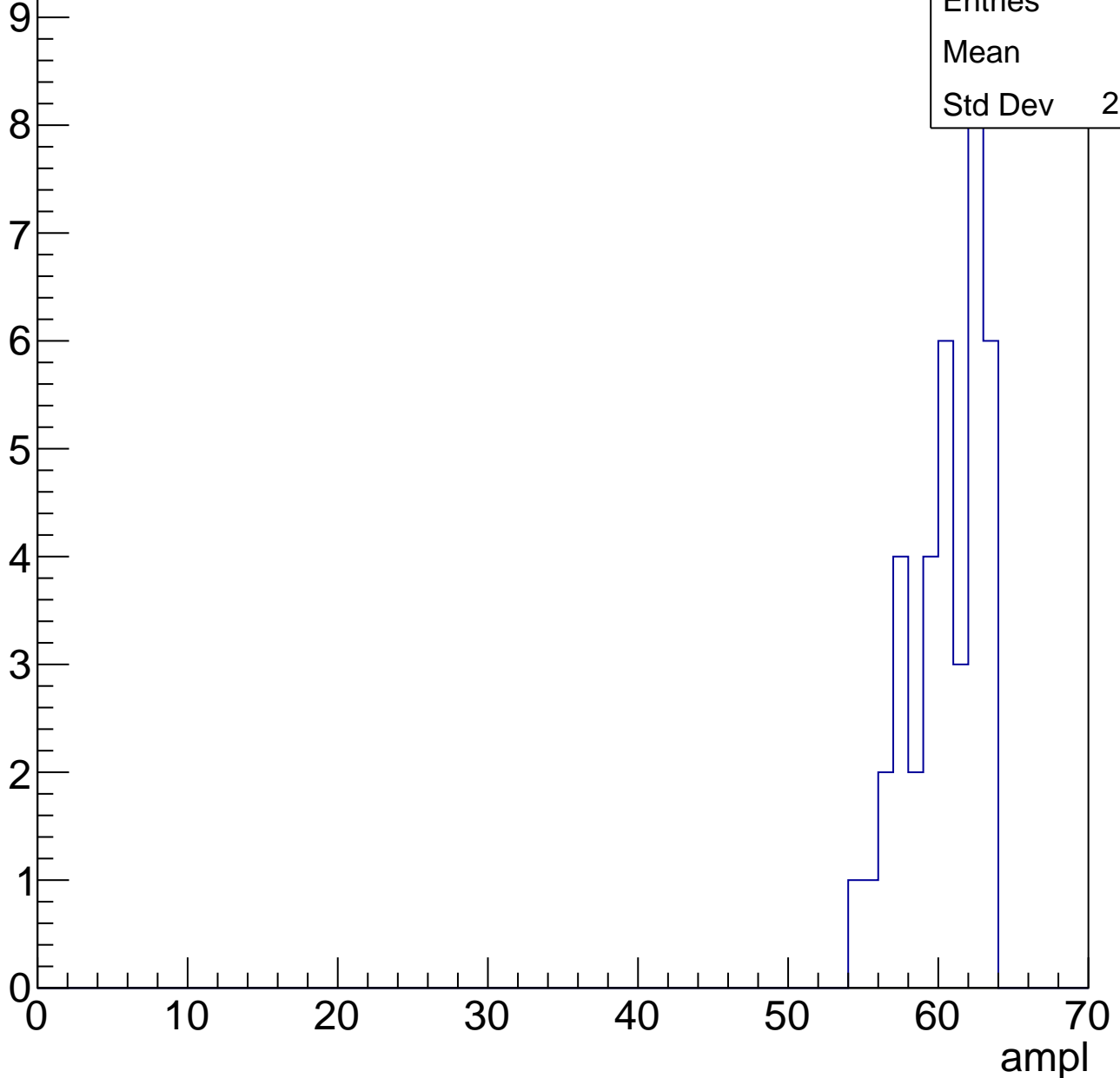


# B0L102S, U17-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	38
Mean	60
Std Dev	2.482



# B0L102S, U17-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch119, adc0

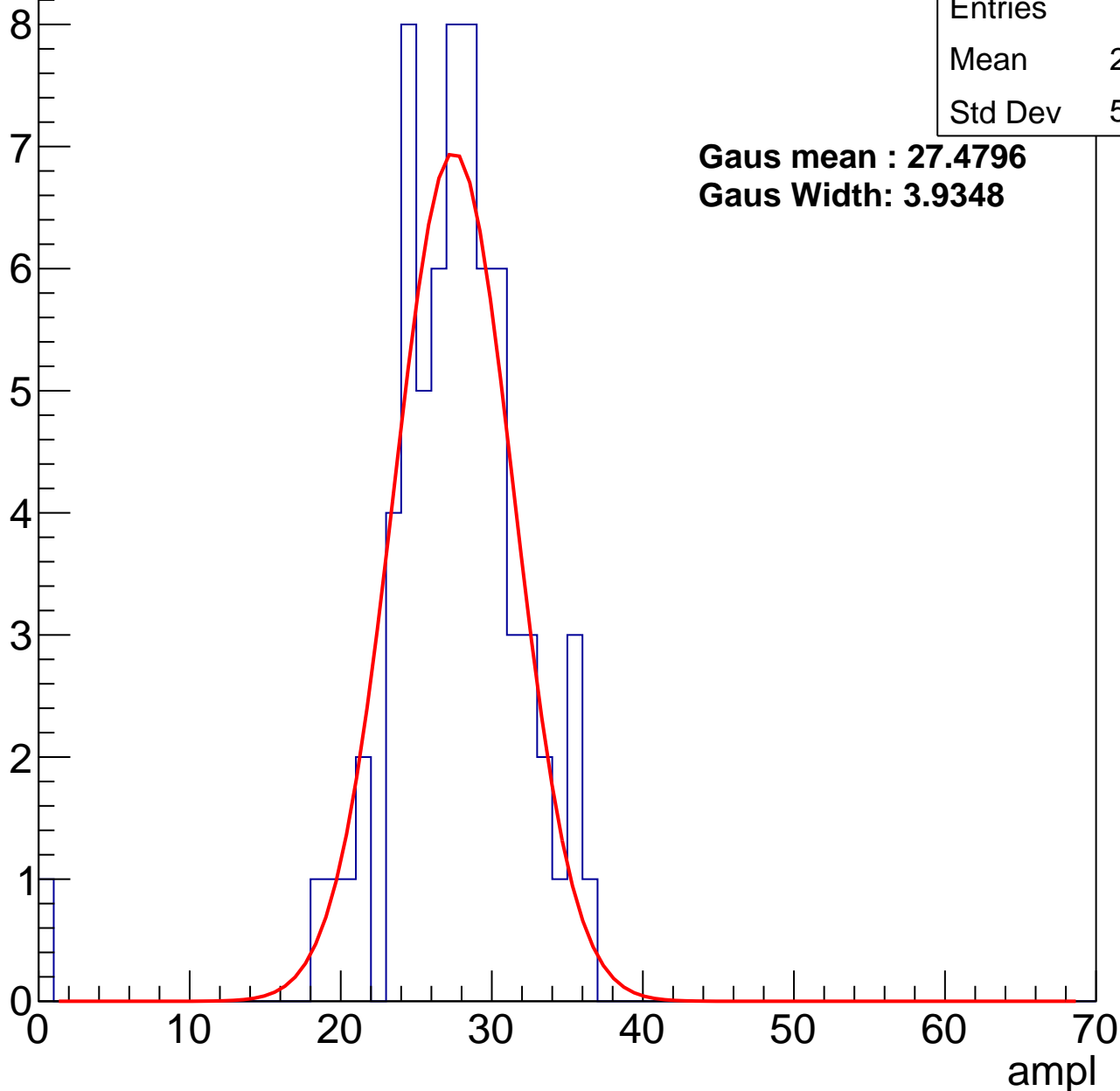
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	26.97
Std Dev	5.014

**Gaus mean : 27.4796**

**Gaus Width: 3.9348**



# B0L102S, U17-ch119, adc1

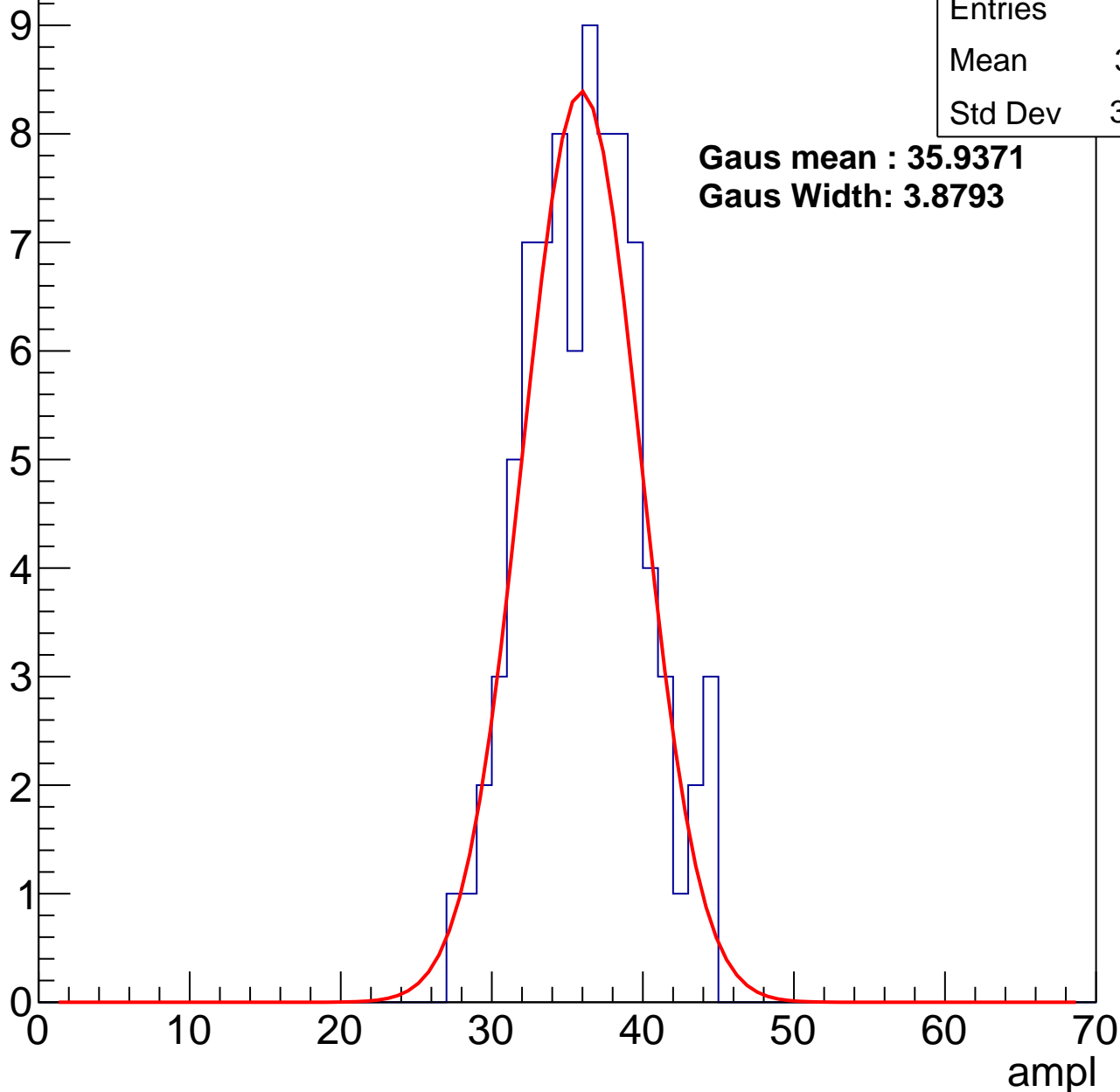
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	85
Mean	35.71
Std Dev	3.834

**Gaus mean : 35.9371**

**Gaus Width: 3.8793**



# B0L102S, U17-ch119, adc2

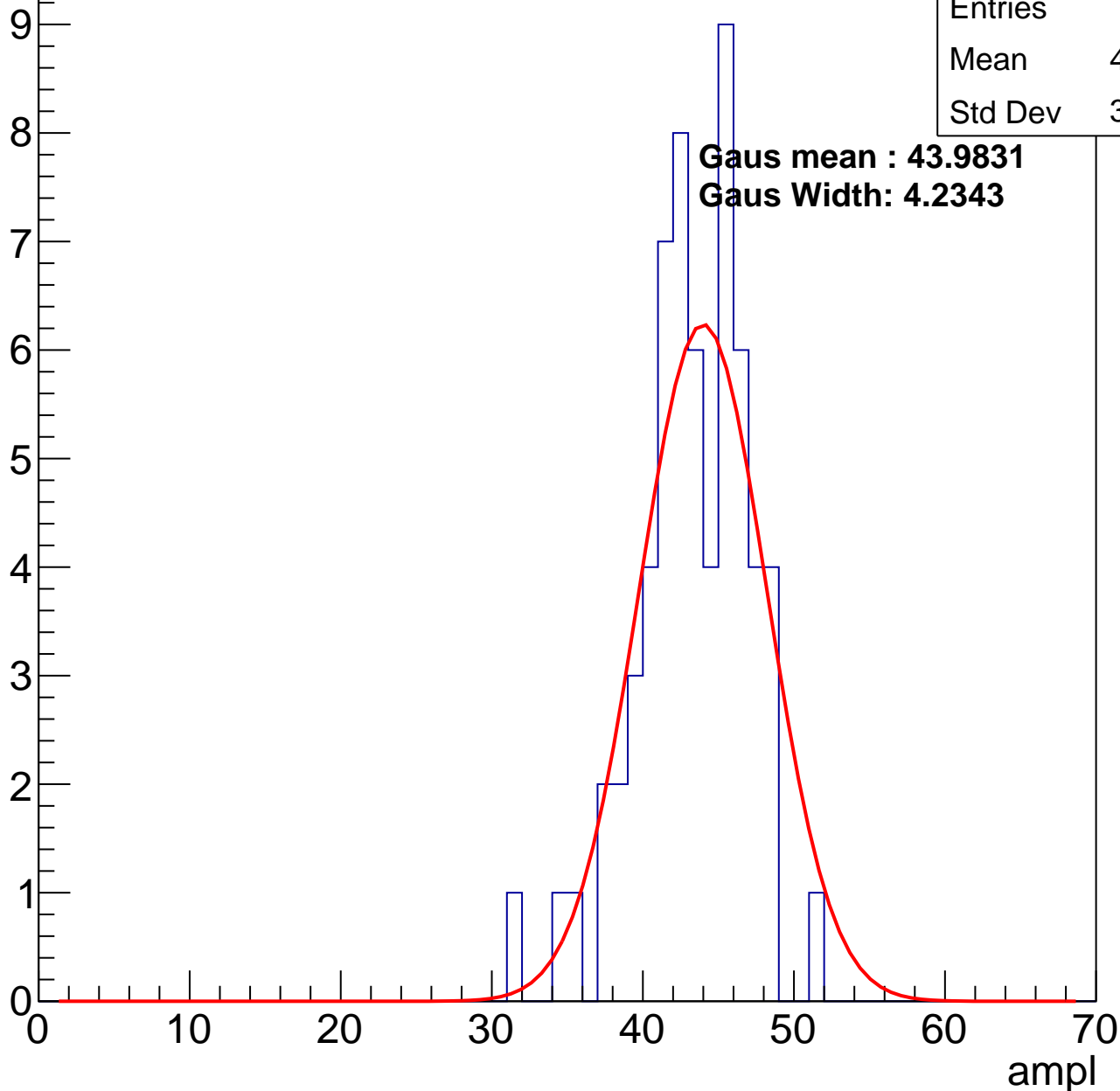
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	42.79
Std Dev	3.674

**Gaus mean : 43.9831**

**Gaus Width: 4.2343**

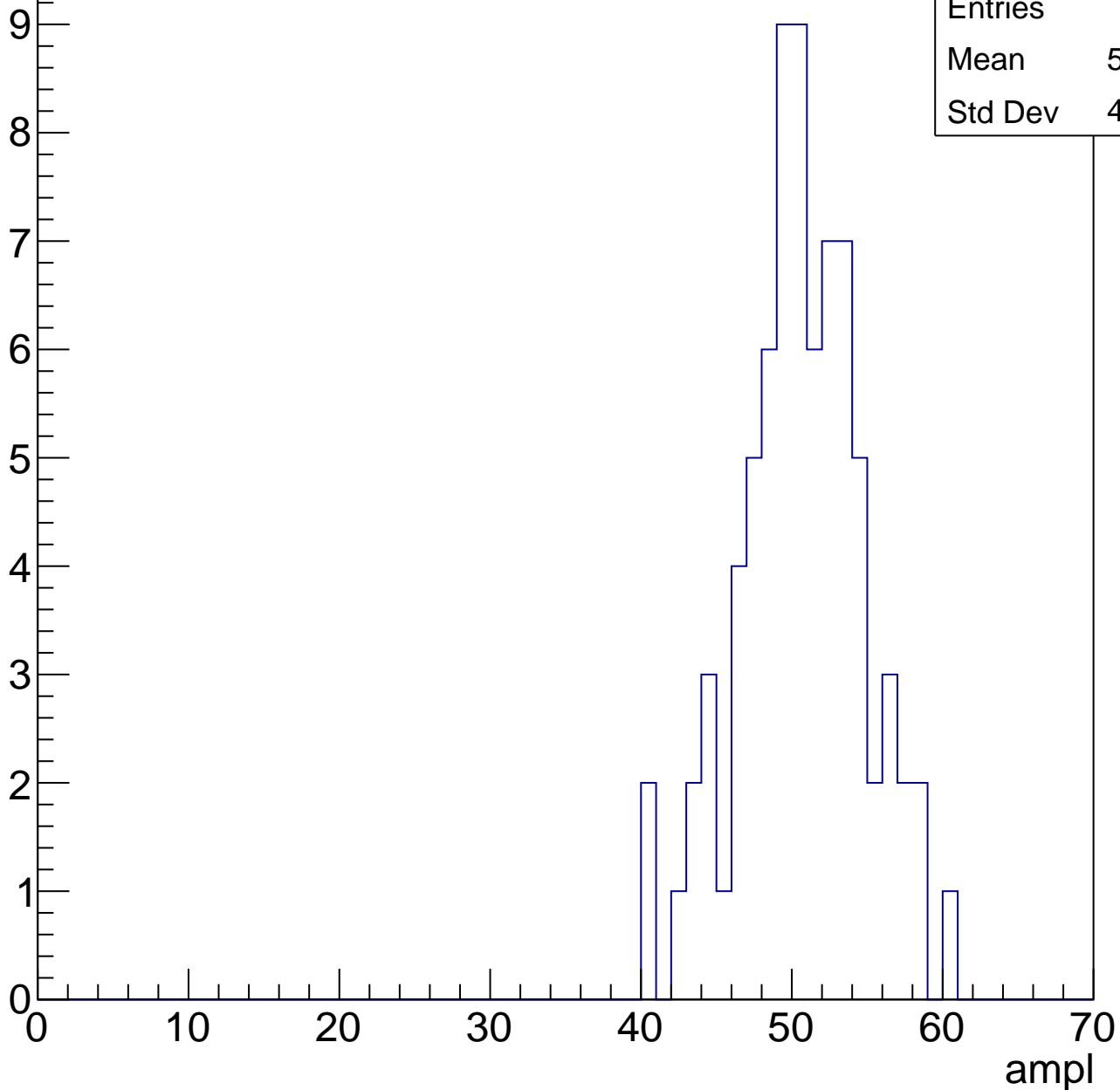


# B0L102S, U17-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	50.16
Std Dev	4.103

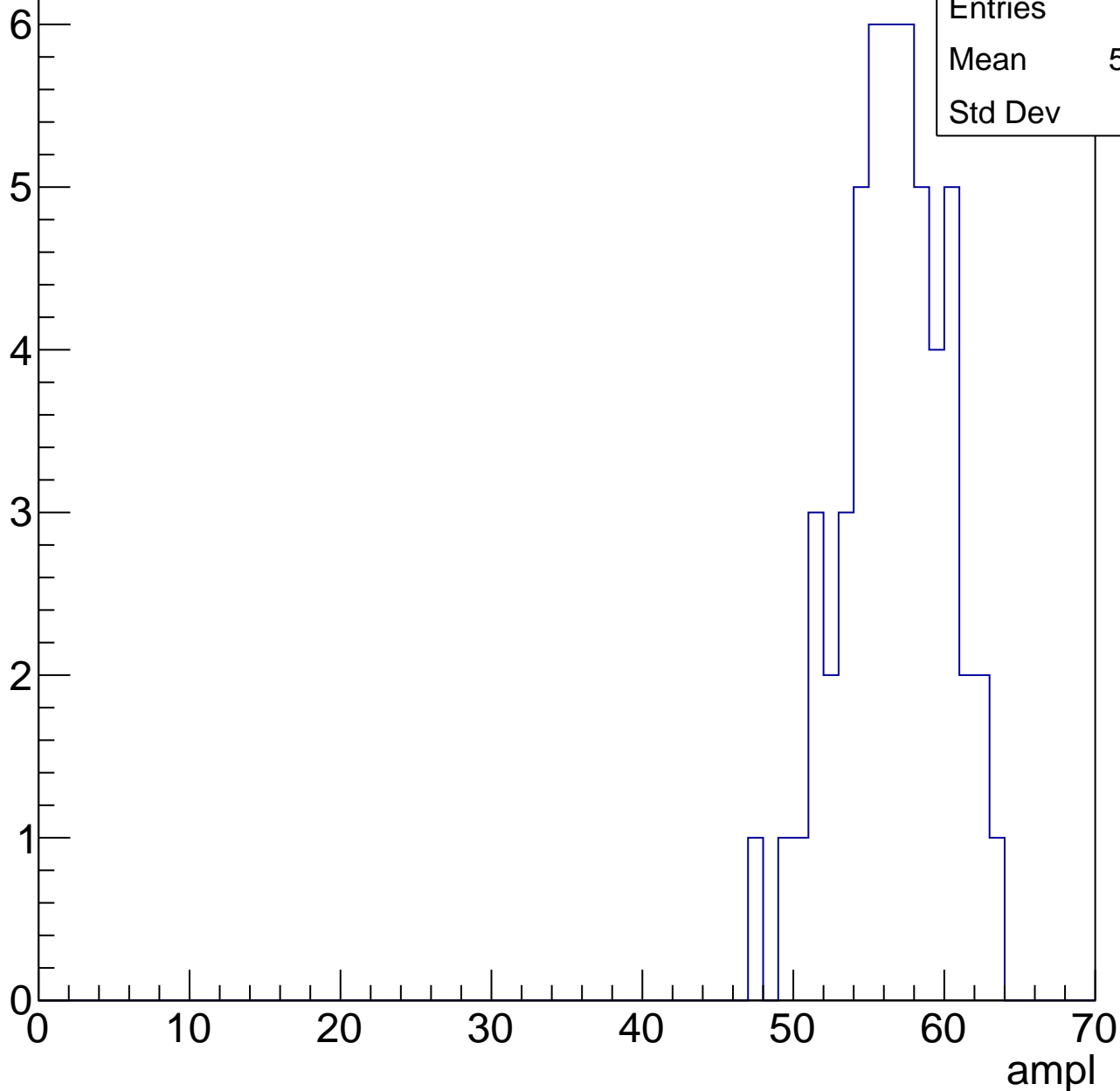


# B0L102S, U17-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	53
Mean	56.13
Std Dev	3.47

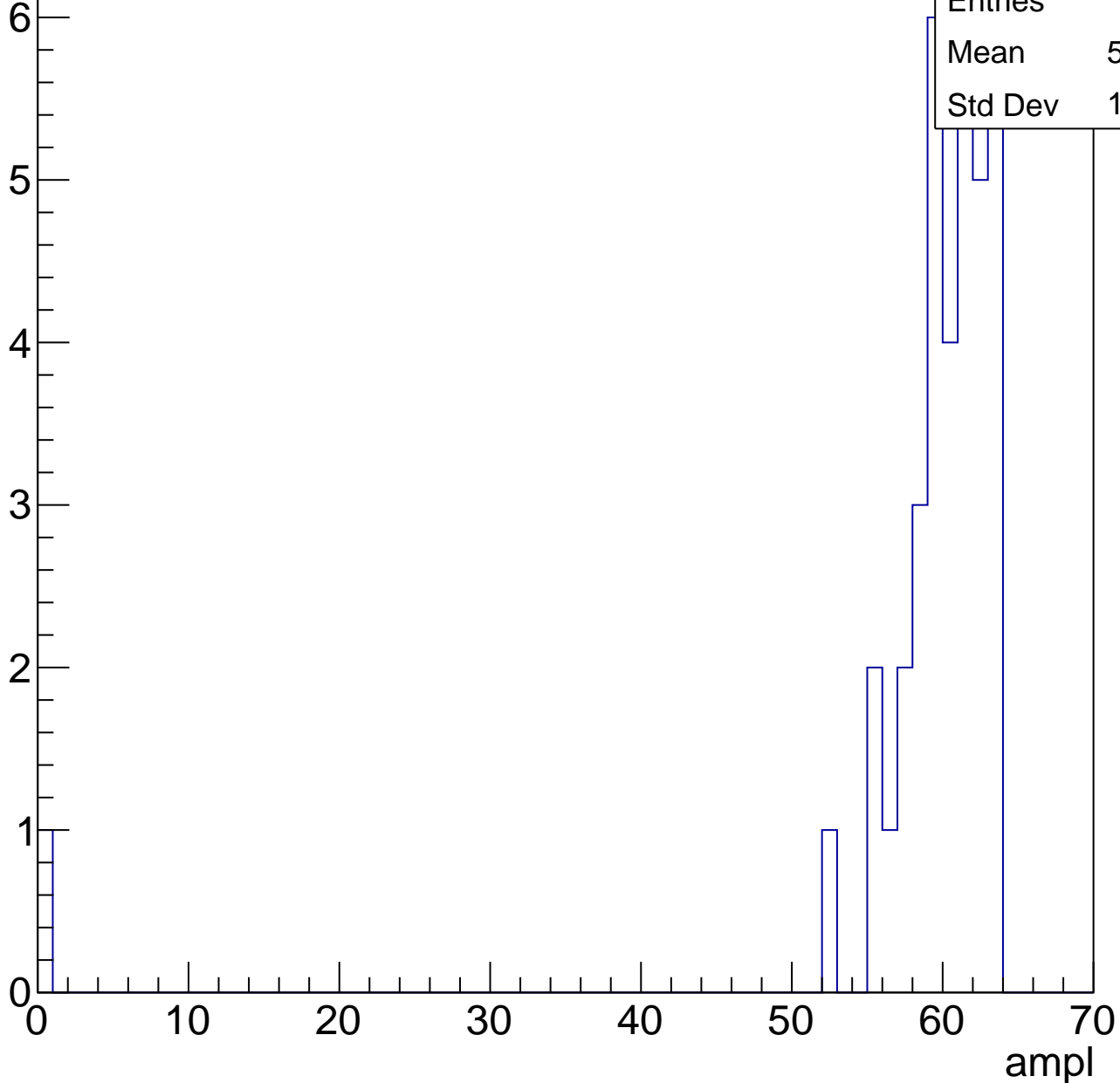


# B0L102S, U17-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

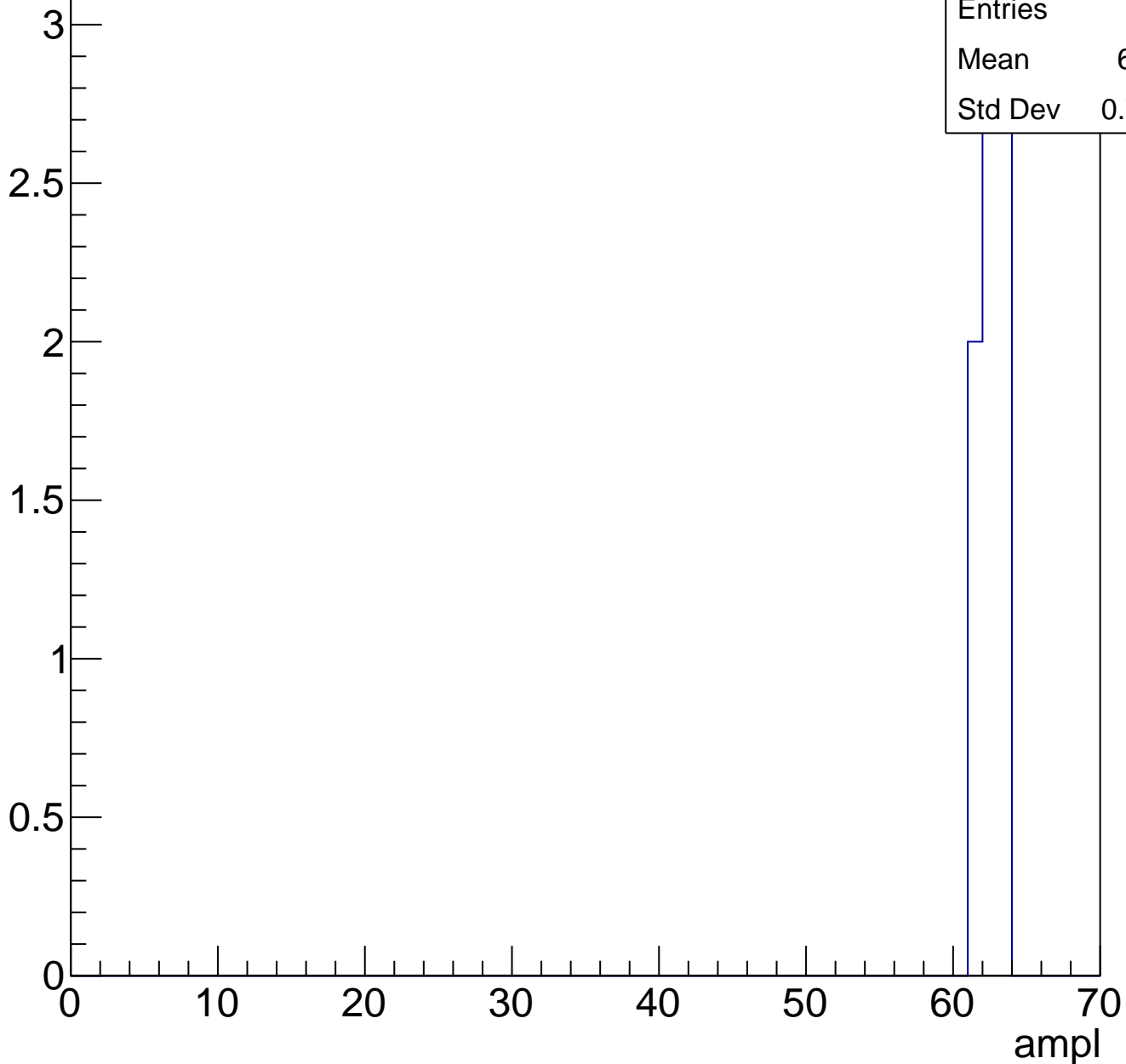
Entries	37
Mean	58.22
Std Dev	10.04



# B0L102S, U17-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L102S, U17-ch120, adc0

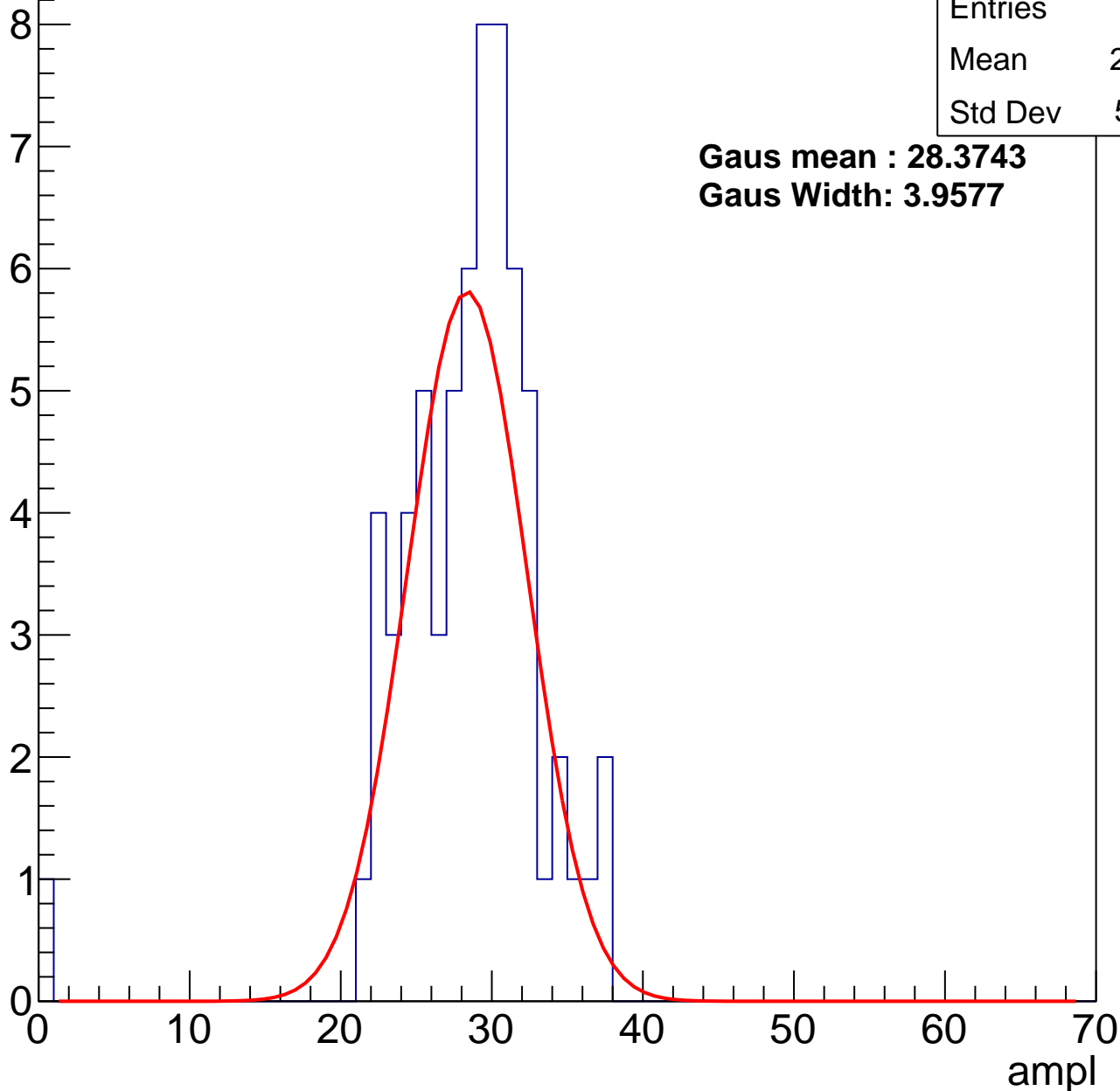
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.94
Std Dev	5.111

**Gaus mean : 28.3743**

**Gaus Width: 3.9577**



# B0L102S, U17-ch120, adc1

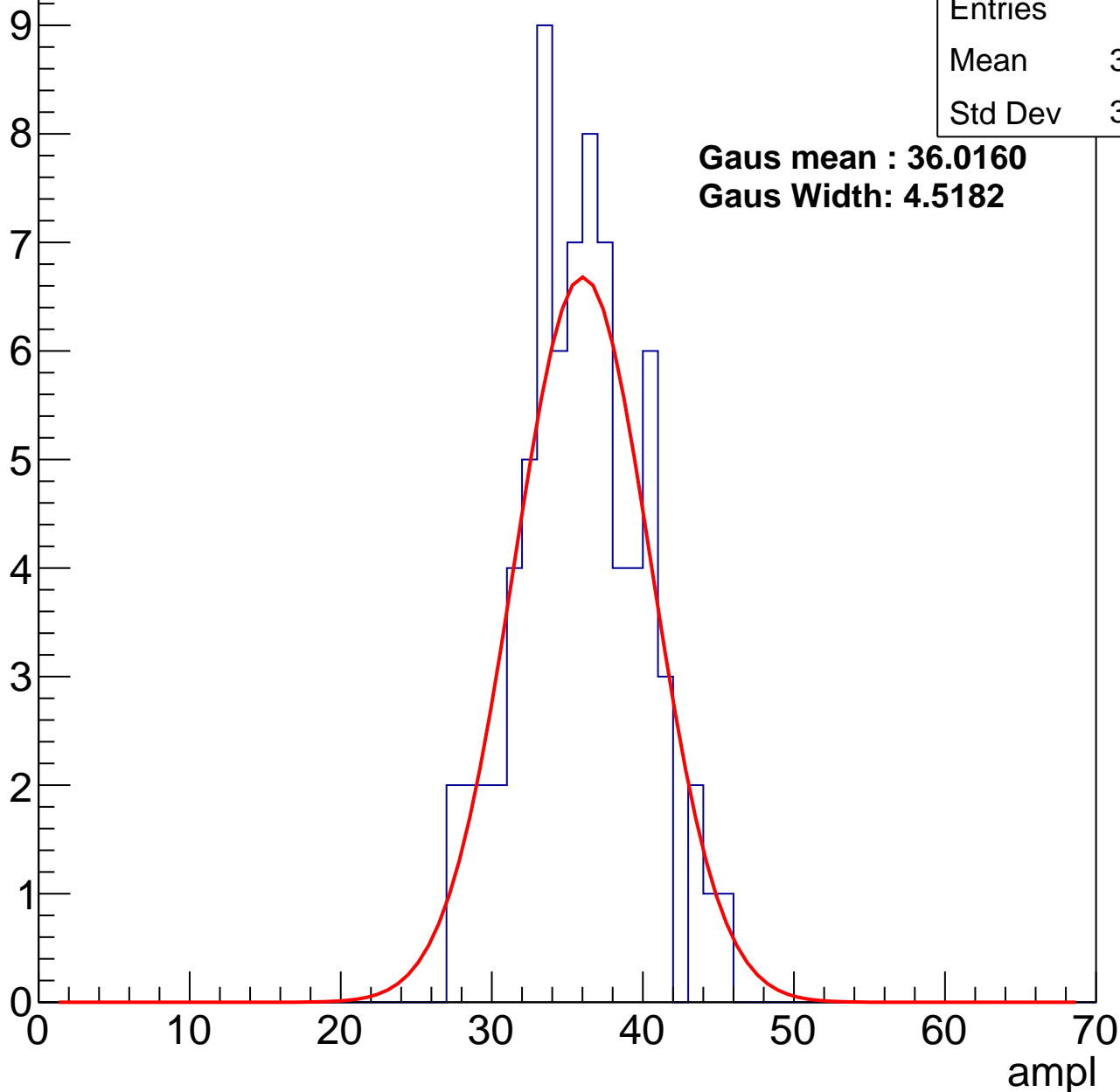
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.35
Std Dev	3.988

**Gaus mean : 36.0160**

**Gaus Width: 4.5182**



# B0L102S, U17-ch120, adc2

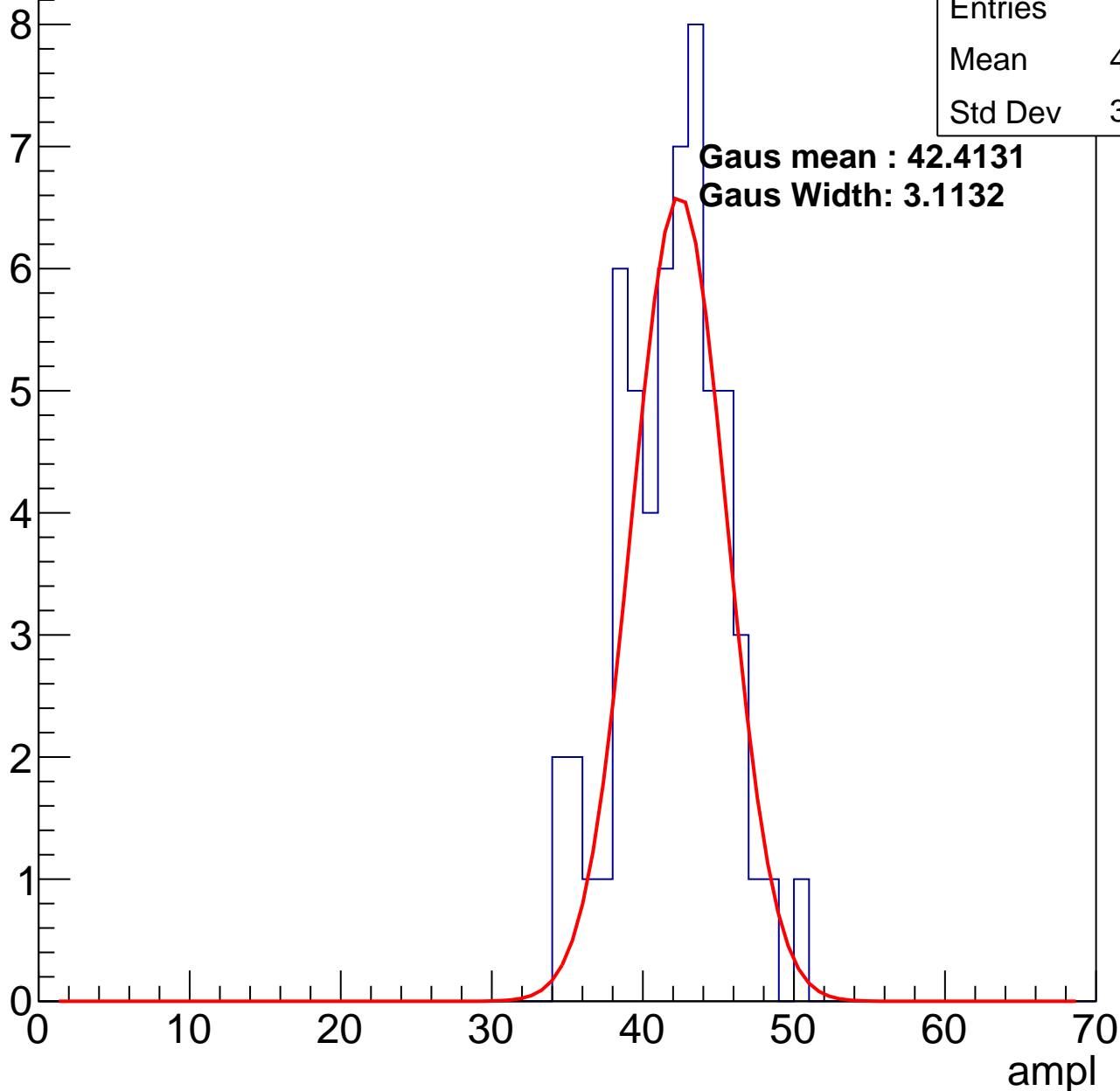
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	41.48
Std Dev	3.425

**Gaus mean : 42.4131**

**Gaus Width: 3.1132**

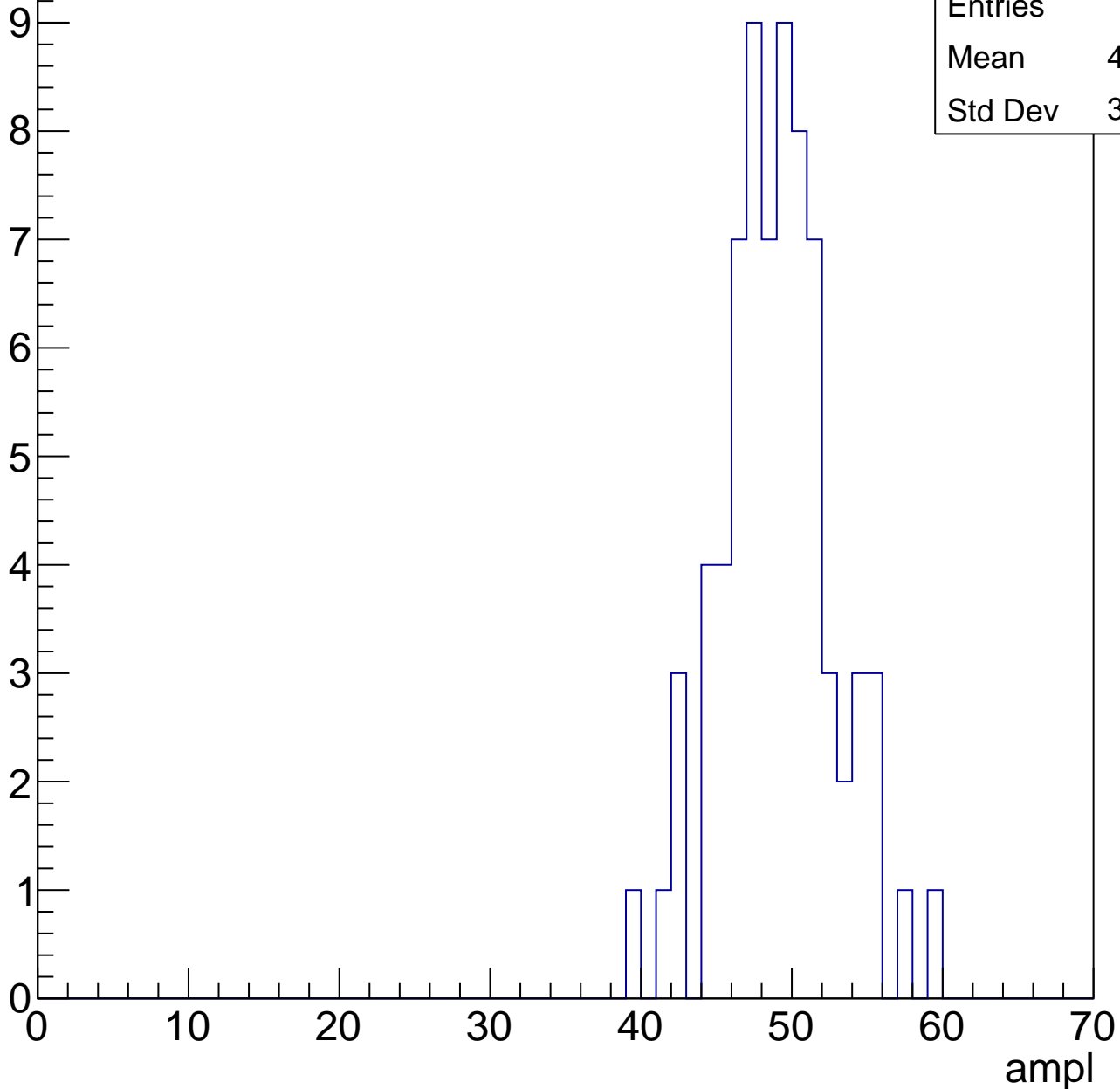


# B0L102S, U17-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	48.58
Std Dev	3.719

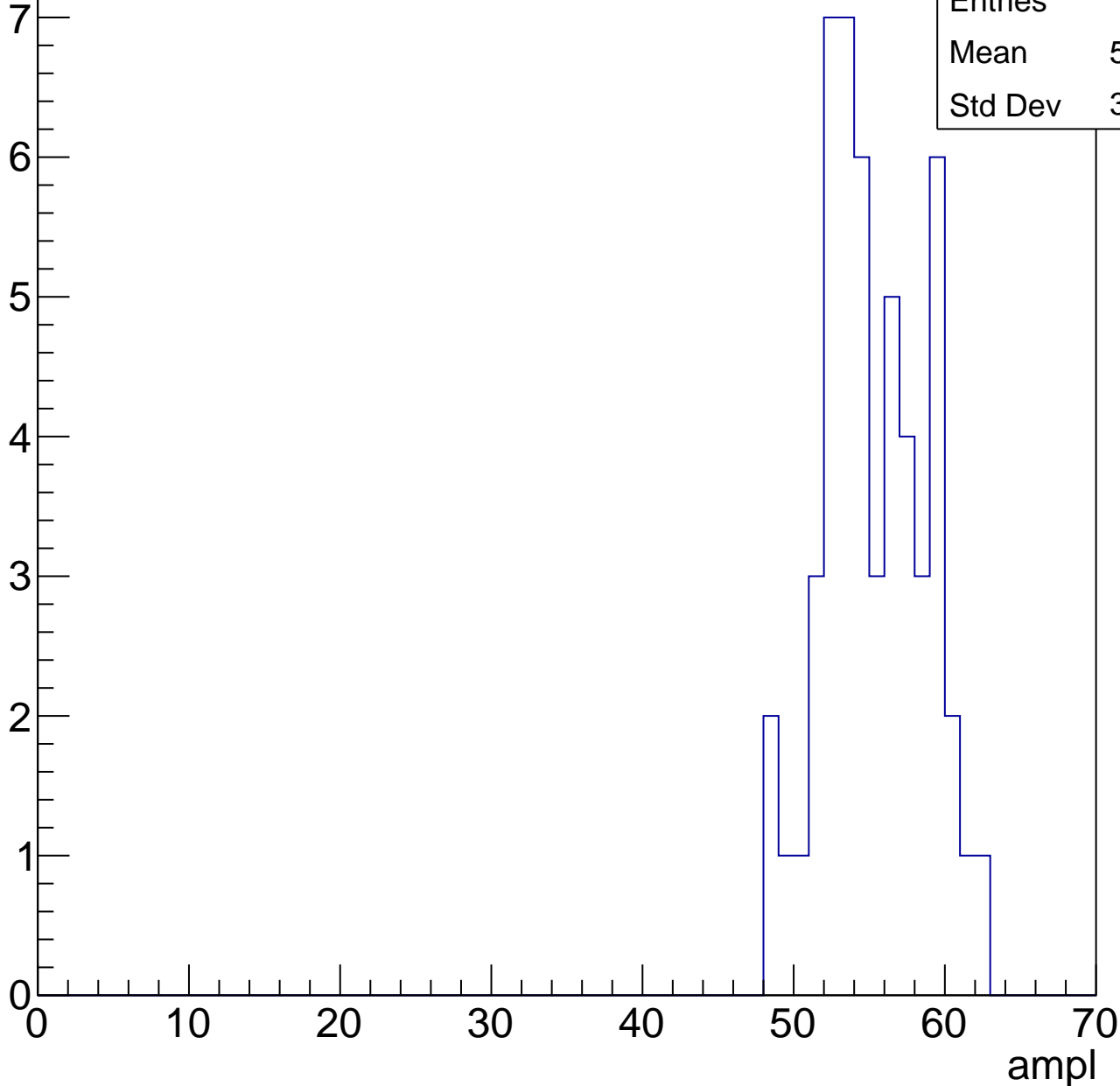


# B0L102S, U17-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

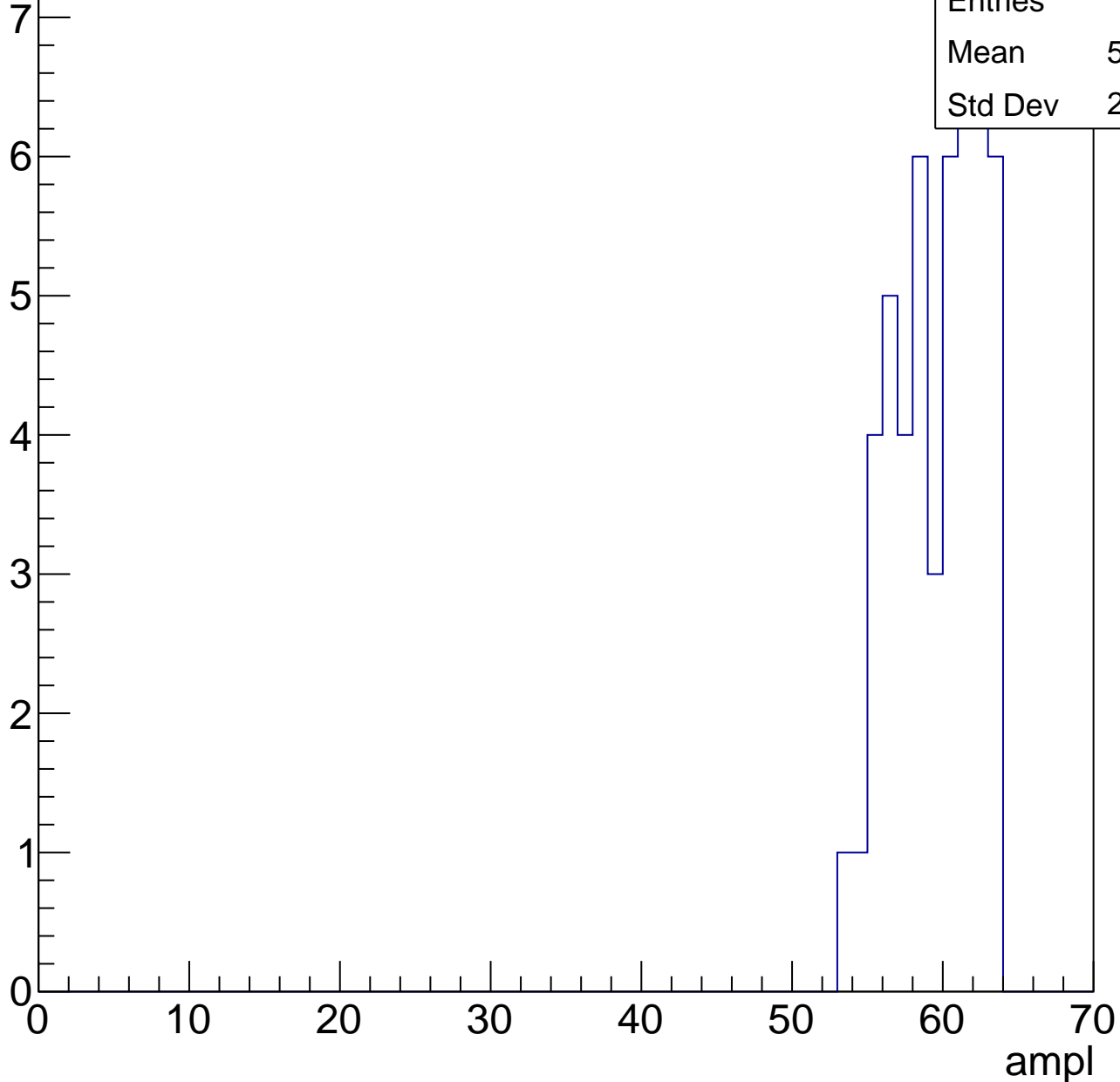
Entries	52
Mean	54.83
Std Dev	3.344



# B0L102S, U17-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

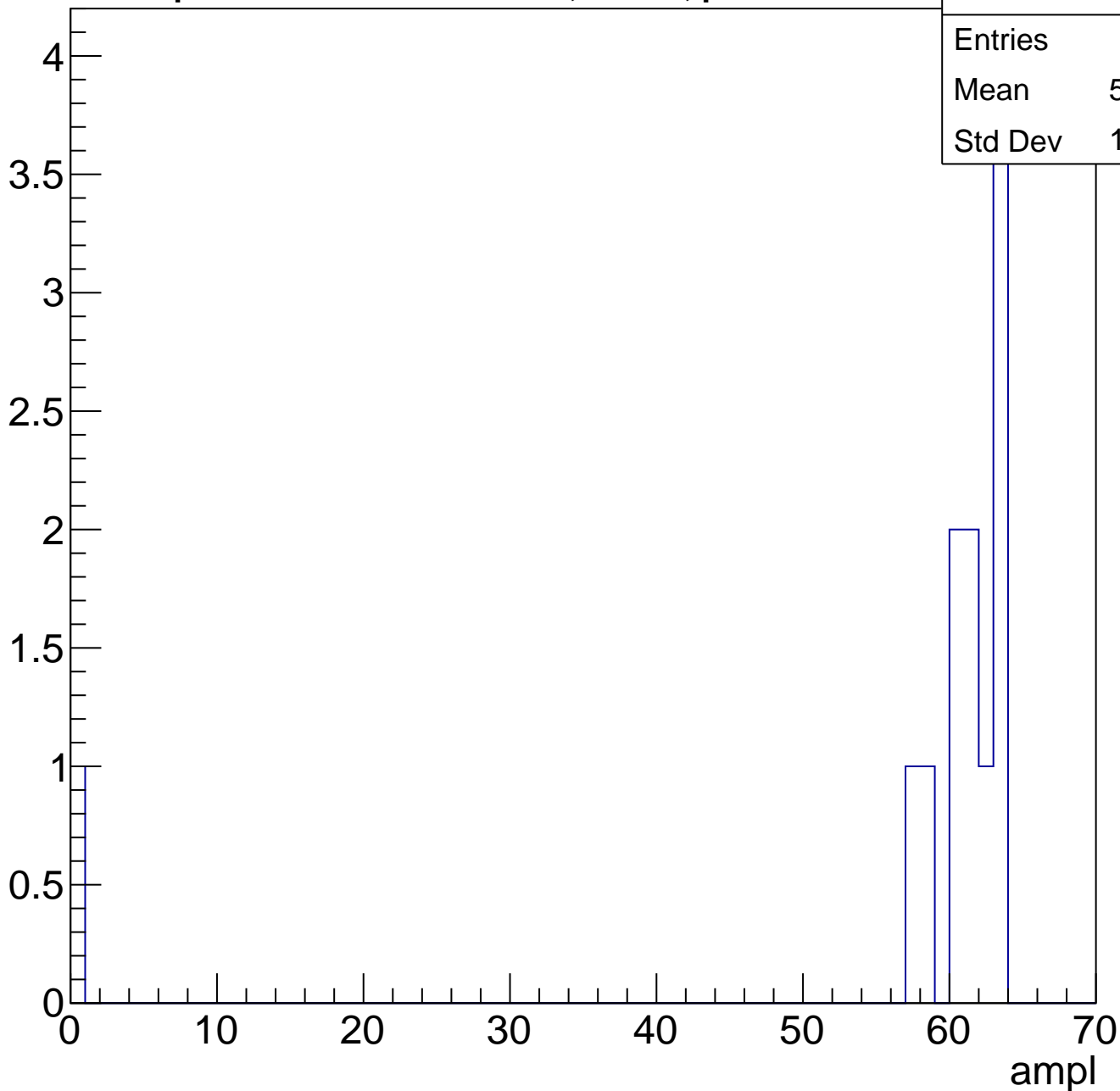
Entry



# B0L102S, U17-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch121, adc0

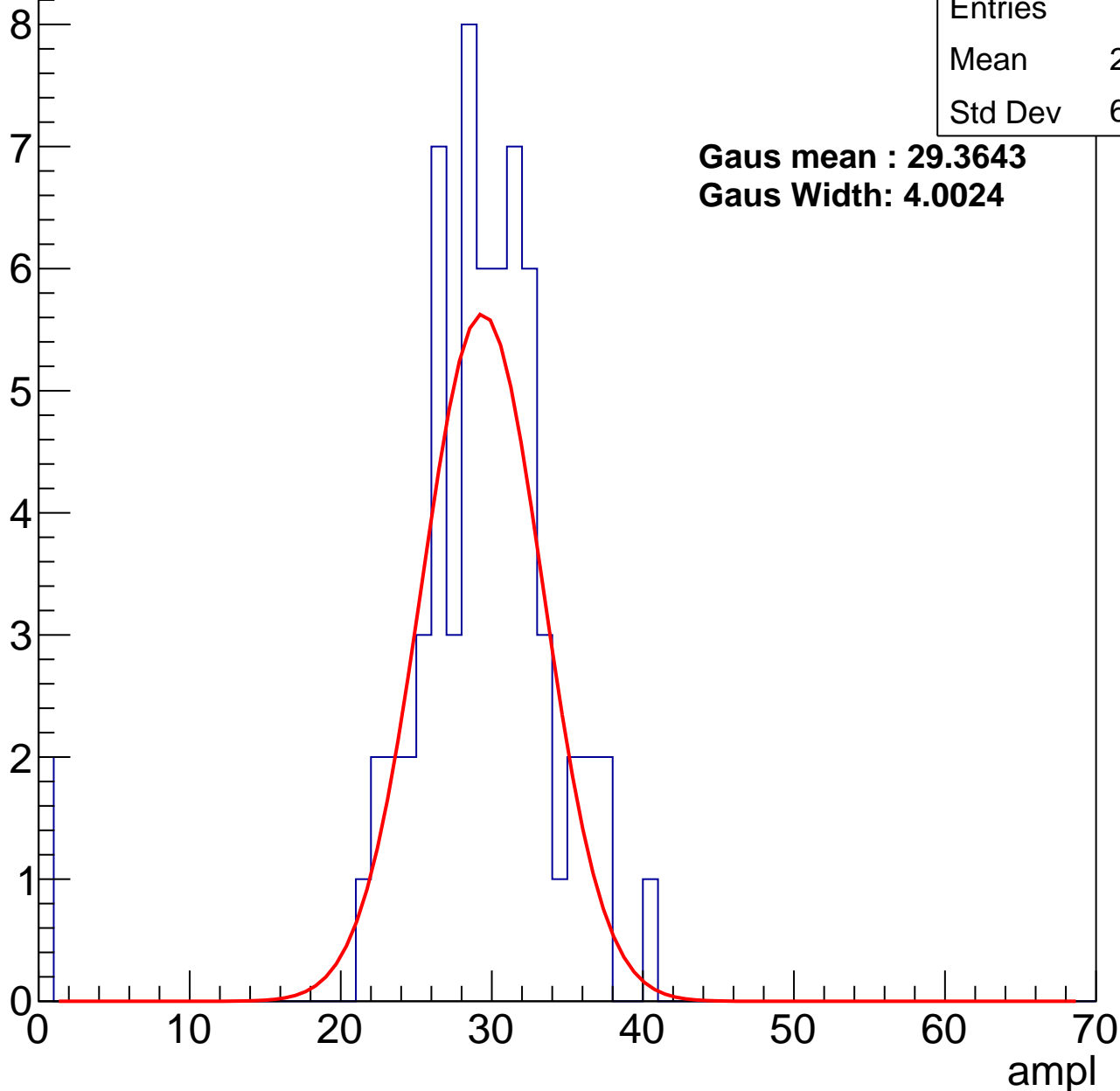
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	28.38
Std Dev	6.338

**Gaus mean : 29.3643**

**Gaus Width: 4.0024**



# B0L102S, U17-ch121, adc1

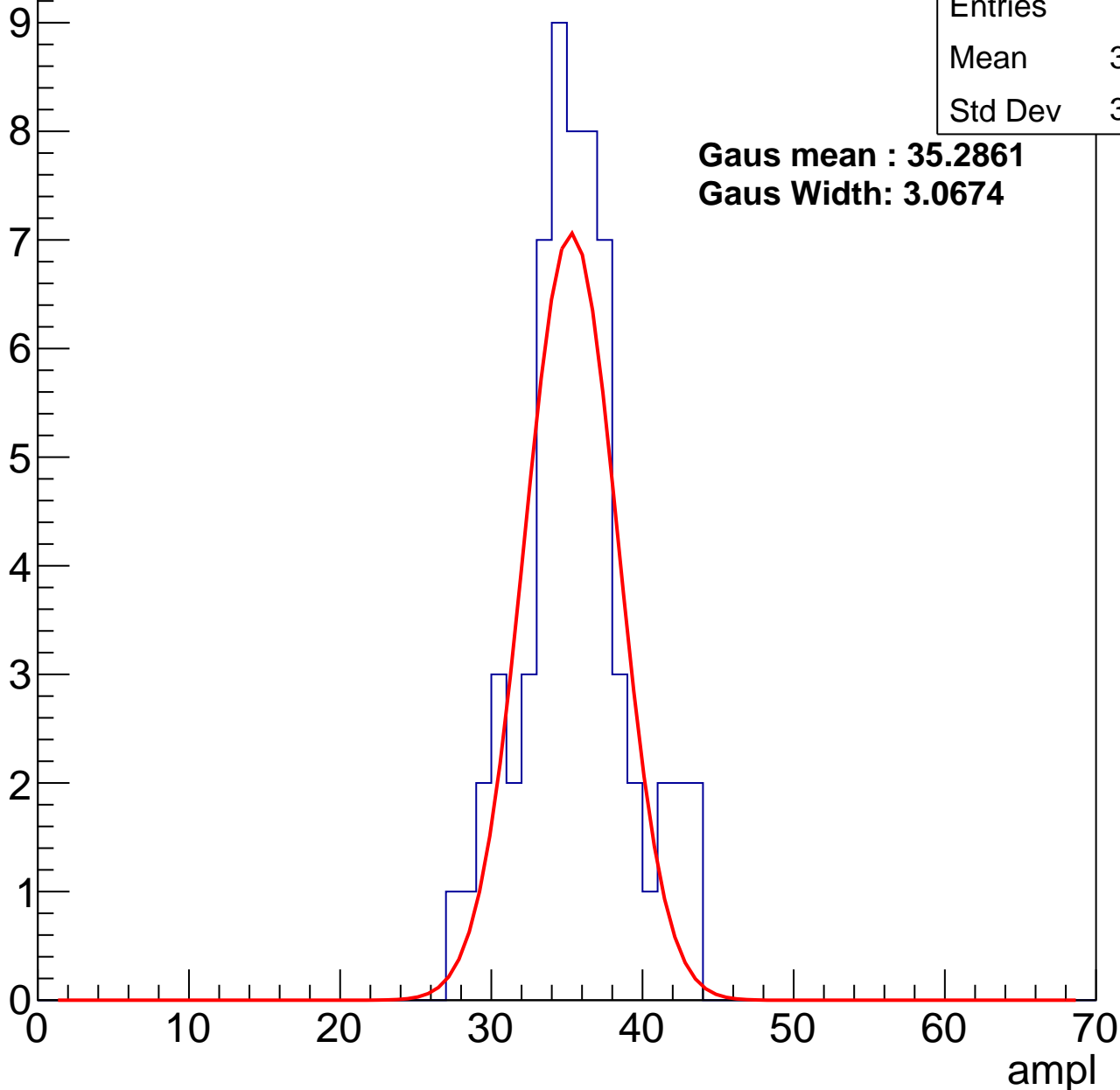
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	35.06
Std Dev	3.505

**Gaus mean : 35.2861**

**Gaus Width: 3.0674**



# B0L102S, U17-ch121, adc2

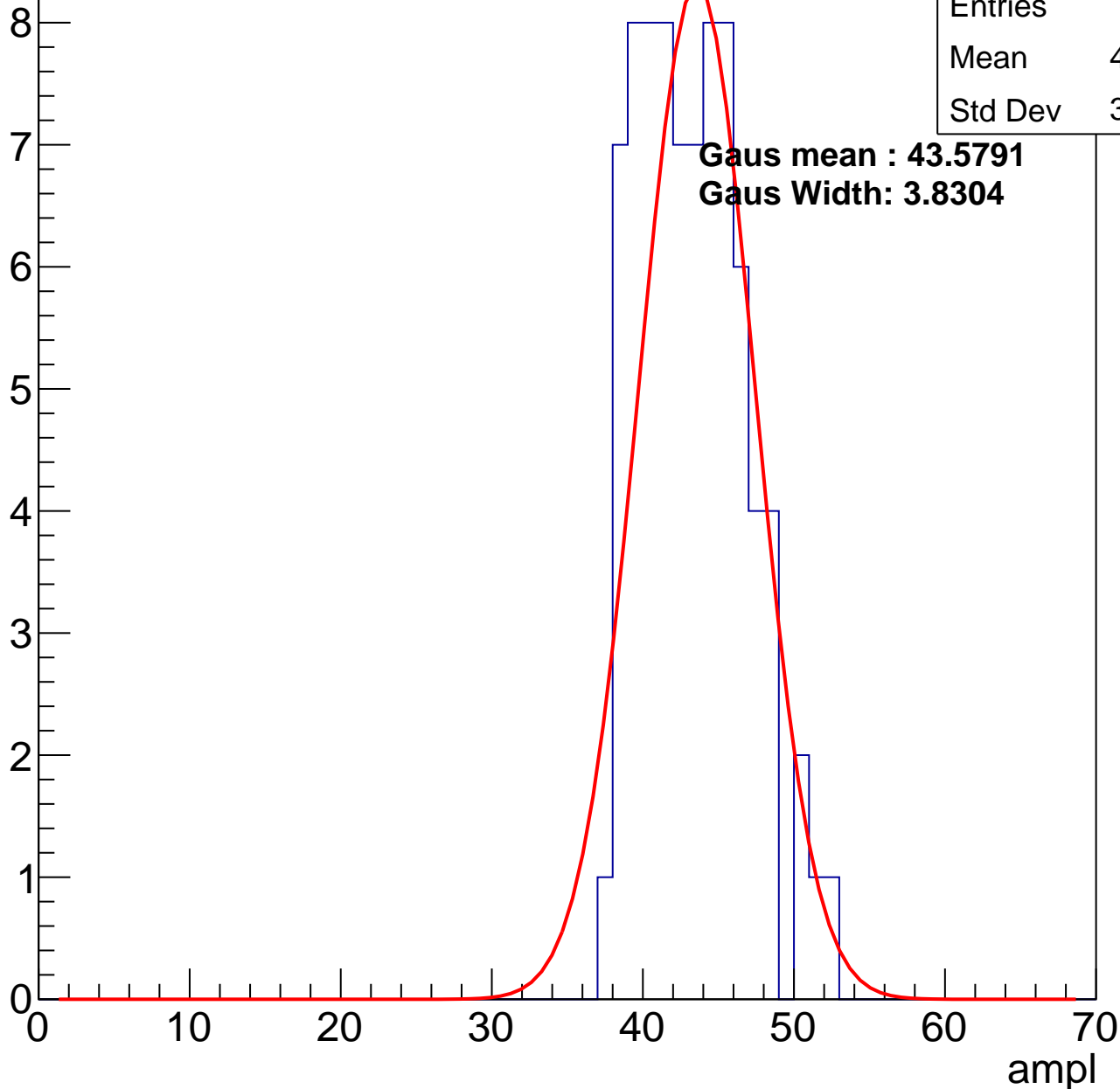
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	80
Mean	42.86
Std Dev	3.445

**Gaus mean : 43.5791**

**Gaus Width: 3.8304**

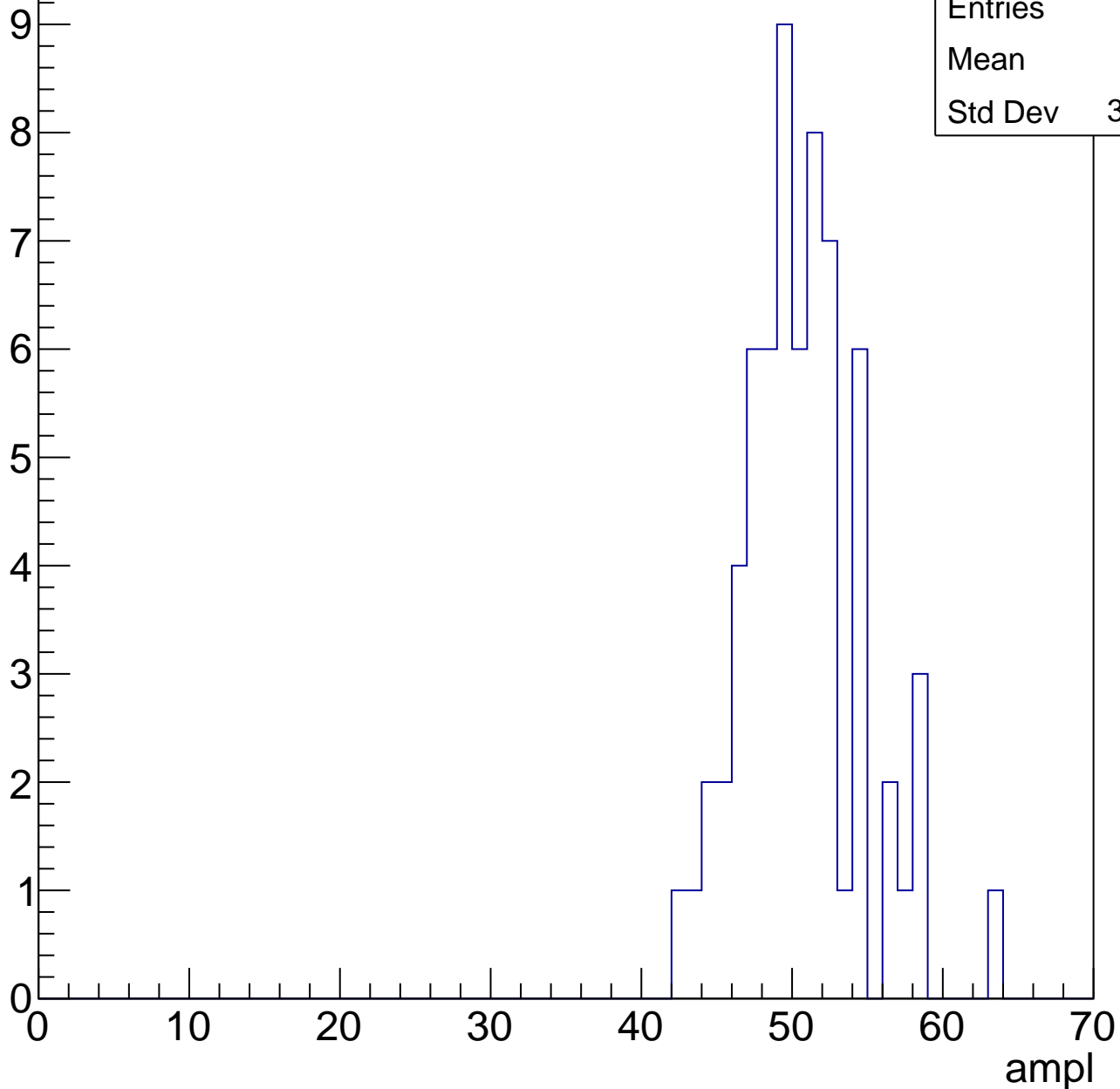


# B0L102S, U17-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	66
Mean	50.2
Std Dev	3.905

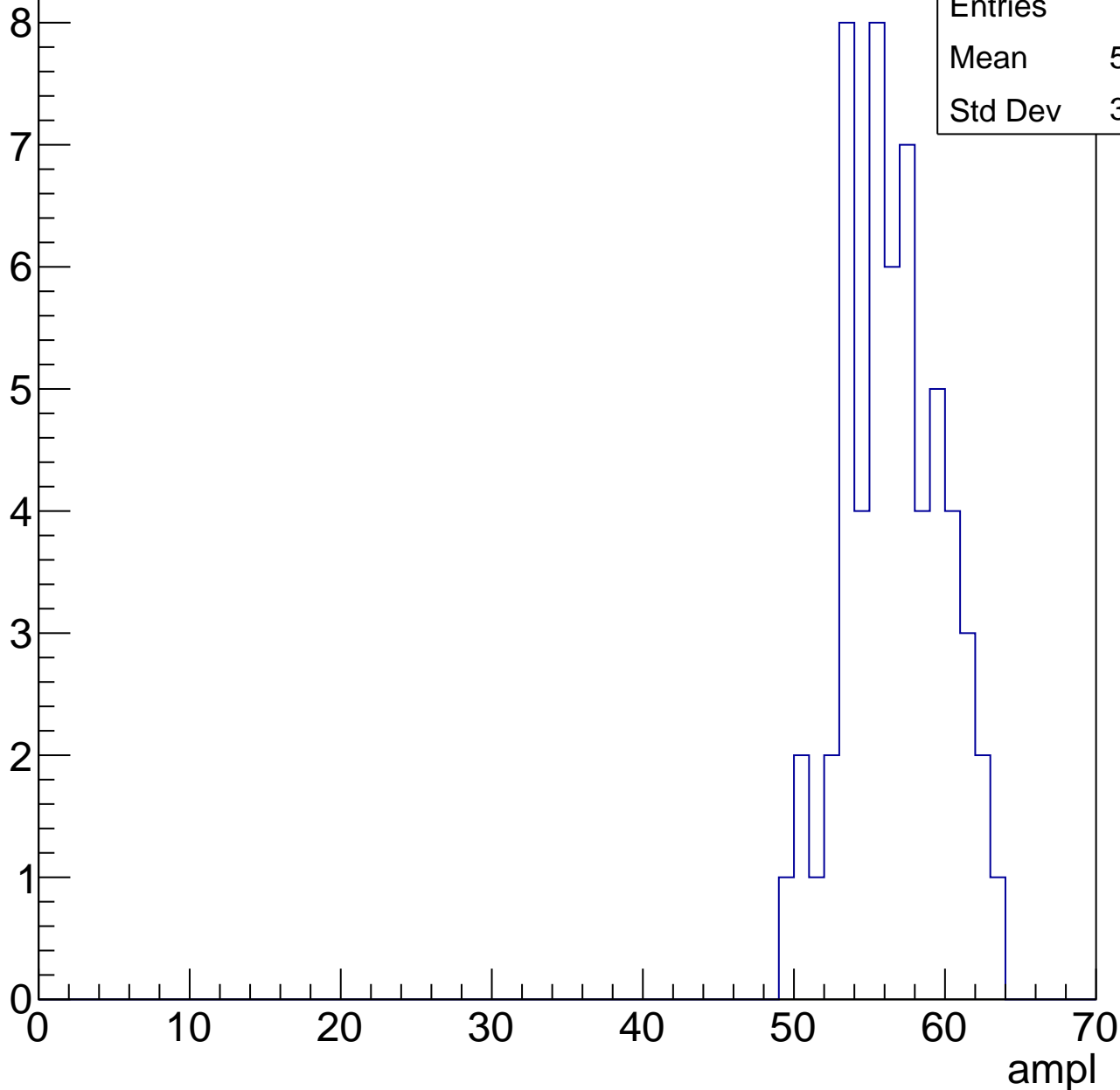


# B0L102S, U17-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	58
Mean	56.14
Std Dev	3.219

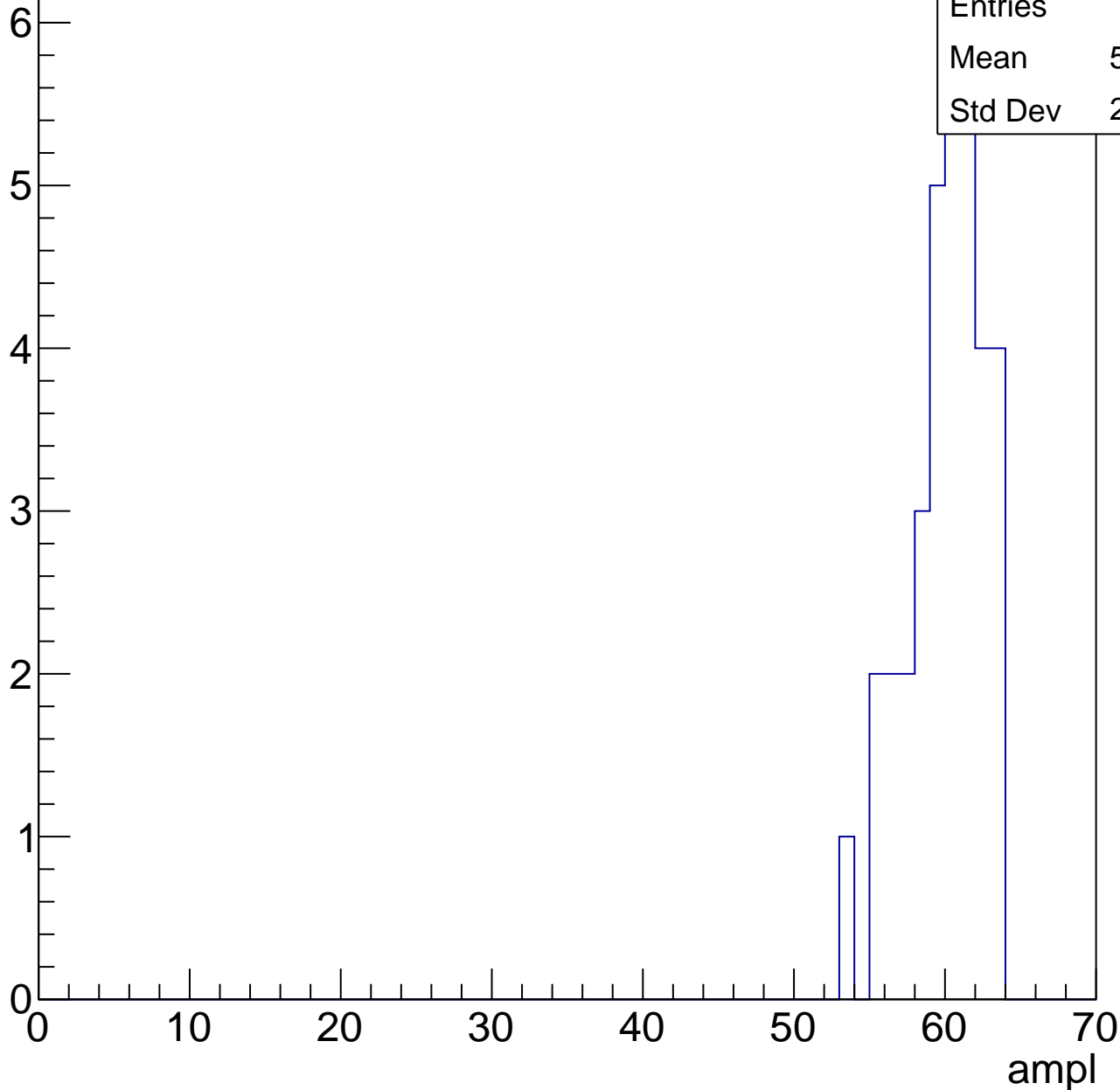


# B0L102S, U17-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

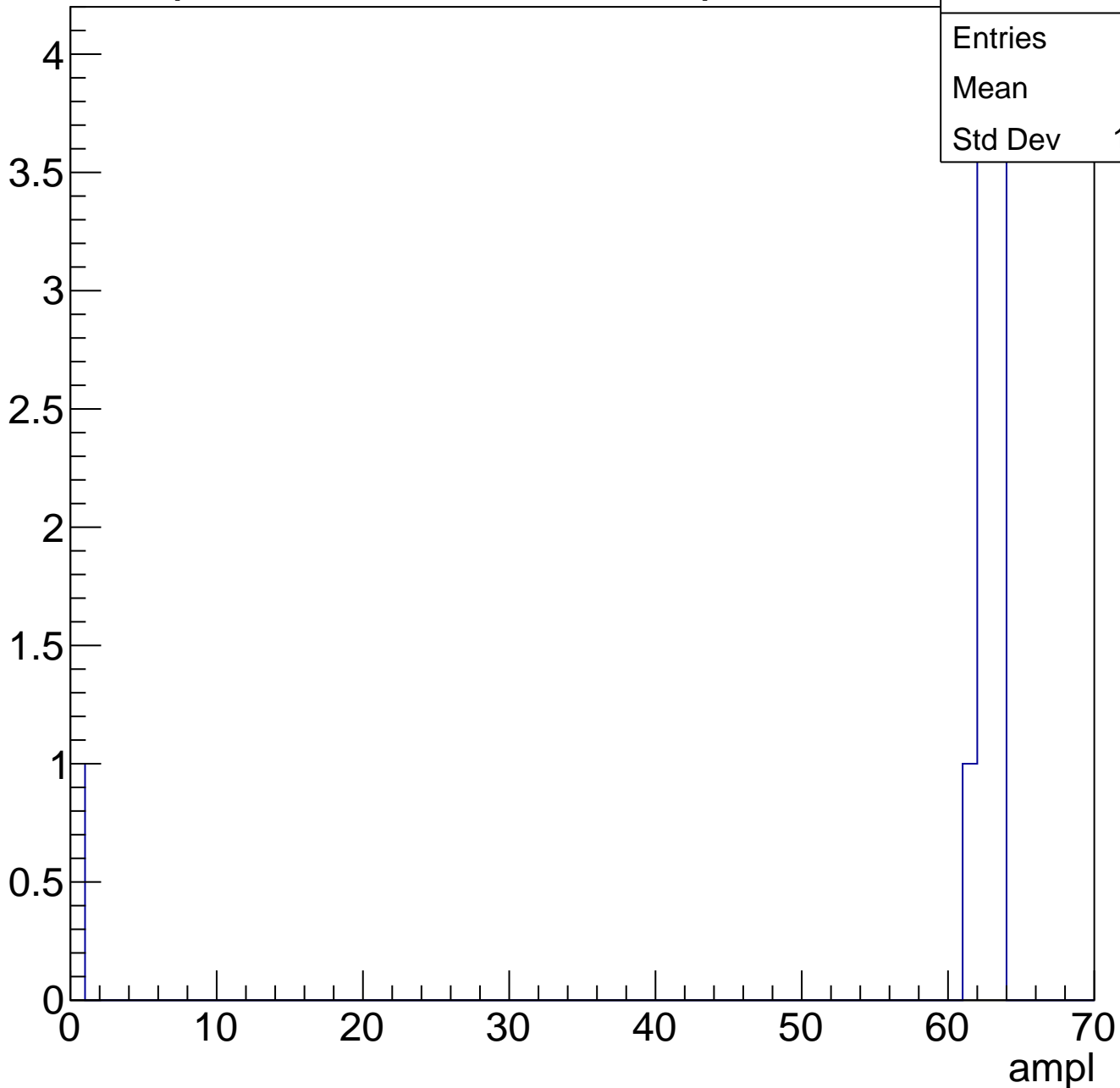
Entries	35
Mean	59.54
Std Dev	2.488



# B0L102S, U17-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L102S, U17-ch122, adc0

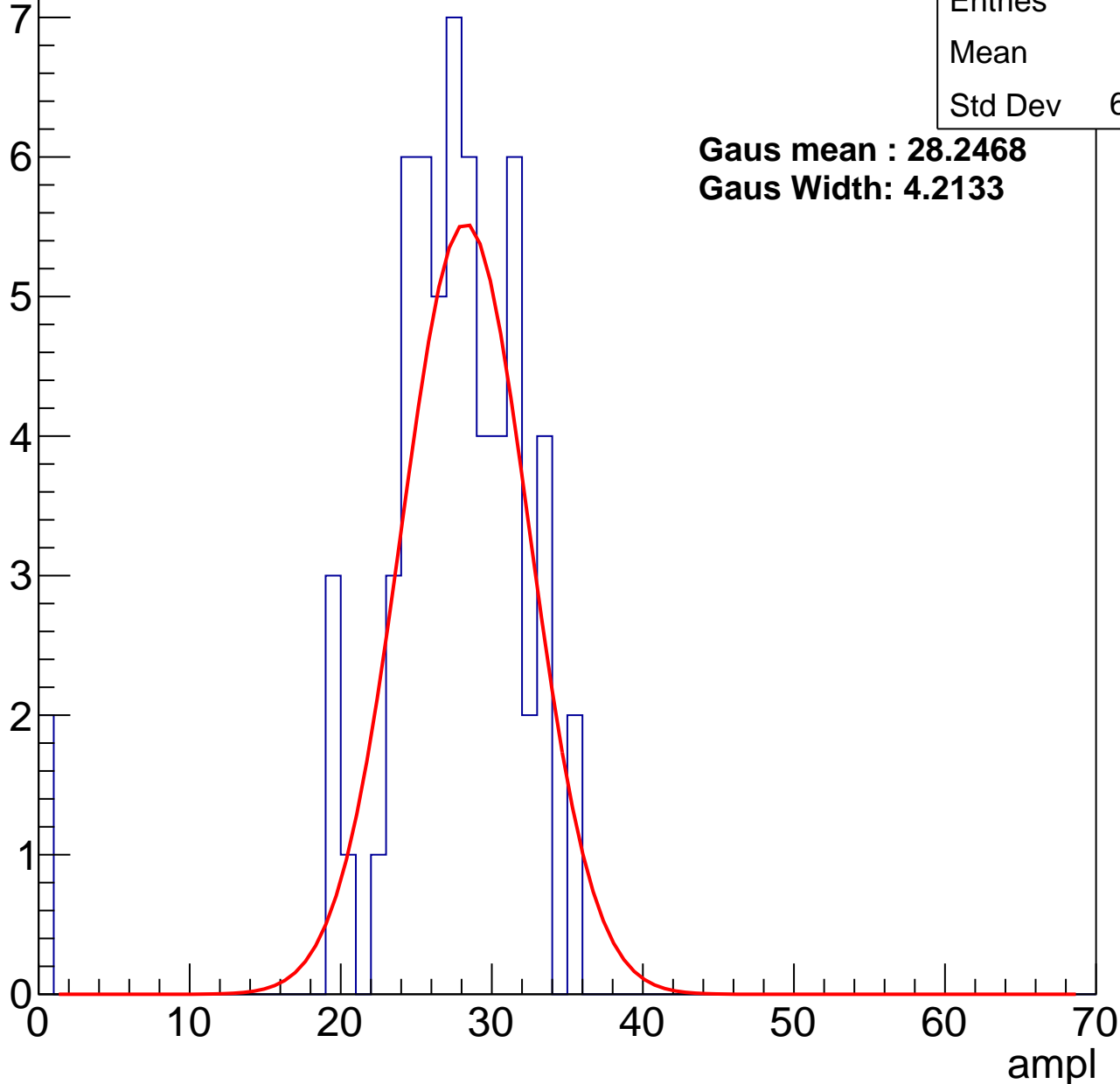
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	62
Mean	26.4
Std Dev	6.103

**Gaus mean : 28.2468**

**Gaus Width: 4.2133**



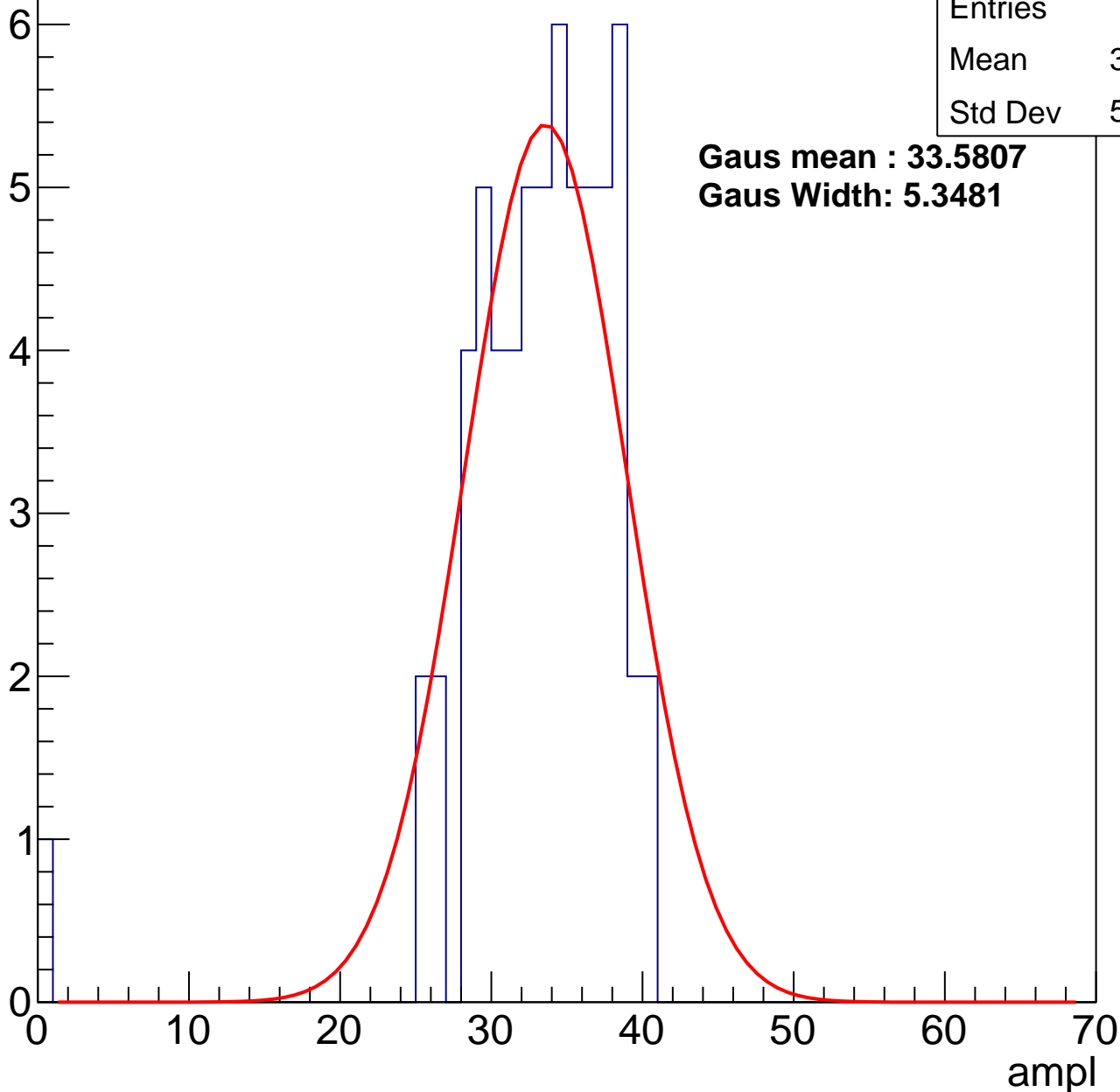
# B0L102S, U17-ch122, adc1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	32.67
Std Dev	5.657

**Gaus mean : 33.5807**  
**Gaus Width: 5.3481**



# B0L102S, U17-ch122, adc2

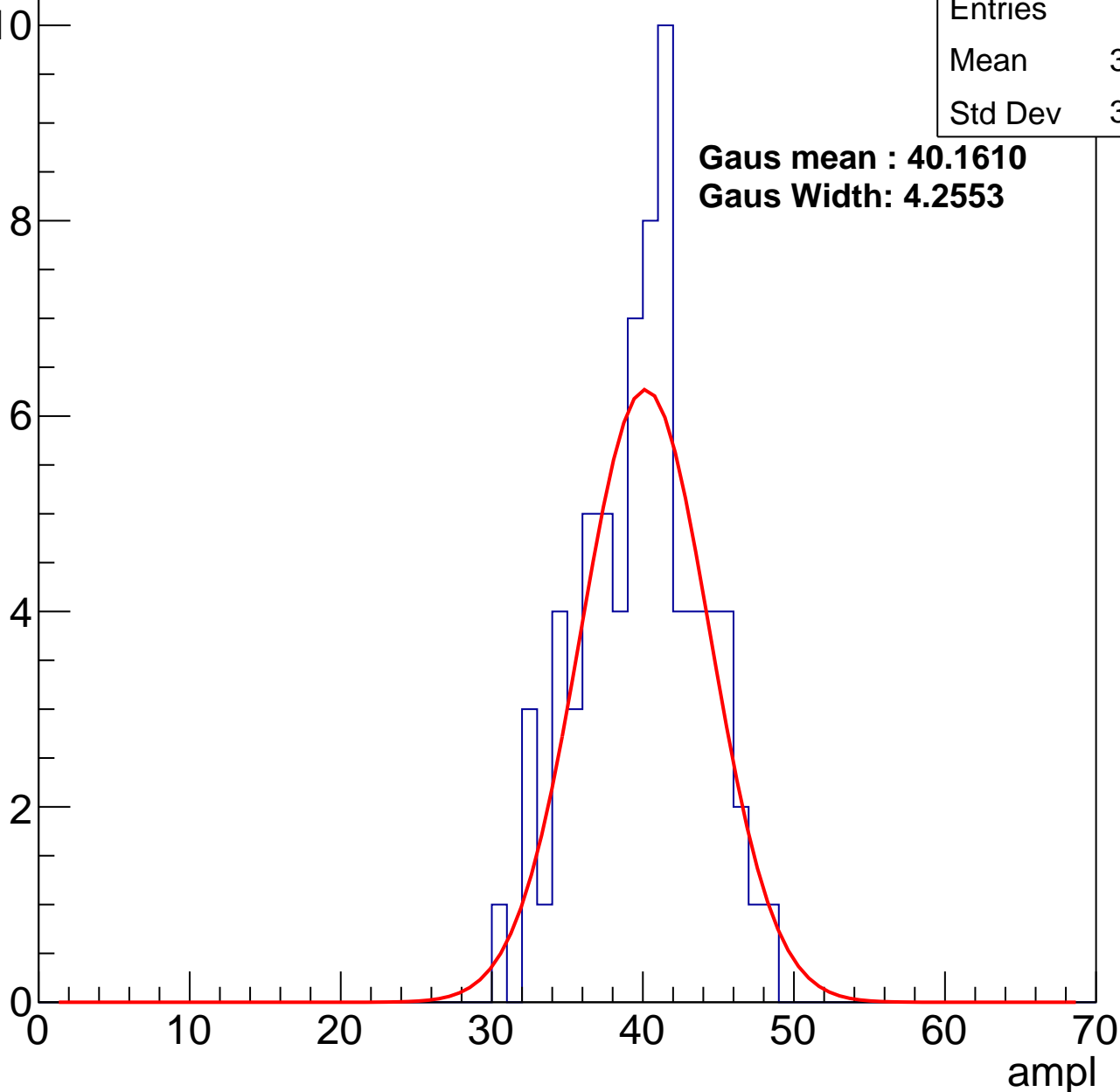
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	71
Mean	39.48
Std Dev	3.936

**Gaus mean : 40.1610**

**Gaus Width: 4.2553**

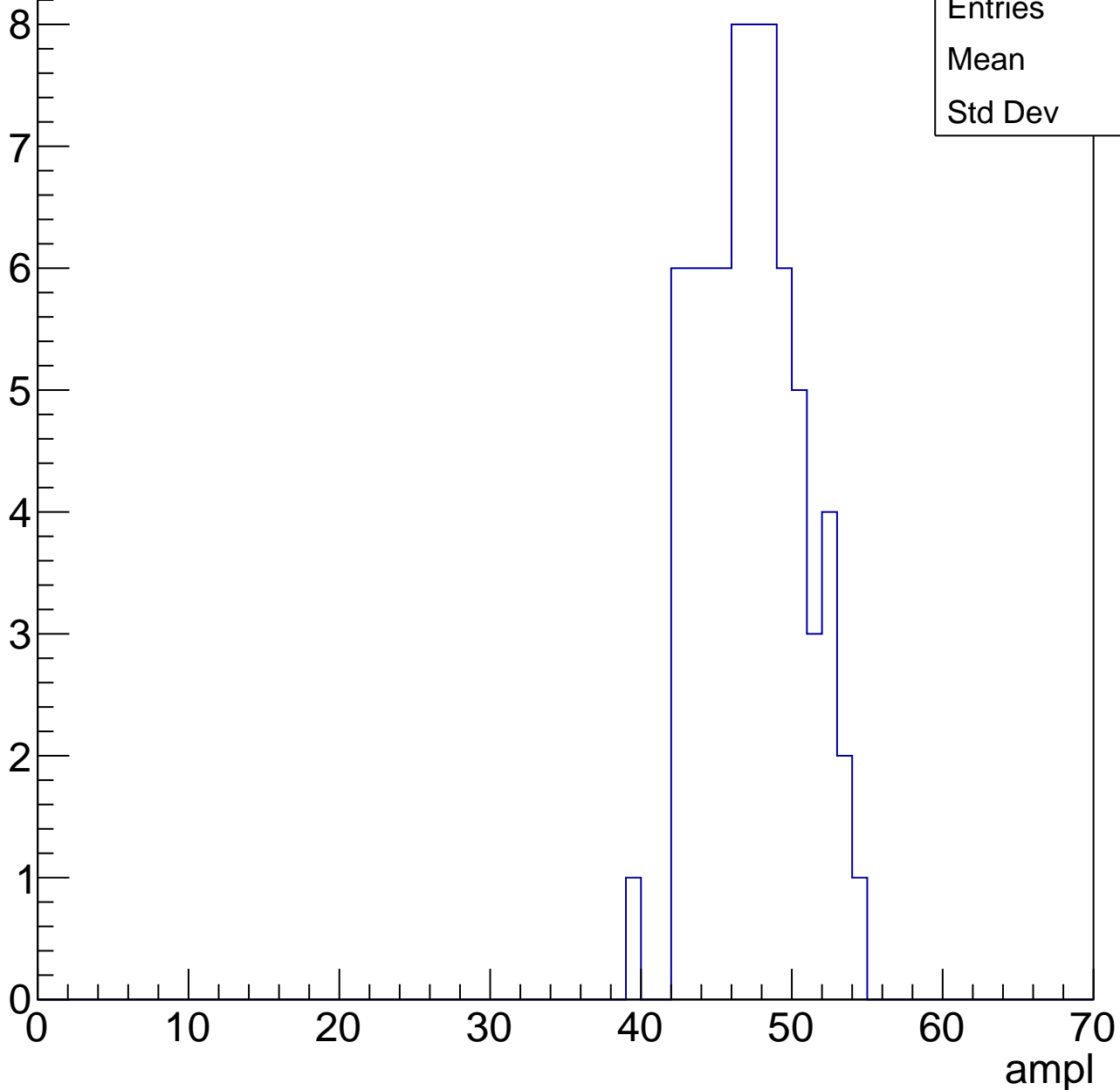


# B0L102S, U17-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	46.8
Std Dev	3.25

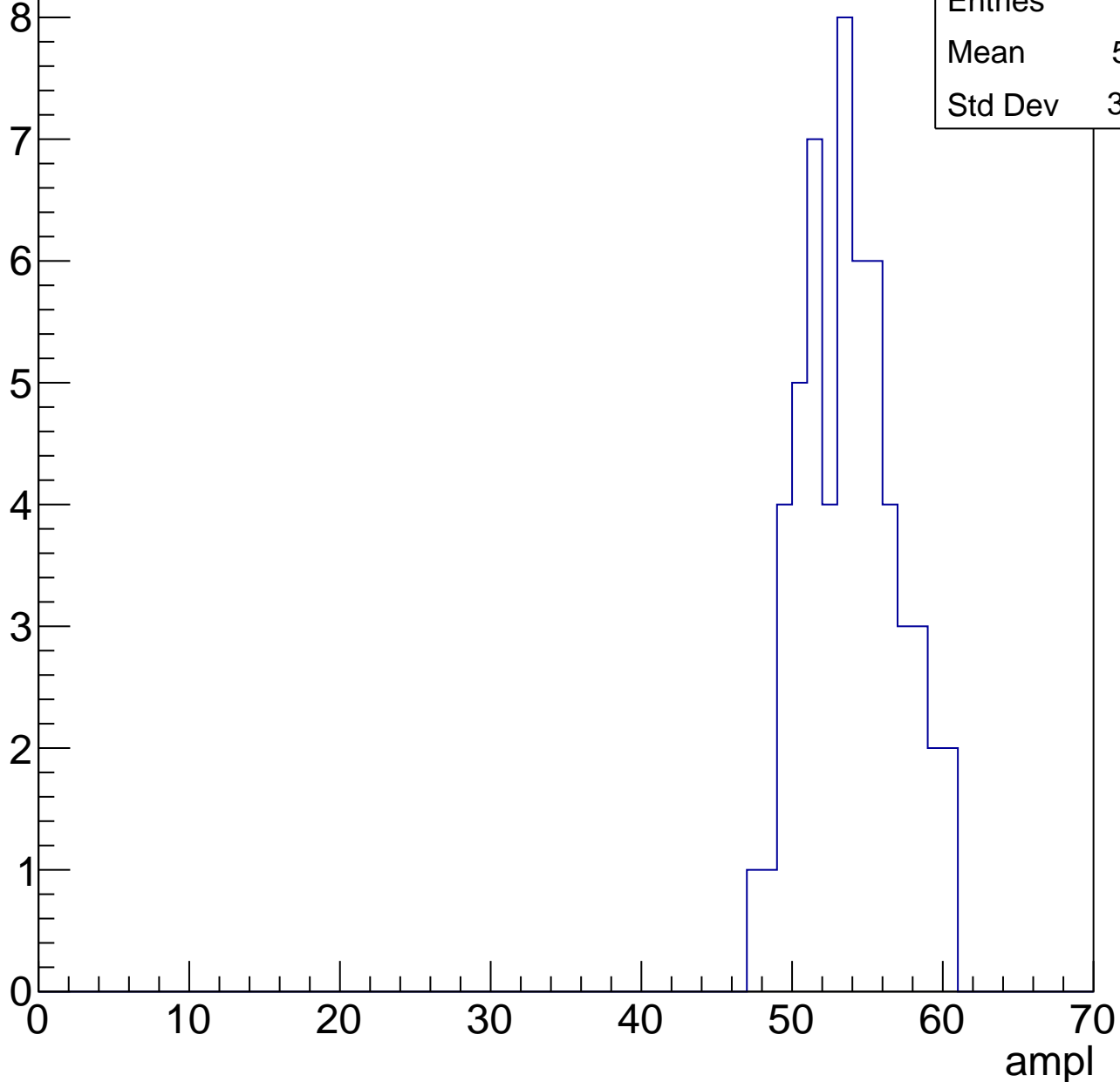


# B0L102S, U17-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	56
Mean	53.41
Std Dev	3.138

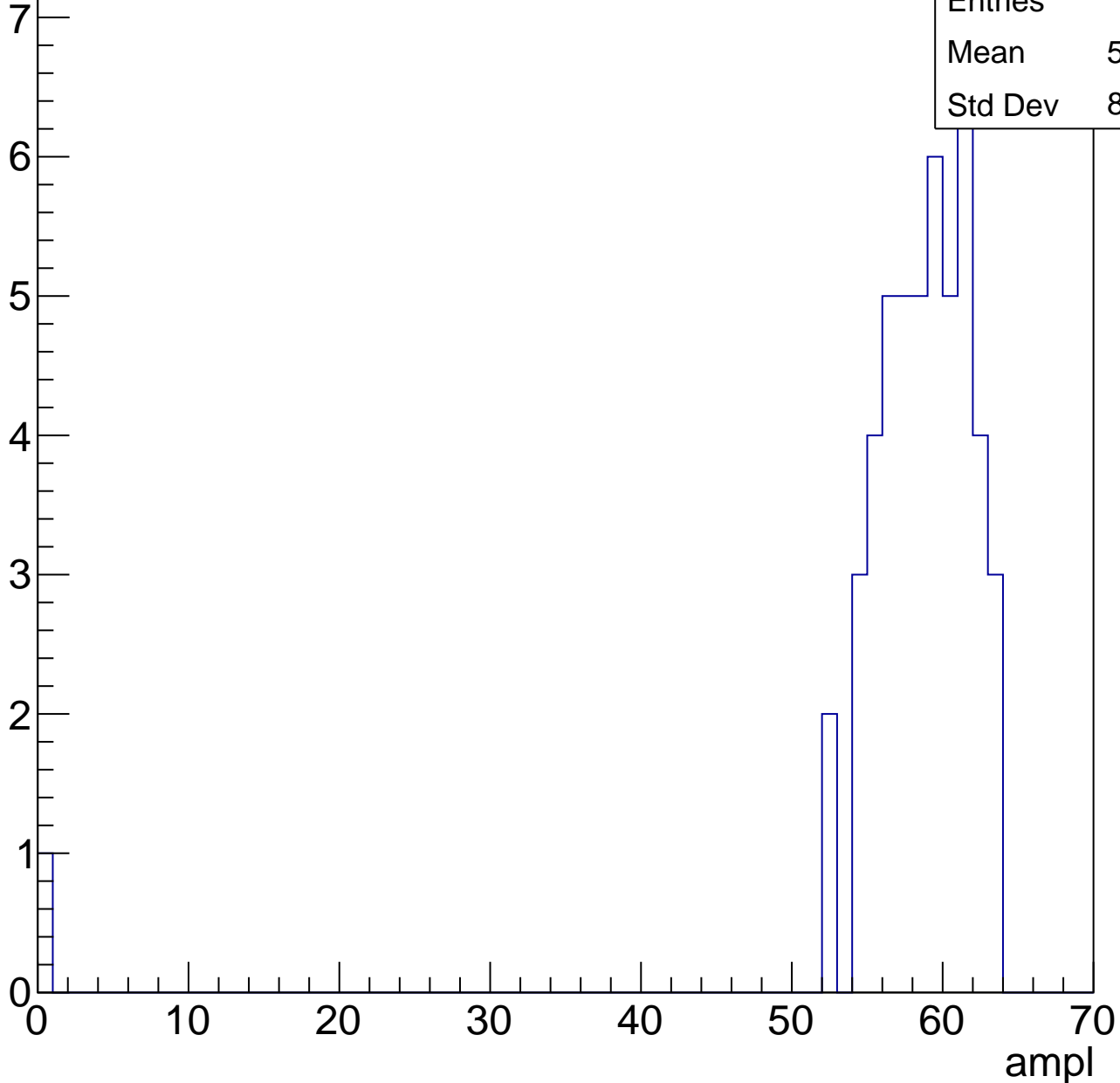


# B0L102S, U17-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	50
Mean	57.18
Std Dev	8.648

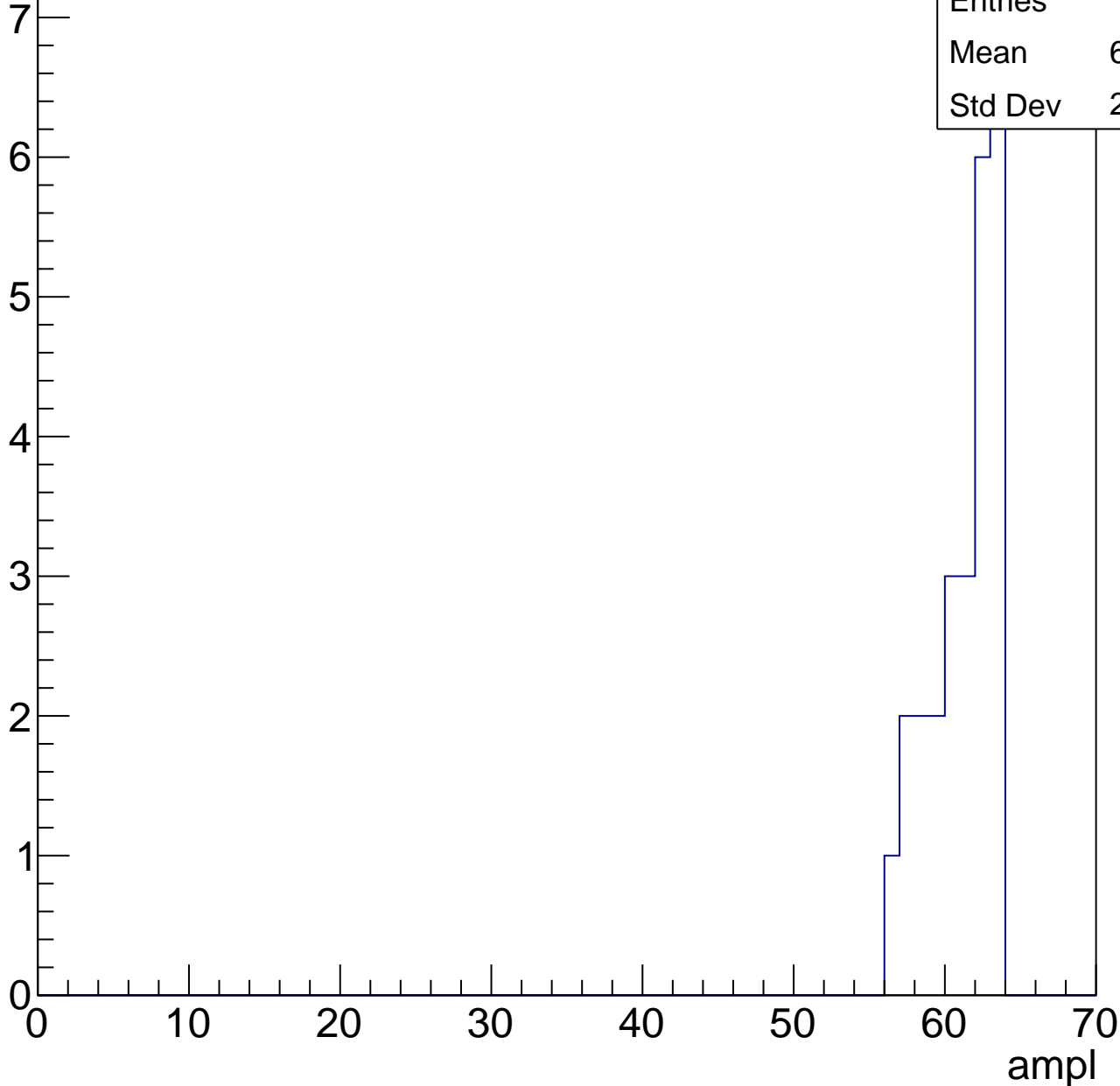


# B0L102S, U17-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	26
Mean	60.77
Std Dev	2.136





# B0L102S, U17-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch123, adc0

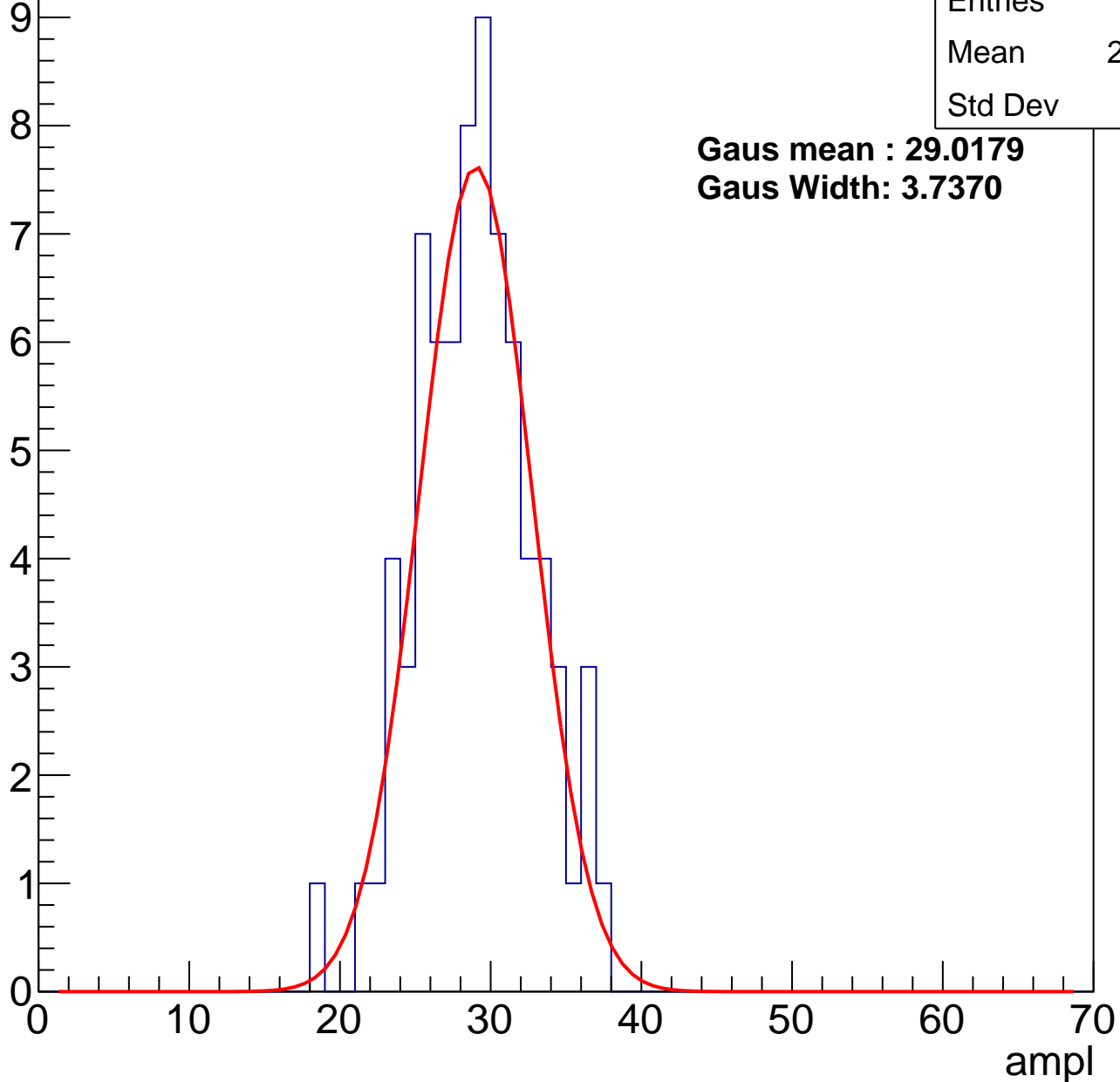
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	28.55
Std Dev	3.81

**Gaus mean : 29.0179**

**Gaus Width: 3.7370**



# B0L102S, U17-ch123, adc1

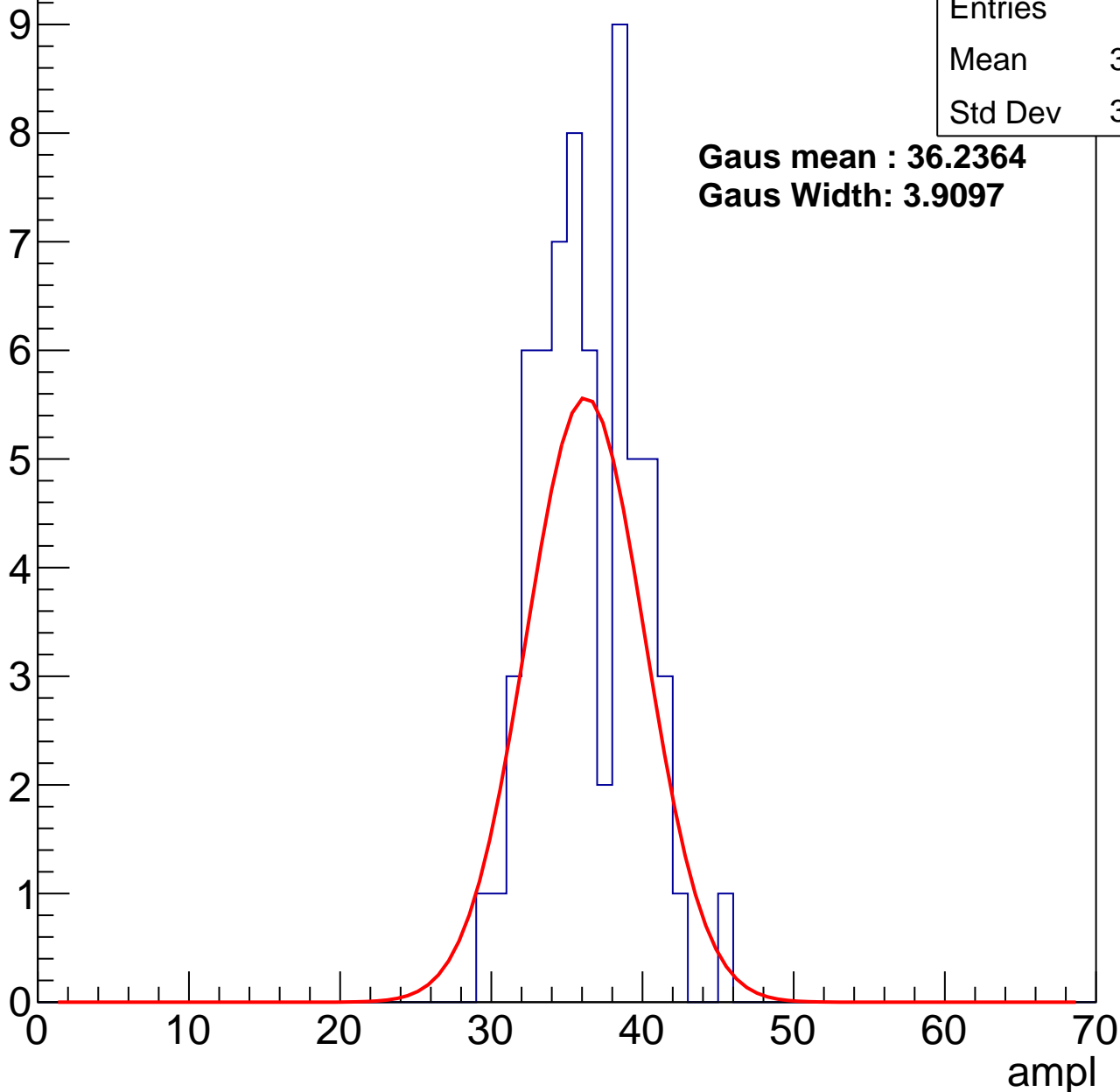
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.89
Std Dev	3.308

**Gaus mean : 36.2364**

**Gaus Width: 3.9097**



# B0L102S, U17-ch123, adc2

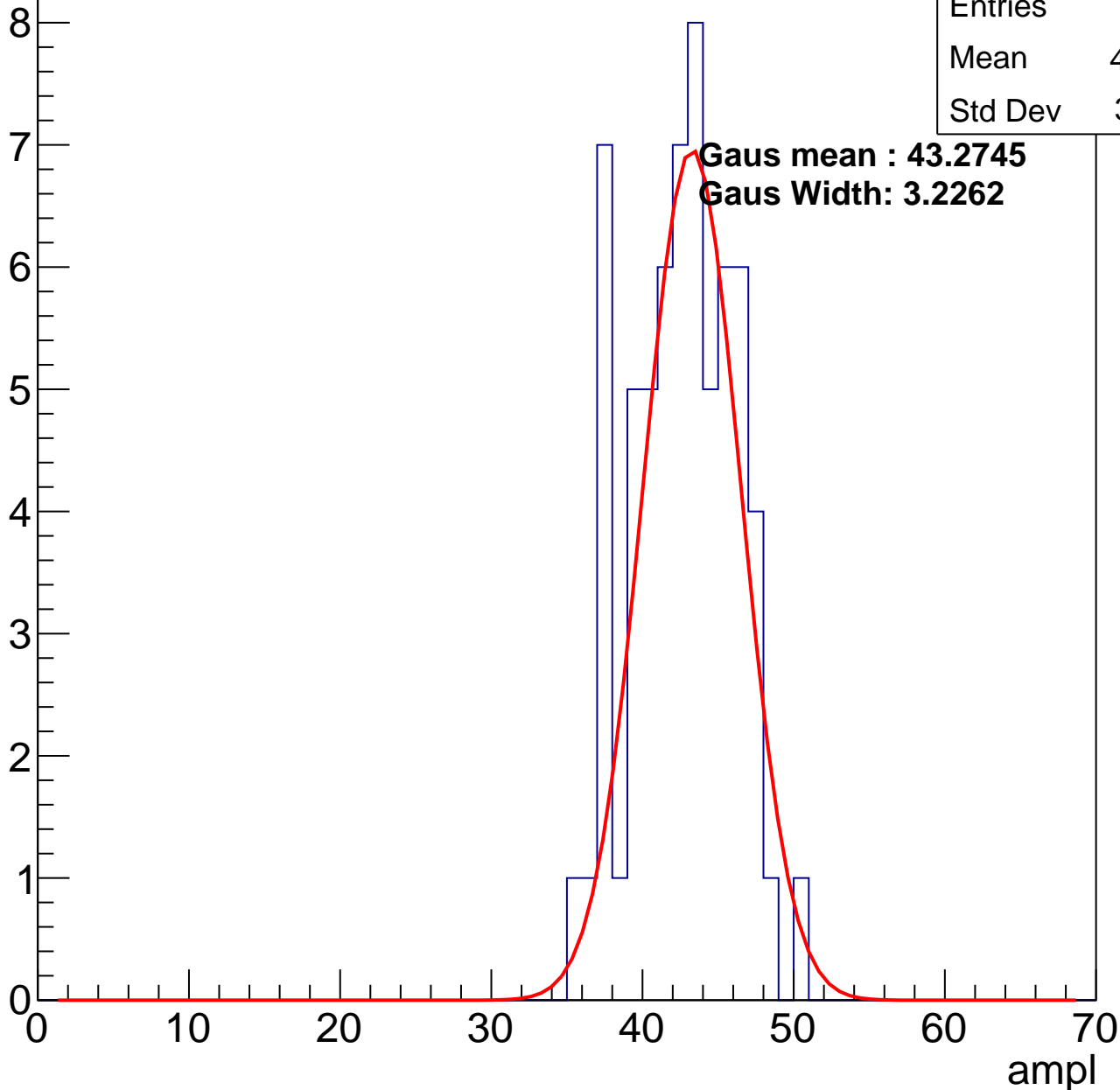
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.17
Std Dev	3.361

**Gaus mean : 43.2745**

**Gaus Width: 3.2262**

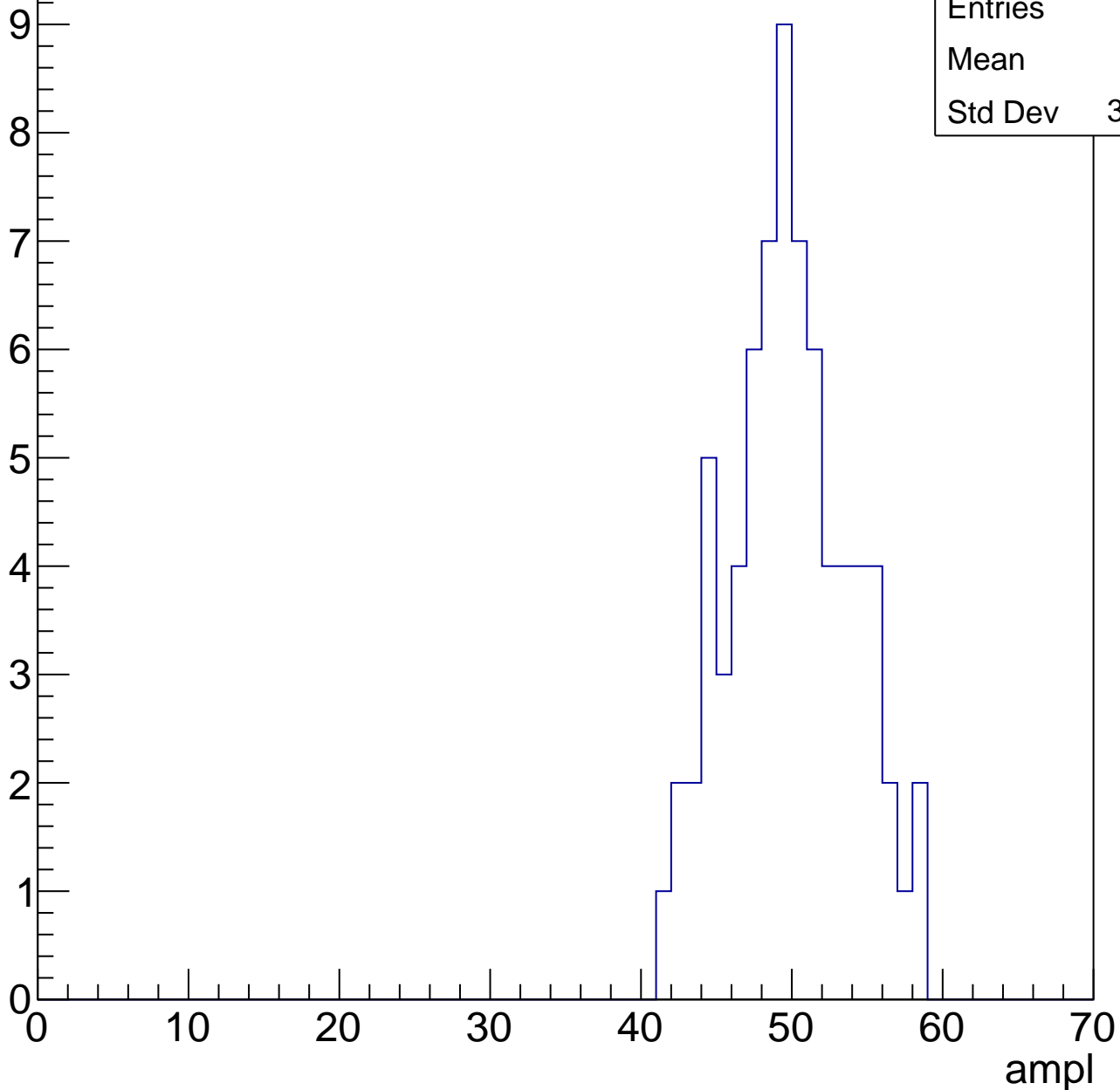


# B0L102S, U17-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	49.4
Std Dev	3.989

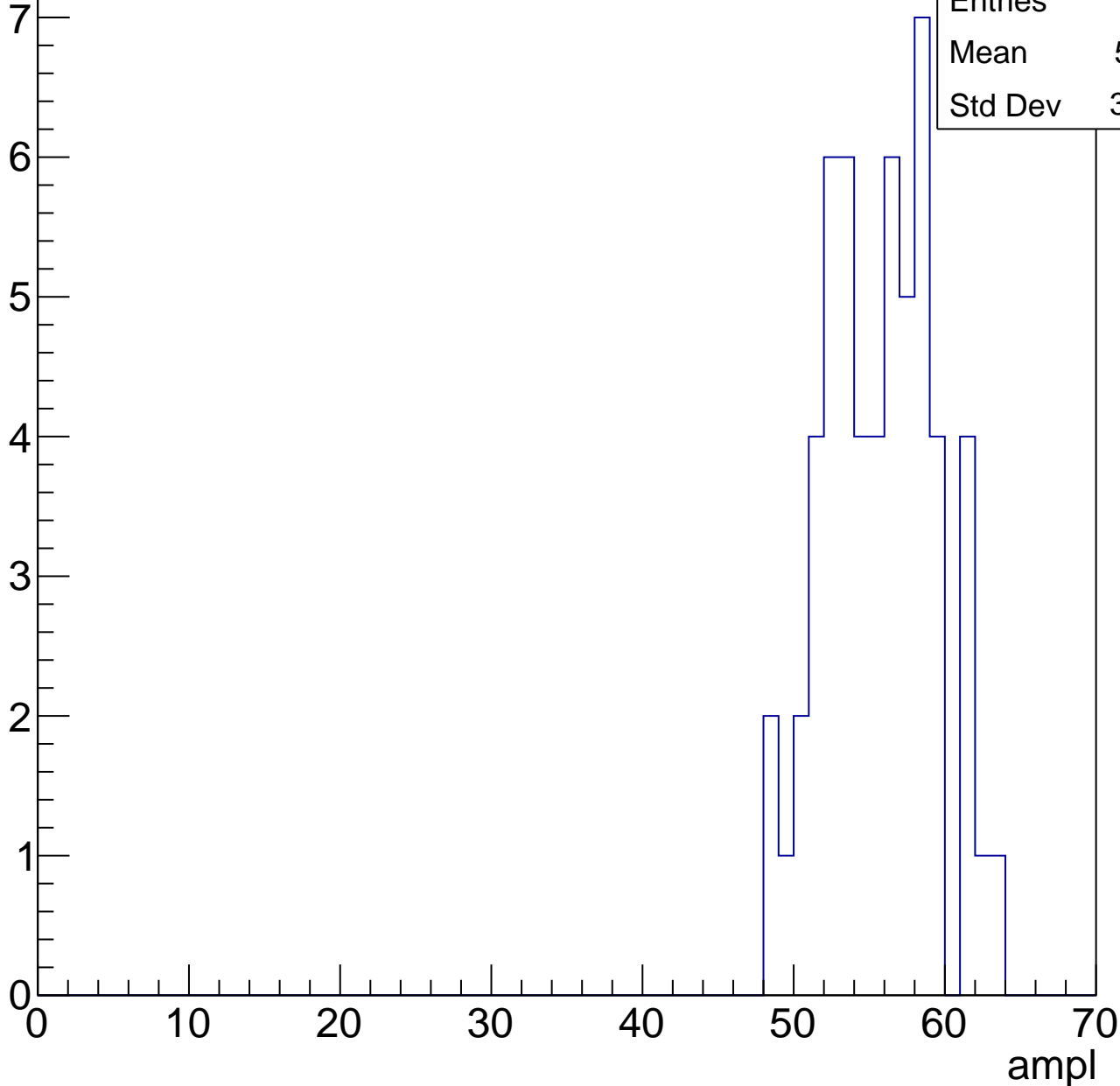


# B0L102S, U17-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

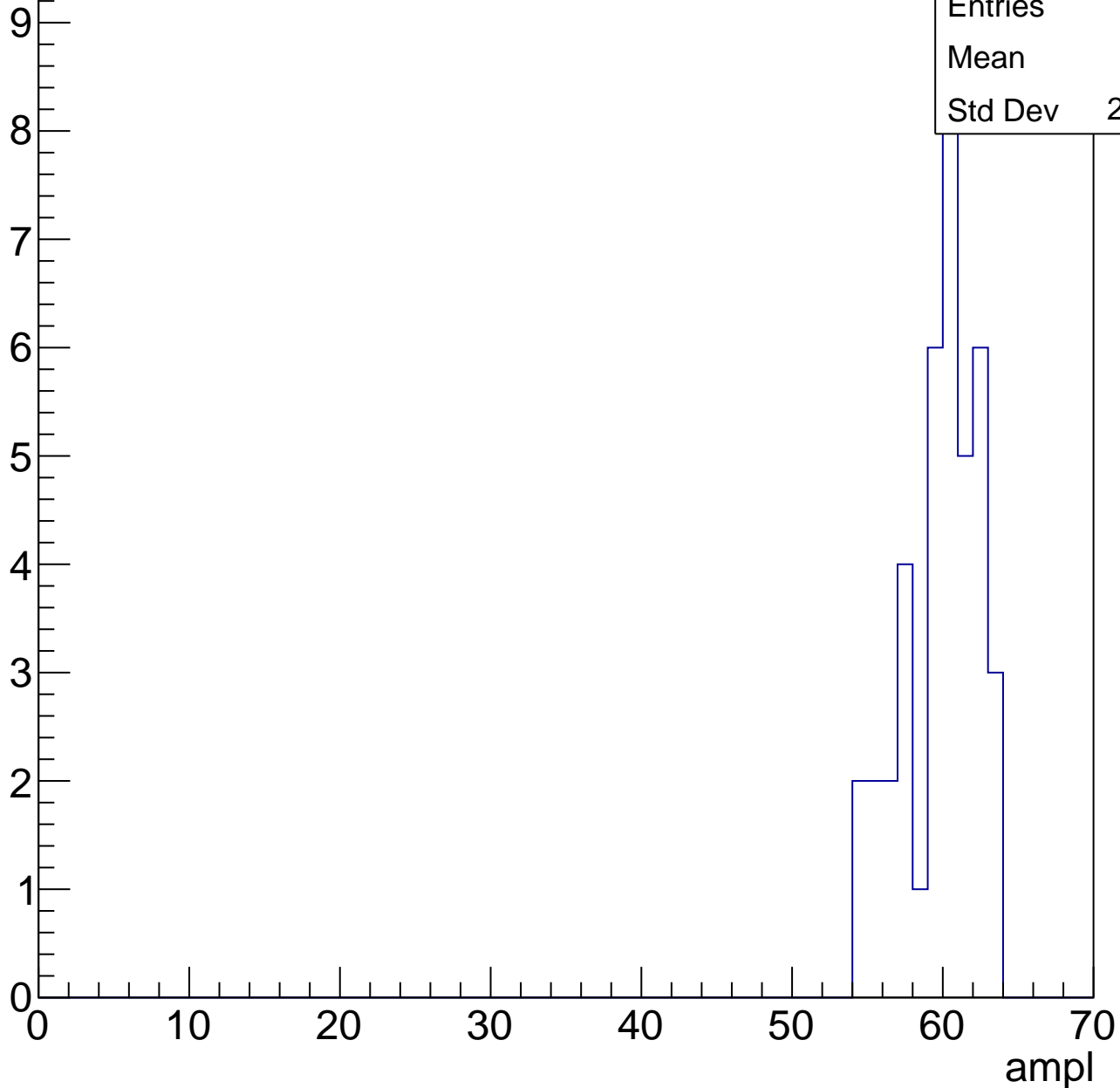
Entries	57
Mean	55.21
Std Dev	3.597



# B0L102S, U17-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

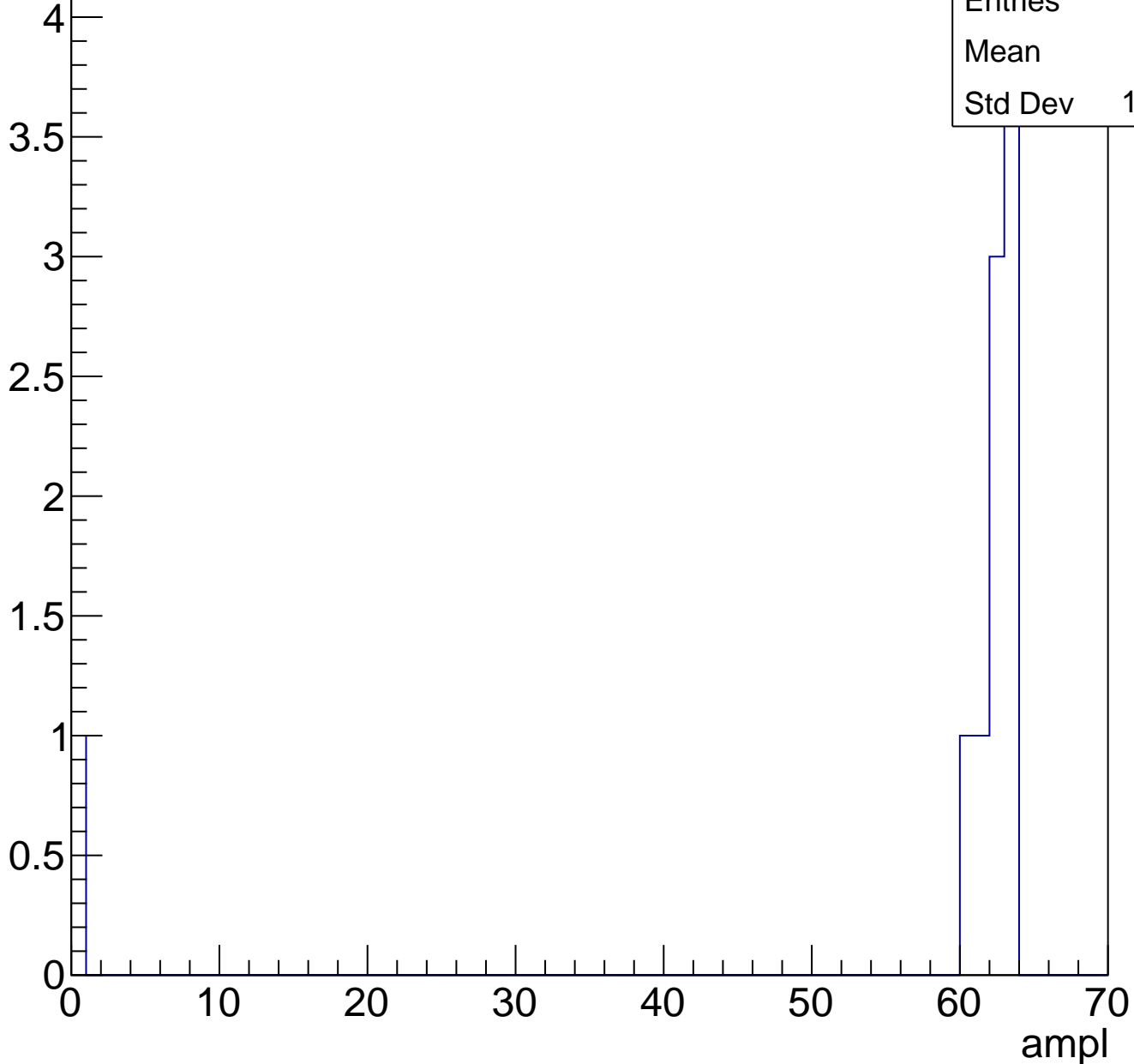


Entries	40
Mean	59.4
Std Dev	2.458

# B0L102S, U17-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

# B0L102S, U17-ch124, adc0

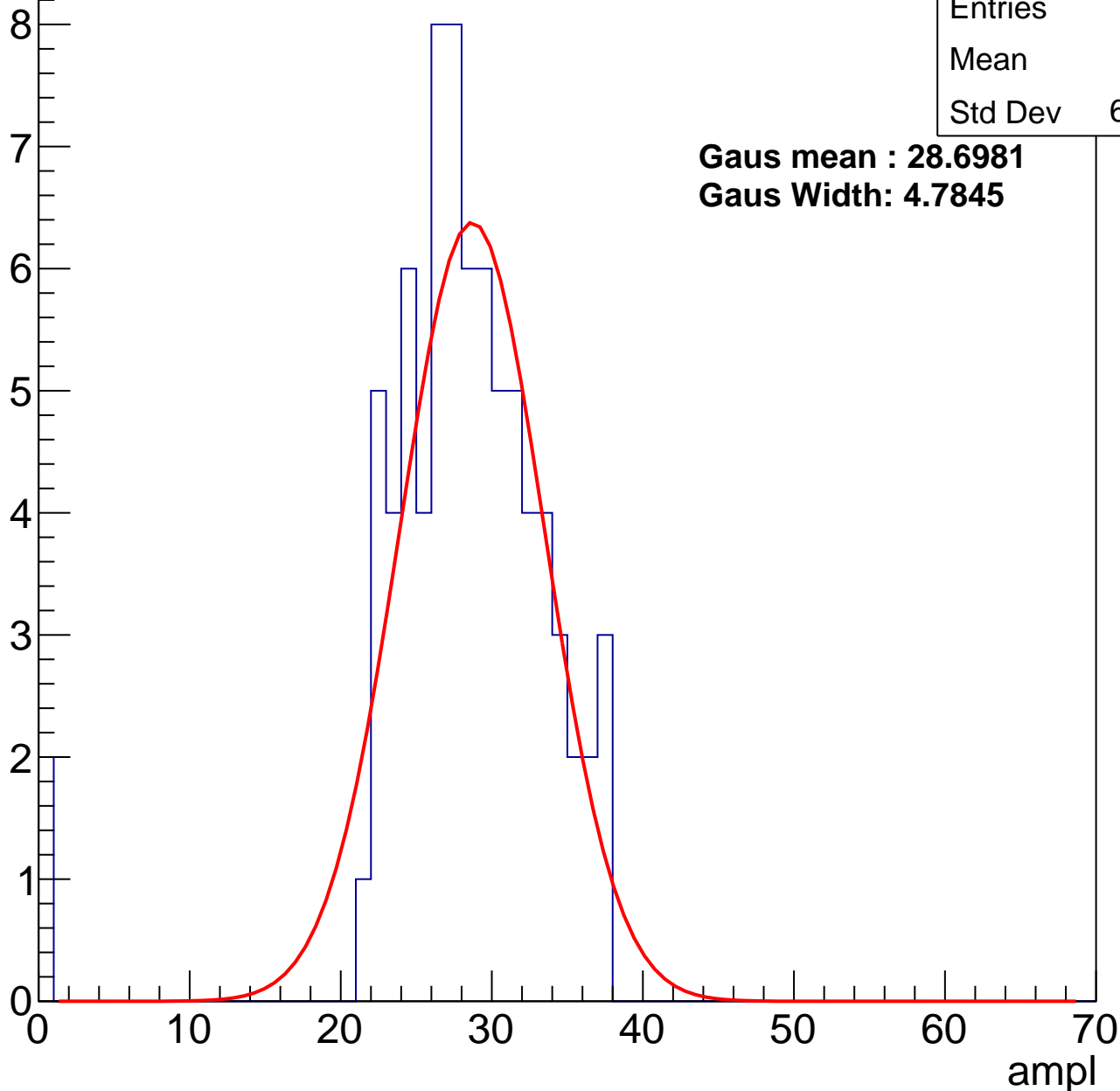
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	78
Mean	27.6
Std Dev	6.064

**Gaus mean : 28.6981**

**Gaus Width: 4.7845**



# B0L102S, U17-ch124, adc1

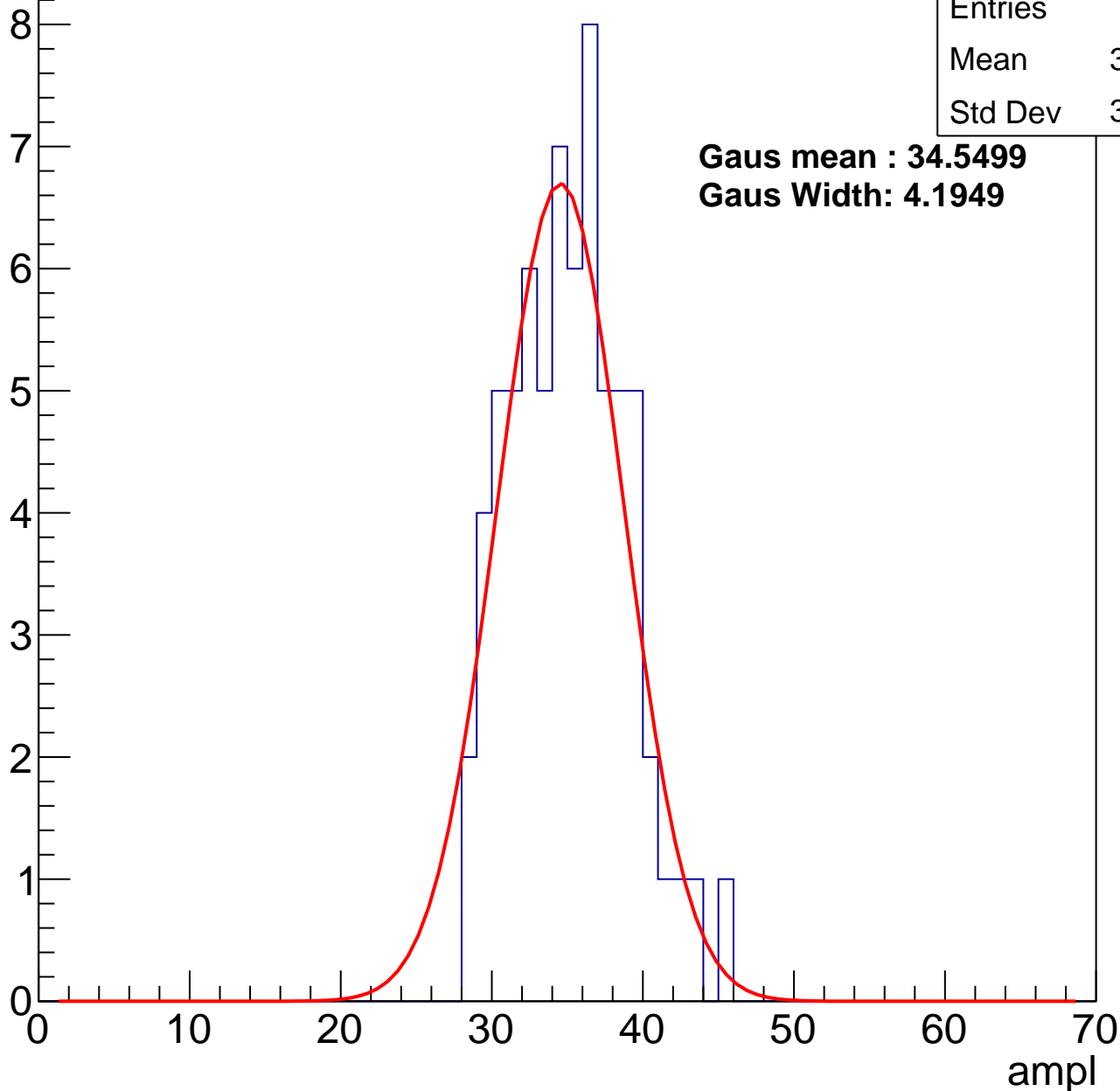
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	34.65
Std Dev	3.753

**Gaus mean : 34.5499**

**Gaus Width: 4.1949**



# B0L102S, U17-ch124, adc2

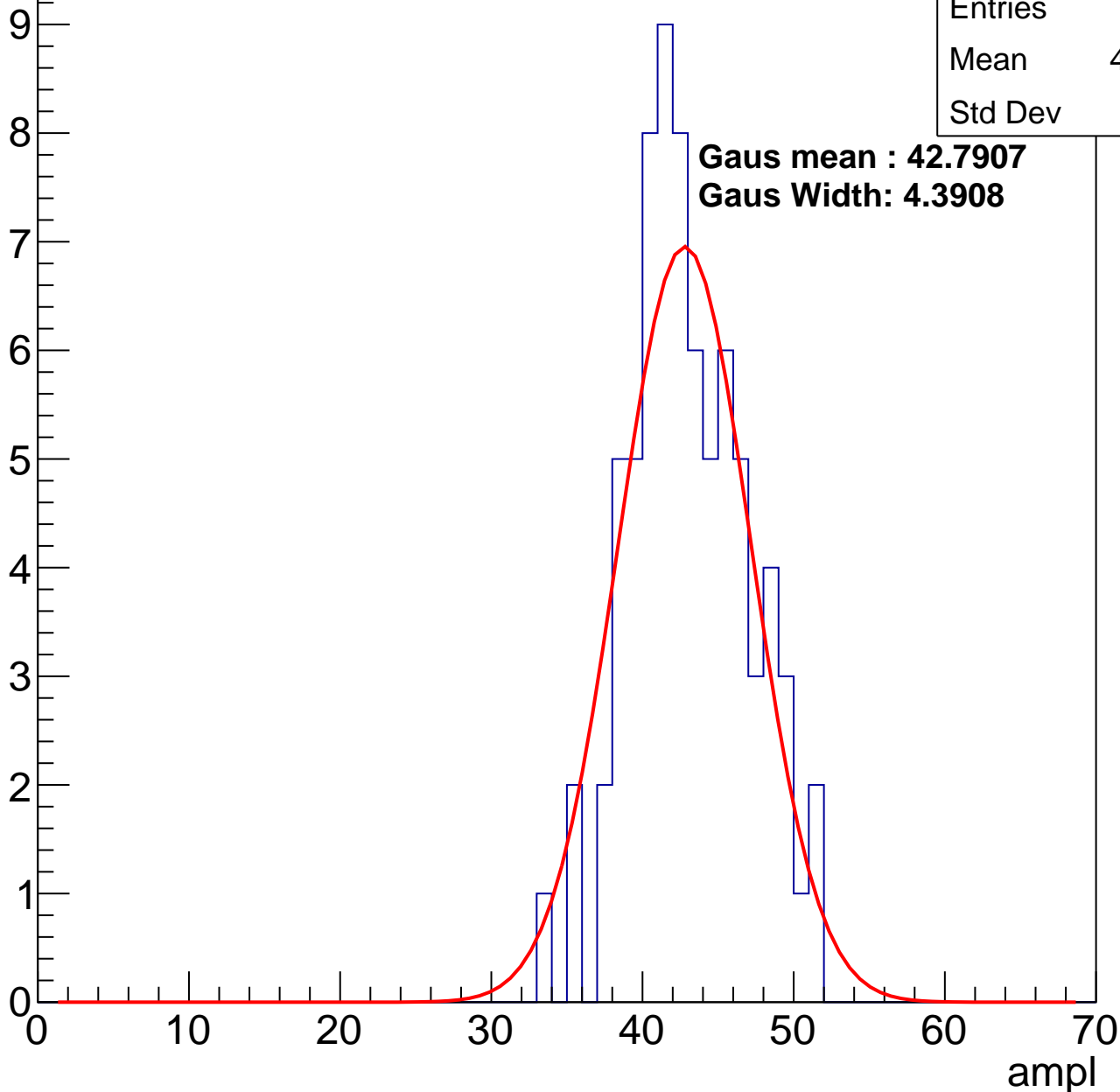
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	42.63
Std Dev	3.86

**Gaus mean : 42.7907**

**Gaus Width: 4.3908**

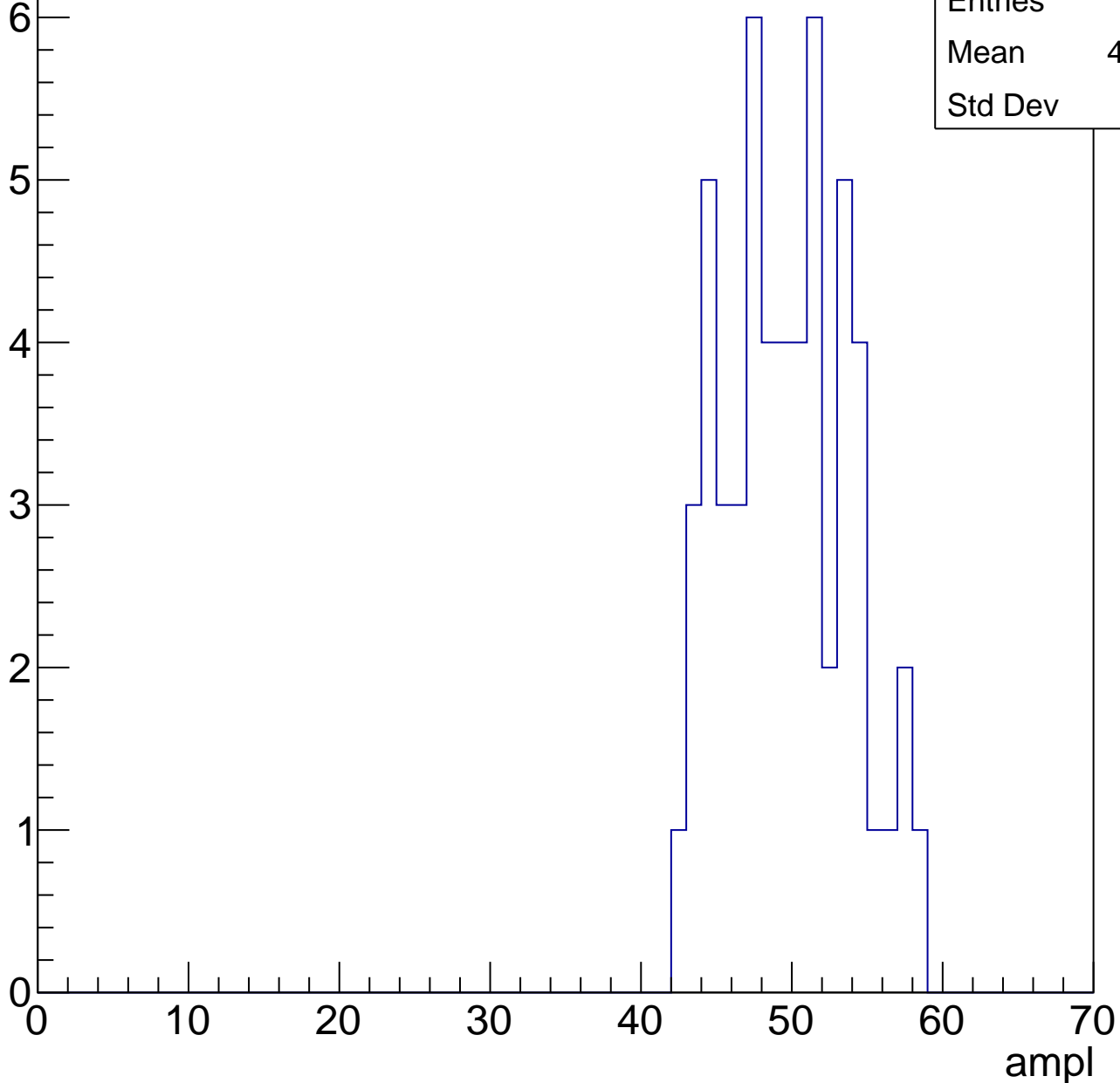


# B0L102S, U17-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	55
Mean	49.24
Std Dev	4.05

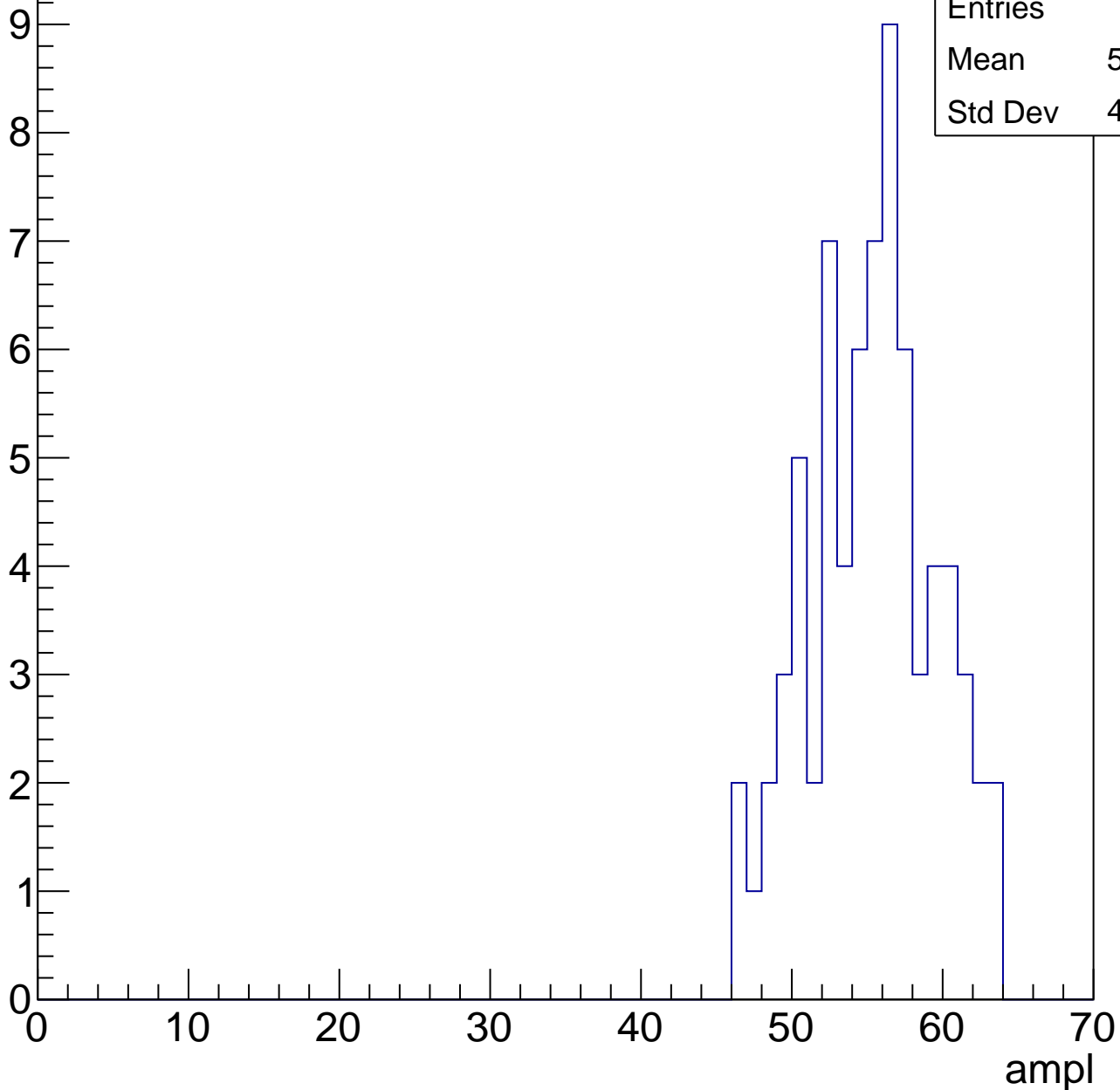


# B0L102S, U17-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	72
Mean	54.83
Std Dev	4.133

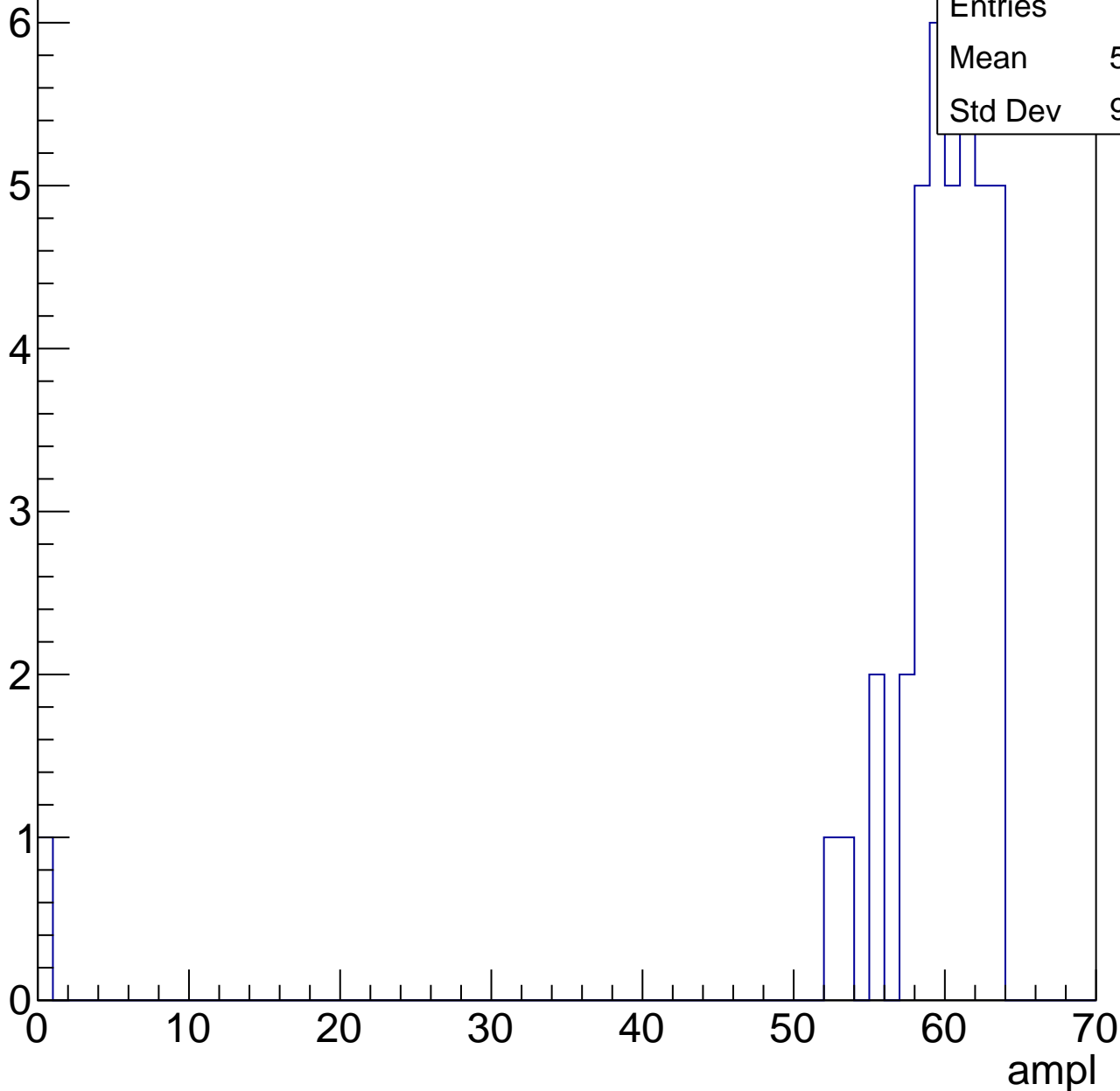


# B0L102S, U17-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

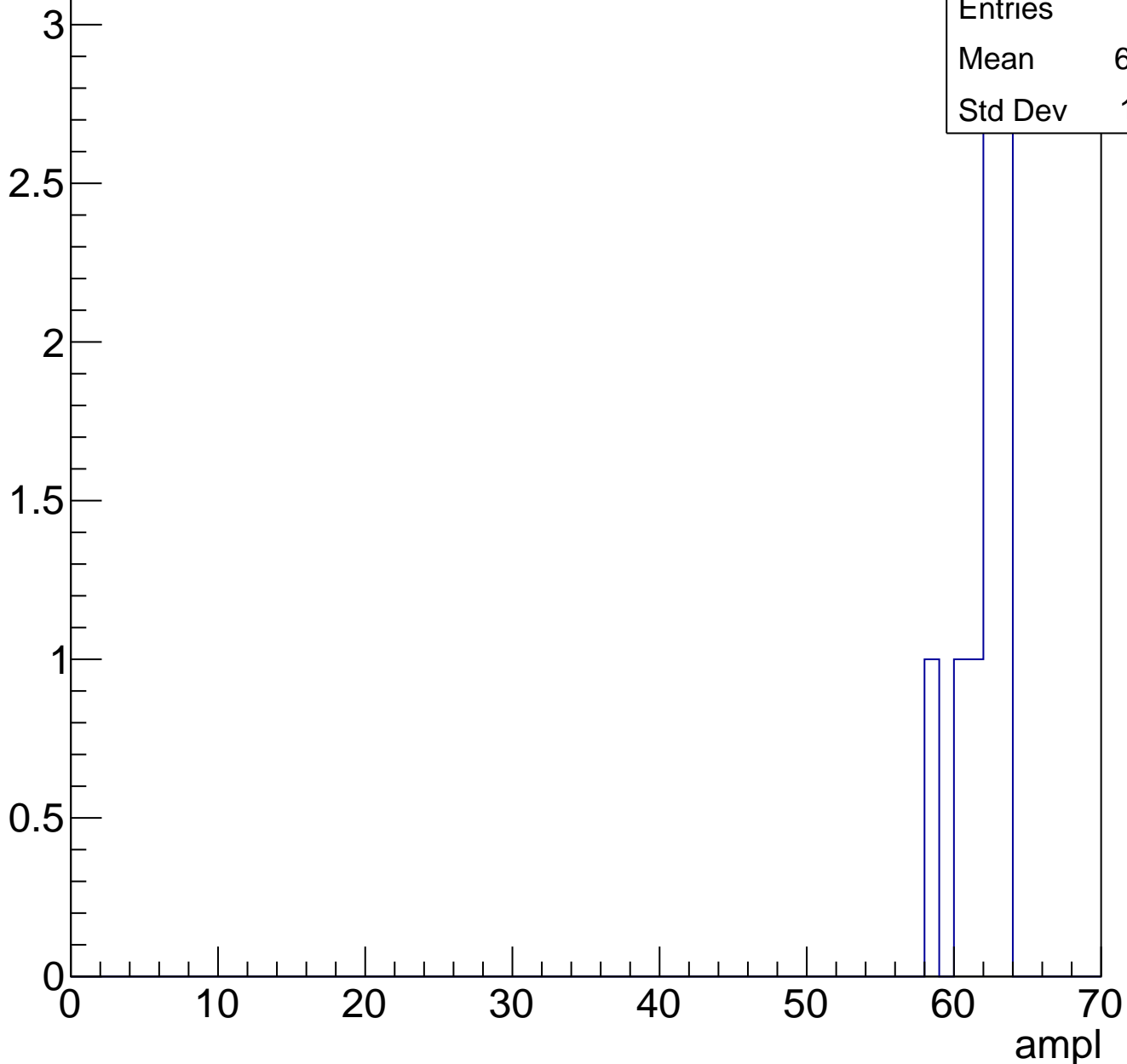
Entries	39
Mean	58.05
Std Dev	9.779



# B0L102S, U17-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

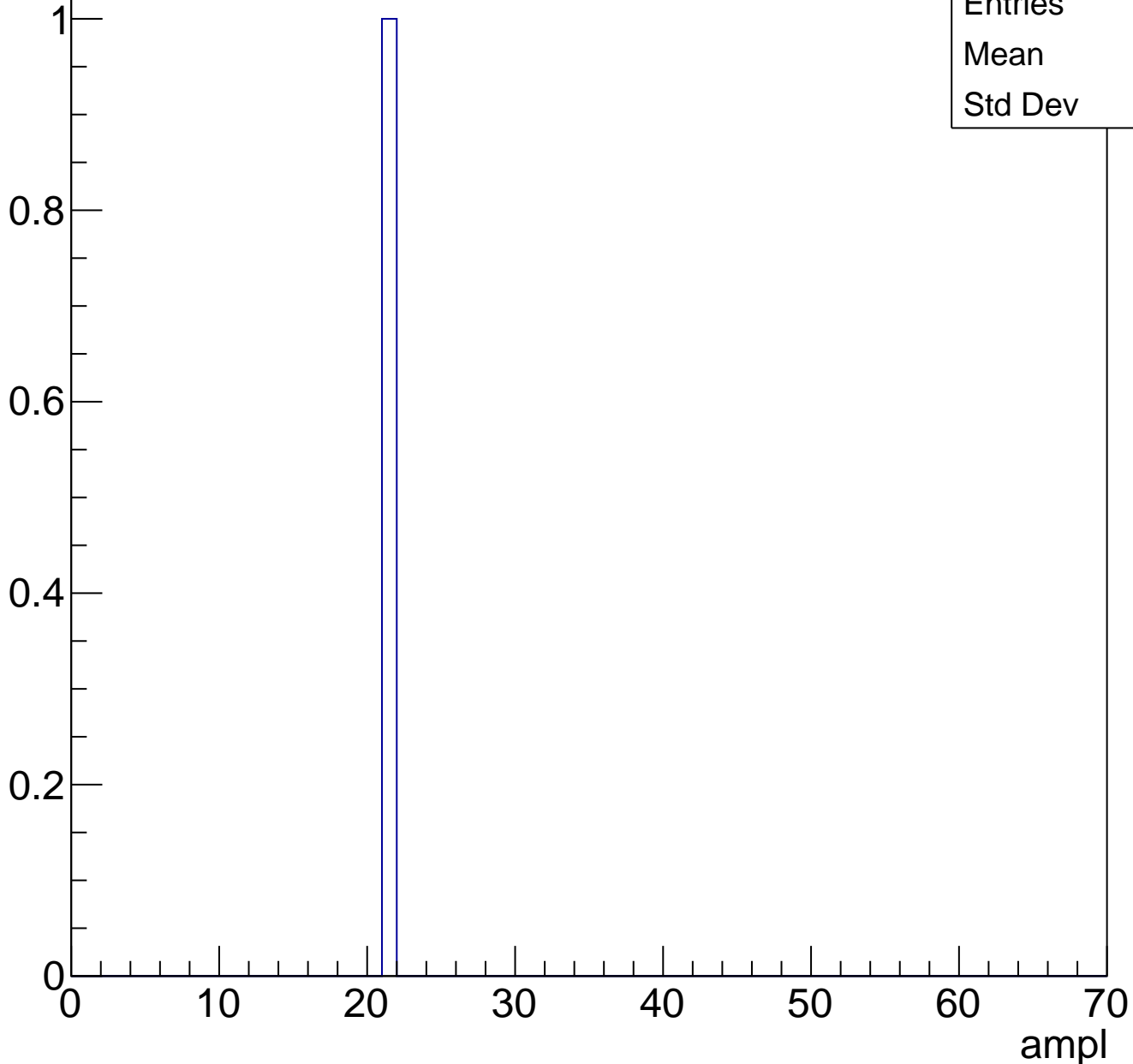




# B0L102S, U17-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch125, adc0

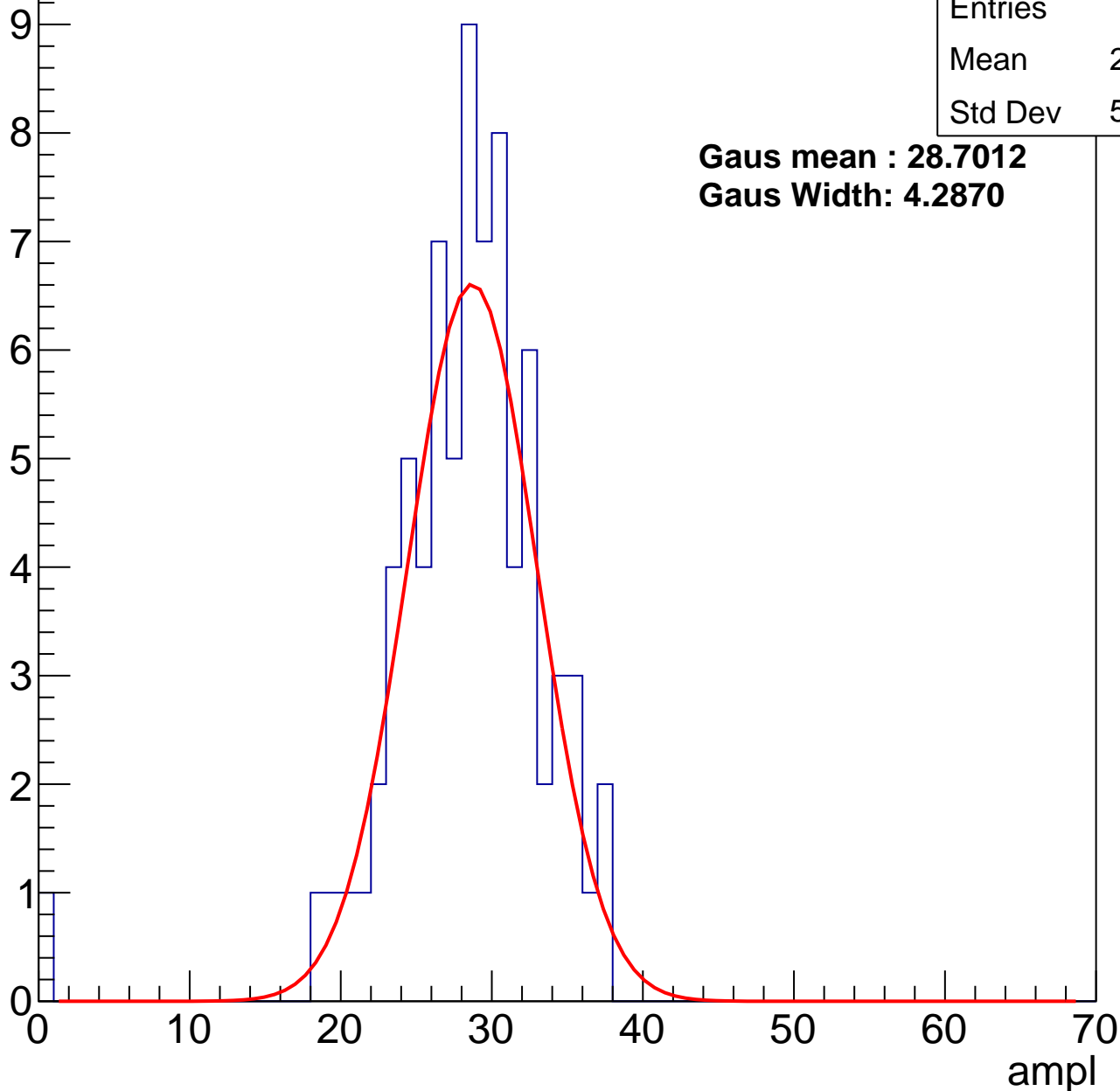
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	77
Mean	27.86
Std Dev	5.222

**Gaus mean : 28.7012**

**Gaus Width: 4.2870**



# B0L102S, U17-ch125, adc1

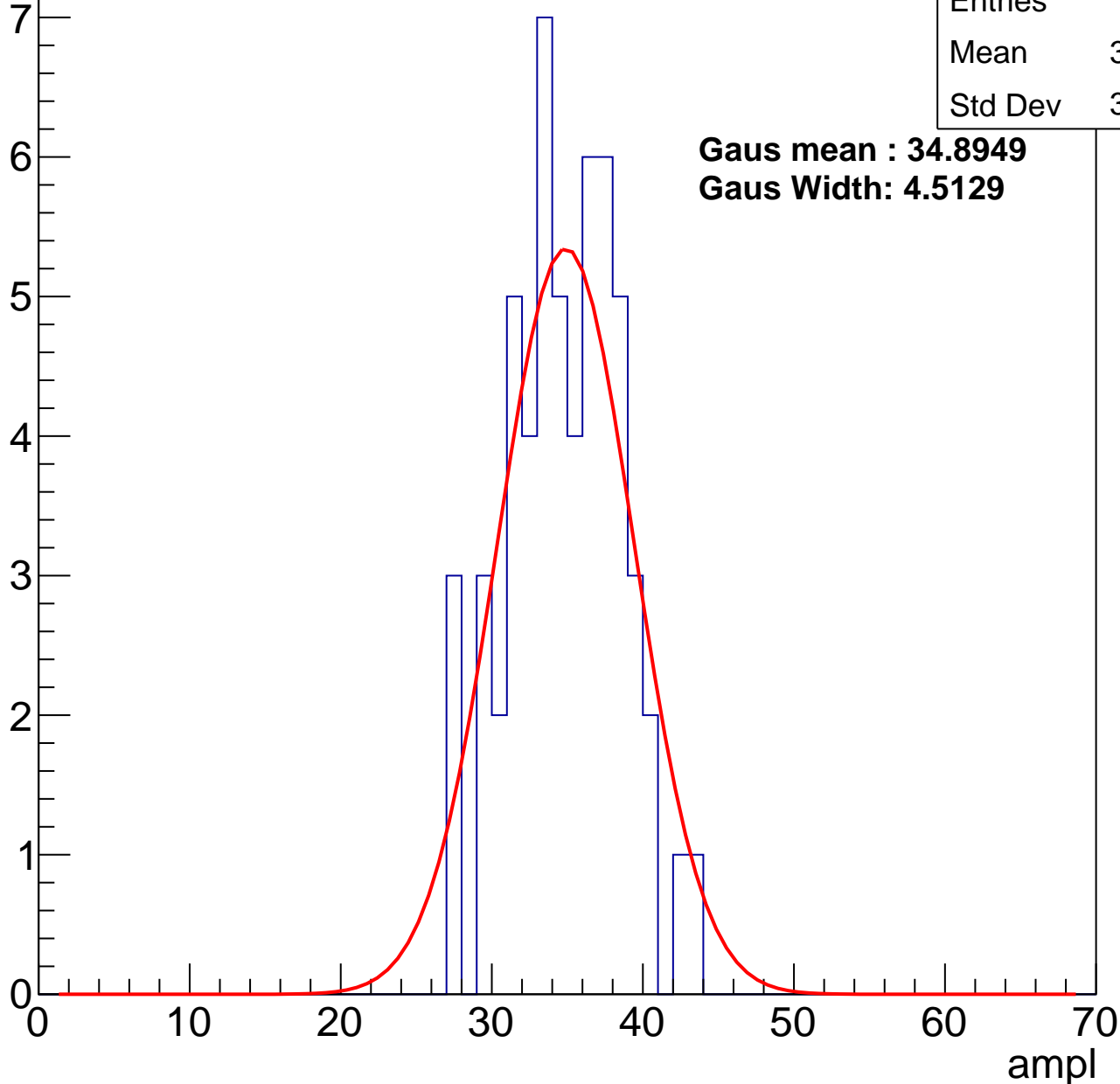
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	57
Mean	34.42
Std Dev	3.666

**Gaus mean : 34.8949**

**Gaus Width: 4.5129**



# B0L102S, U17-ch125, adc2

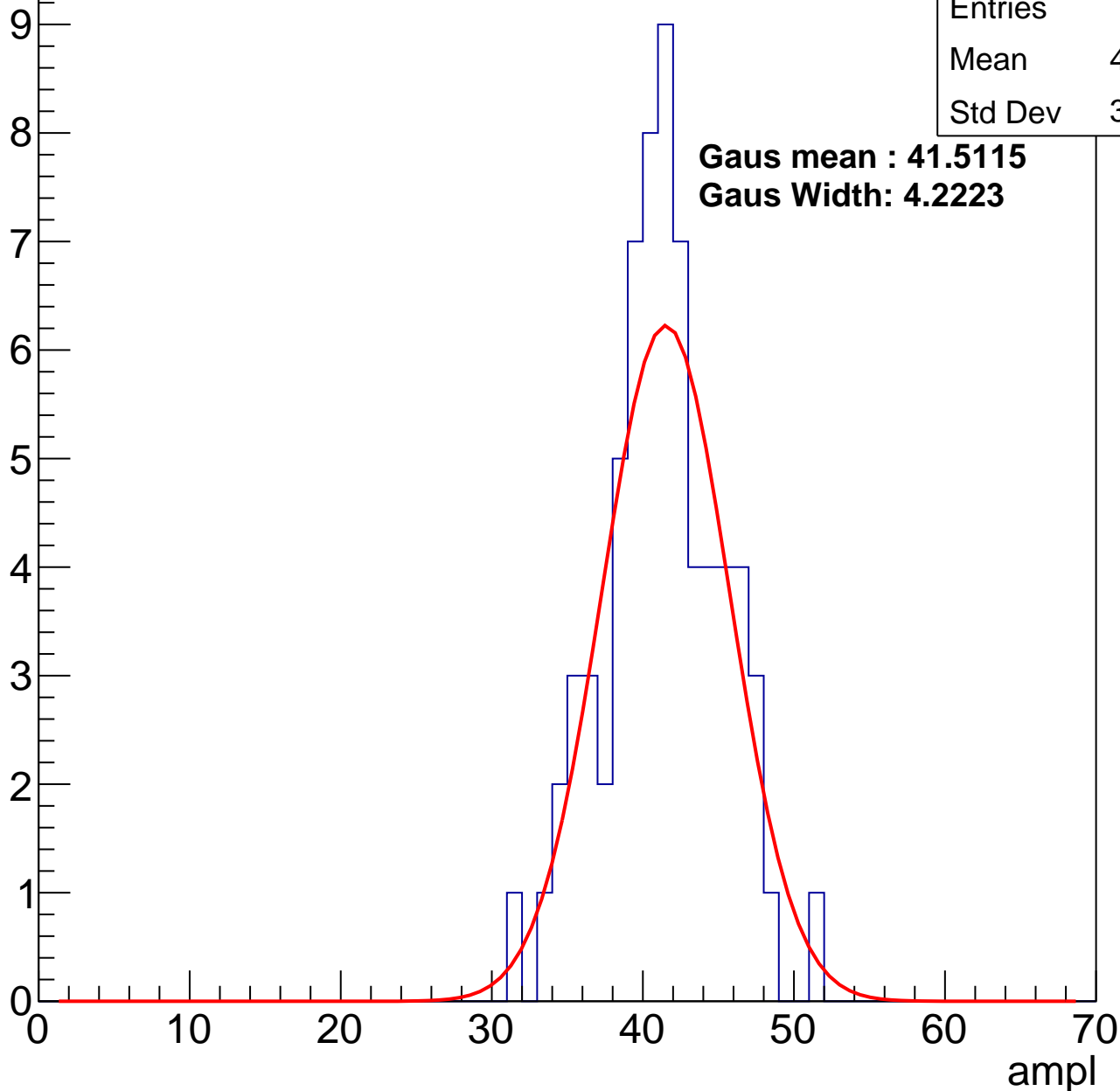
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	69
Mean	40.83
Std Dev	3.875

**Gaus mean : 41.5115**

**Gaus Width: 4.2223**

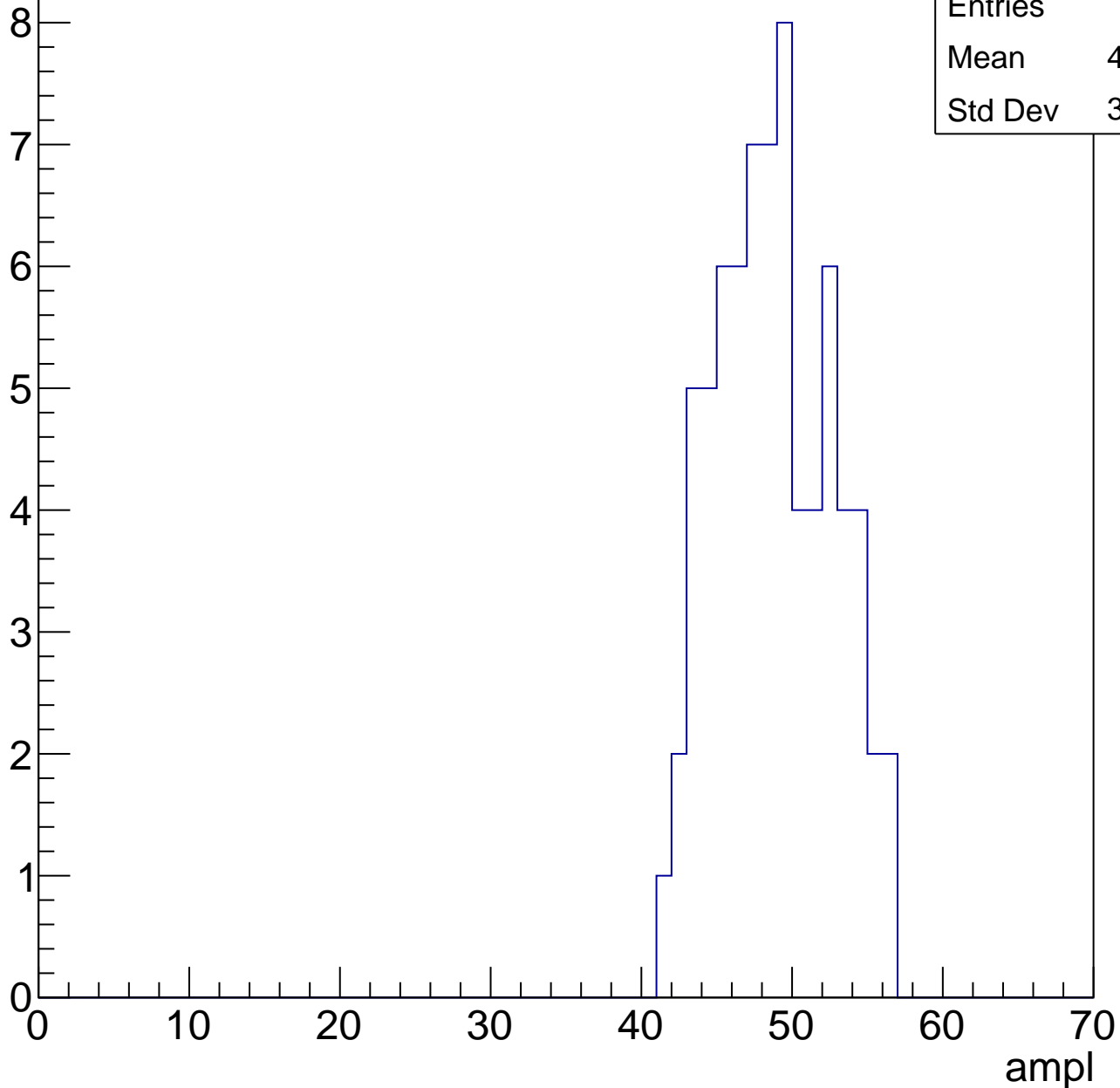


# B0L102S, U17-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	48.34
Std Dev	3.753

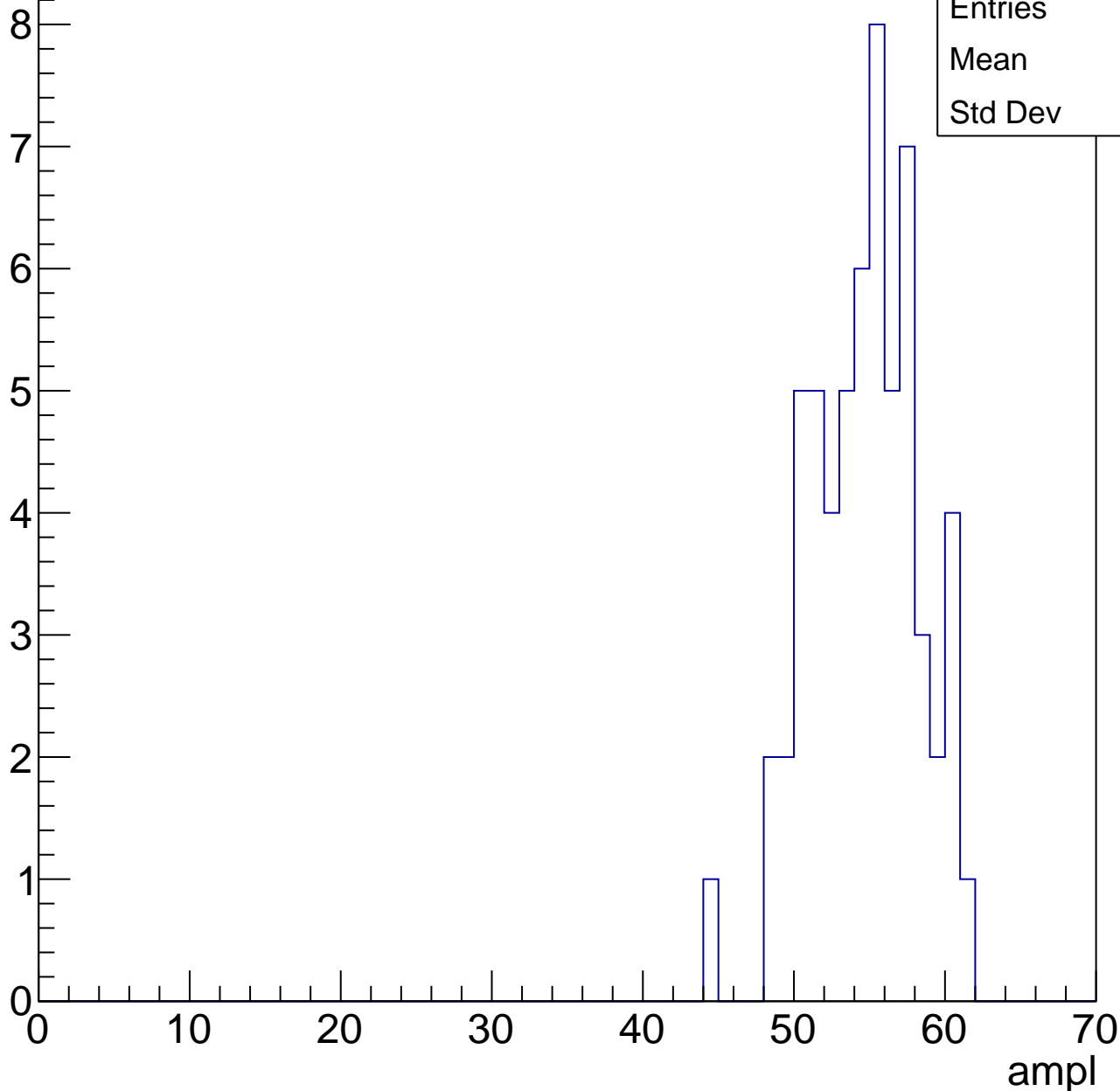


# B0L102S, U17-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

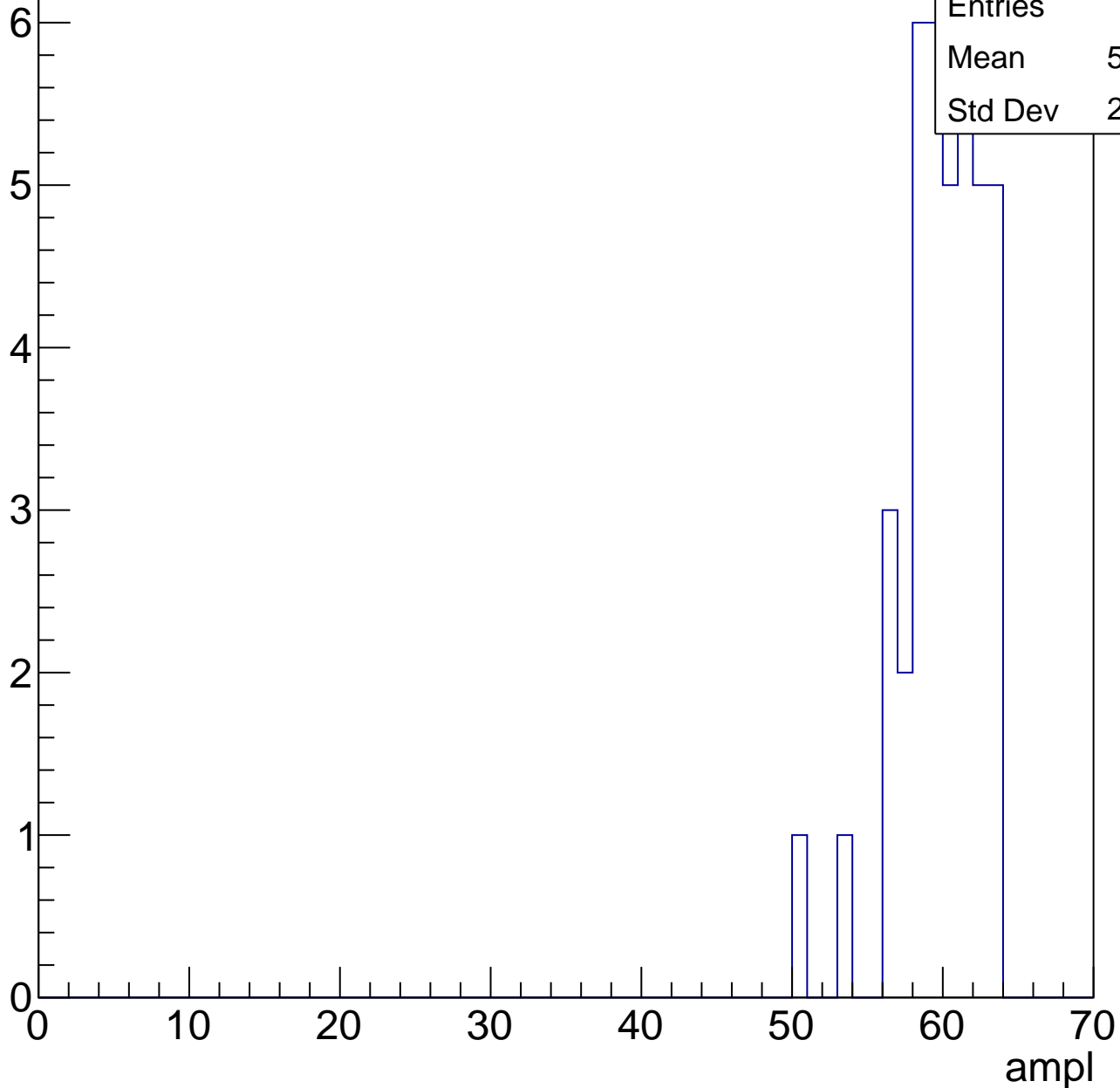
Entries	60
Mean	54.2
Std Dev	3.53



# B0L102S, U17-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

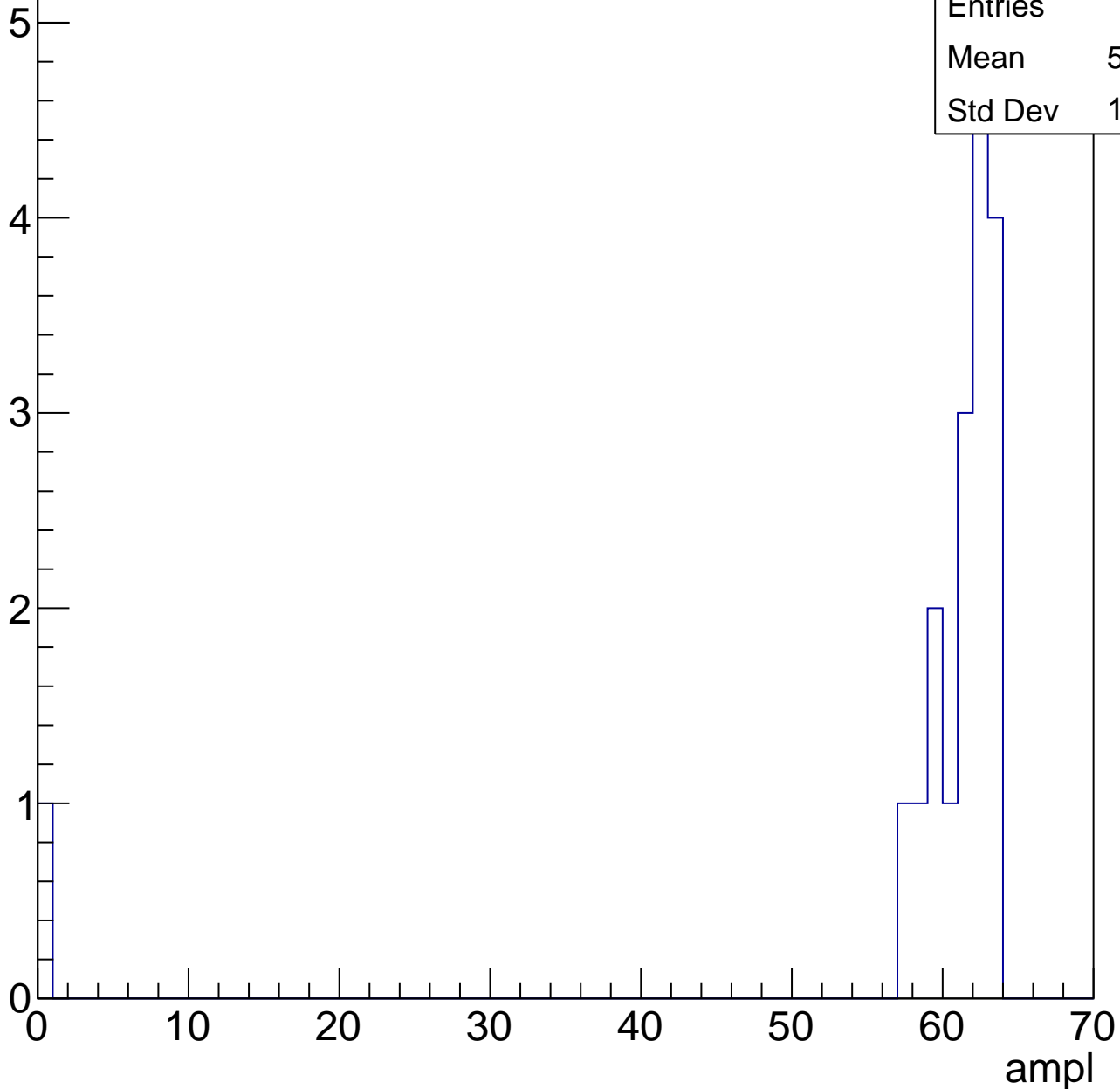


# B0L102S, U17-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	18
Mean	57.67
Std Dev	14.09





# B0L102S, U17-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

# B0L102S, U17-ch126, adc0

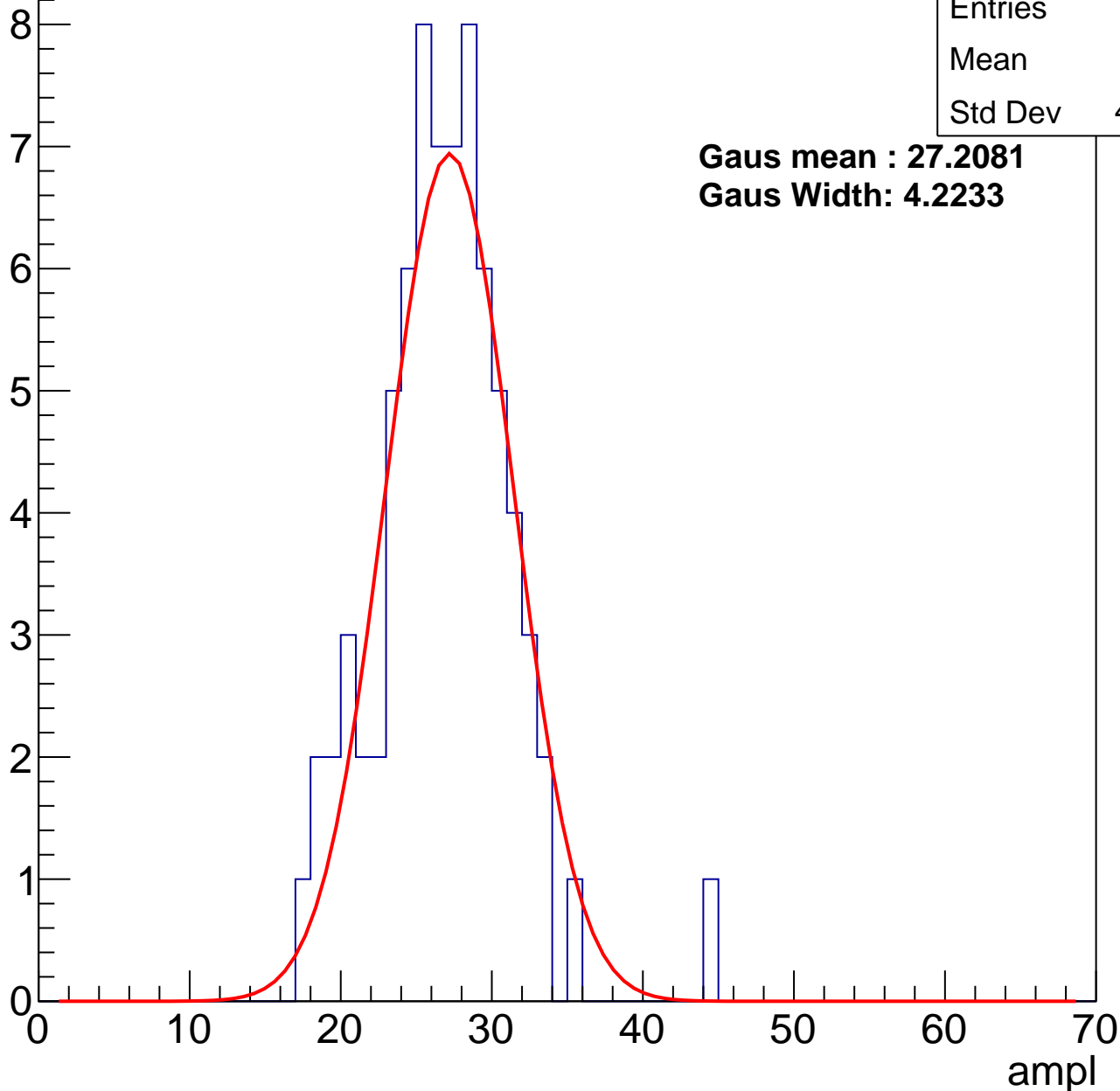
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	75
Mean	26.4
Std Dev	4.391

**Gaus mean : 27.2081**

**Gaus Width: 4.2233**



# B0L102S, U17-ch126, adc1

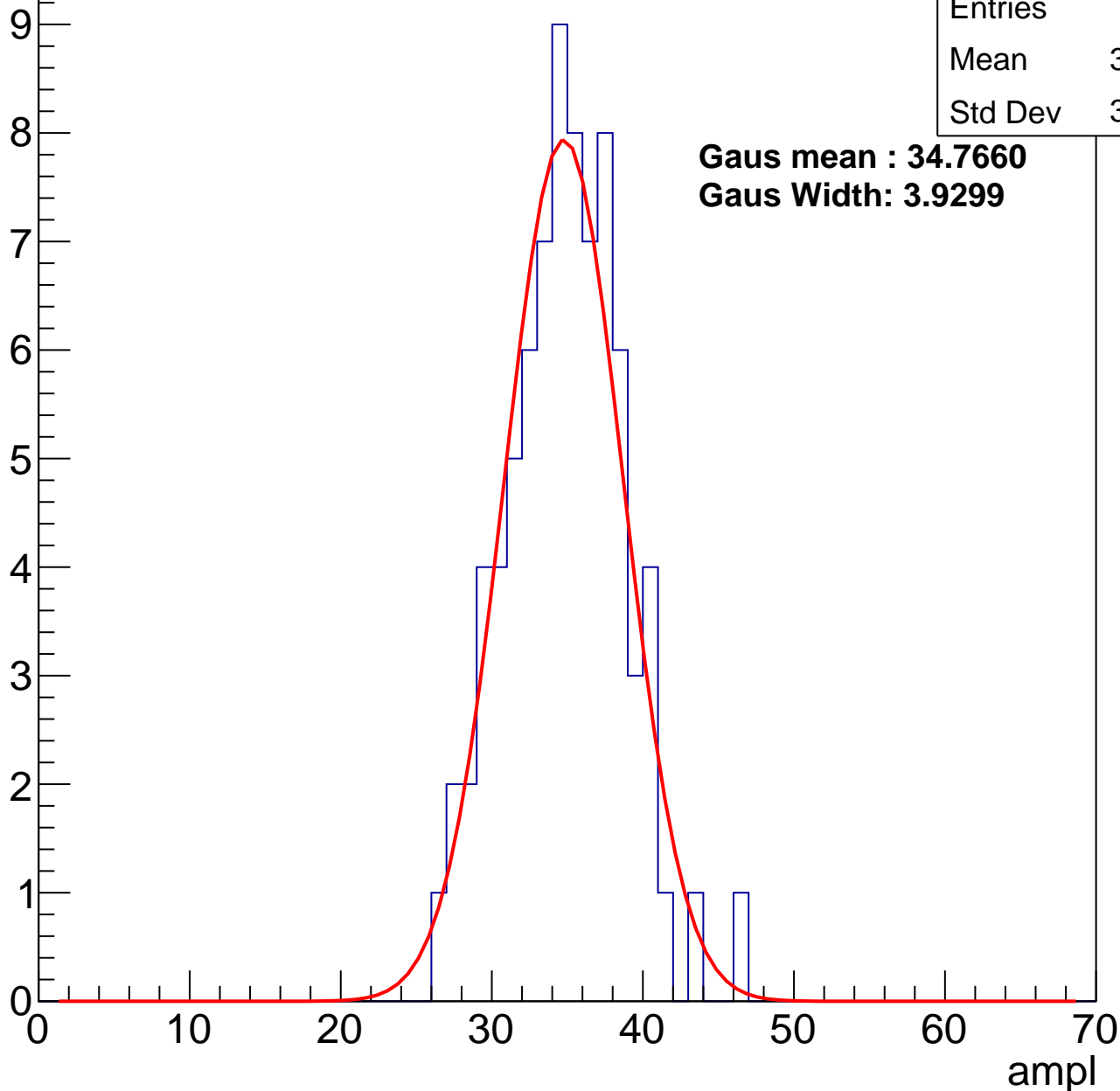
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	79
Mean	34.42
Std Dev	3.827

**Gaus mean : 34.7660**

**Gaus Width: 3.9299**



# B0L102S, U17-ch126, adc2

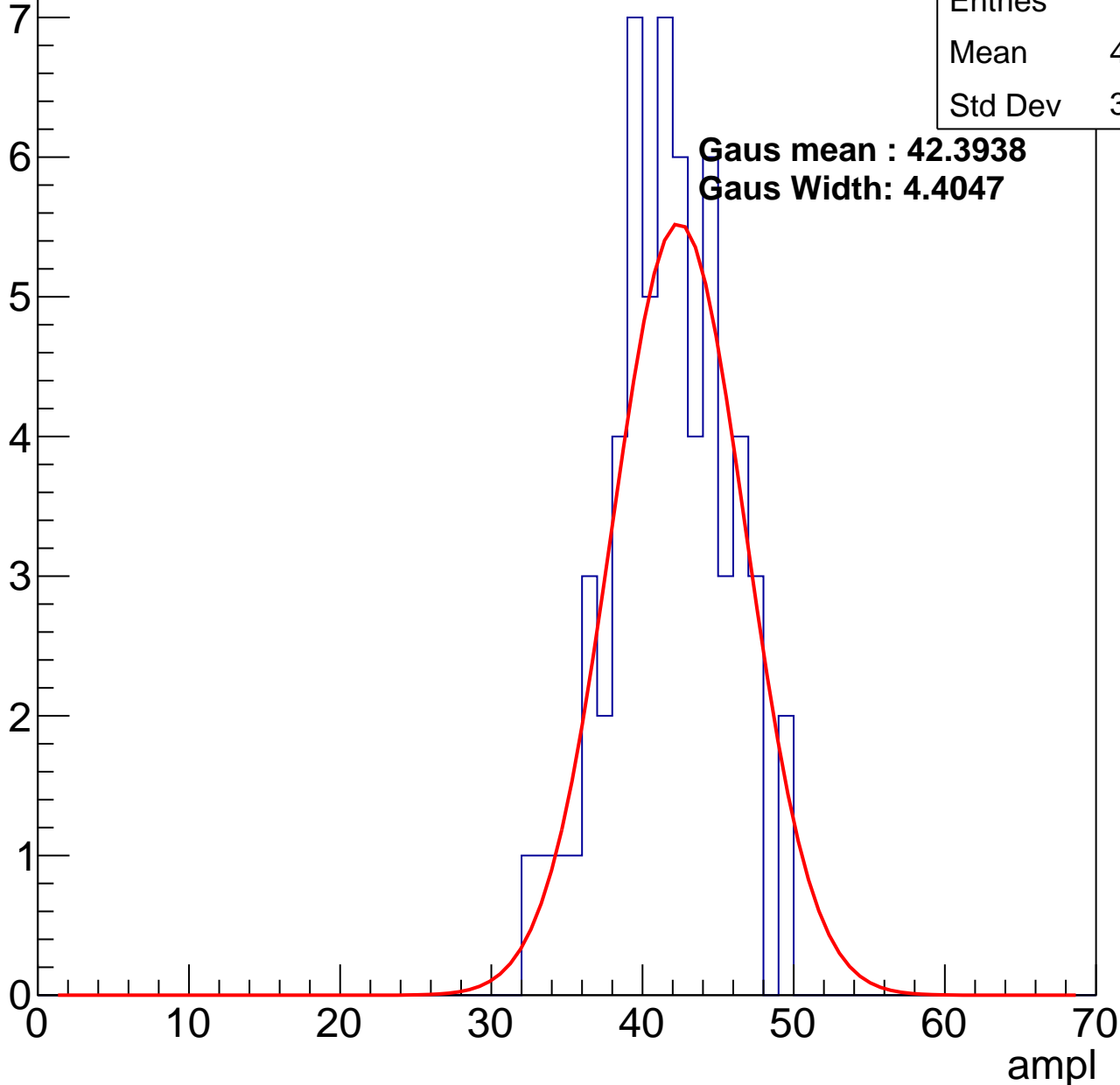
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.23
Std Dev	3.792

**Gaus mean : 42.3938**

**Gaus Width: 4.4047**

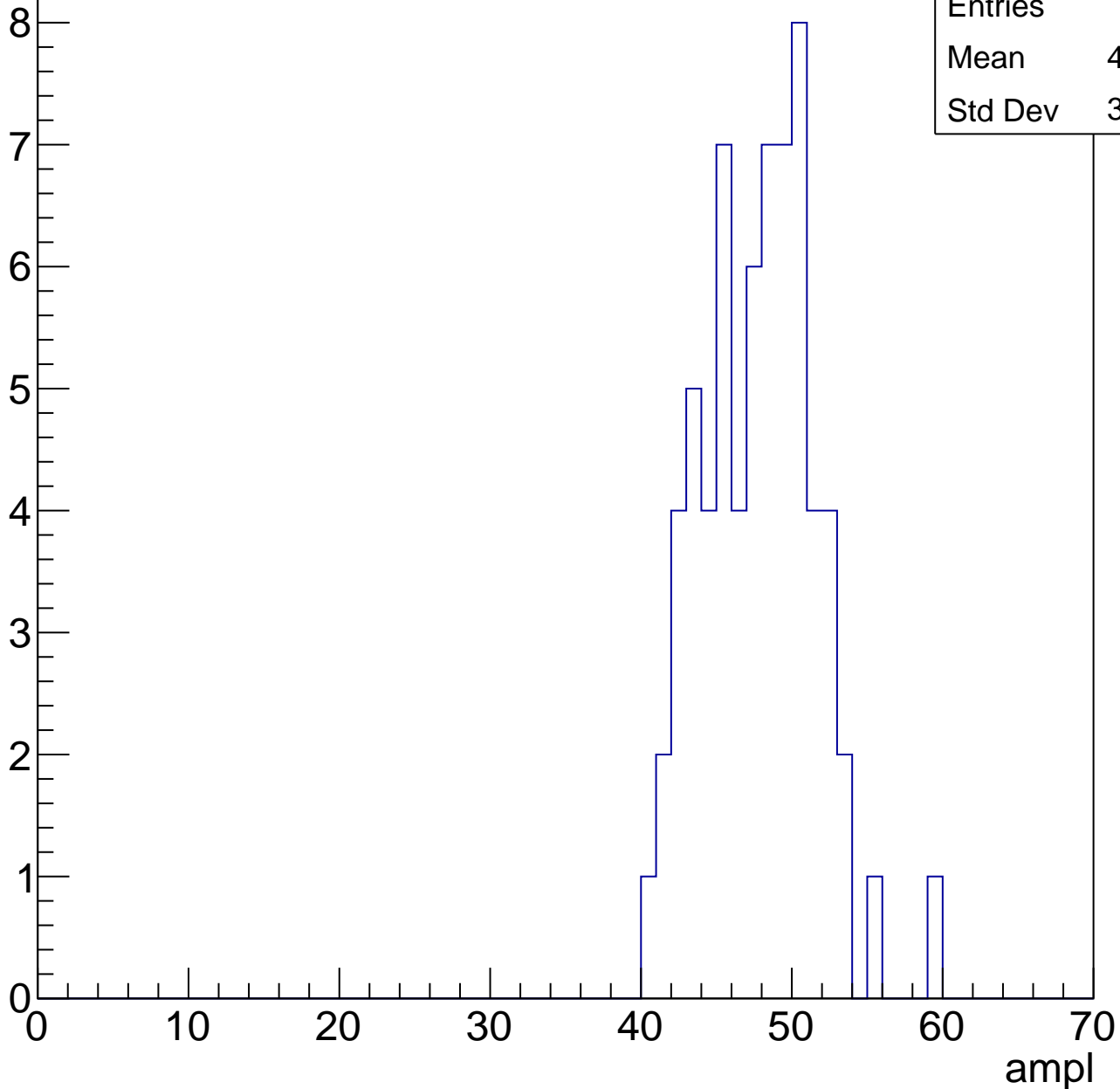


# B0L102S, U17-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	67
Mean	47.36
Std Dev	3.688

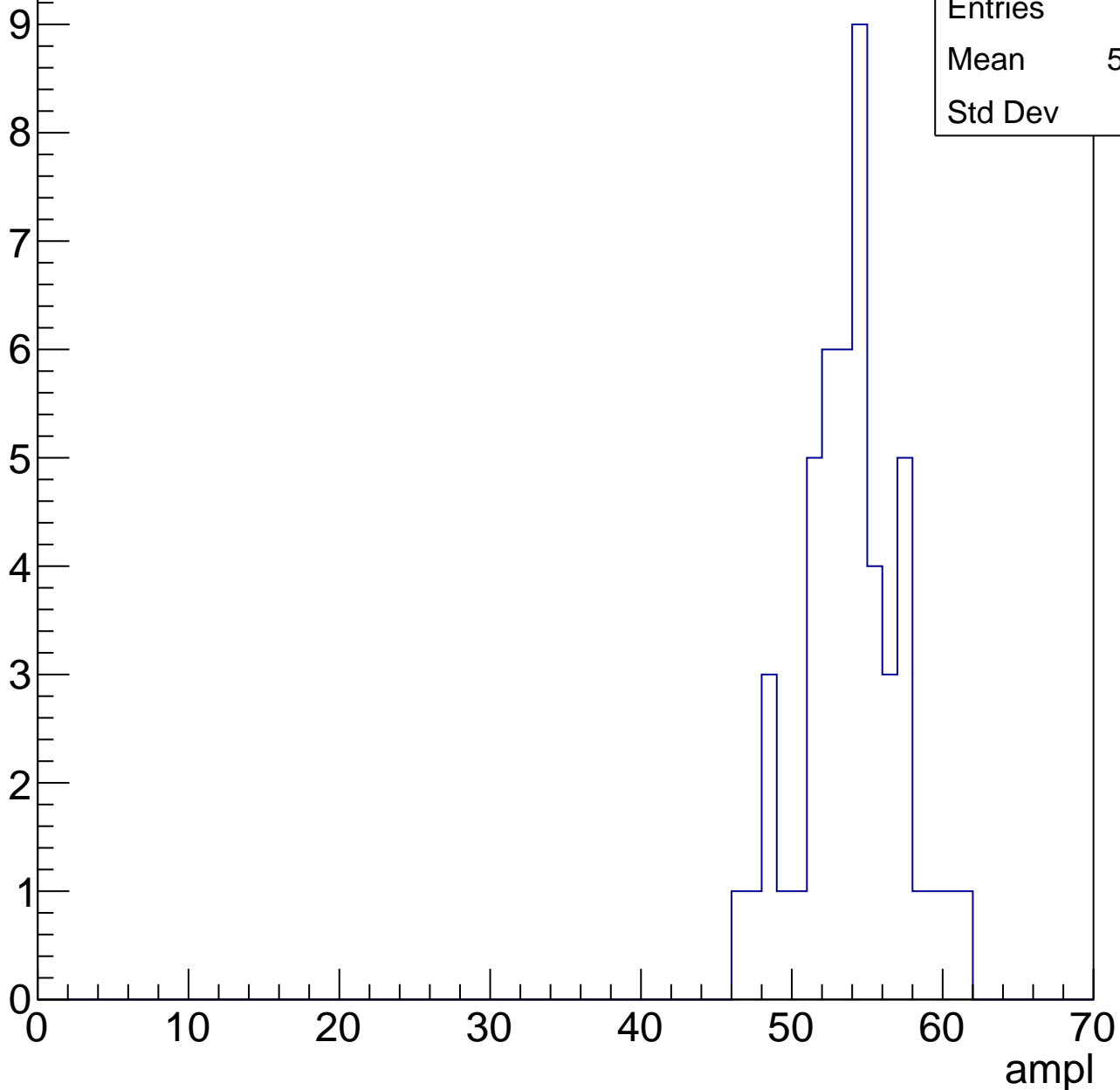


# B0L102S, U17-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	49
Mean	53.43
Std Dev	3.22

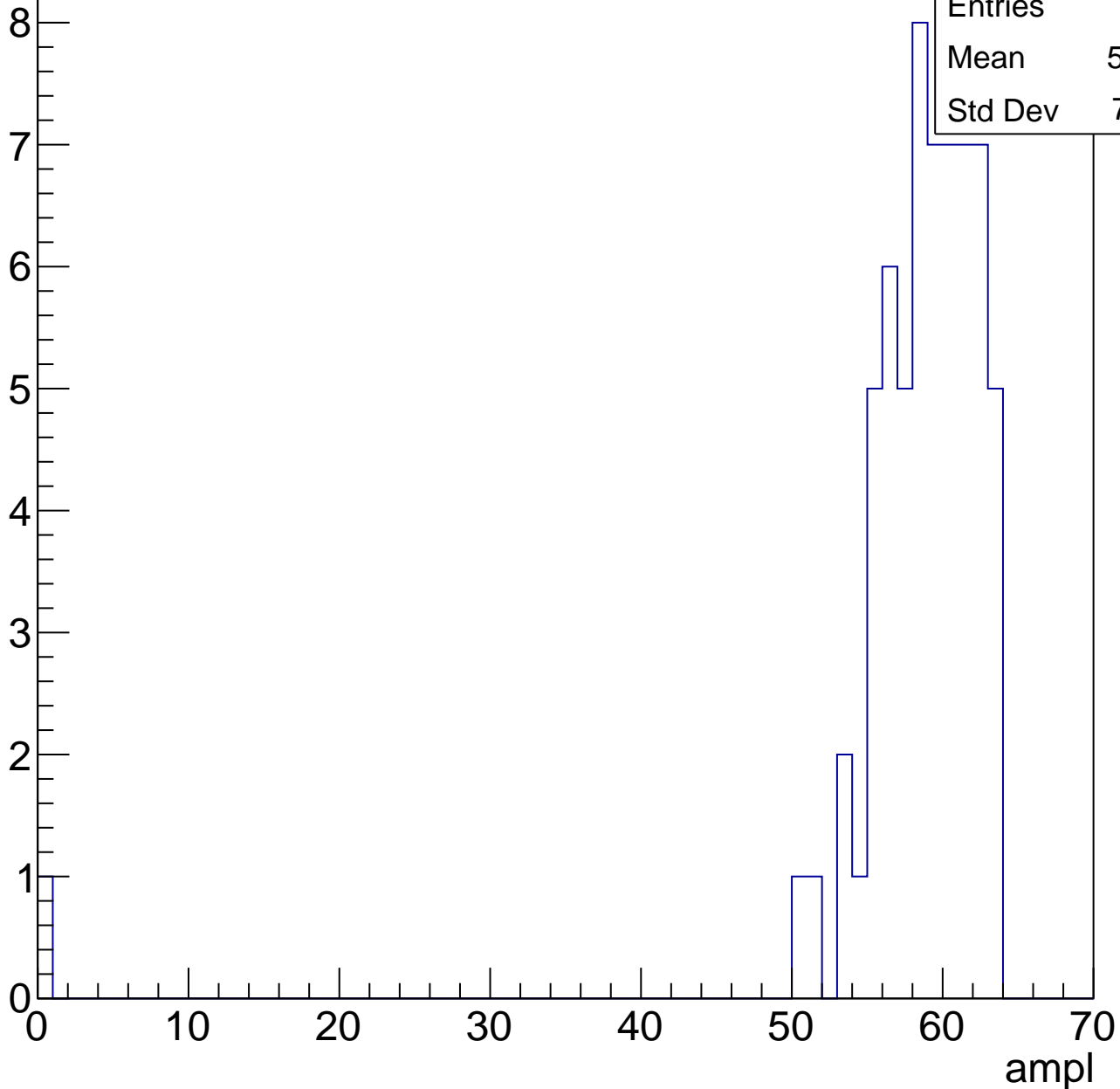


# B0L102S, U17-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

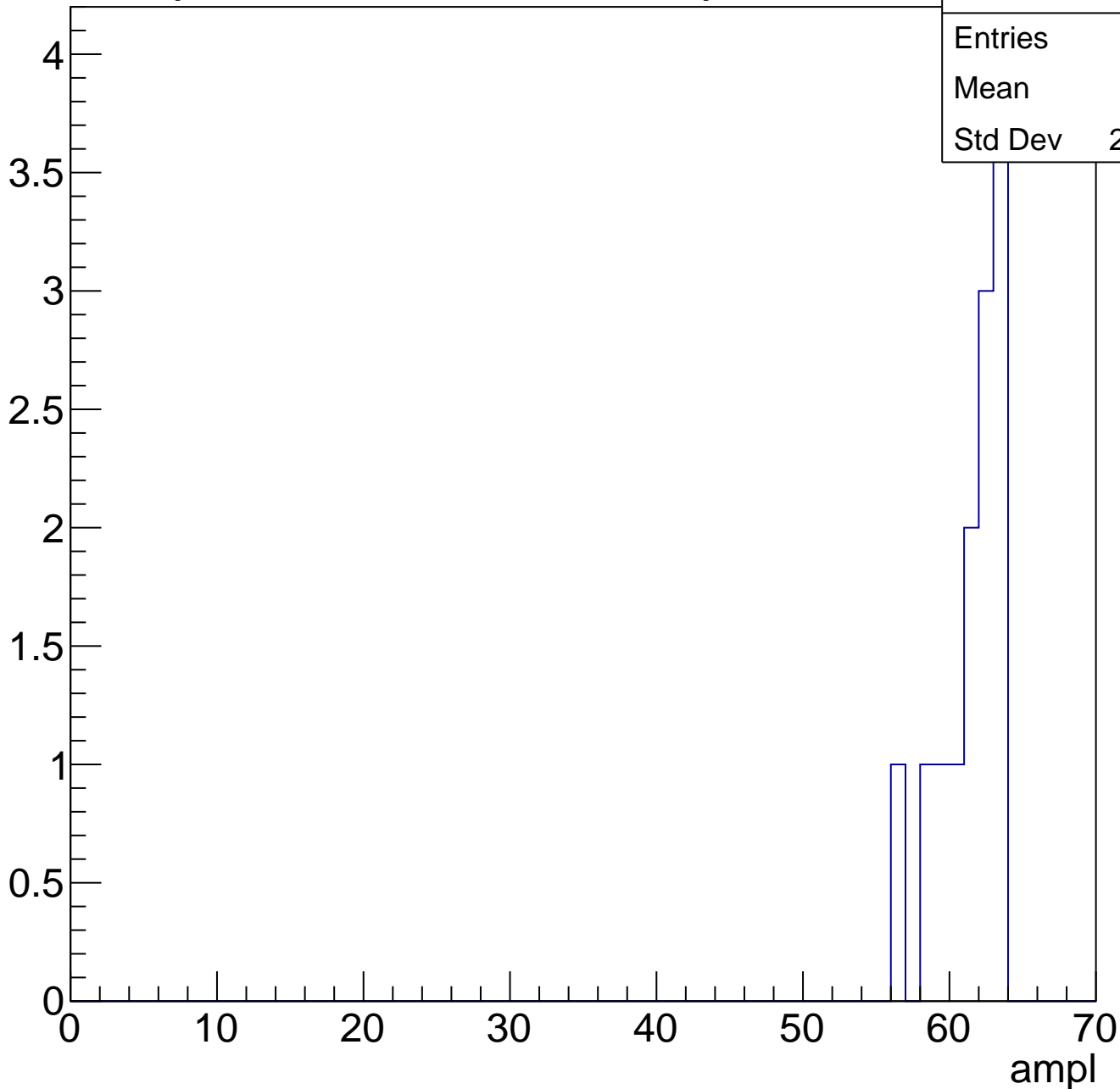
Entries	63
Mean	57.62
Std Dev	7.911



# B0L102S, U17-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

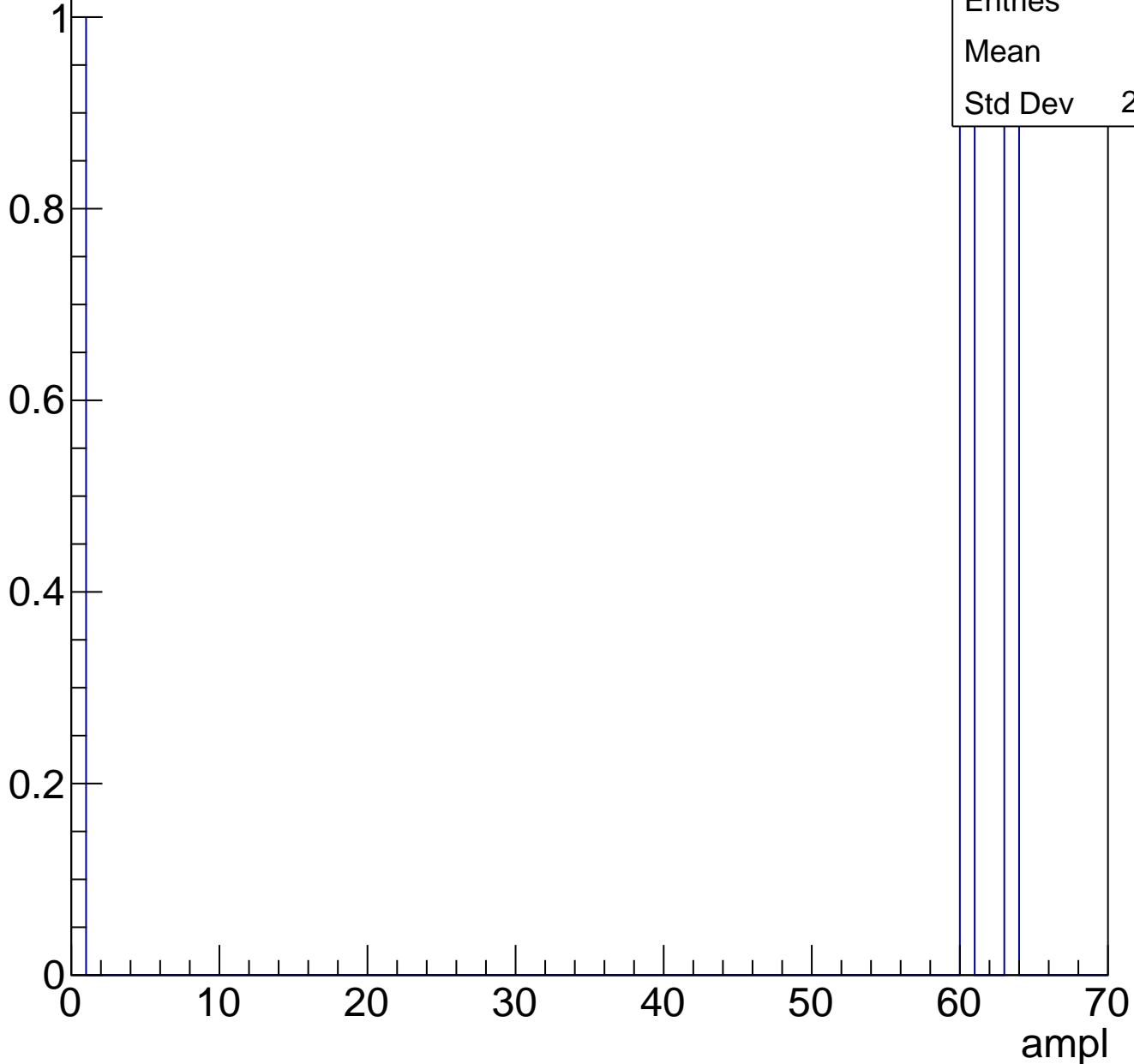




# B0L102S, U17-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



# B0L102S, U17-ch127, adc0

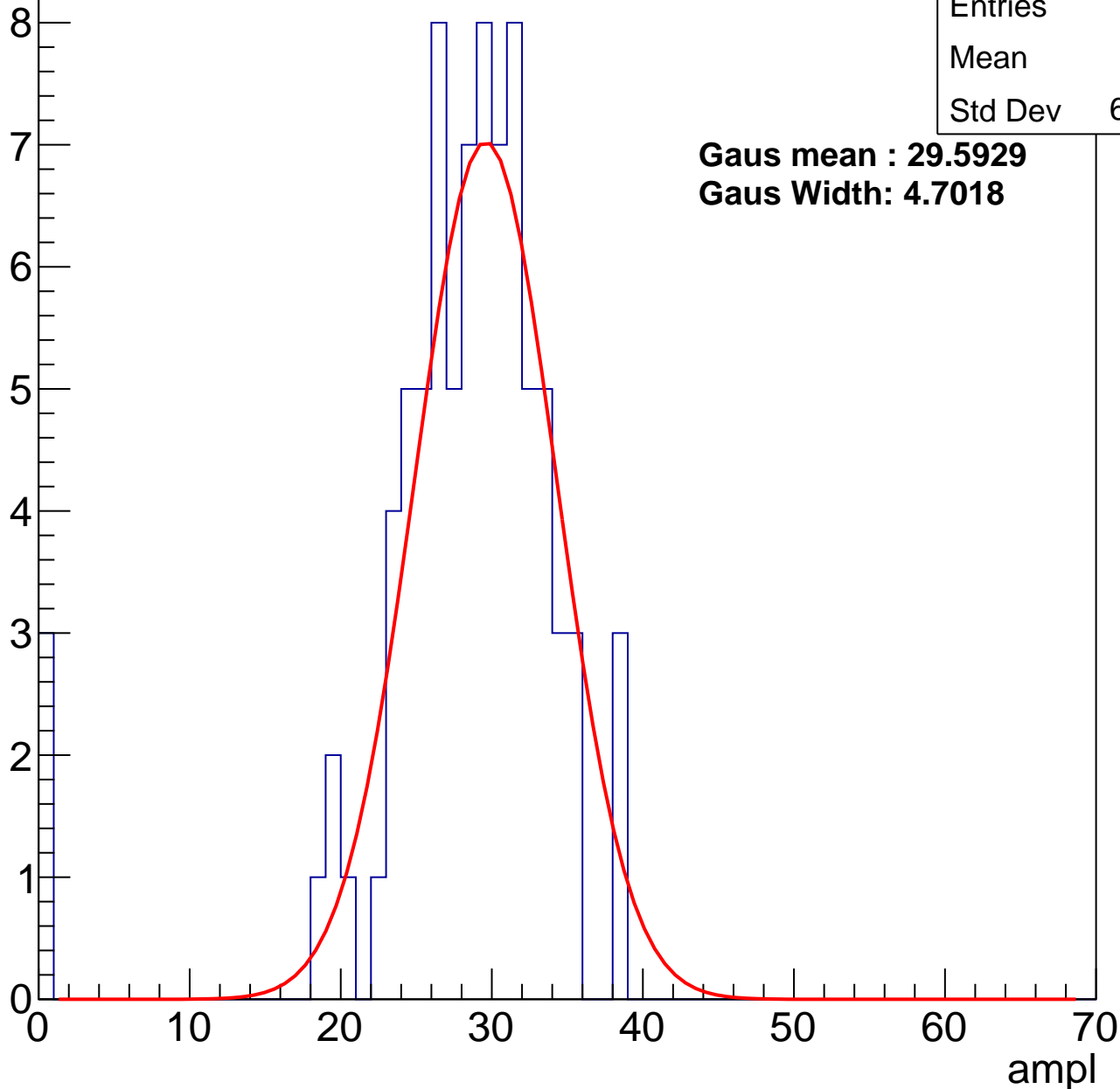
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	84
Mean	27.5
Std Dev	6.746

**Gaus mean : 29.5929**

**Gaus Width: 4.7018**



# B0L102S, U17-ch127, adc1

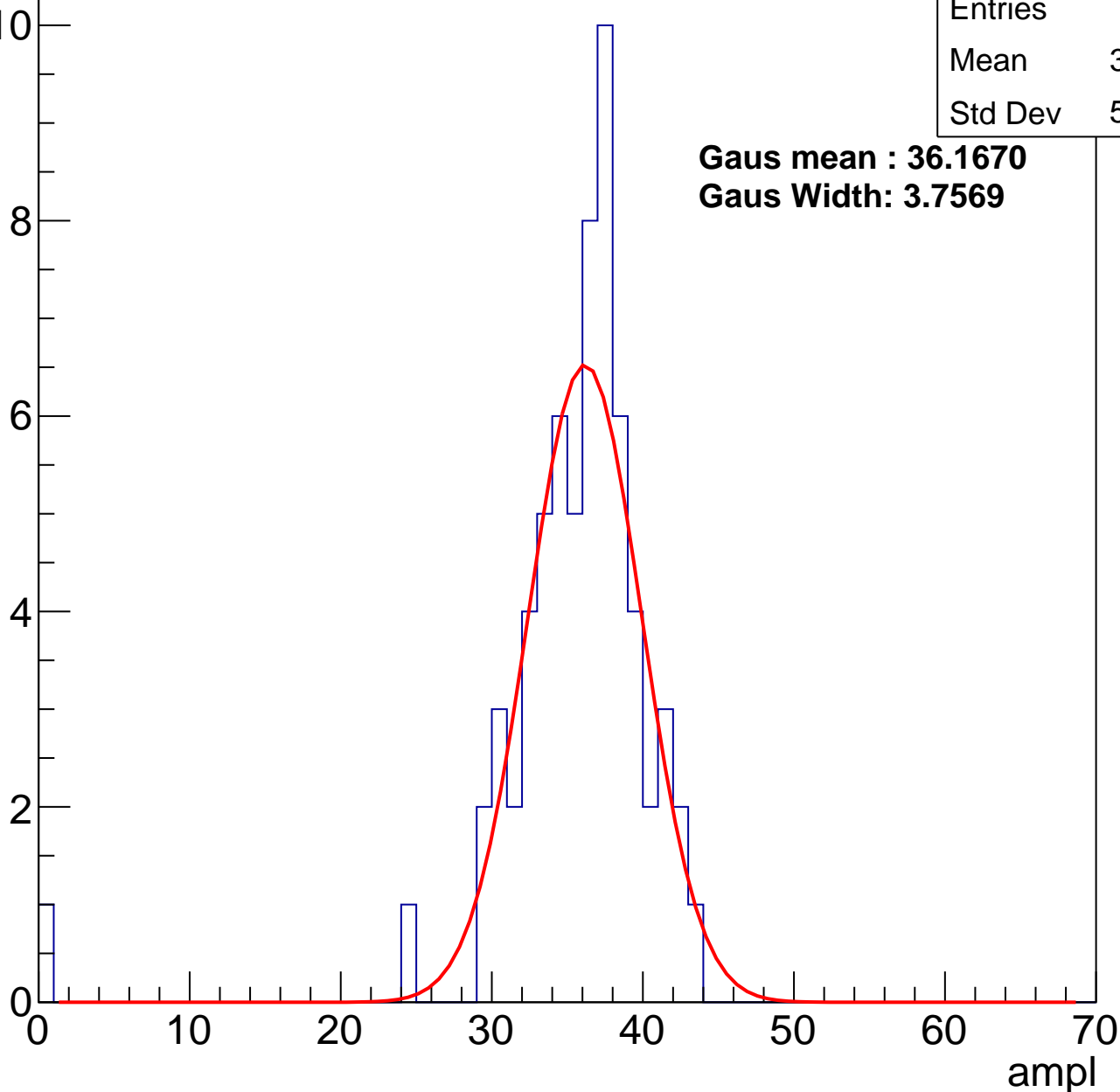
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	65
Mean	35.05
Std Dev	5.647

**Gaus mean : 36.1670**

**Gaus Width: 3.7569**



# B0L102S, U17-ch127, adc2

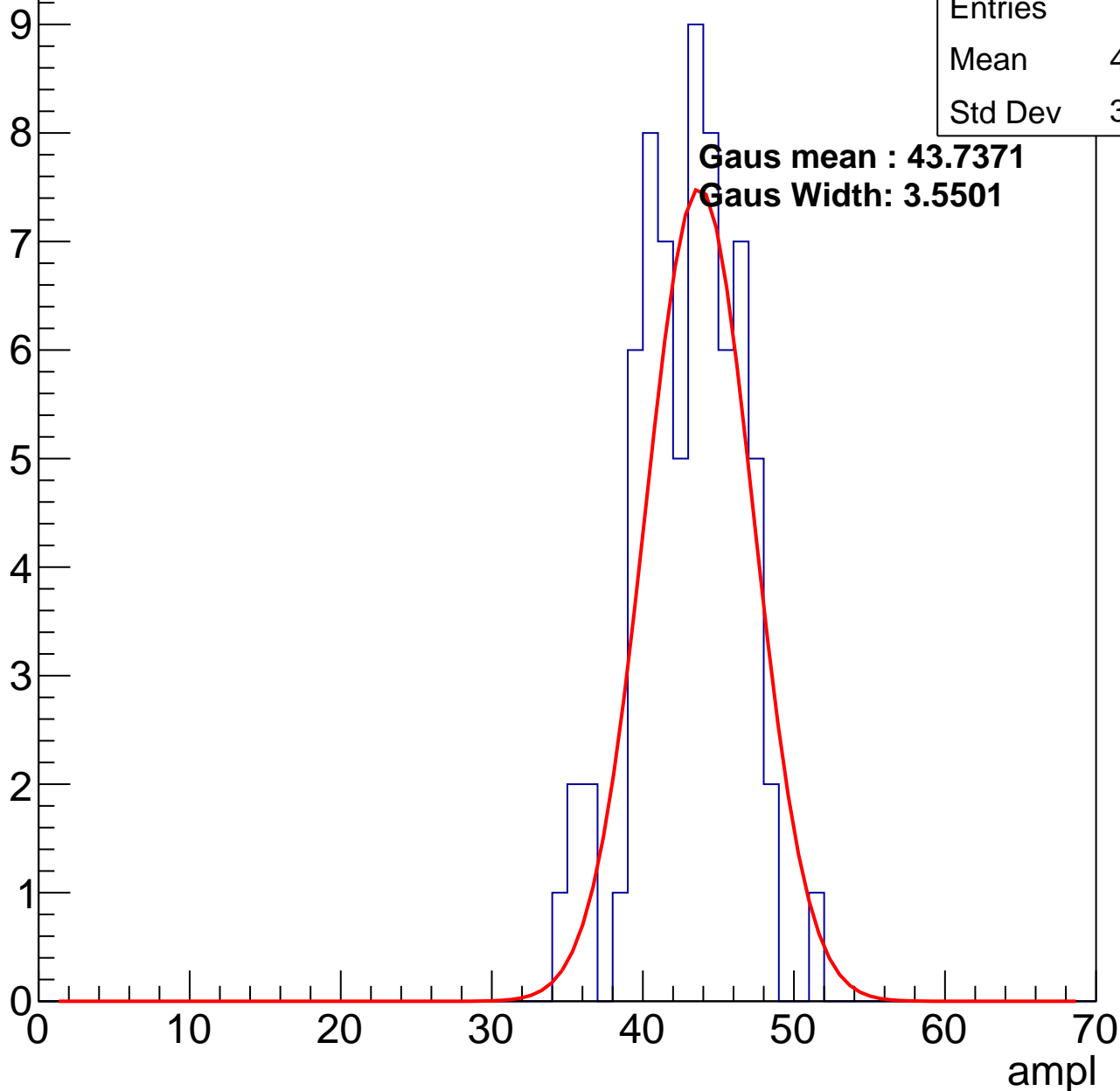
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	70
Mean	42.54
Std Dev	3.396

**Gaus mean : 43.7371**

**Gaus Width: 3.5501**

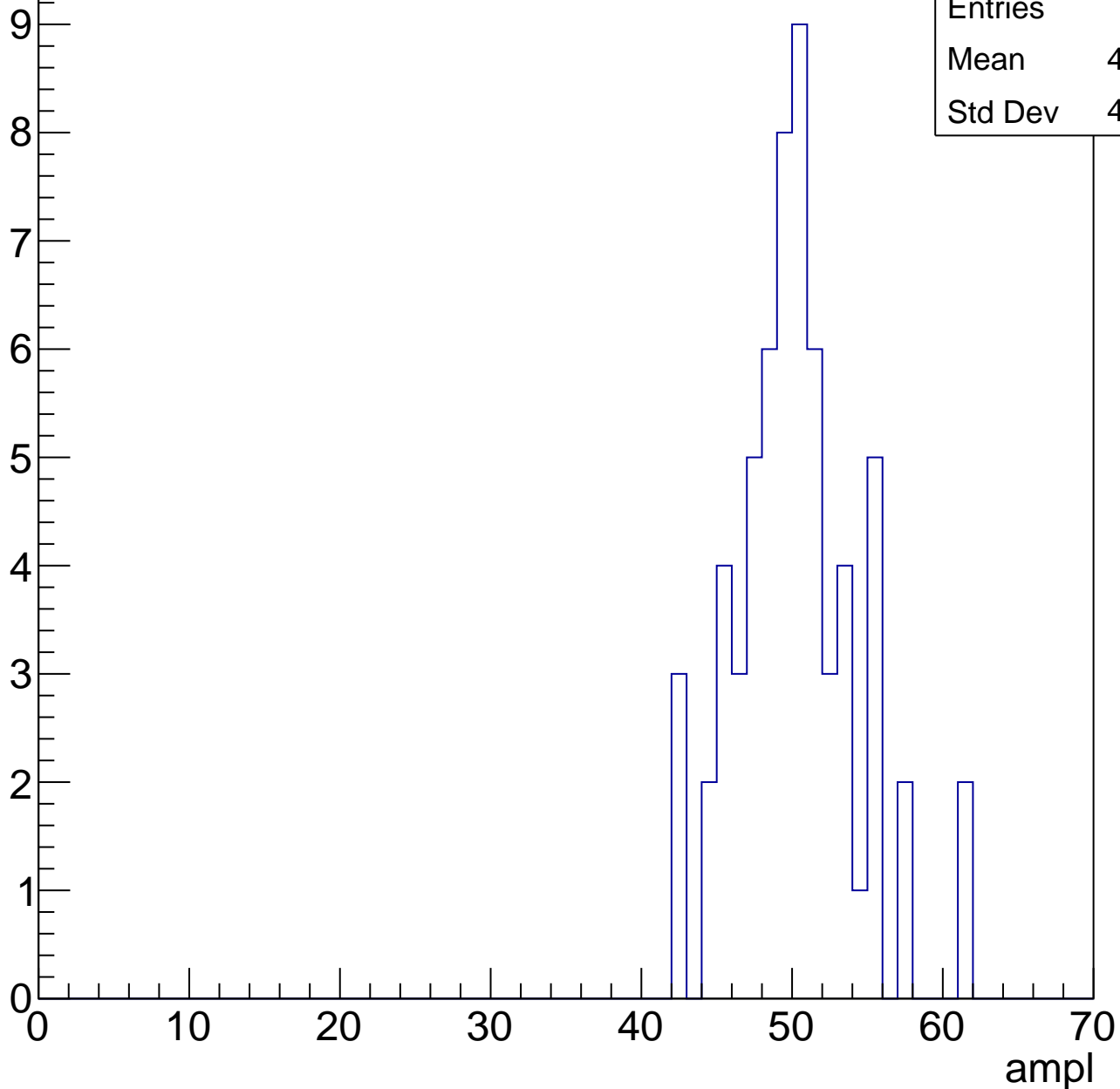


# B0L102S, U17-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.78
Std Dev	4.025

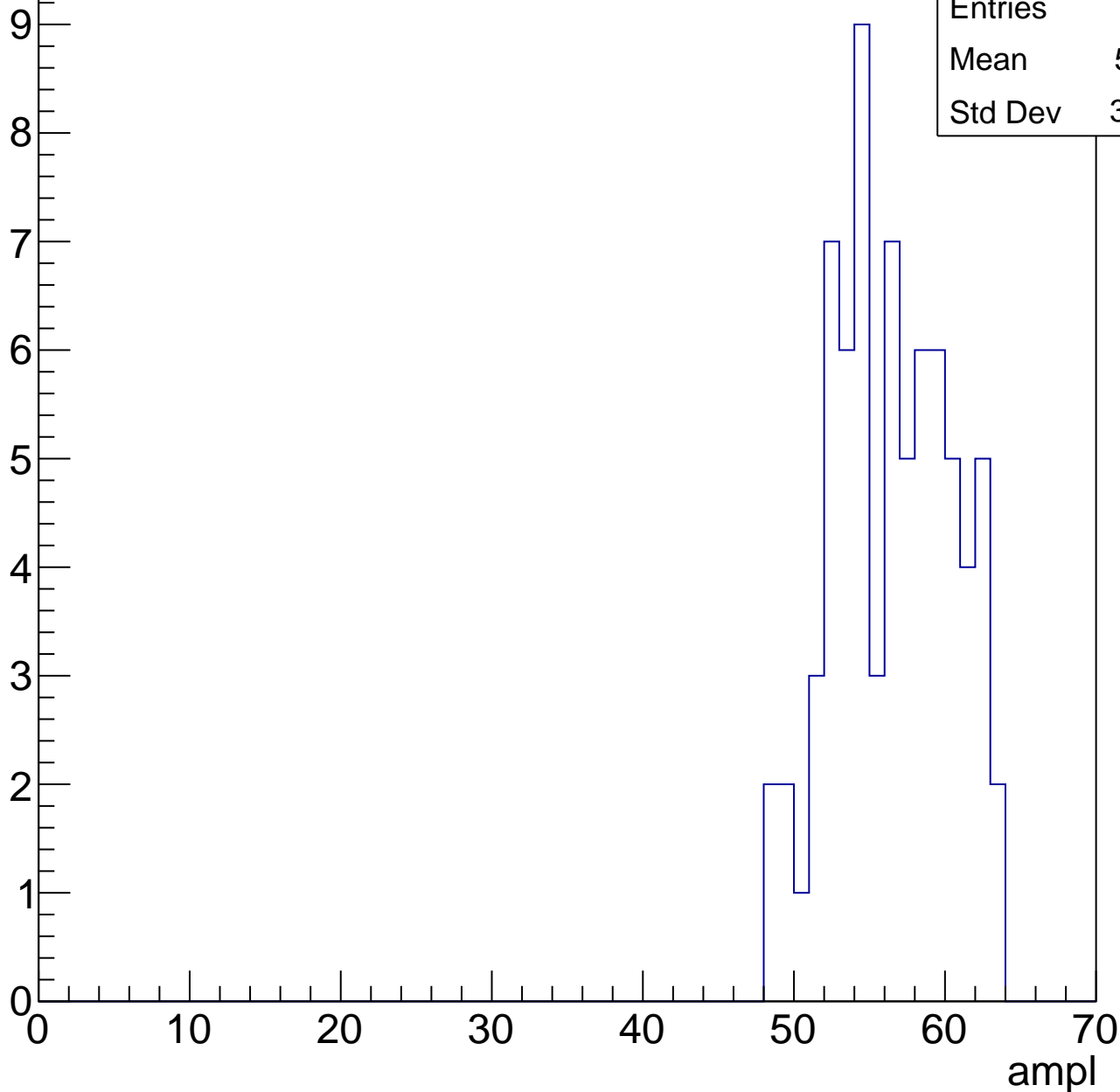


# B0L102S, U17-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

Entries	73
Mean	56.01
Std Dev	3.852

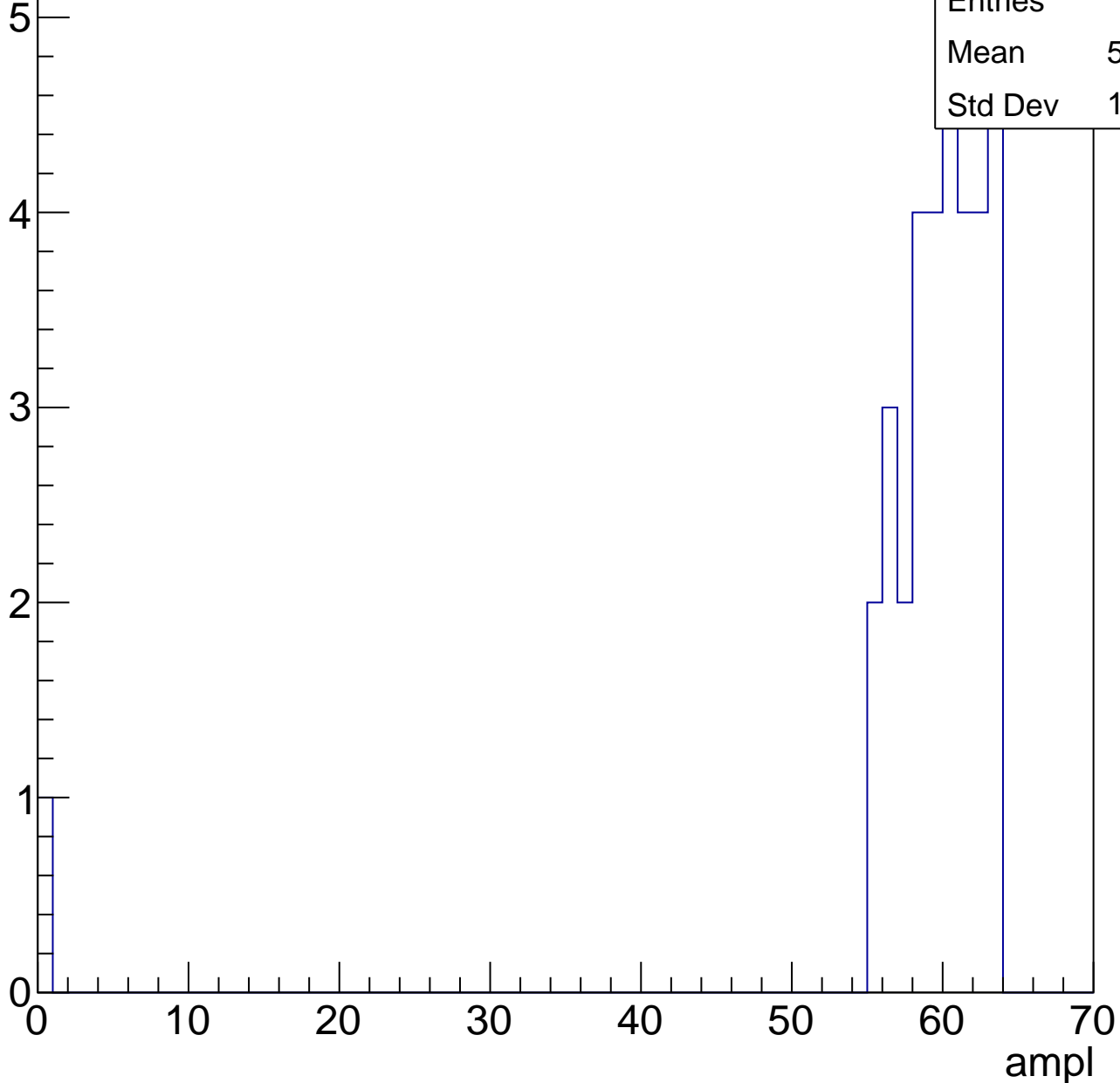


# B0L102S, U17-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry

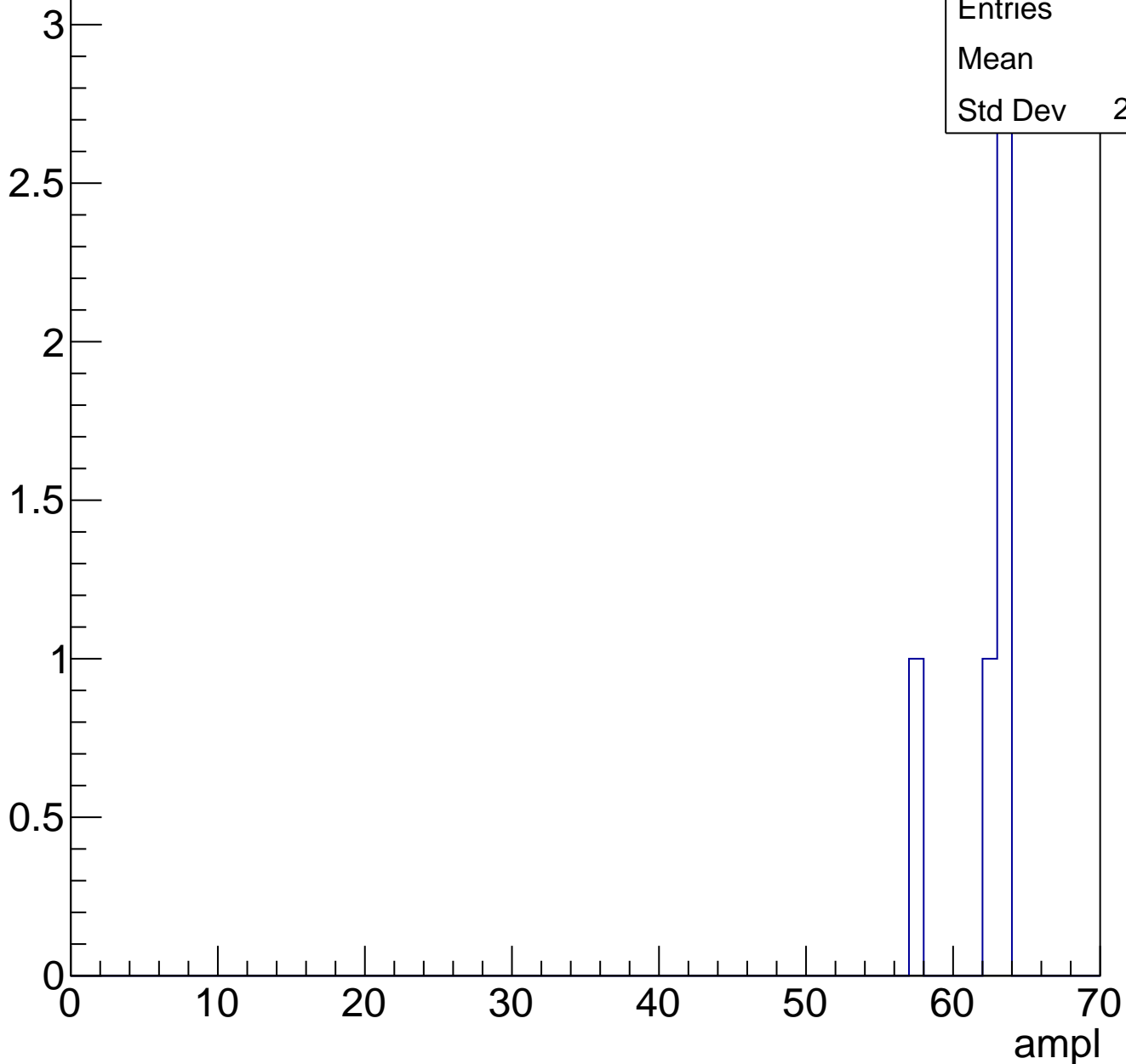
Entries	34
Mean	57.85
Std Dev	10.35



# B0L102S, U17-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry





# B0L102S, U17-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

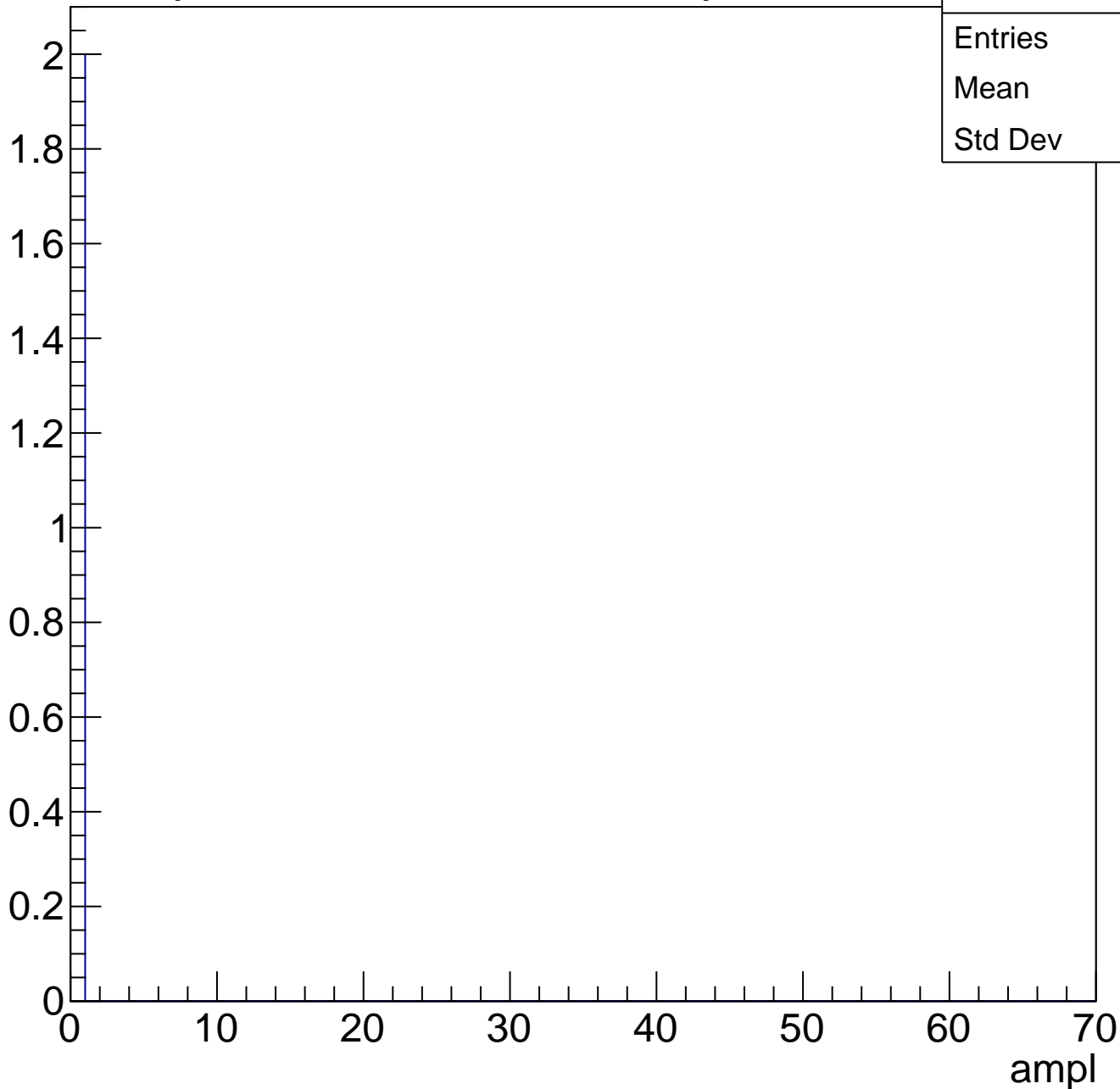
Entry



# B0L102S, U17-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0