



# B1L102S, U24-ch0, adc0

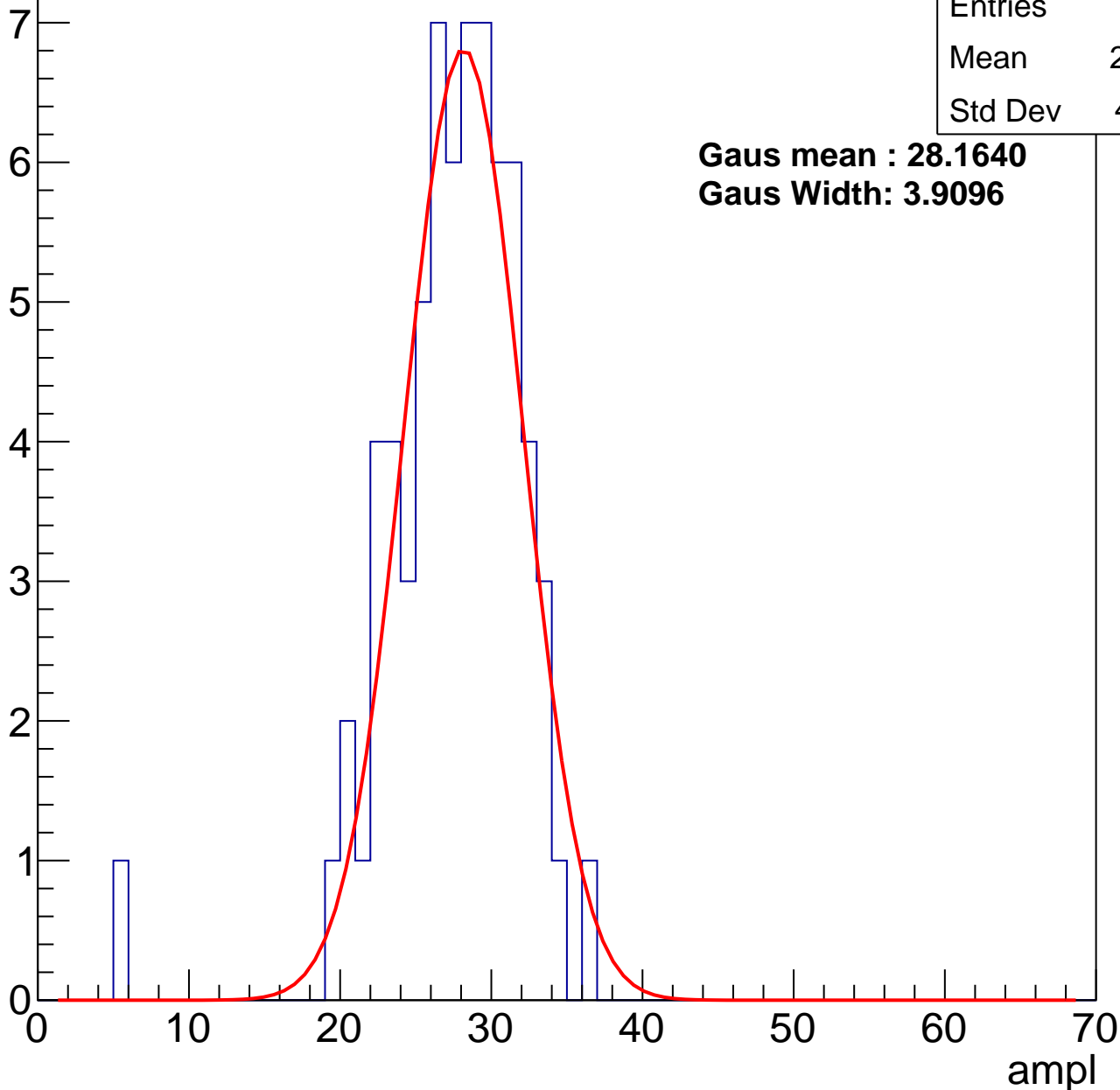
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.07
Std Dev	4.541

**Gaus mean : 28.1640**

**Gaus Width: 3.9096**



# B1L102S, U24-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	34.6
Std Dev	6.13

**Gaus mean : 34.9913**

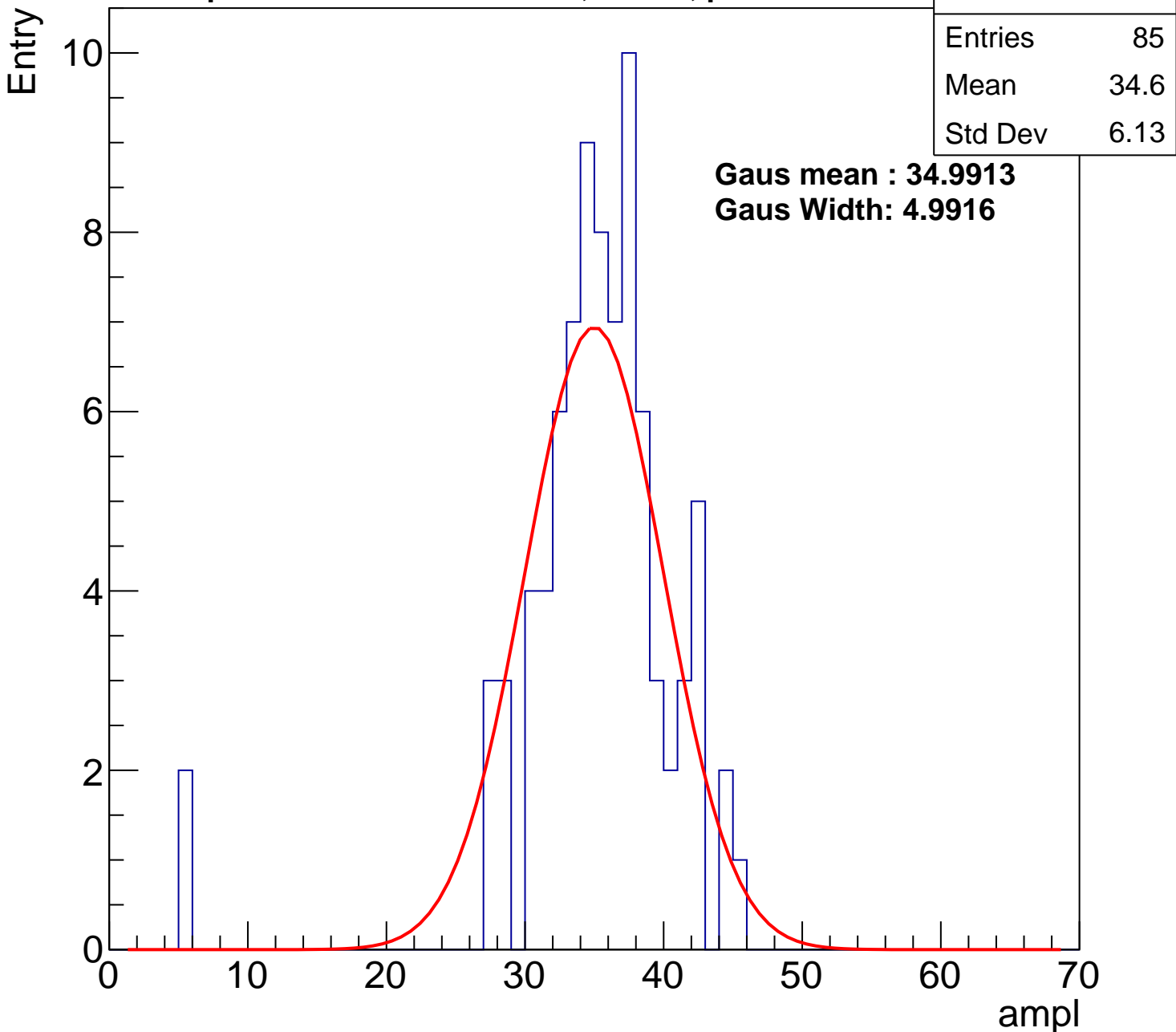
**Gaus Width: 4.9916**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch0, adc2

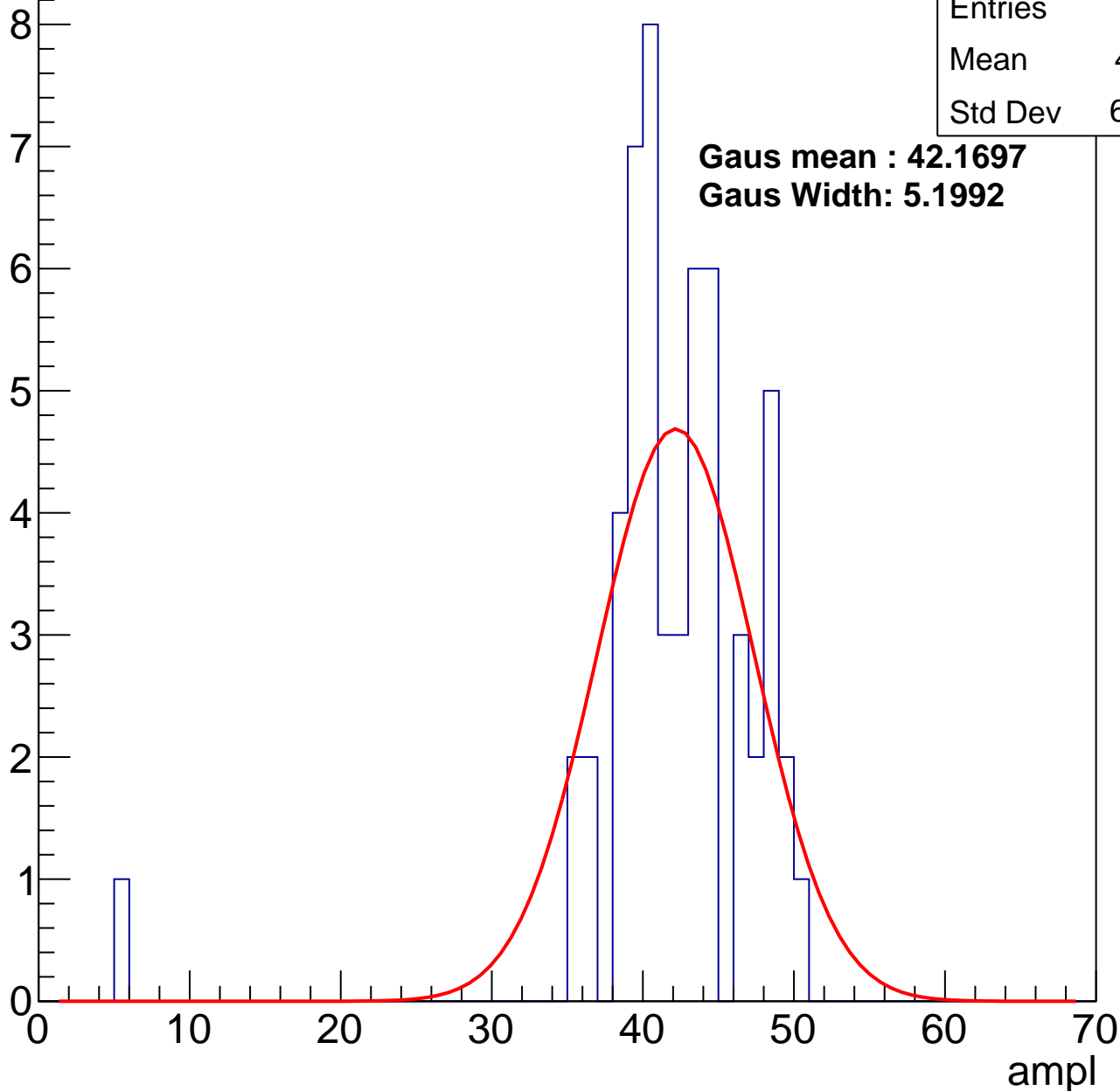
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.51
Std Dev	6.264

**Gaus mean : 42.1697**

**Gaus Width: 5.1992**

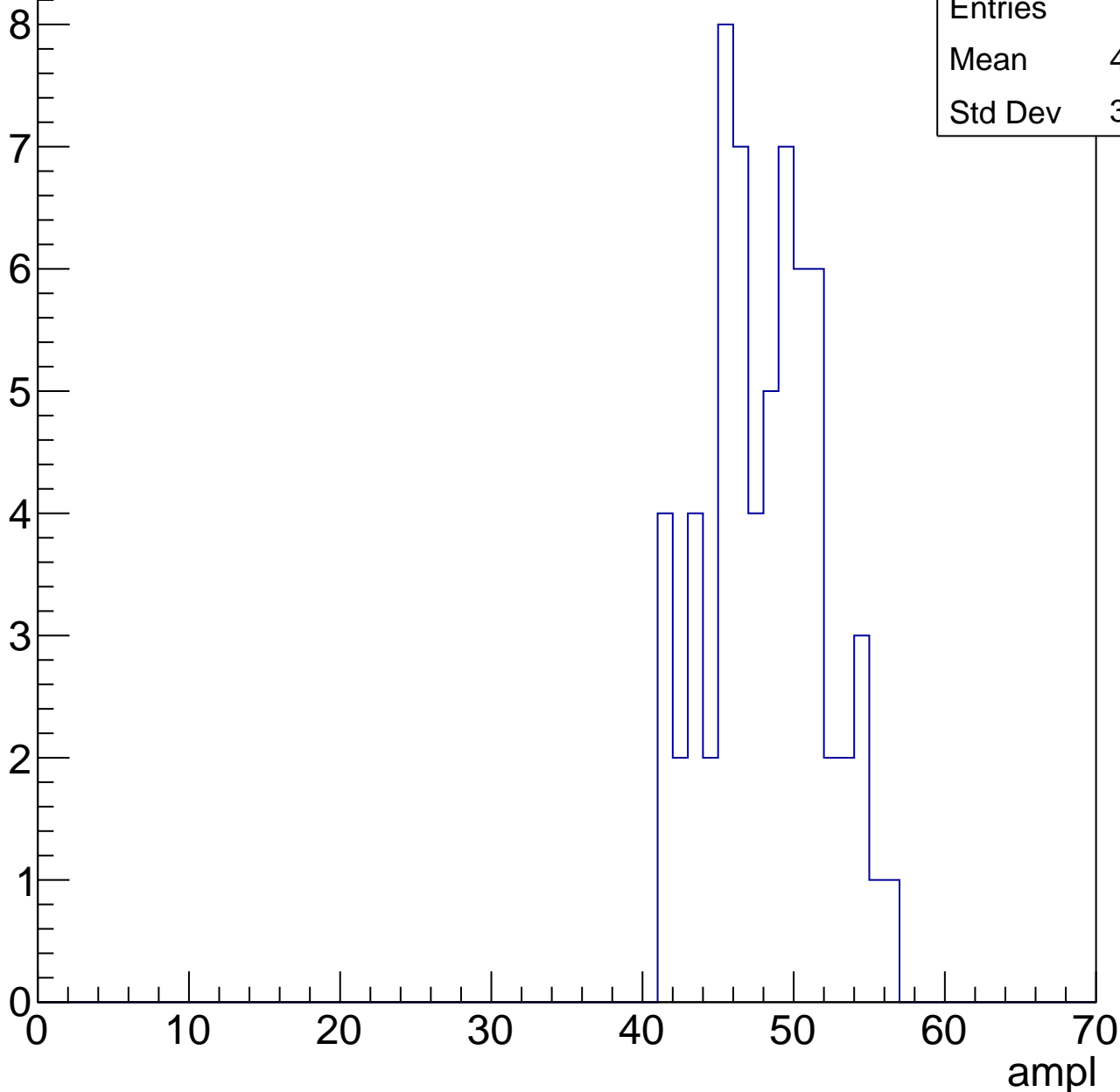


# B1L102S, U24-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

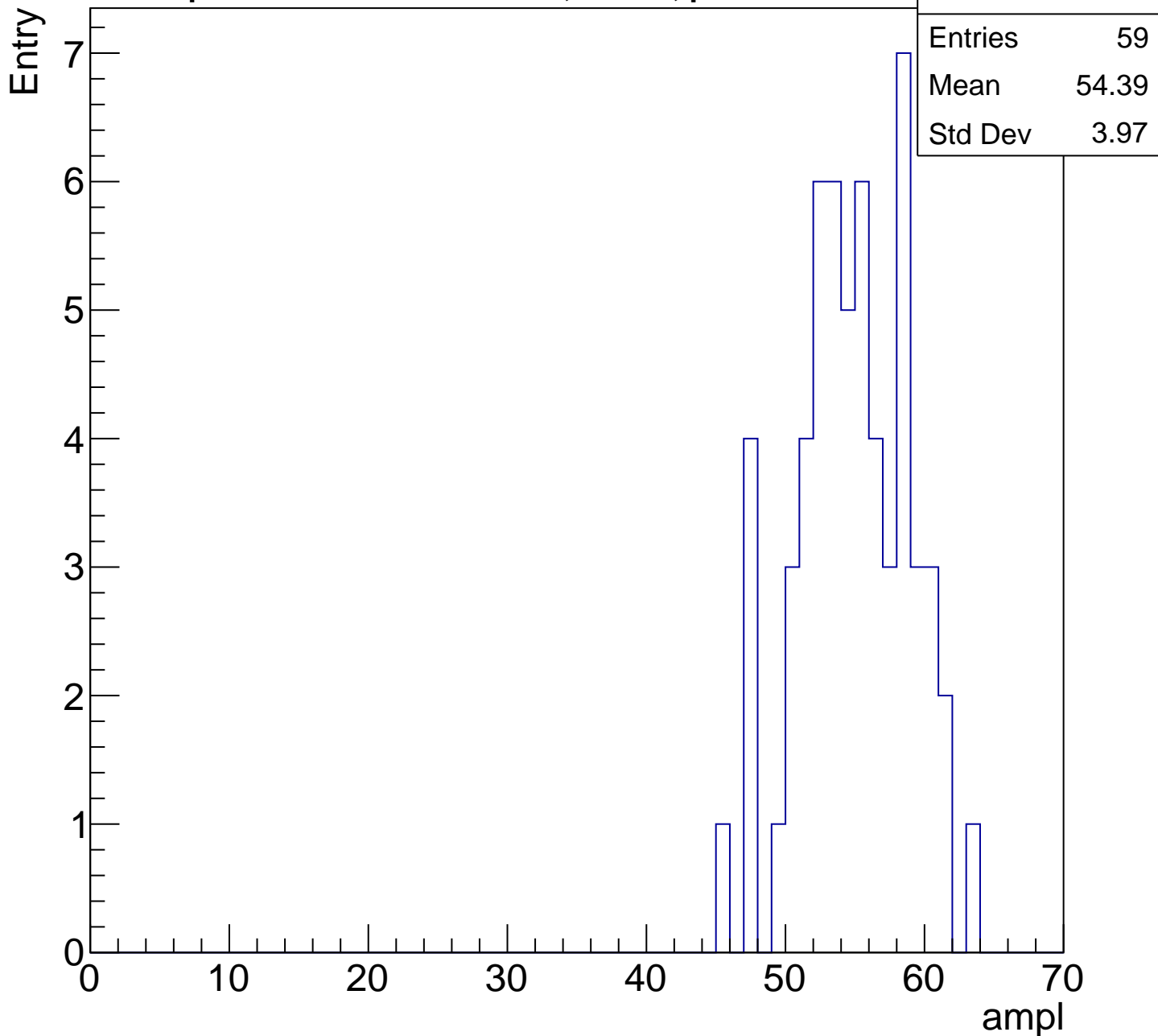
Entry

Entries	64
Mean	47.66
Std Dev	3.717



# B1L102S, U24-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

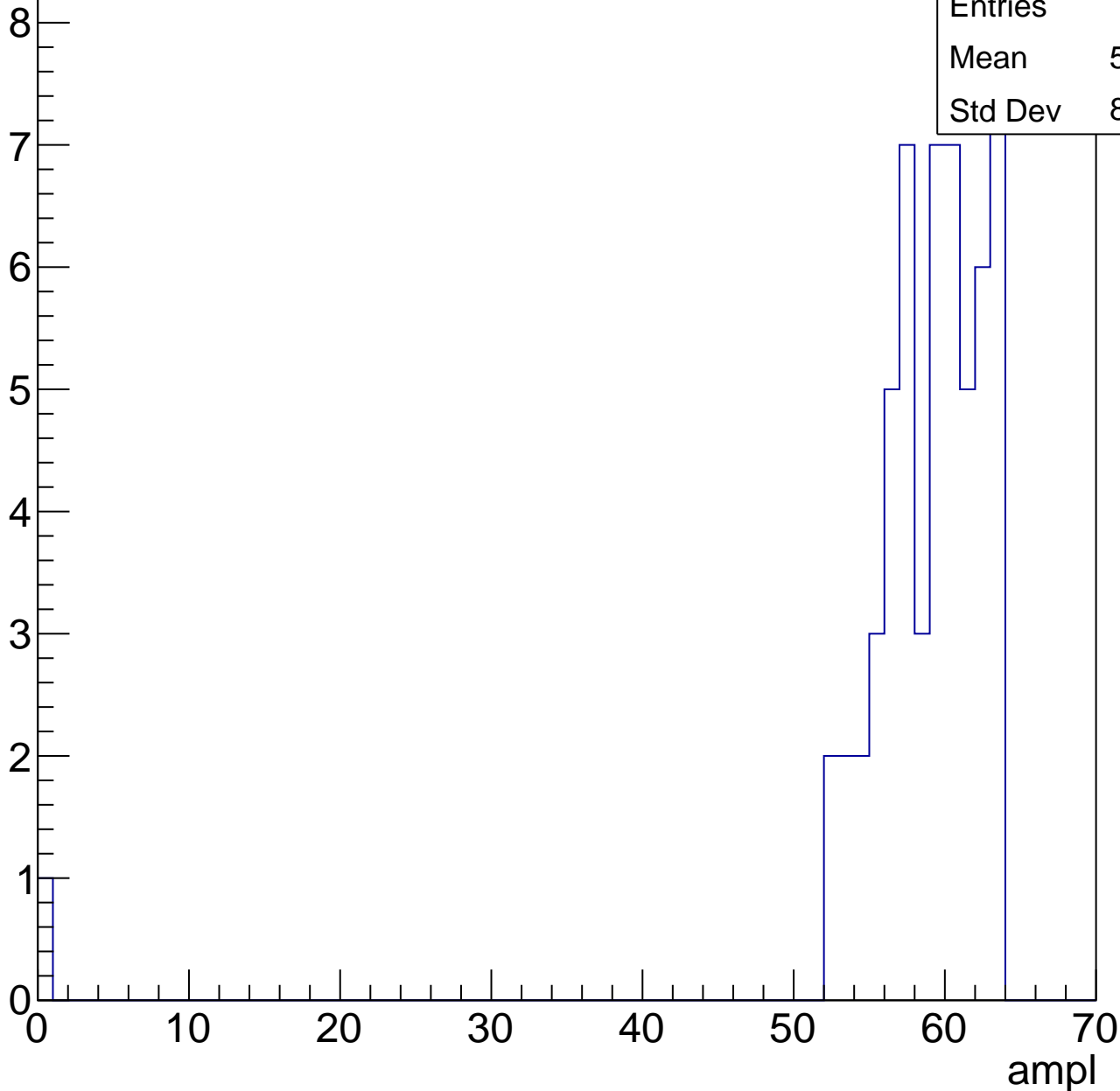


# B1L102S, U24-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

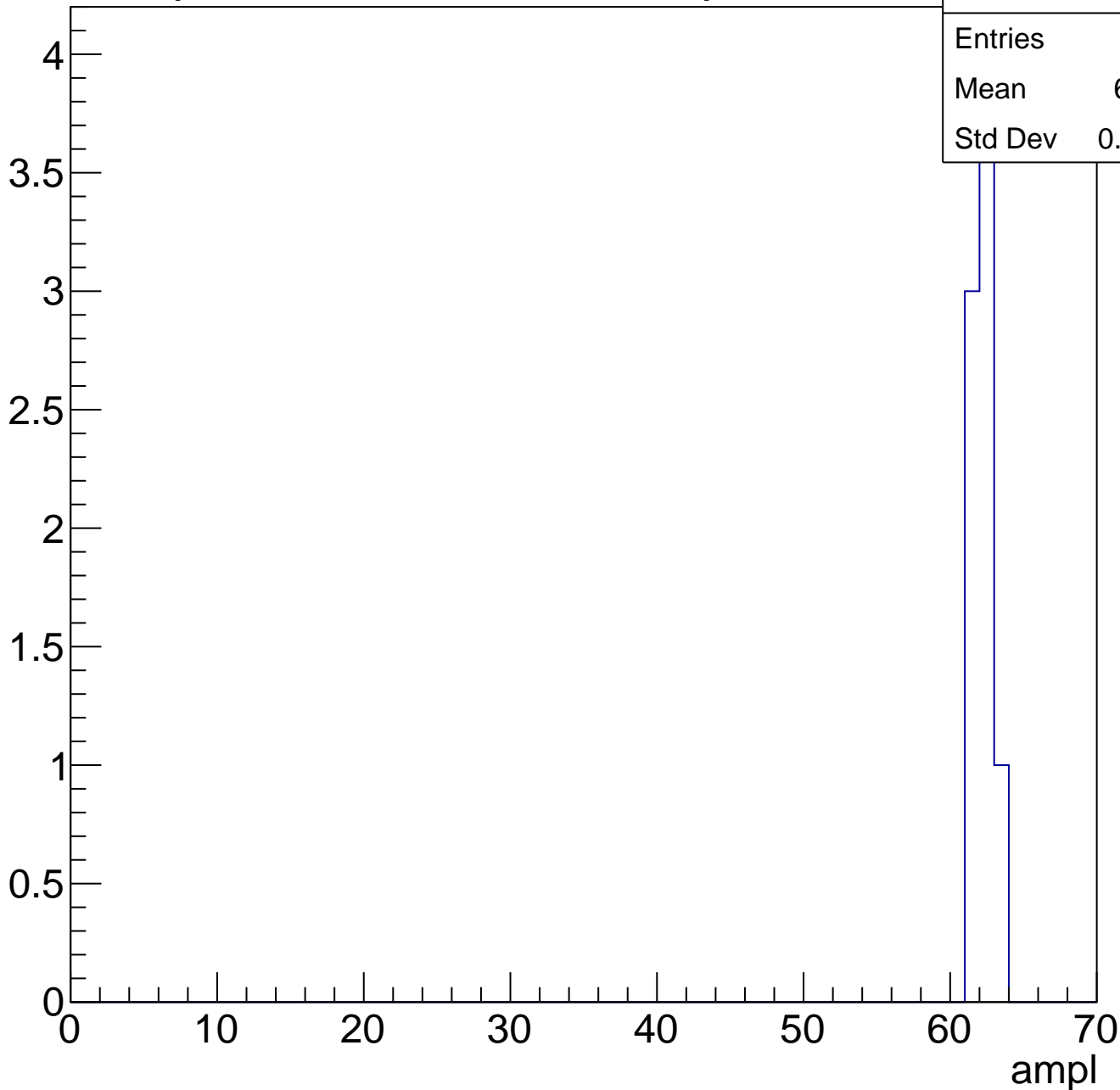
Entries	58
Mean	57.76
Std Dev	8.247



# B1L102S, U24-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch1, adc0

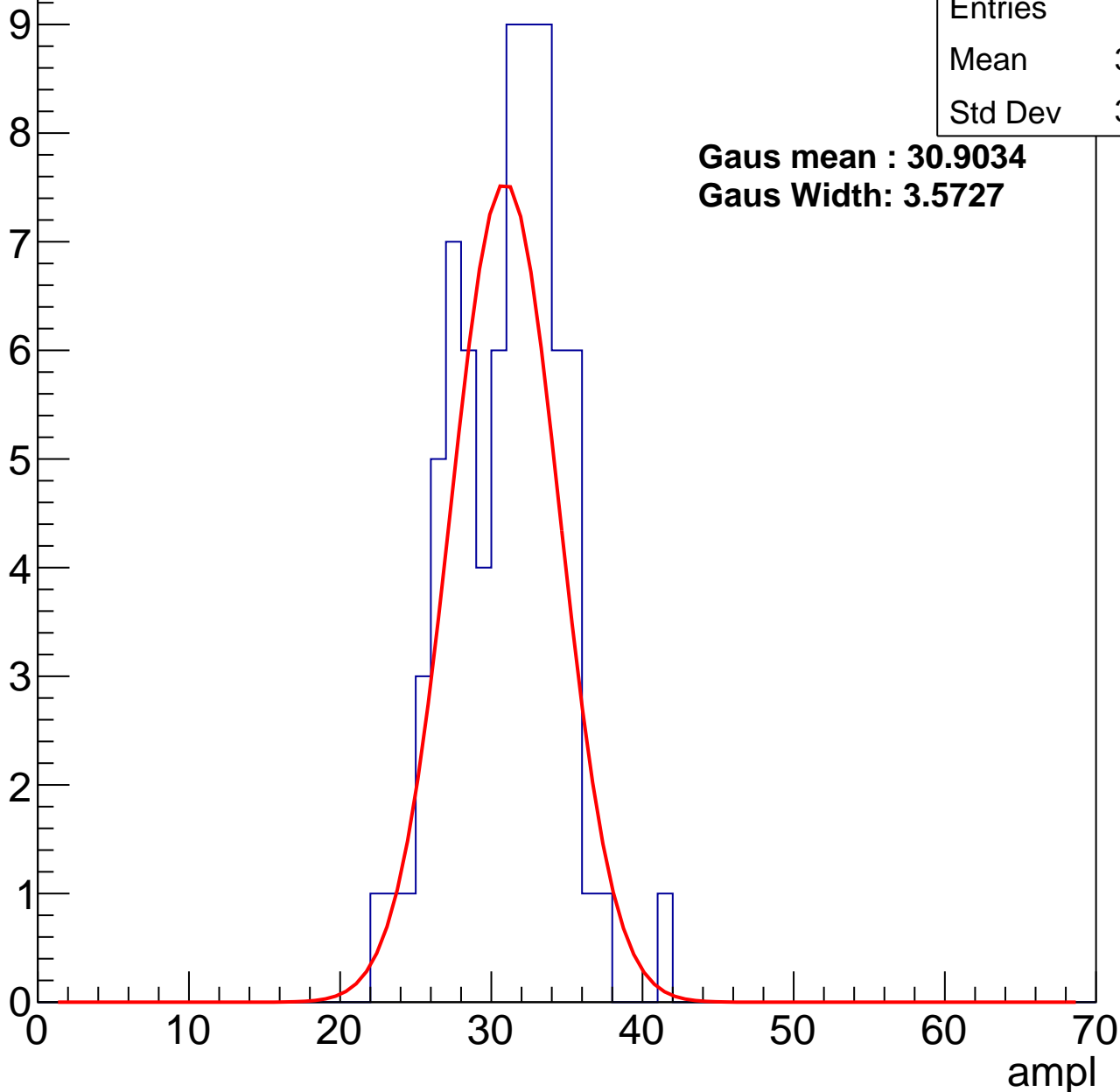
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.51
Std Dev	3.541

**Gaus mean : 30.9034**

**Gaus Width: 3.5727**



# B1L102S, U24-ch1, adc1

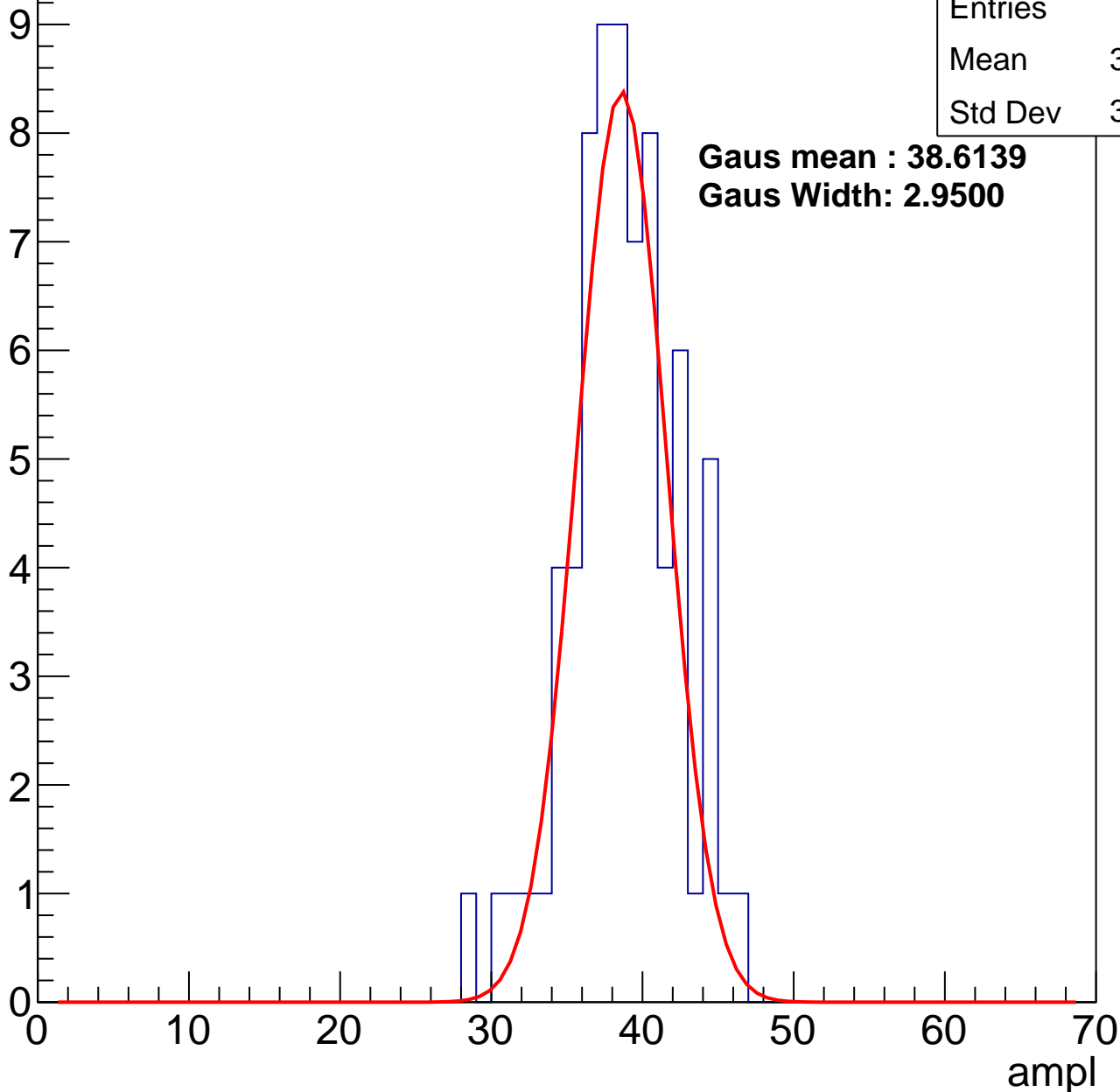
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	38.28
Std Dev	3.532

**Gaus mean : 38.6139**

**Gaus Width: 2.9500**



# B1L102S, U24-ch1, adc2

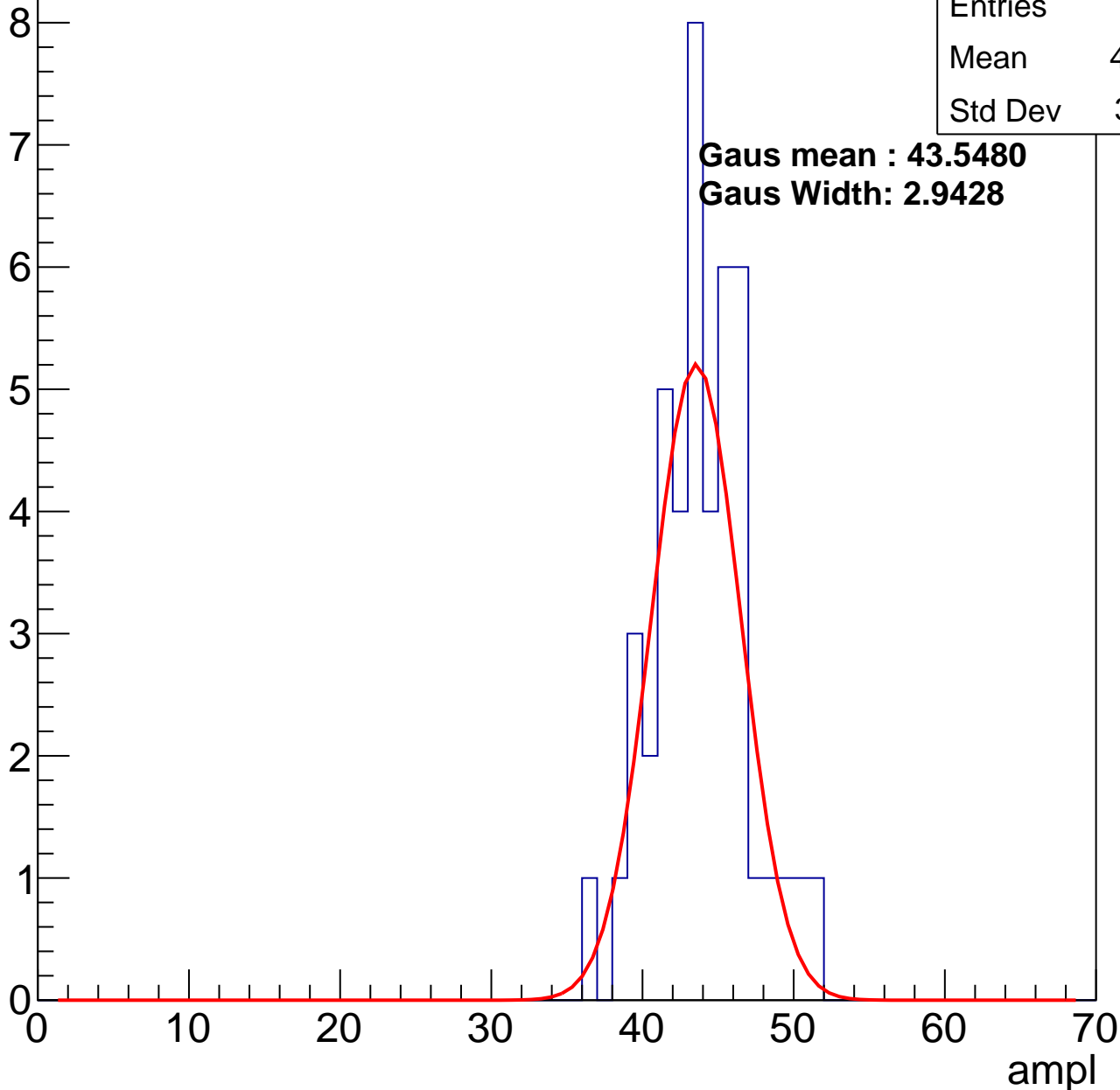
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	43.44
Std Dev	3.081

**Gaus mean : 43.5480**

**Gaus Width: 2.9428**

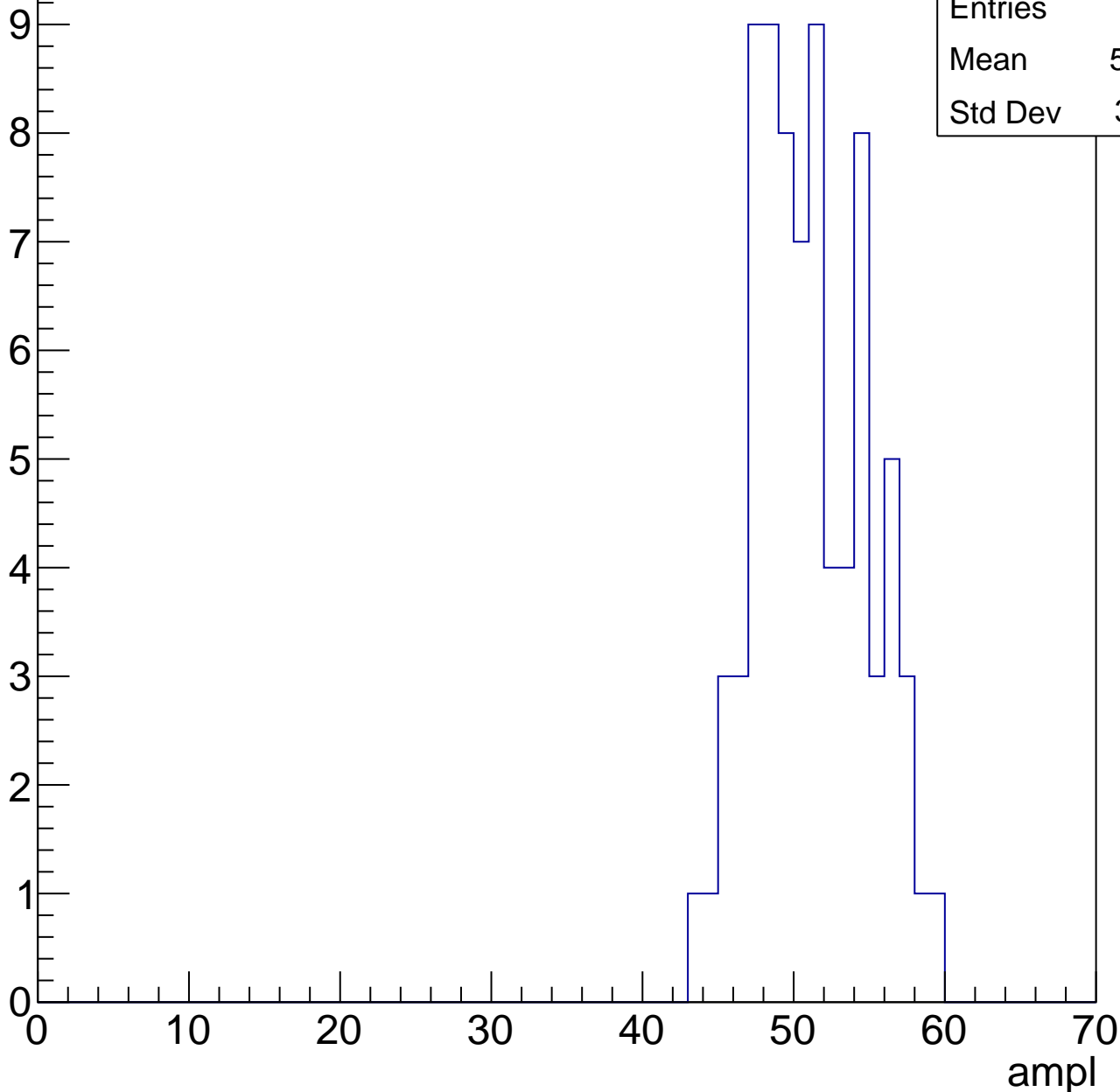


# B1L102S, U24-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50.65
Std Dev	3.621

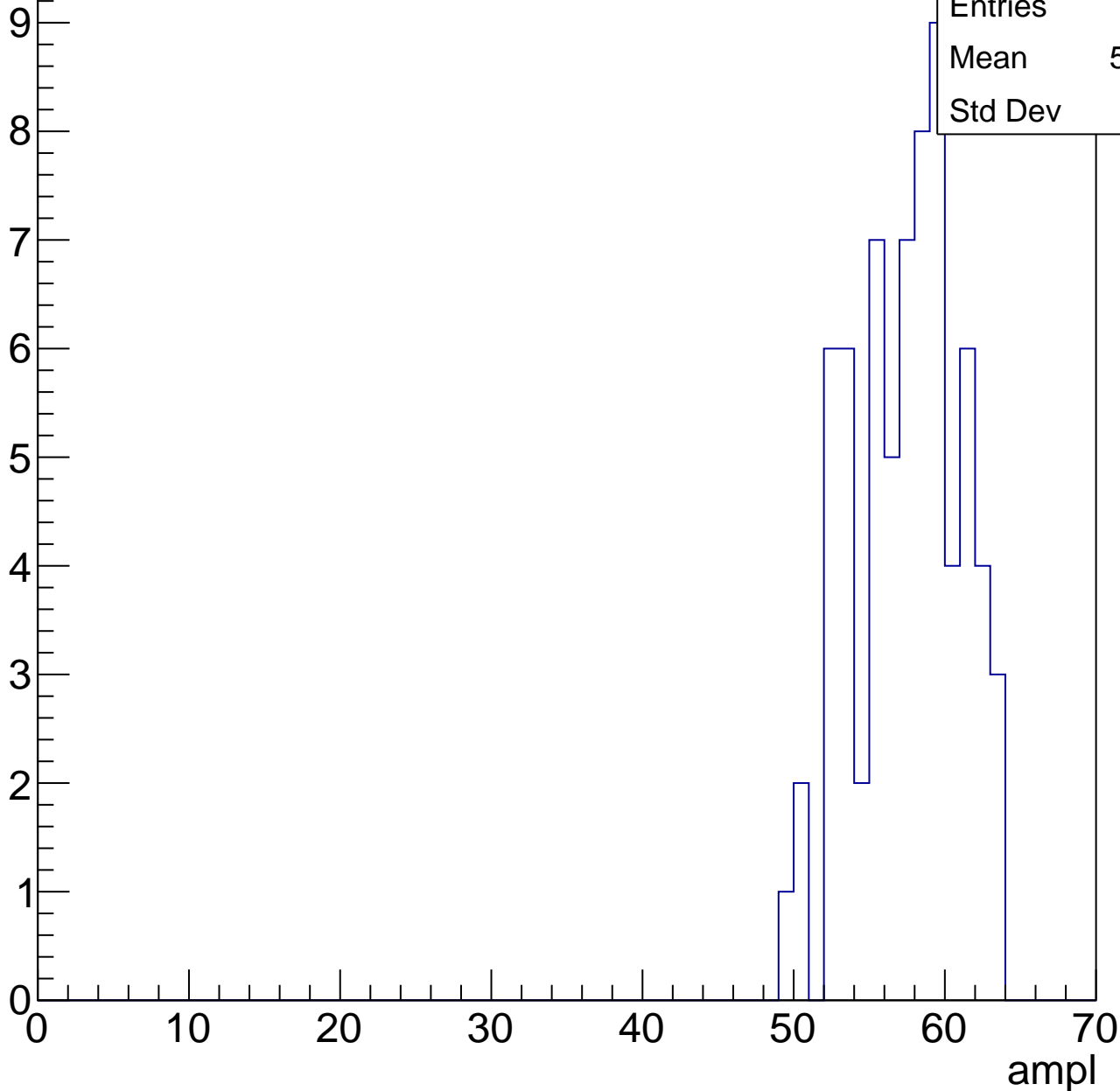


# B1L102S, U24-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	56.99
Std Dev	3.47

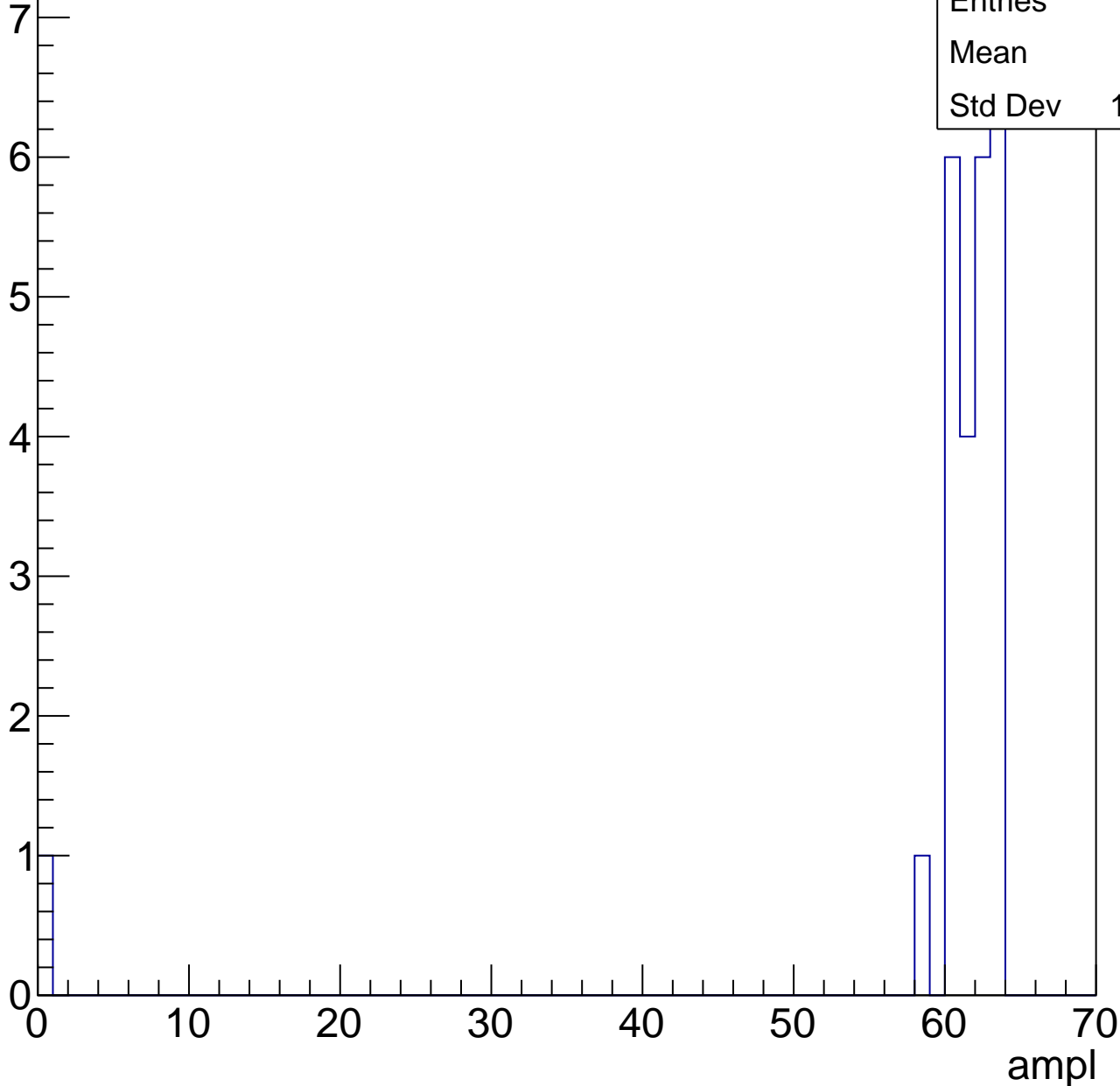


# B1L102S, U24-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	25
Mean	59
Std Dev	12.12



# B1L102S, U24-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0



# B1L102S, U24-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch2, adc0

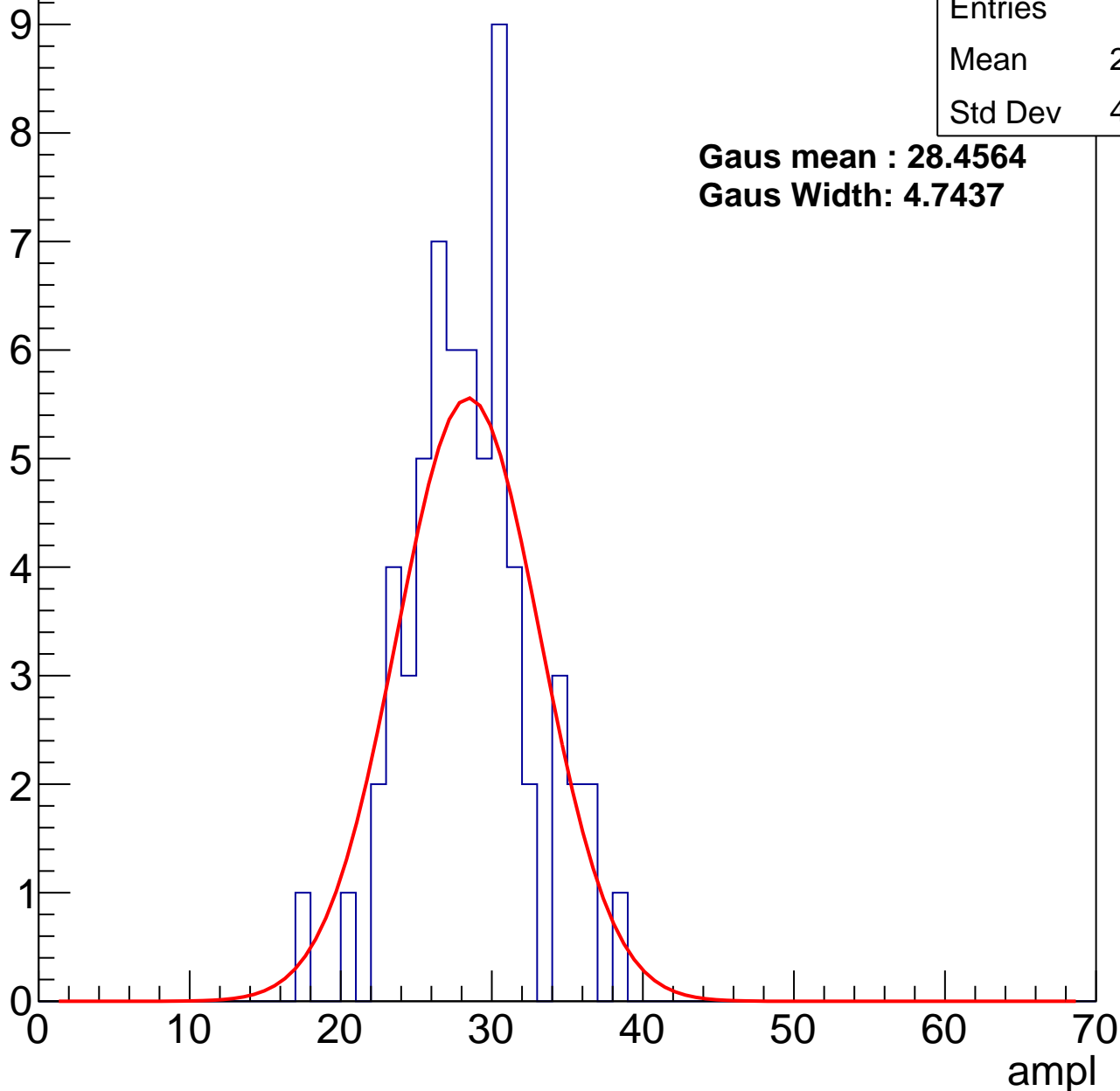
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.05
Std Dev	4.037

**Gaus mean : 28.4564**

**Gaus Width: 4.7437**



# B1L102S, U24-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	34.9
Std Dev	3.846

**Gaus mean : 35.7025**

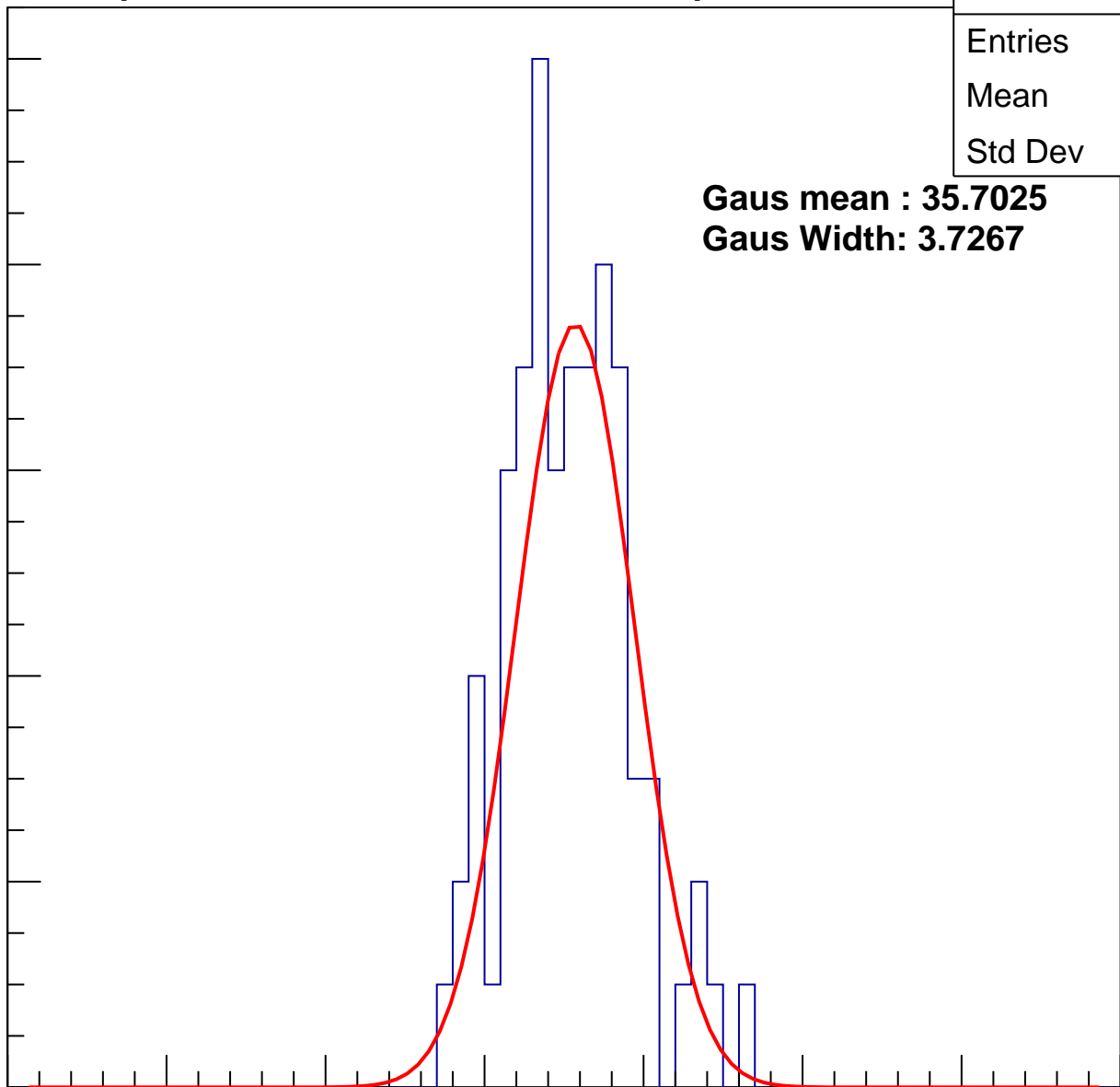
**Gaus Width: 3.7267**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch2, adc2

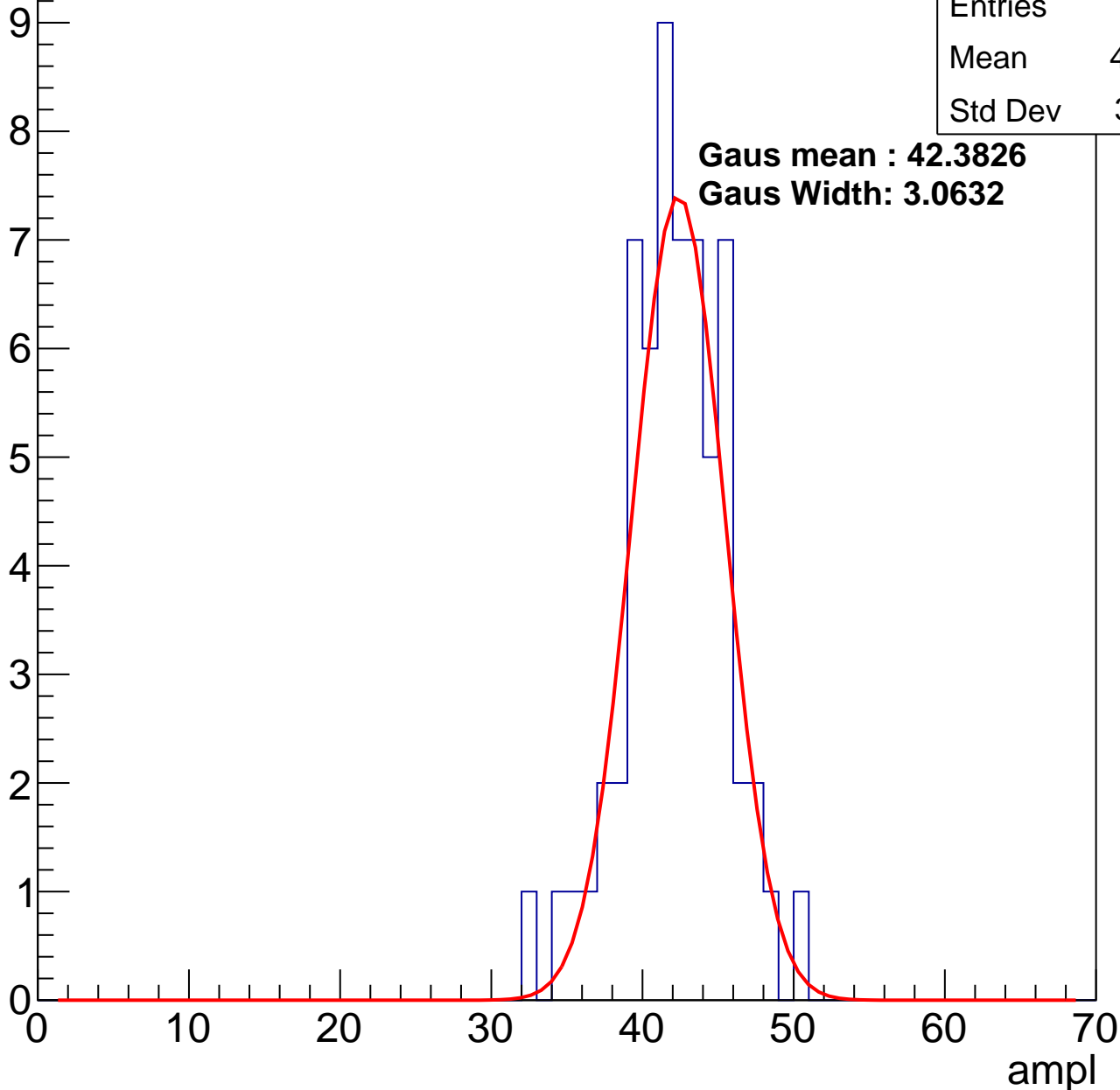
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.66
Std Dev	3.341

**Gaus mean : 42.3826**

**Gaus Width: 3.0632**

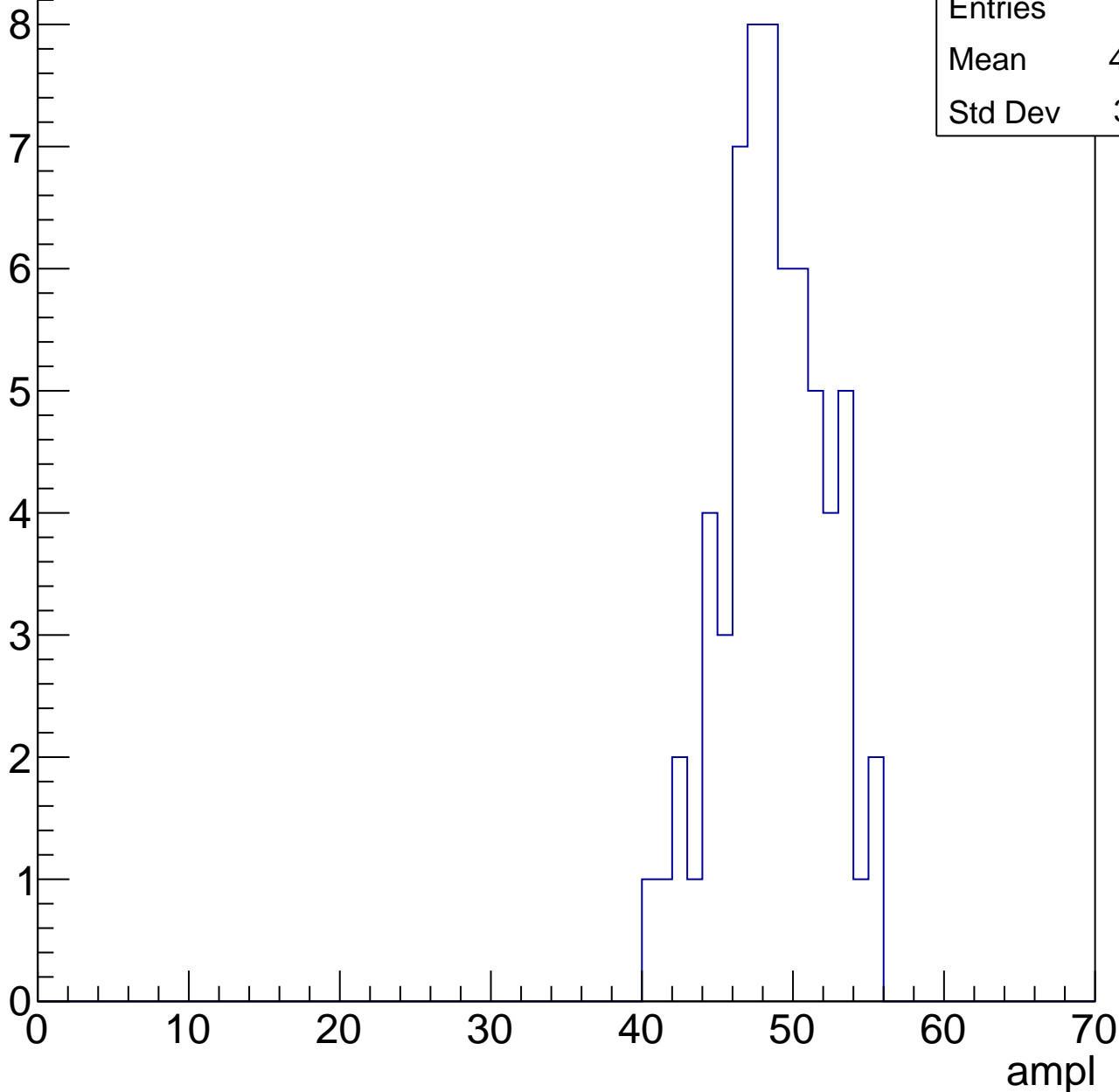


# B1L102S, U24-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.23
Std Dev	3.381

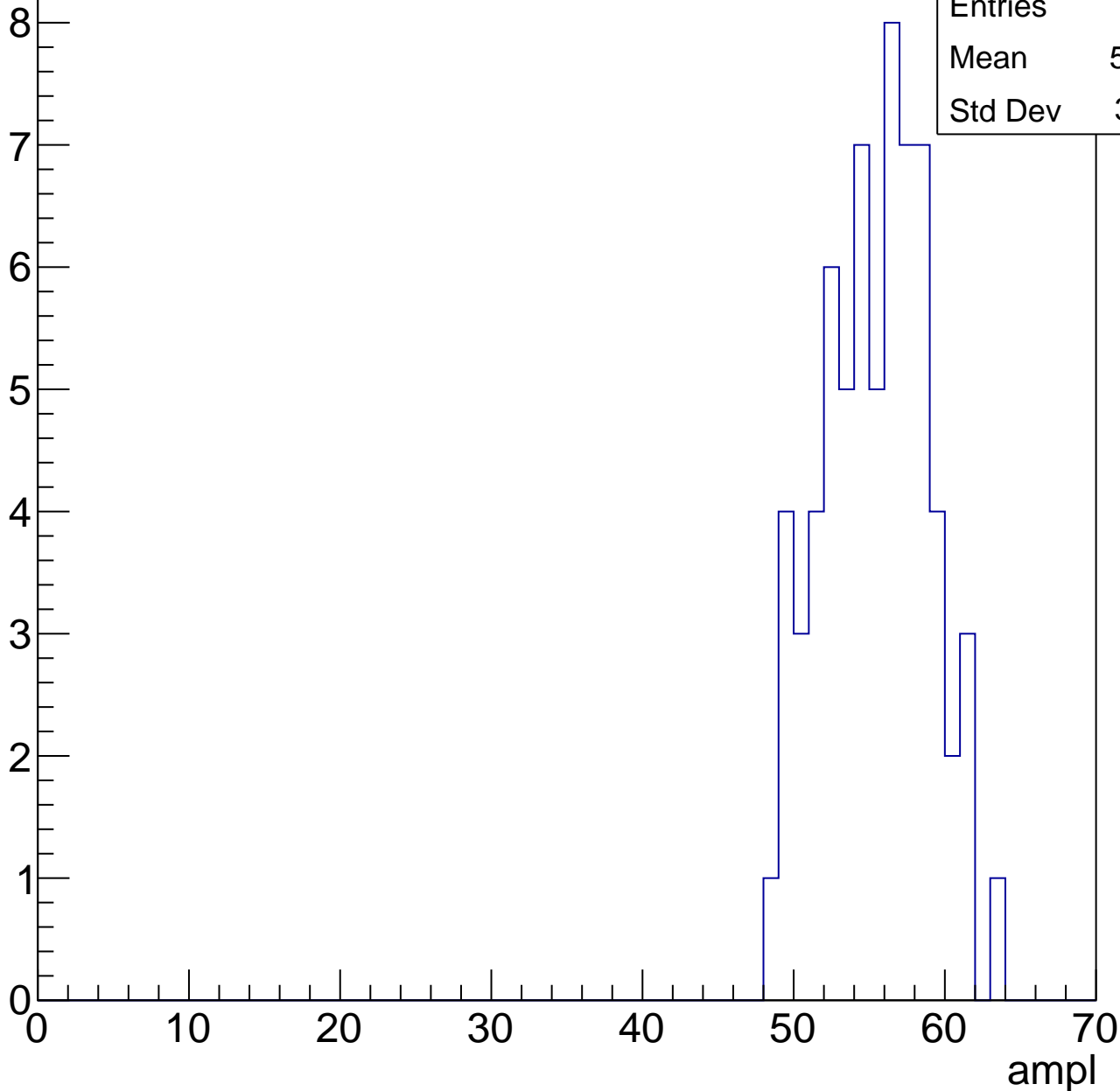


# B1L102S, U24-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.97
Std Dev	3.451

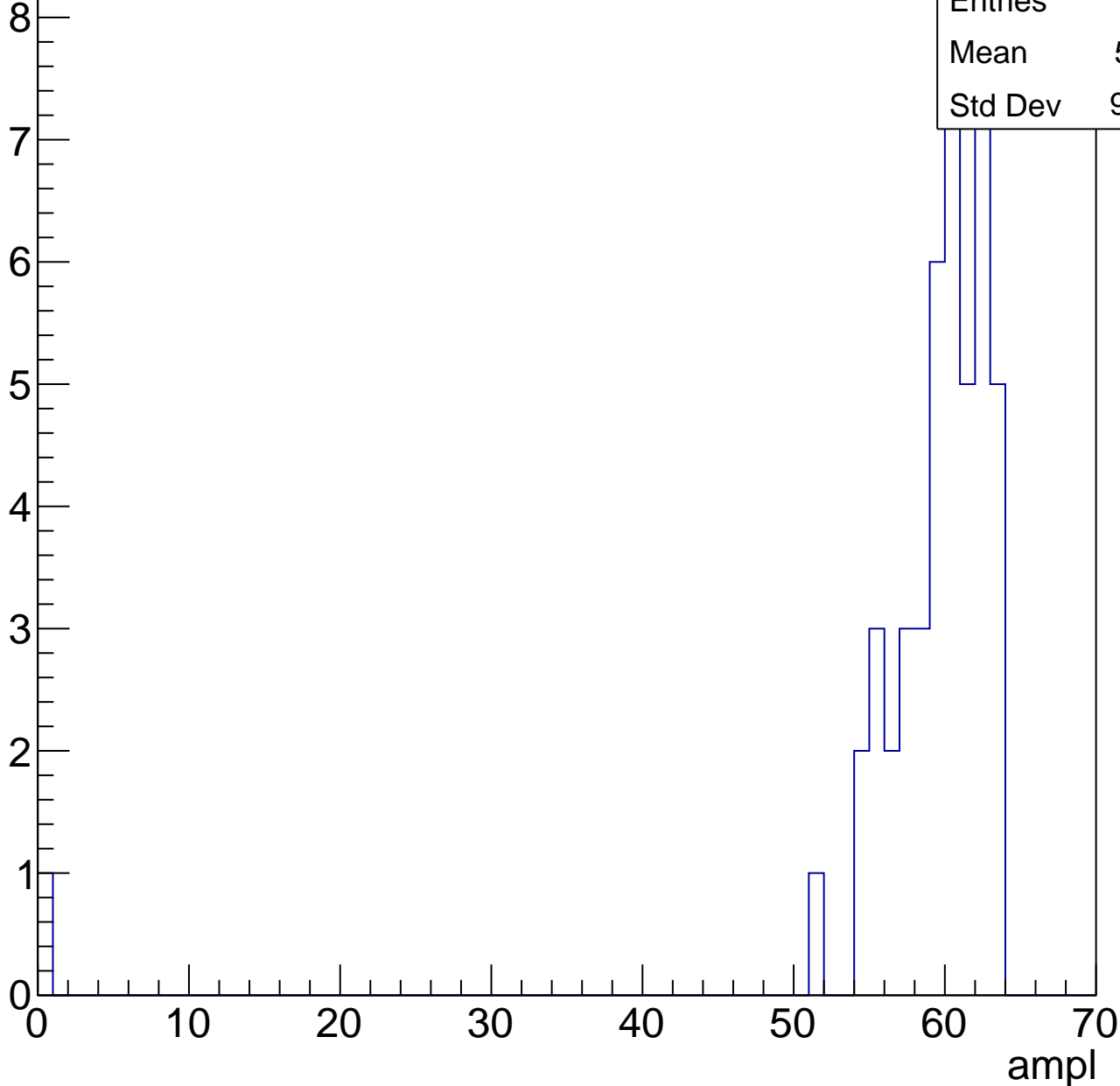


# B1L102S, U24-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

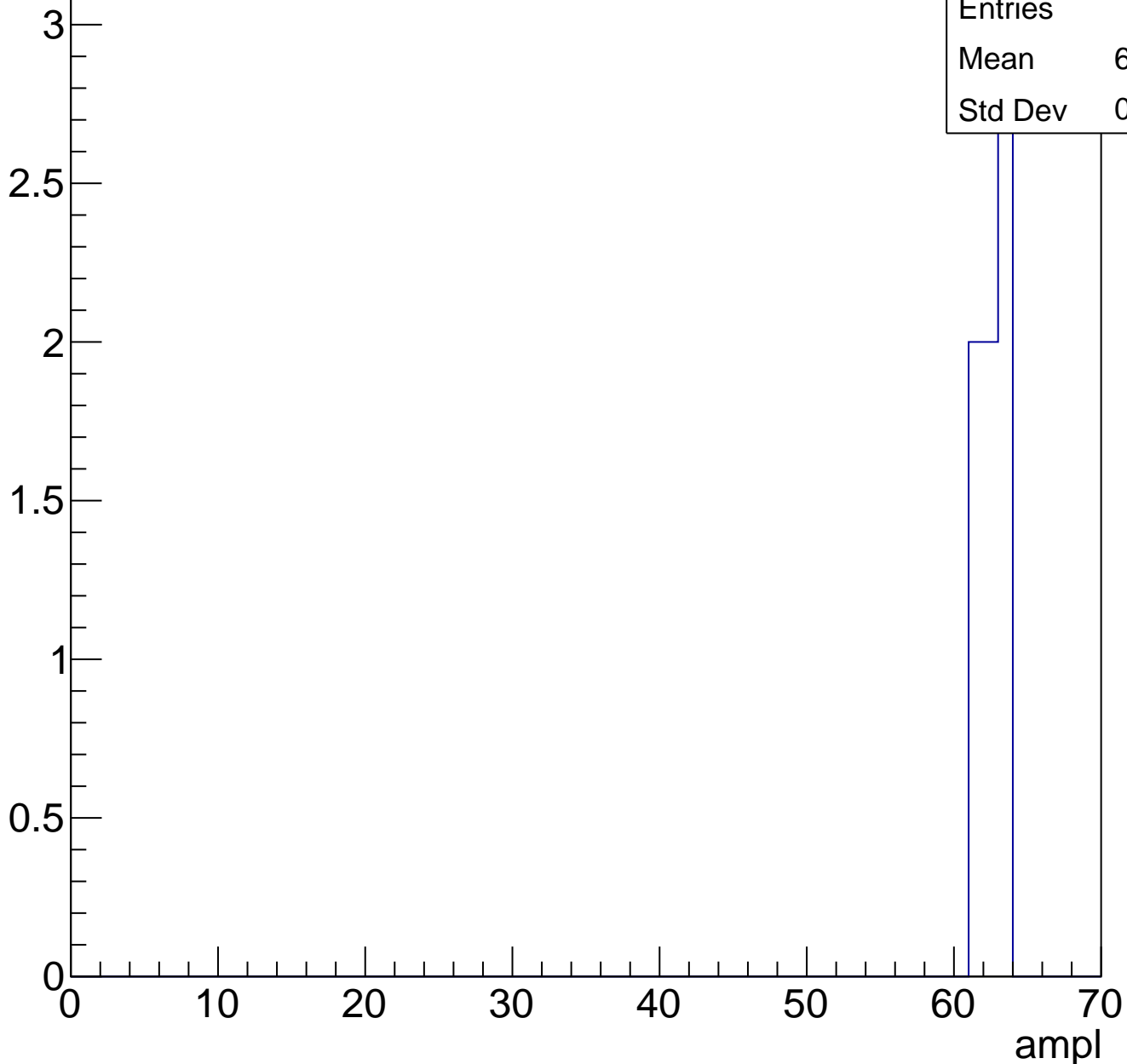
Entries	47
Mean	58.11
Std Dev	9.014



# B1L102S, U24-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

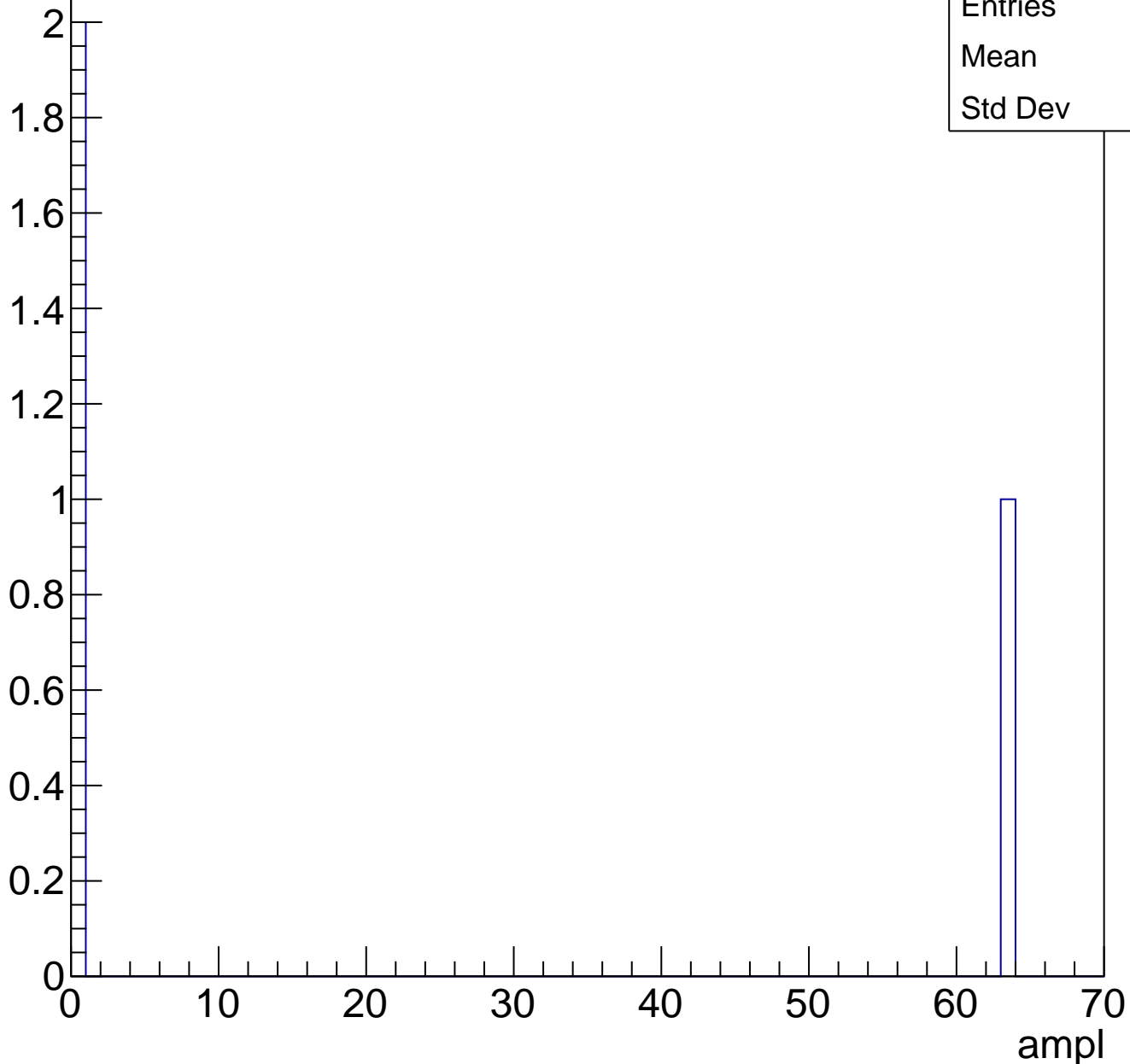




# B1L102S, U24-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L102S, U24-ch3, adc0

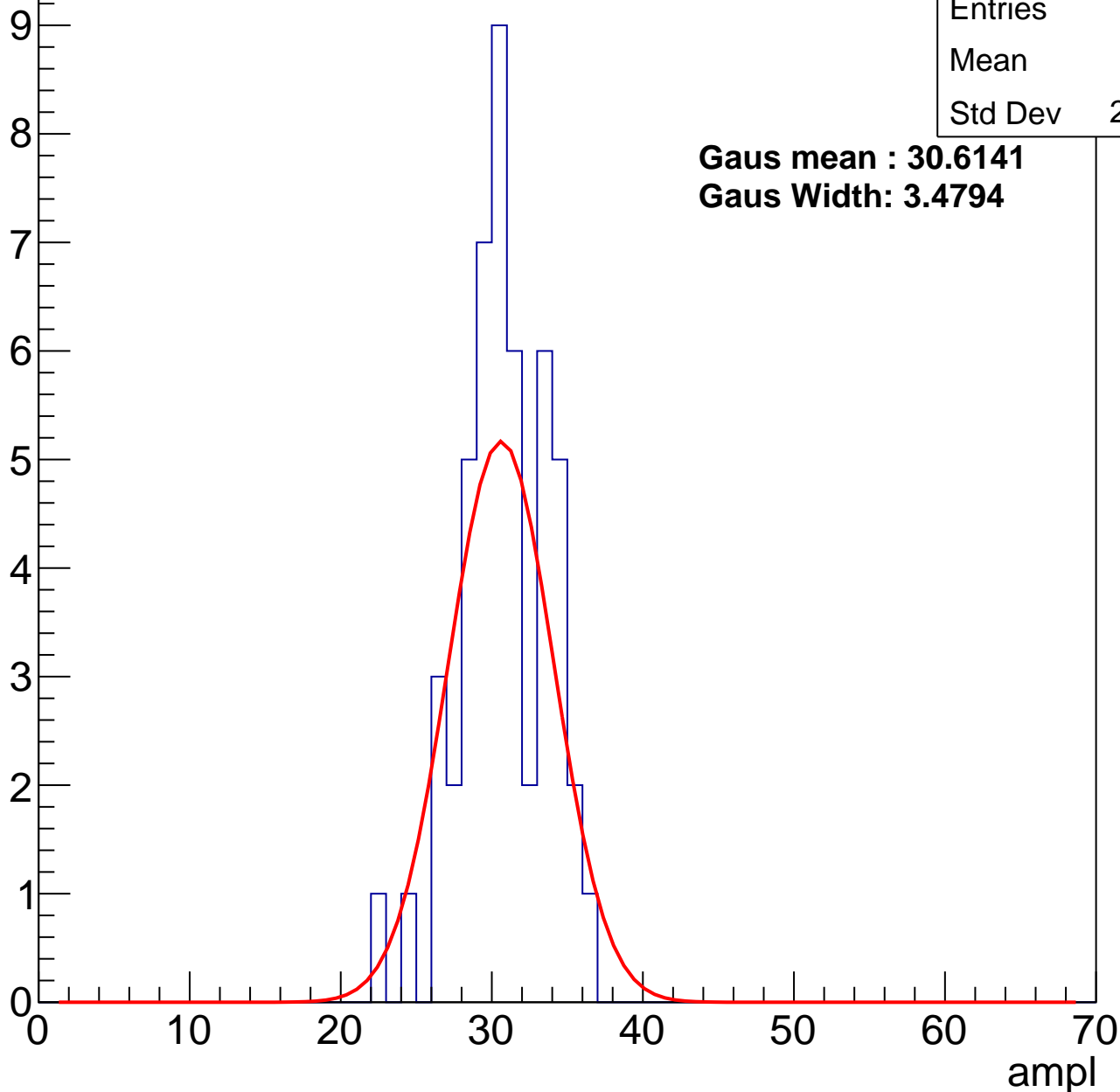
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	30.3
Std Dev	2.907

**Gaus mean : 30.6141**

**Gaus Width: 3.4794**



# B1L102S, U24-ch3, adc1

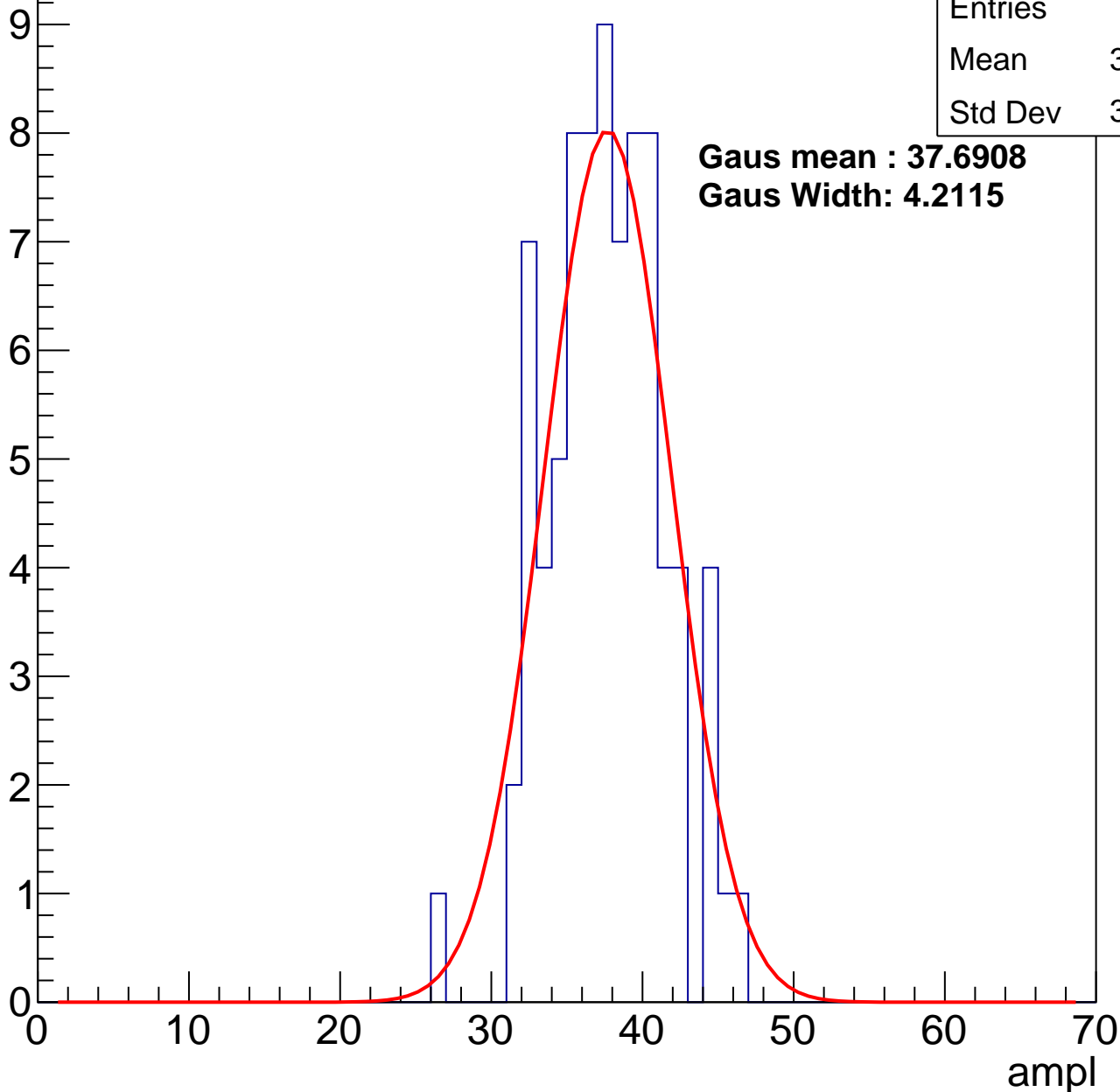
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	37.19
Std Dev	3.735

**Gaus mean : 37.6908**

**Gaus Width: 4.2115**



# B1L102S, U24-ch3, adc2

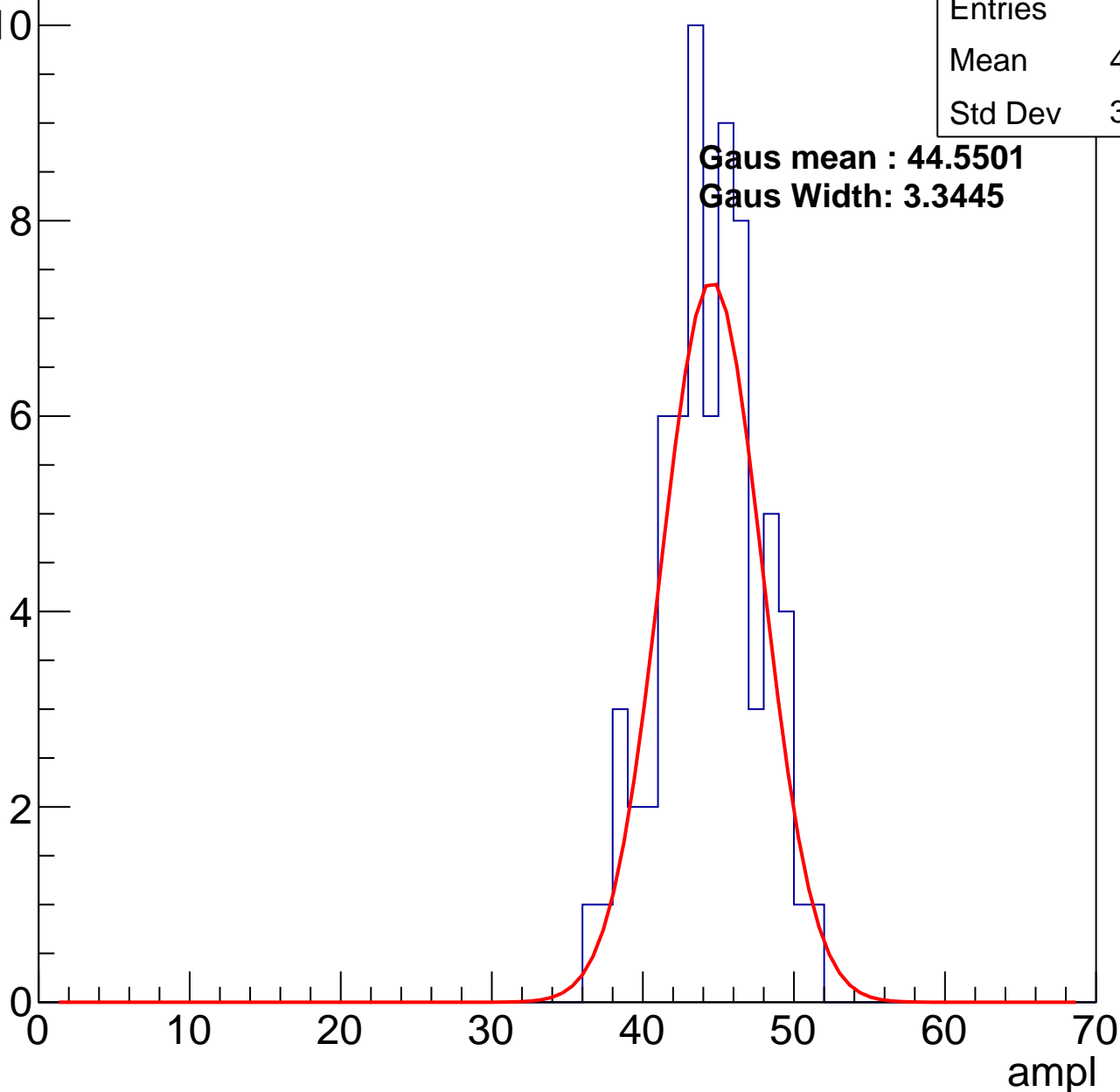
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.94
Std Dev	3.262

**Gaus mean : 44.5501**

**Gaus Width: 3.3445**

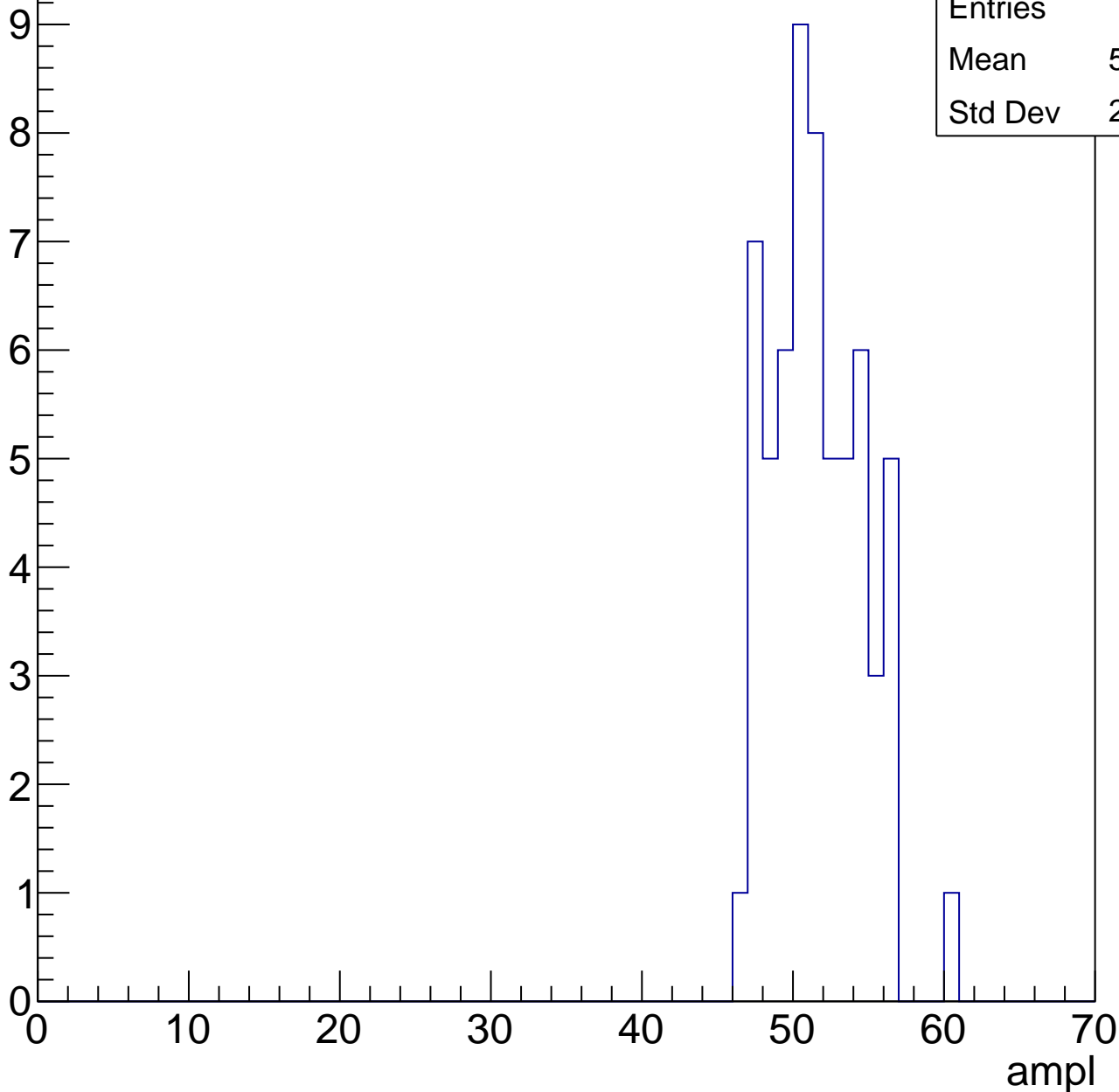


# B1L102S, U24-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	51.16
Std Dev	2.993

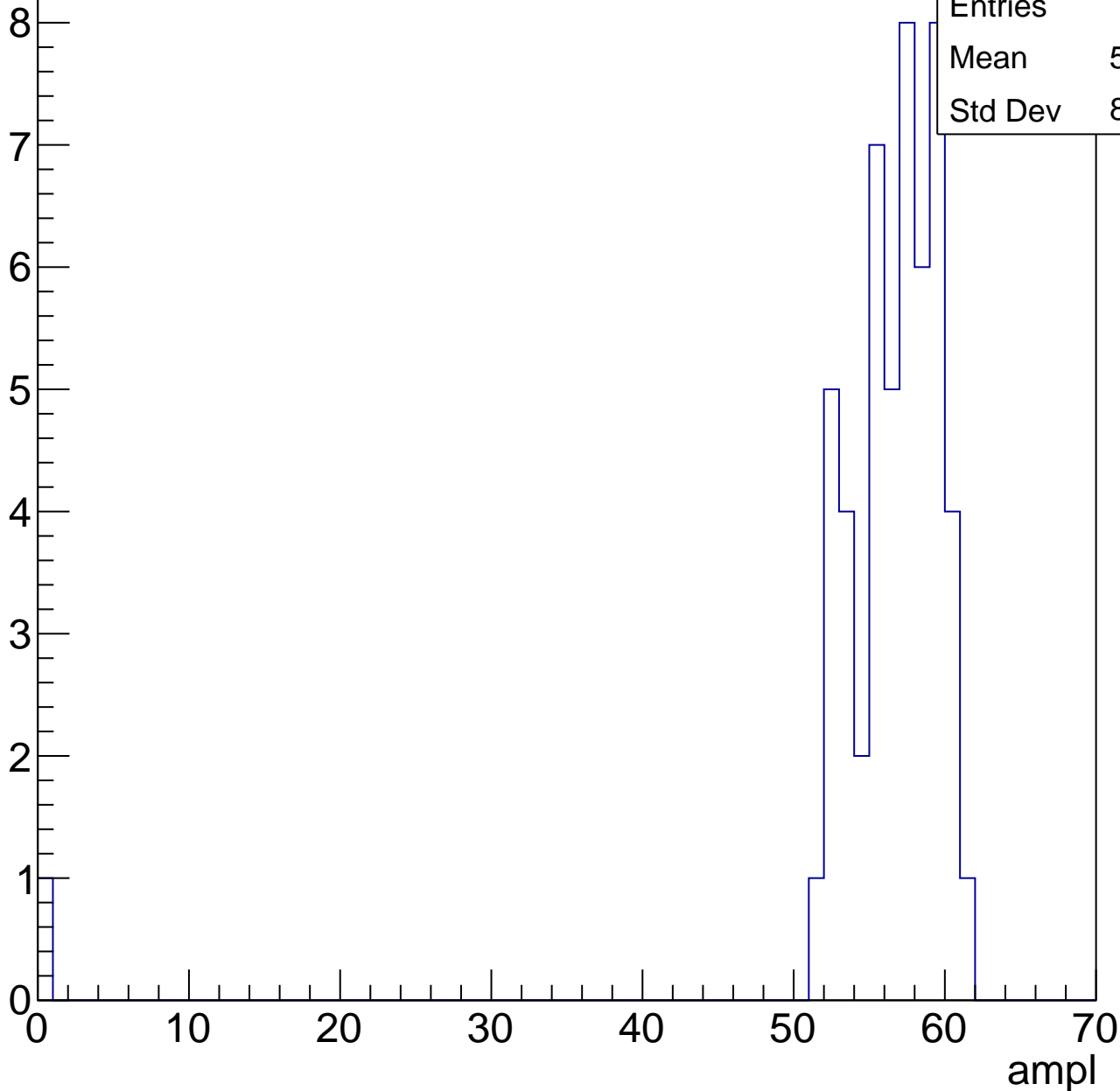


# B1L102S, U24-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.25
Std Dev	8.152

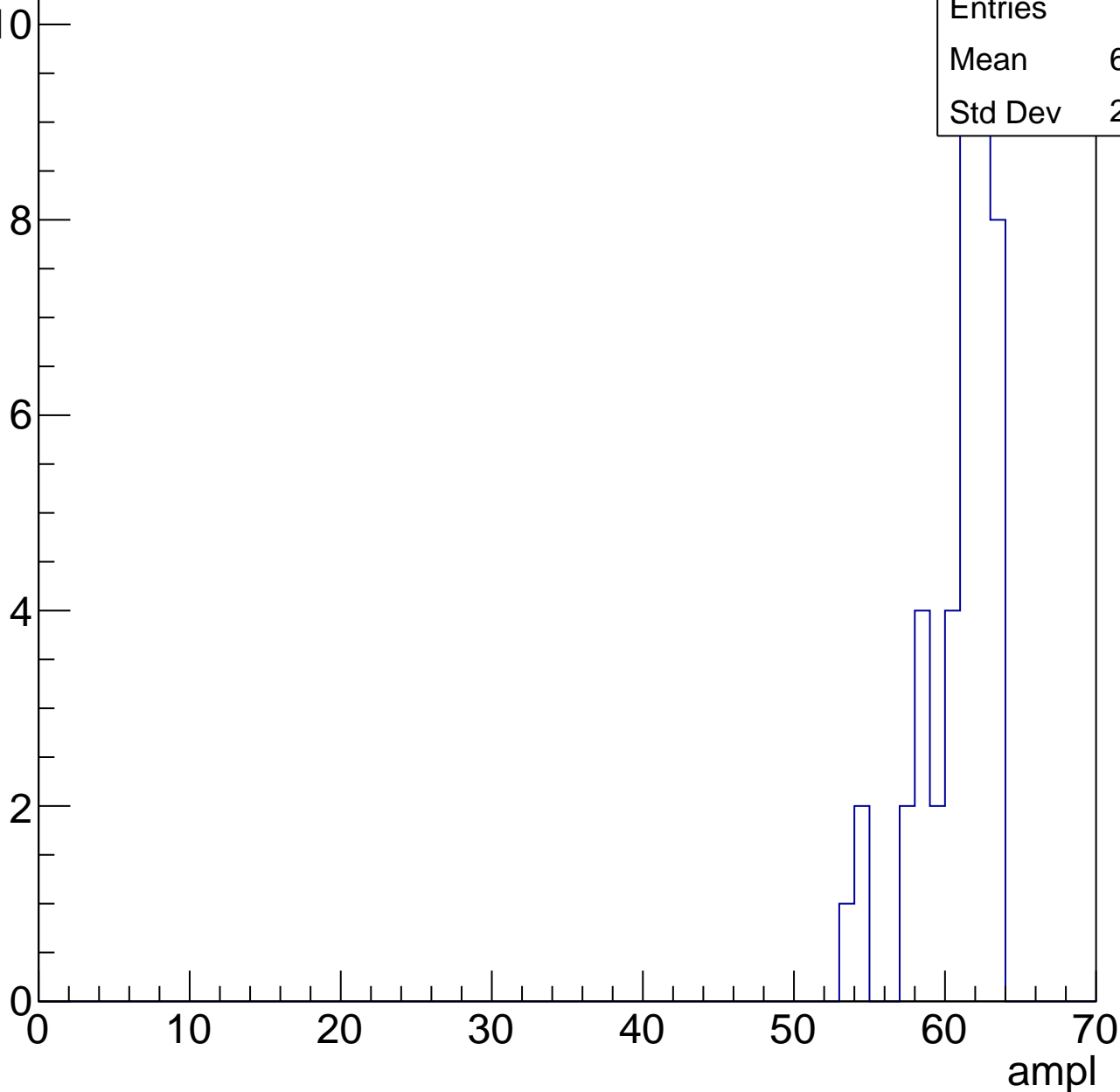


# B1L102S, U24-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	60.43
Std Dev	2.537



# B1L102S, U24-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

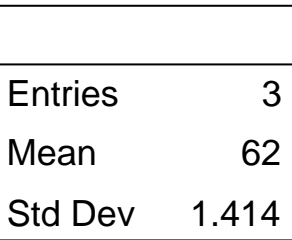
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62
Std Dev	1.414

ampl

0 10 20 30 40 50 60 70





# B1L102S, U24-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch4, adc0

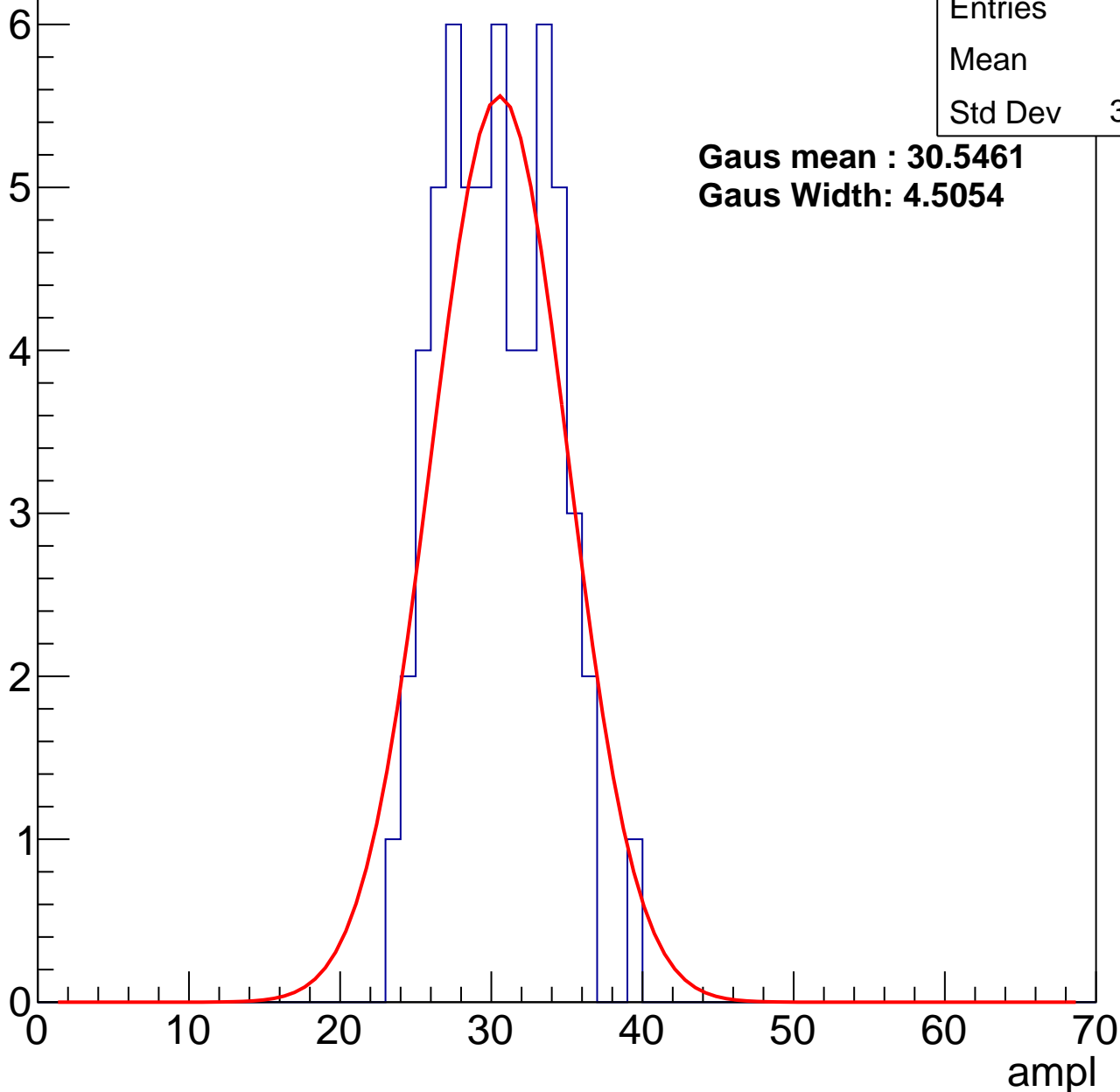
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.9
Std Dev	3.592

**Gaus mean : 30.5461**

**Gaus Width: 4.5054**



# B1L102S, U24-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	36.27
Std Dev	3.526

**Gaus mean : 36.5291**

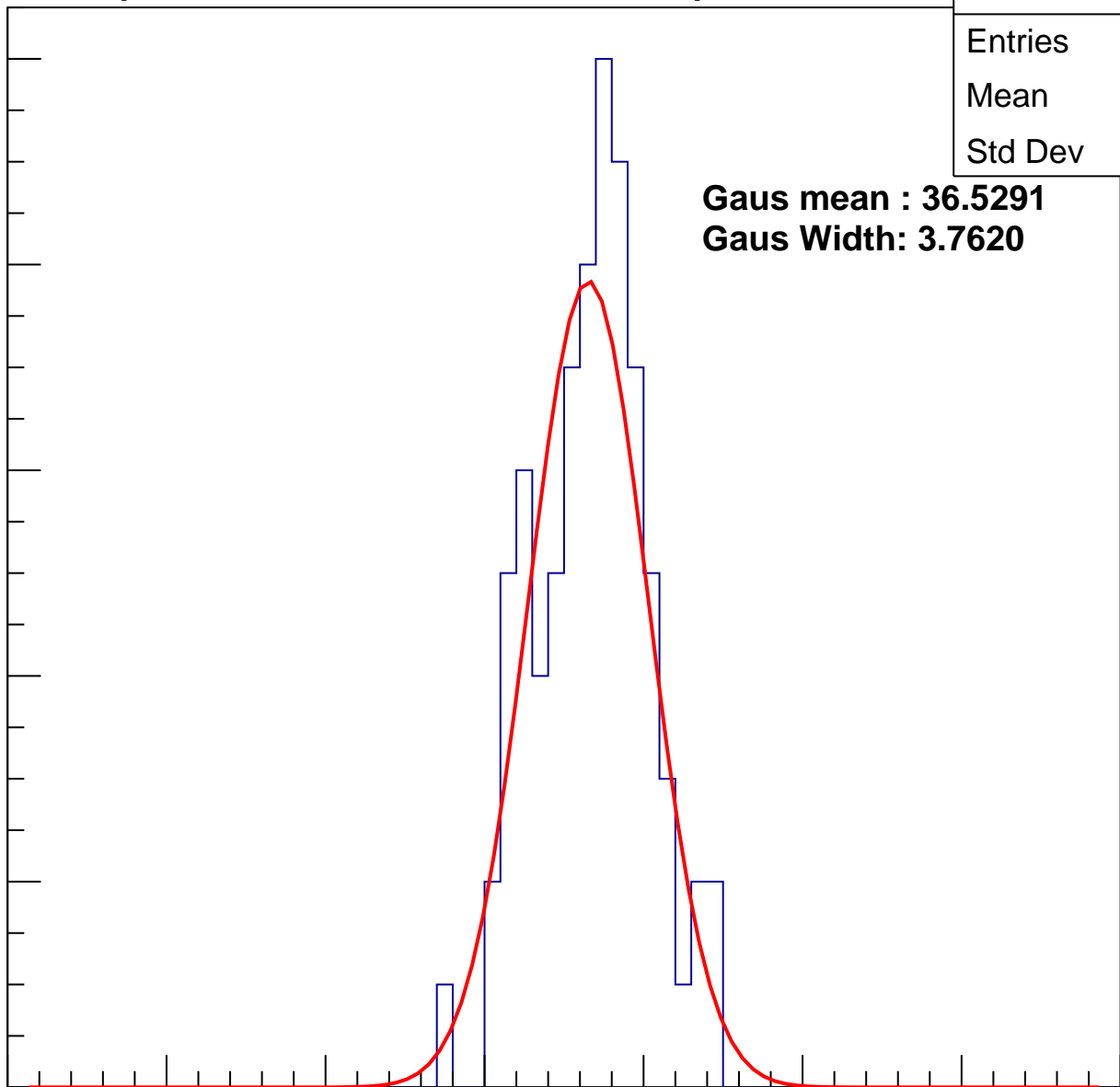
**Gaus Width: 3.7620**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch4, adc2

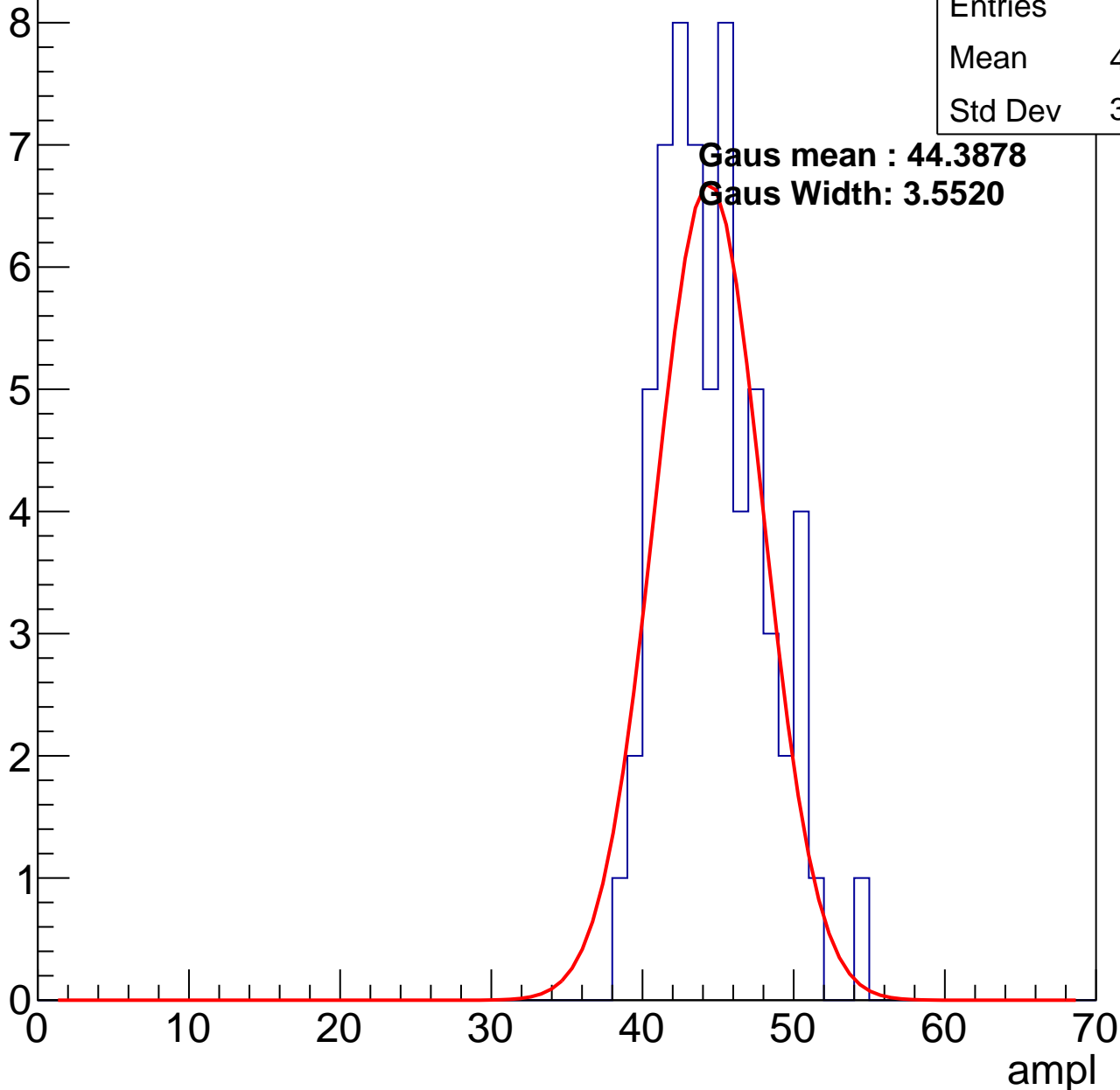
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	44.22
Std Dev	3.392

**Gaus mean : 44.3878**

**Gaus Width: 3.5520**

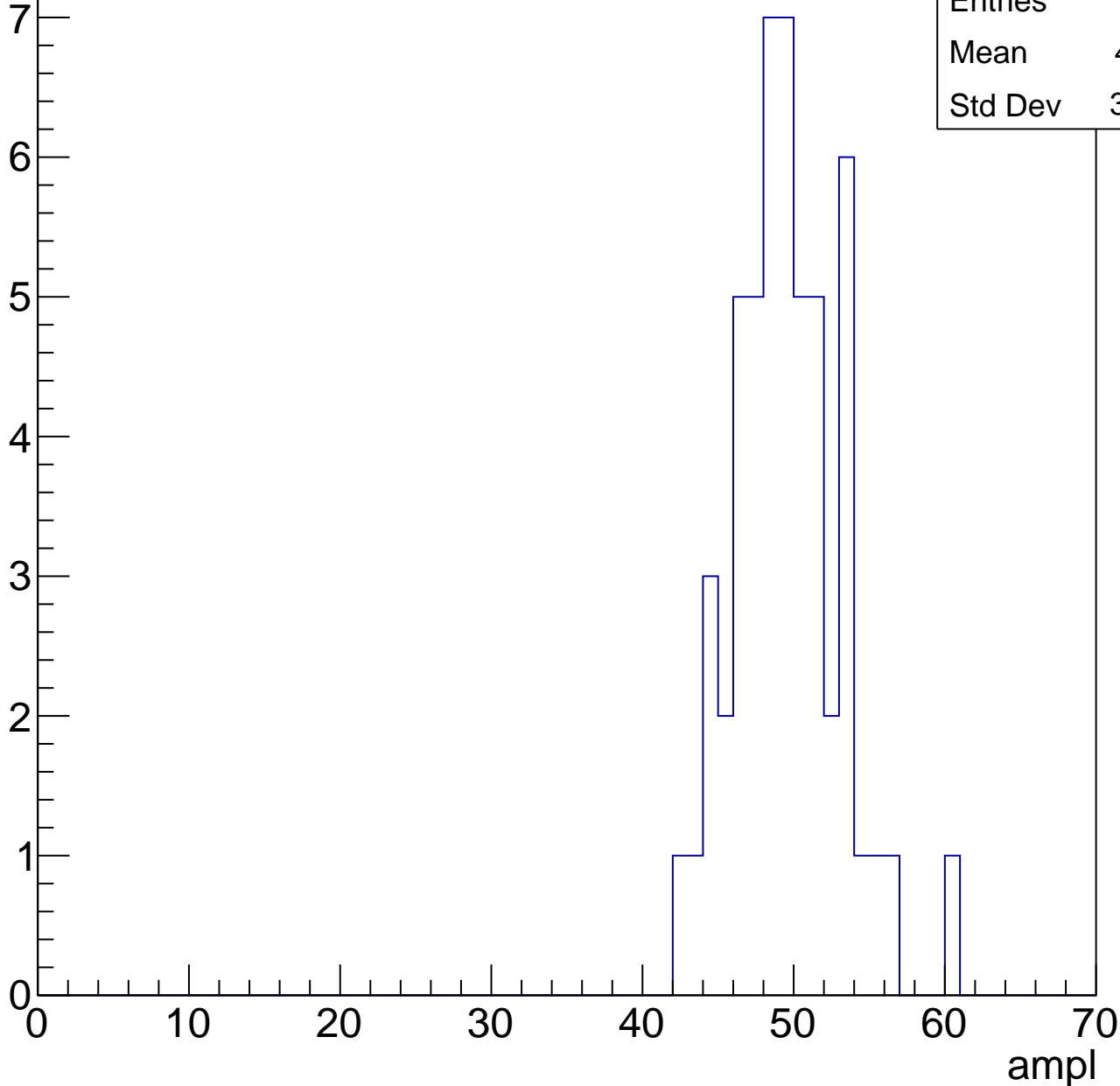


# B1L102S, U24-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

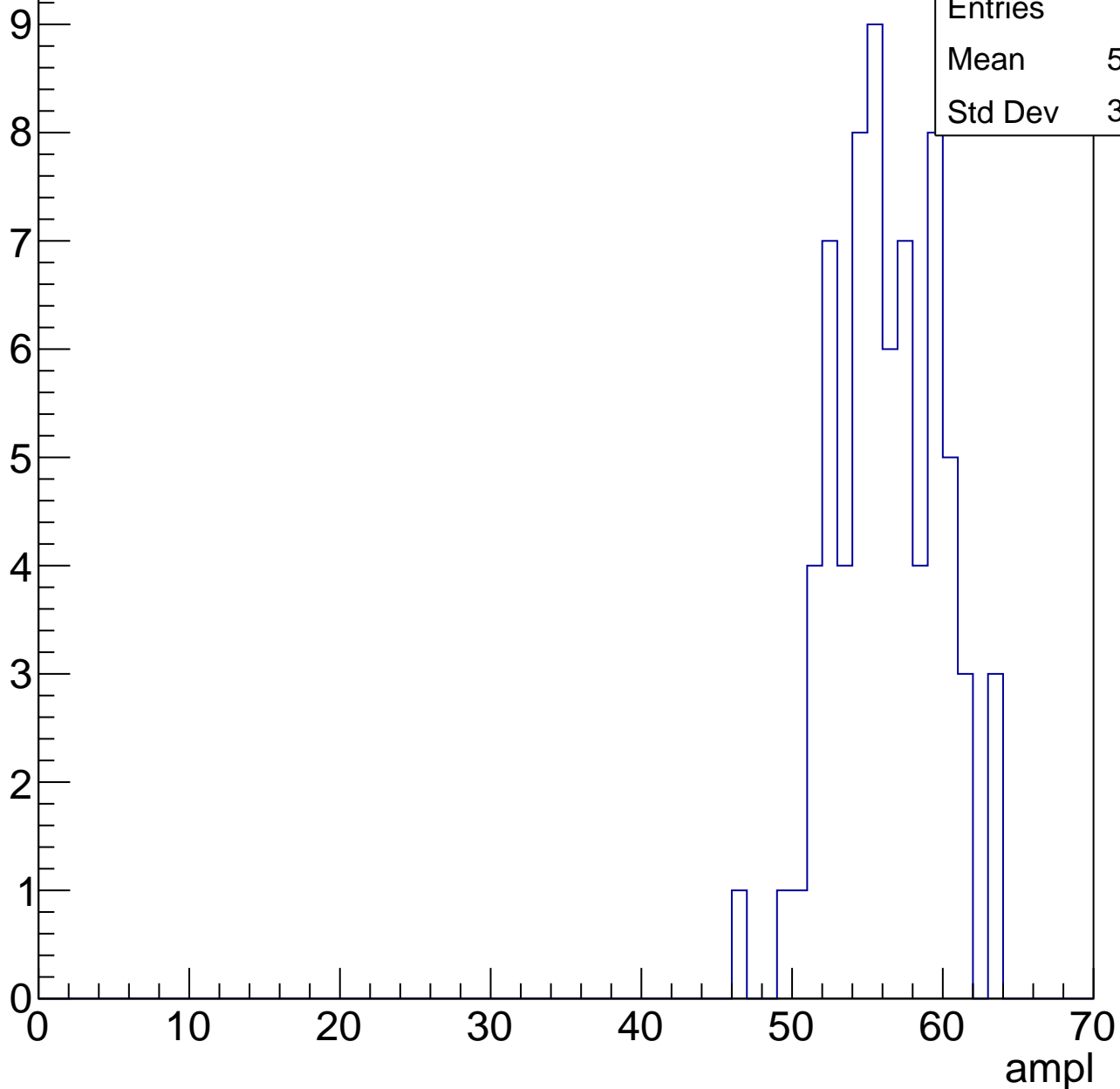
Entries	53
Mean	49.11
Std Dev	3.457



# B1L102S, U24-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



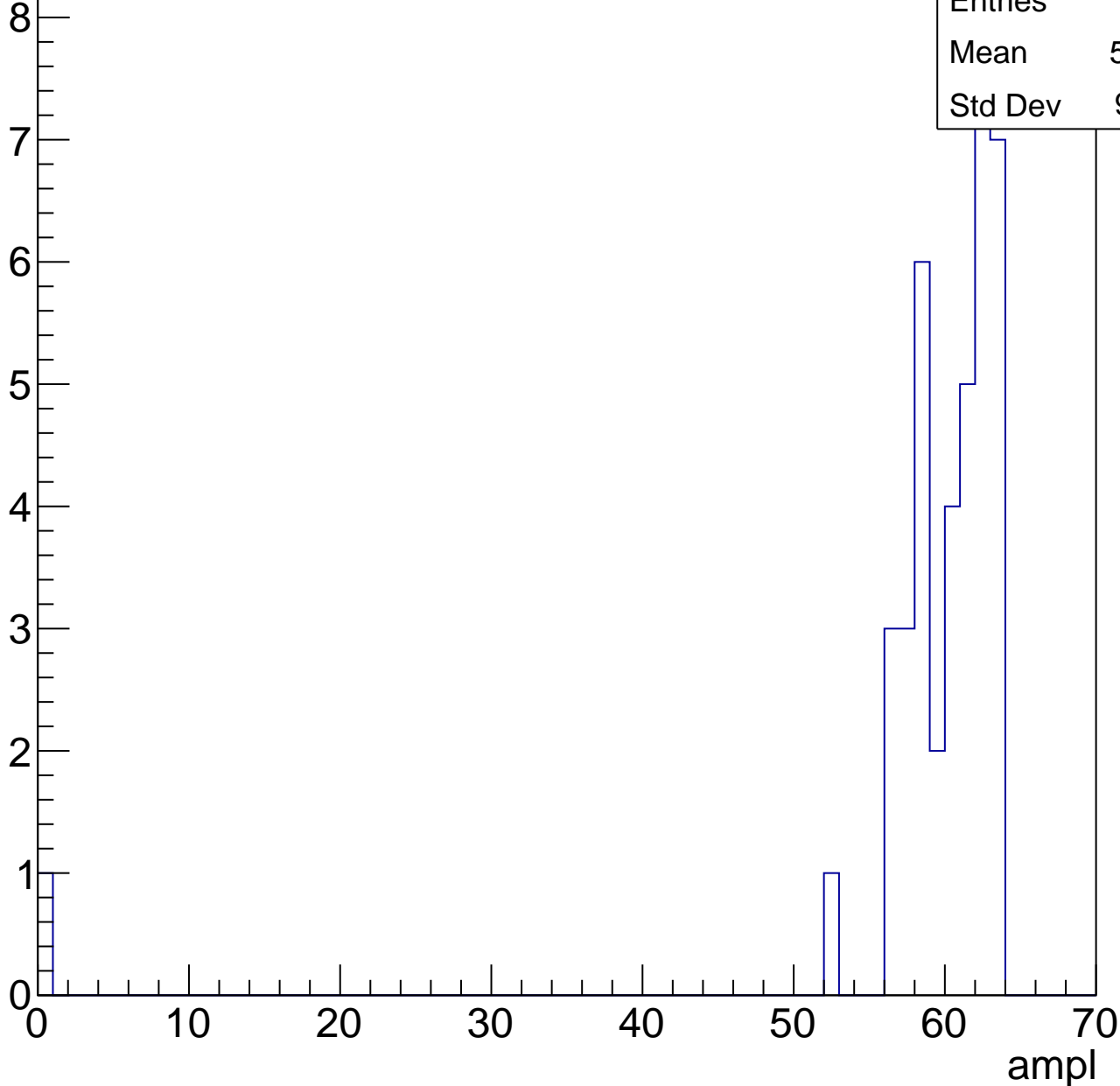
Entries	71
Mean	55.82
Std Dev	3.498

# B1L102S, U24-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.48
Std Dev	9.711



# B1L102S, U24-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl



# B1L102S, U24-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch5, adc0

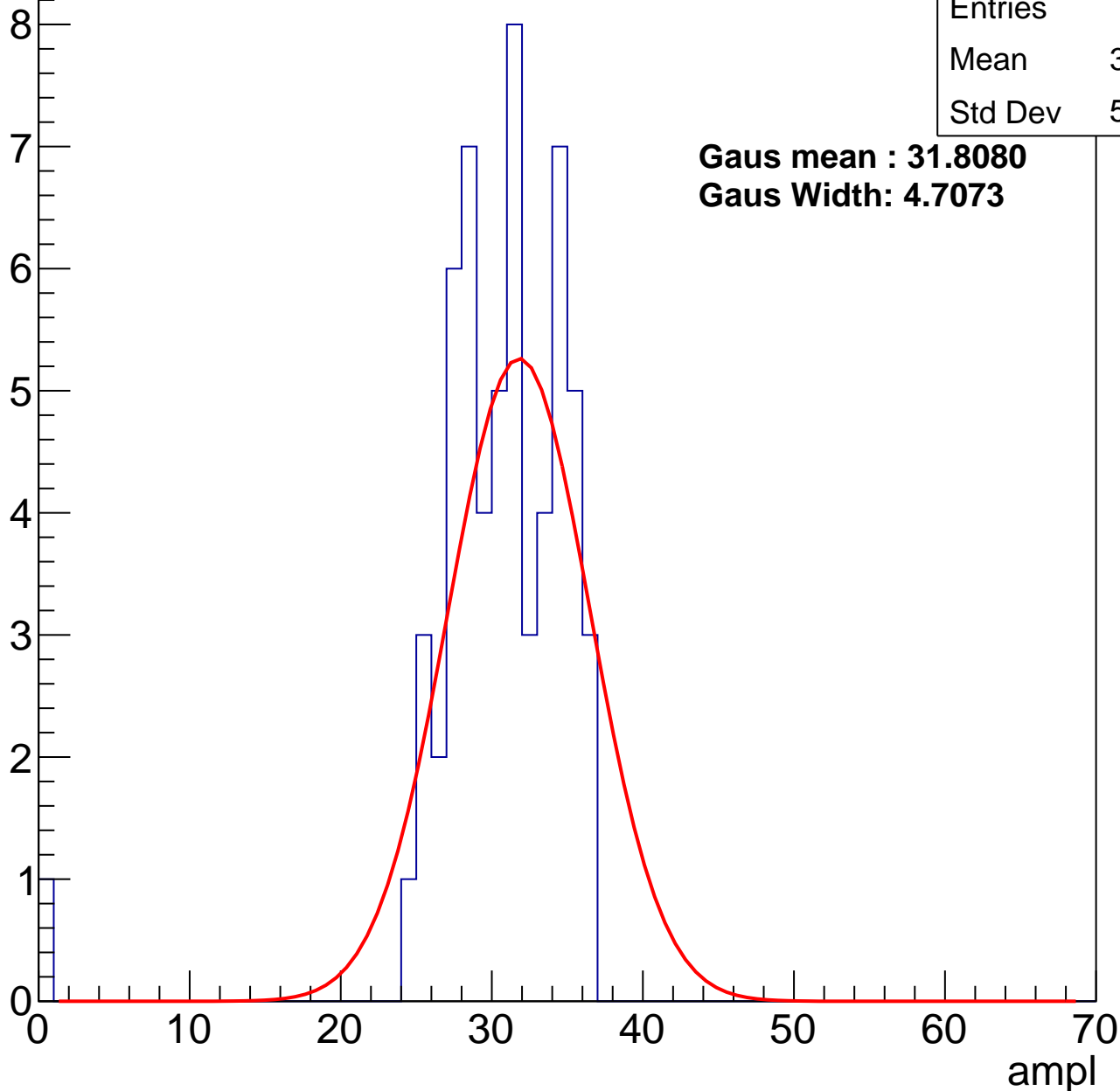
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	30.03
Std Dev	5.092

**Gaus mean : 31.8080**

**Gaus Width: 4.7073**



# B1L102S, U24-ch5, adc1

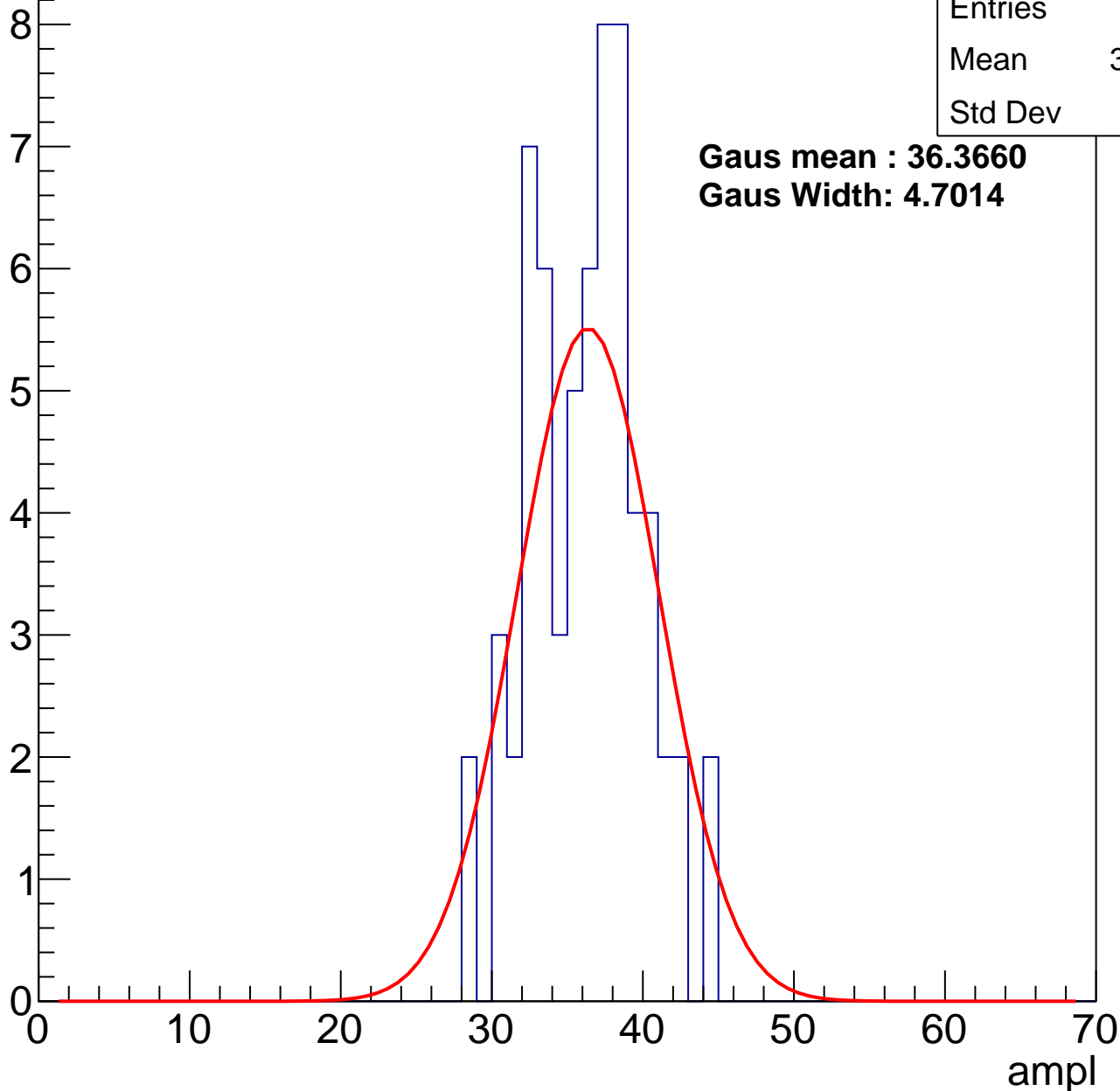
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.83
Std Dev	3.66

**Gaus mean : 36.3660**

**Gaus Width: 4.7014**



# B1L102S, U24-ch5, adc2

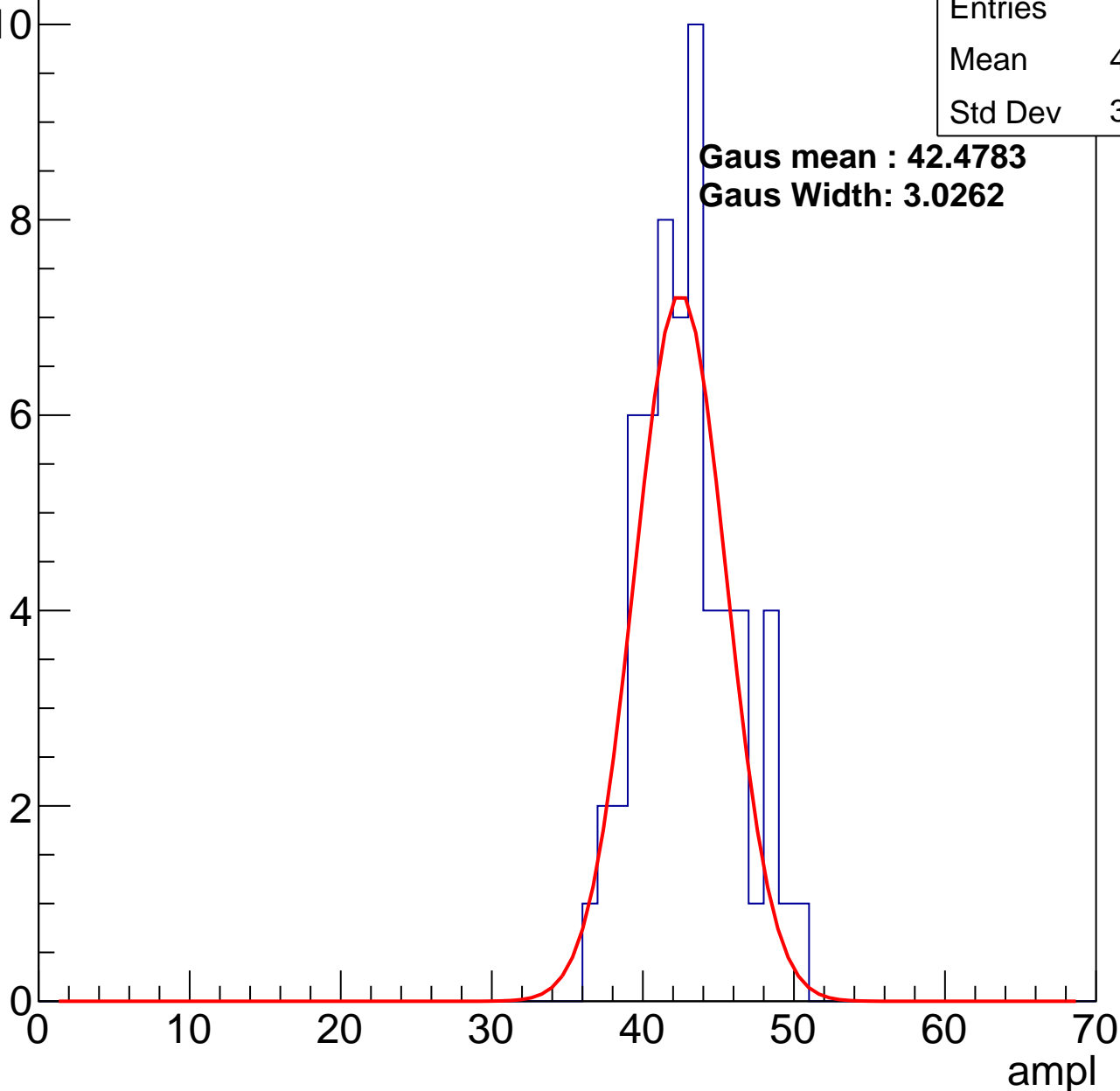
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.46
Std Dev	3.134

**Gaus mean : 42.4783**

**Gaus Width: 3.0262**

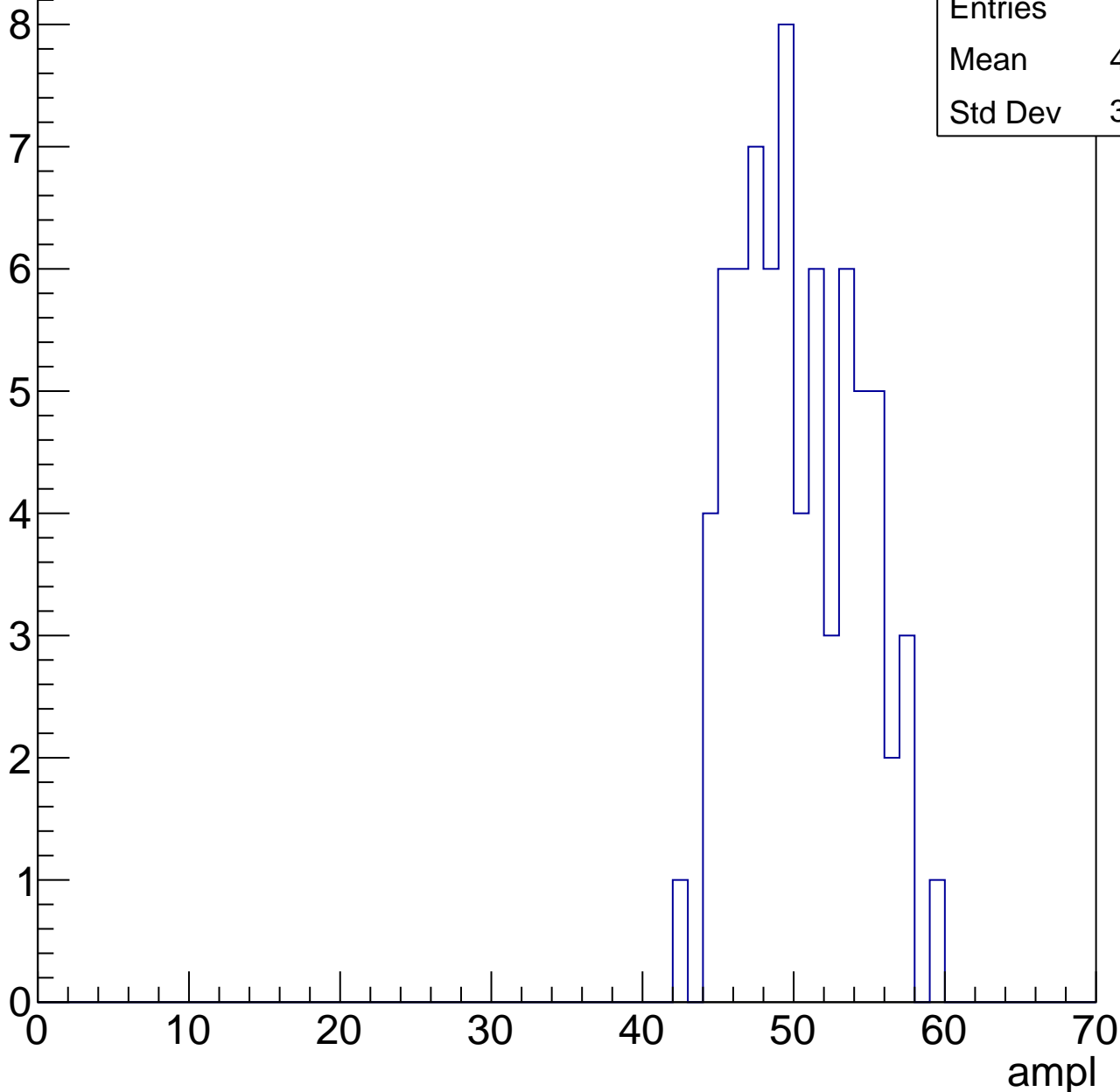


# B1L102S, U24-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.86
Std Dev	3.925

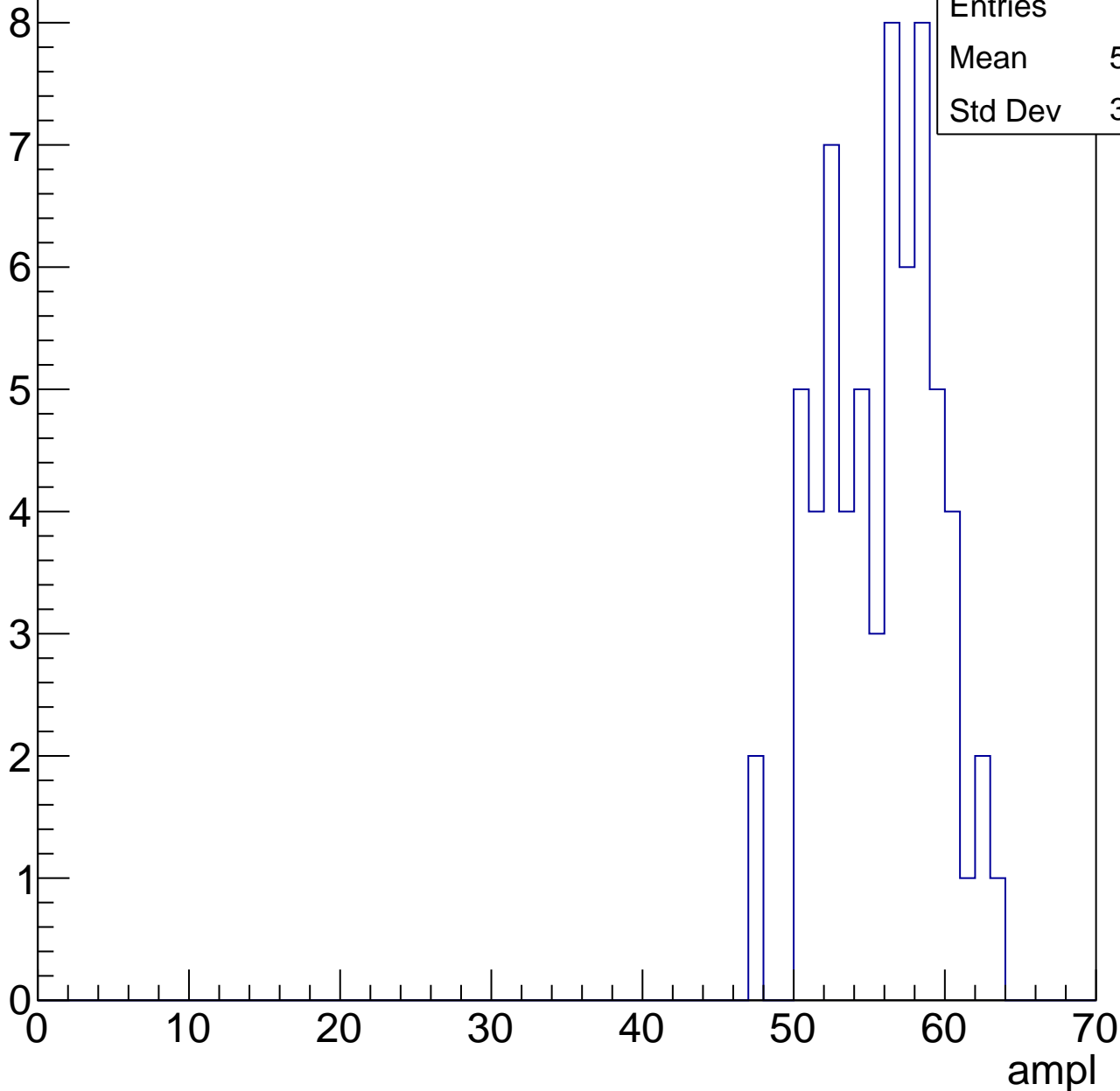


# B1L102S, U24-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.32
Std Dev	3.672

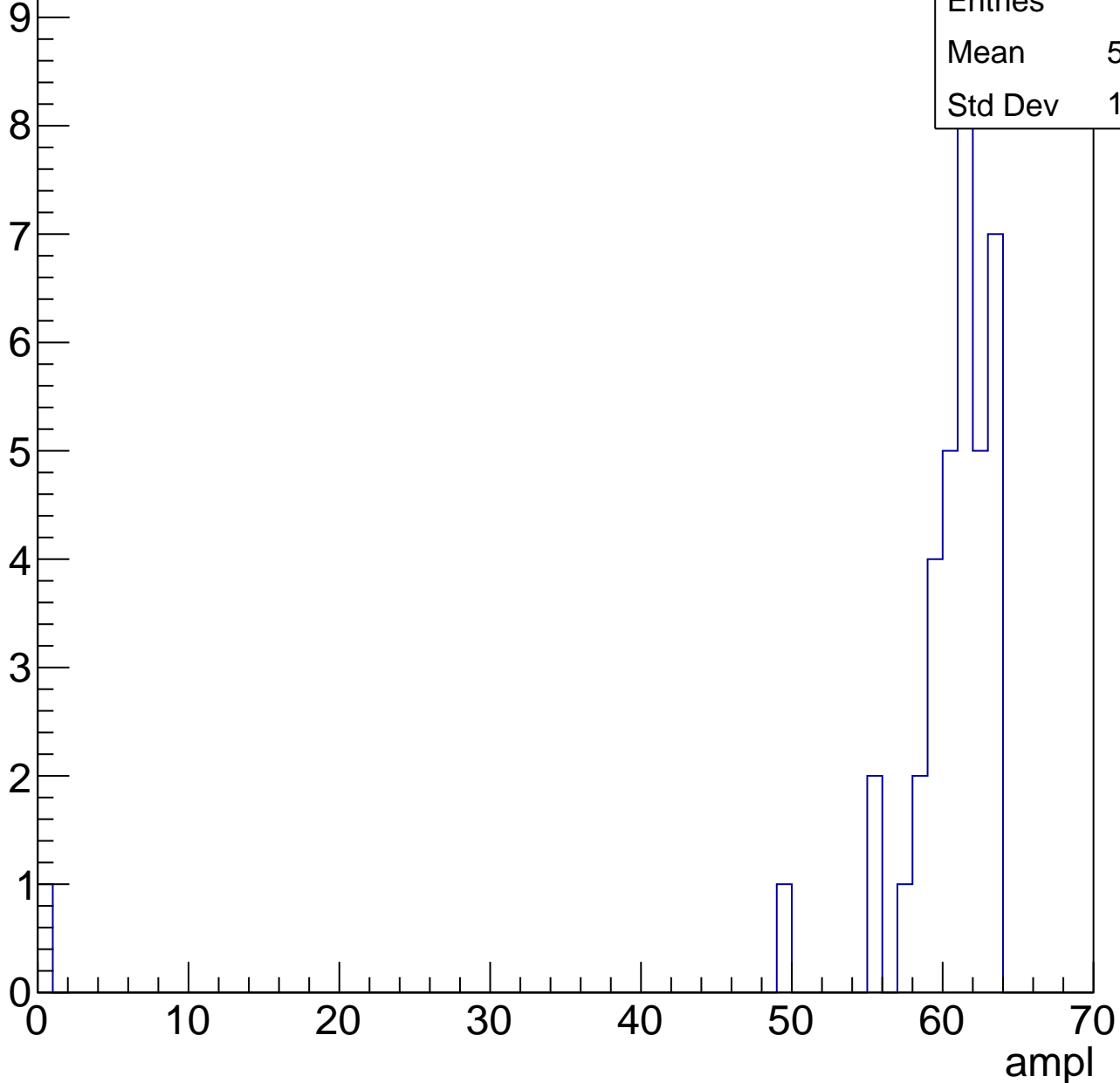


# B1L102S, U24-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

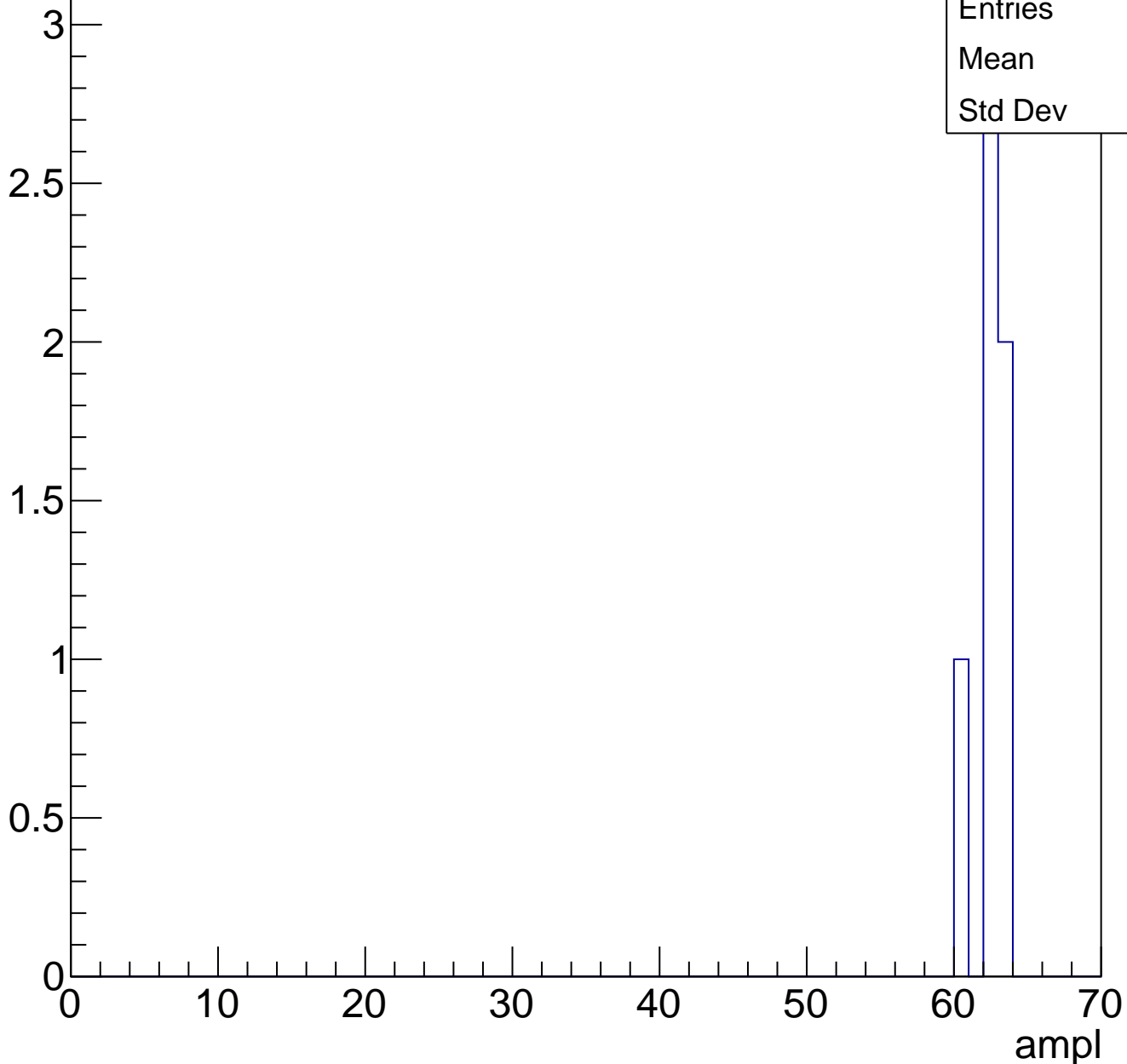
Entries	37
Mean	58.59
Std Dev	10.15



# B1L102S, U24-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch6, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	82
Mean	30.02
Std Dev	5.394

**Gaus mean : 31.0476**

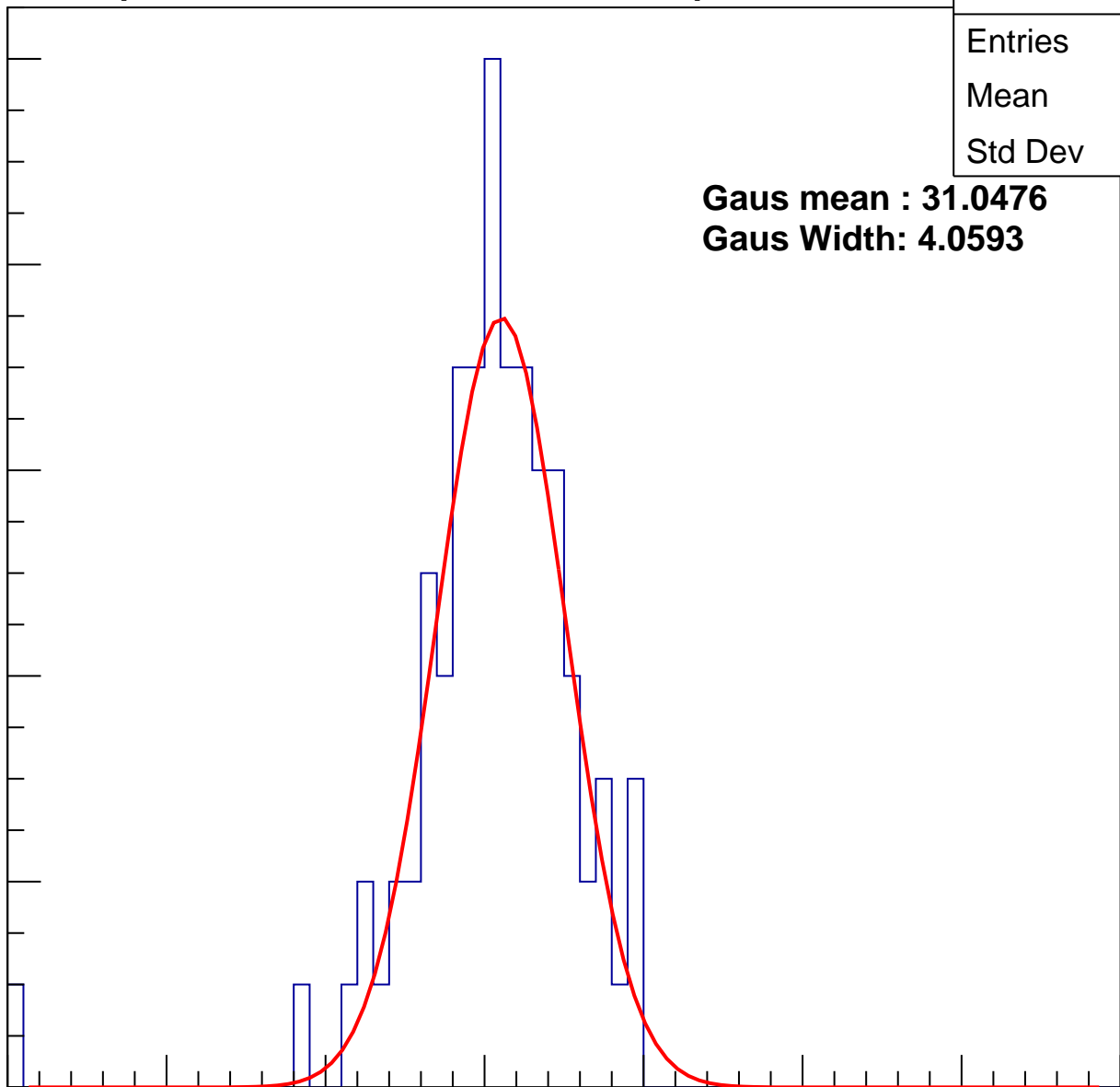
**Gaus Width: 4.0593**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch6, adc1

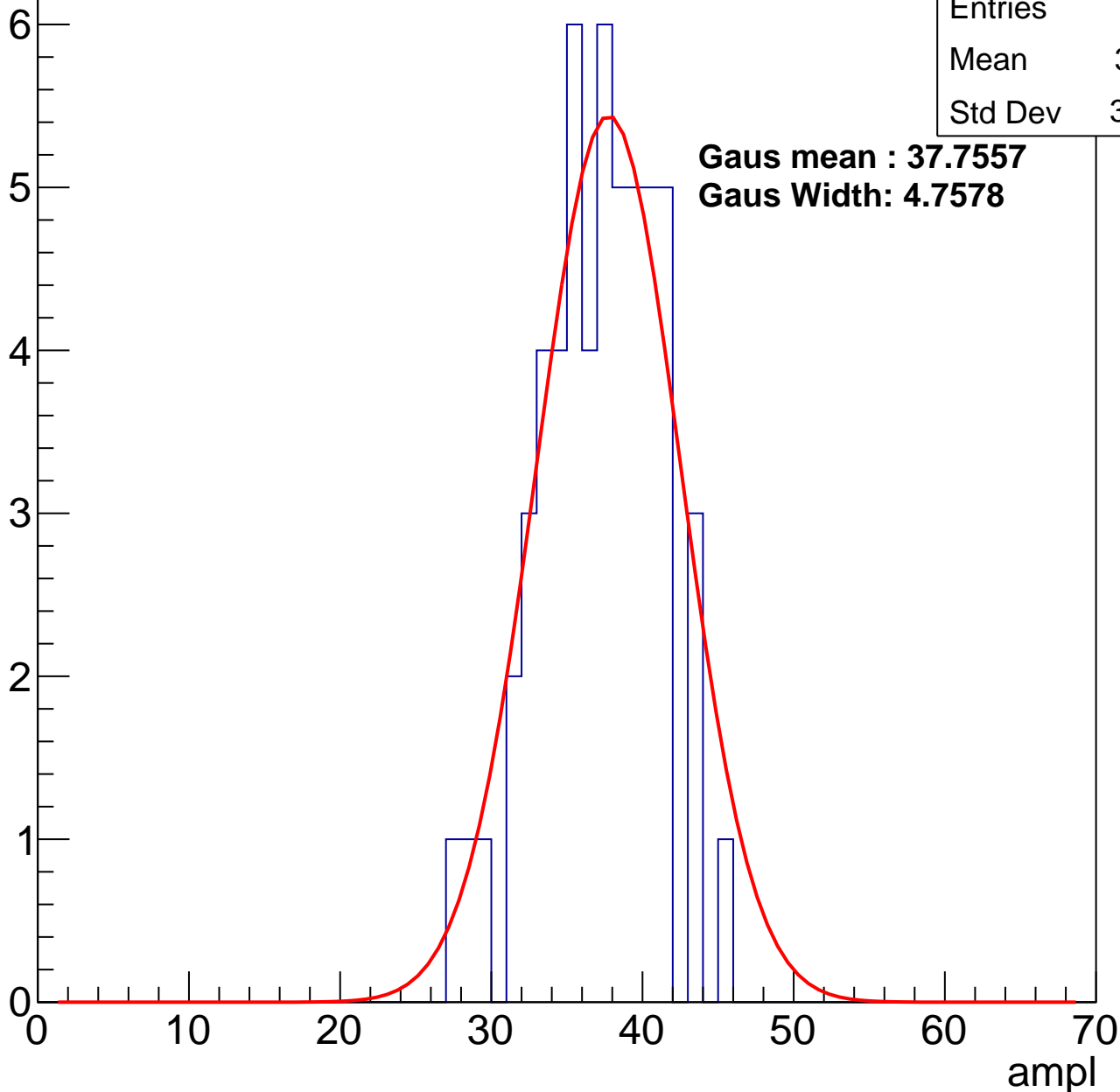
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	36.61
Std Dev	3.858

**Gaus mean : 37.7557**

**Gaus Width: 4.7578**



# B1L102S, U24-ch6, adc2

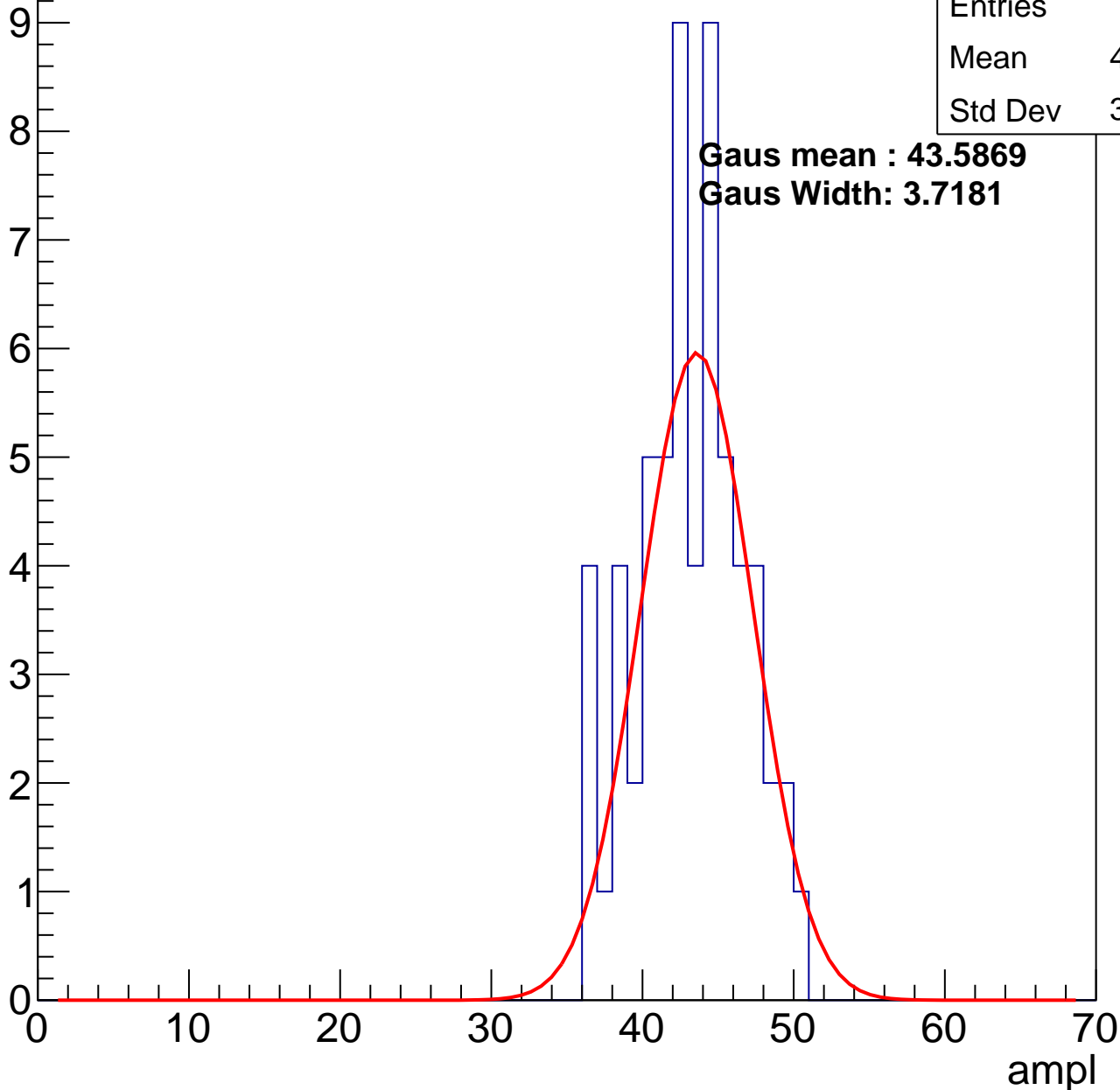
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.67
Std Dev	3.463

**Gaus mean : 43.5869**

**Gaus Width: 3.7181**

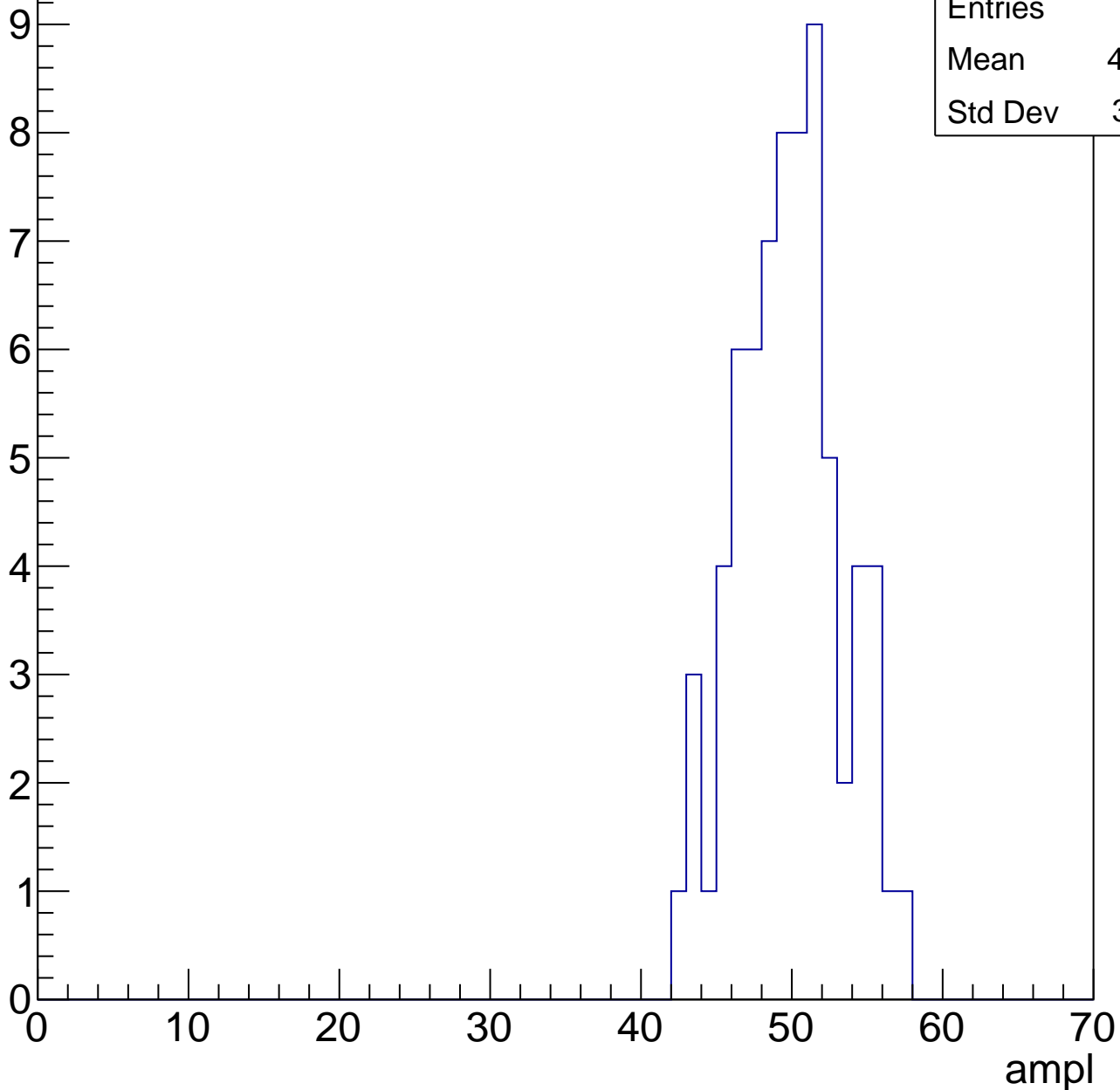


# B1L102S, U24-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.36
Std Dev	3.381

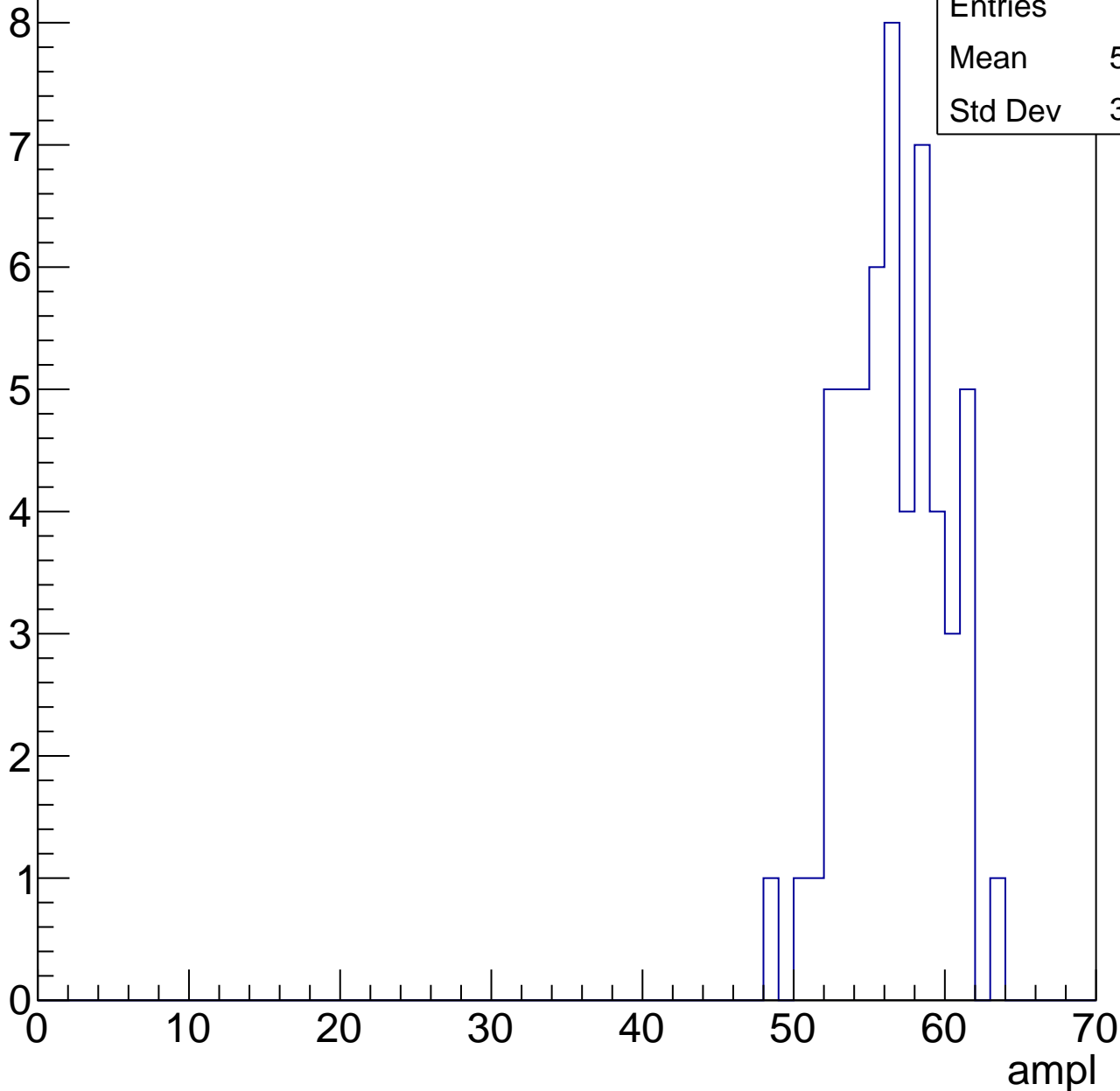


# B1L102S, U24-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	56.07
Std Dev	3.178

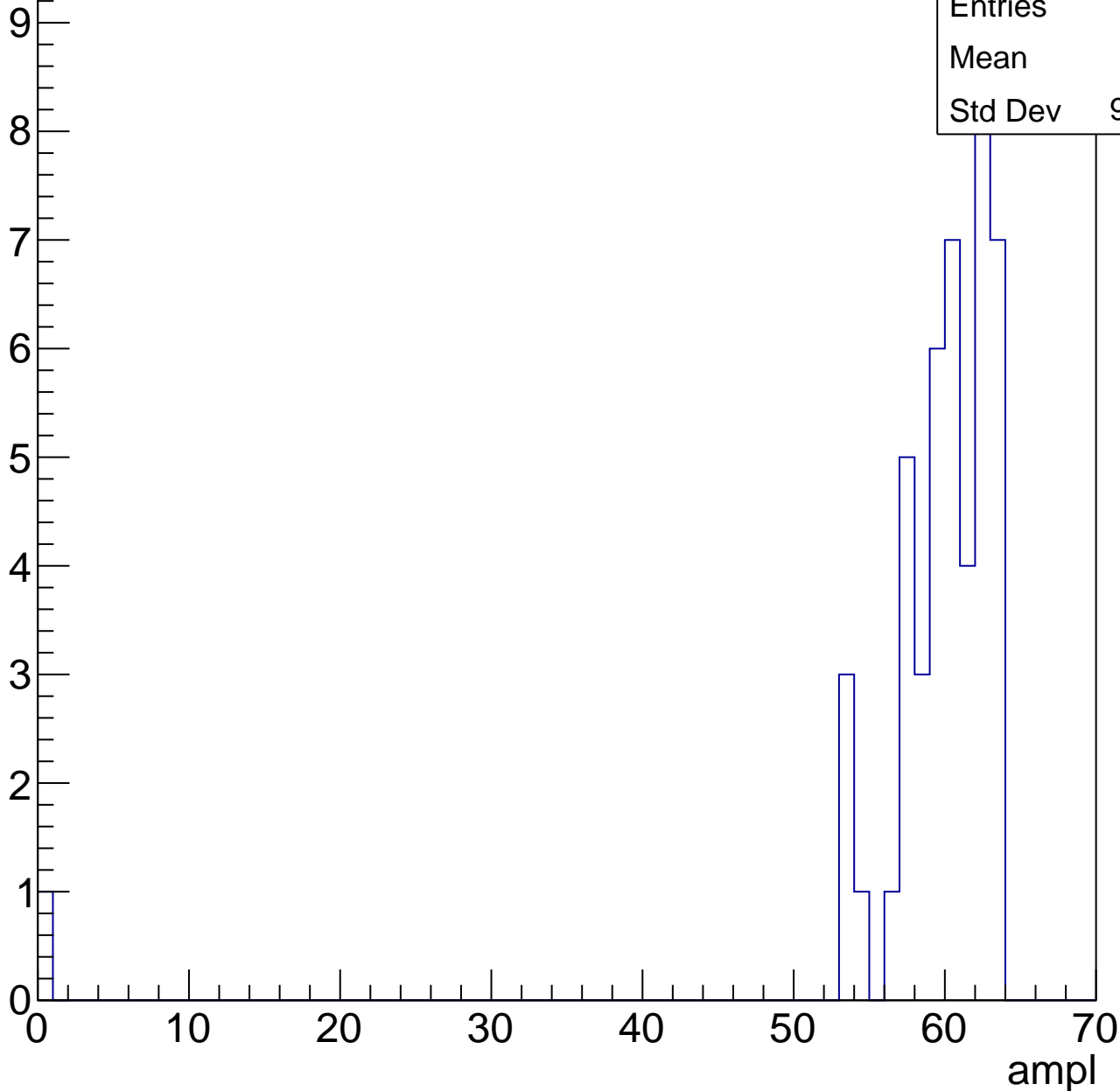


# B1L102S, U24-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.4
Std Dev	9.045



# B1L102S, U24-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	18
Std Dev	0

ampl

# B1L102S, U24-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	87
Mean	29.87
Std Dev	4.945

**Gaus mean : 30.4081**

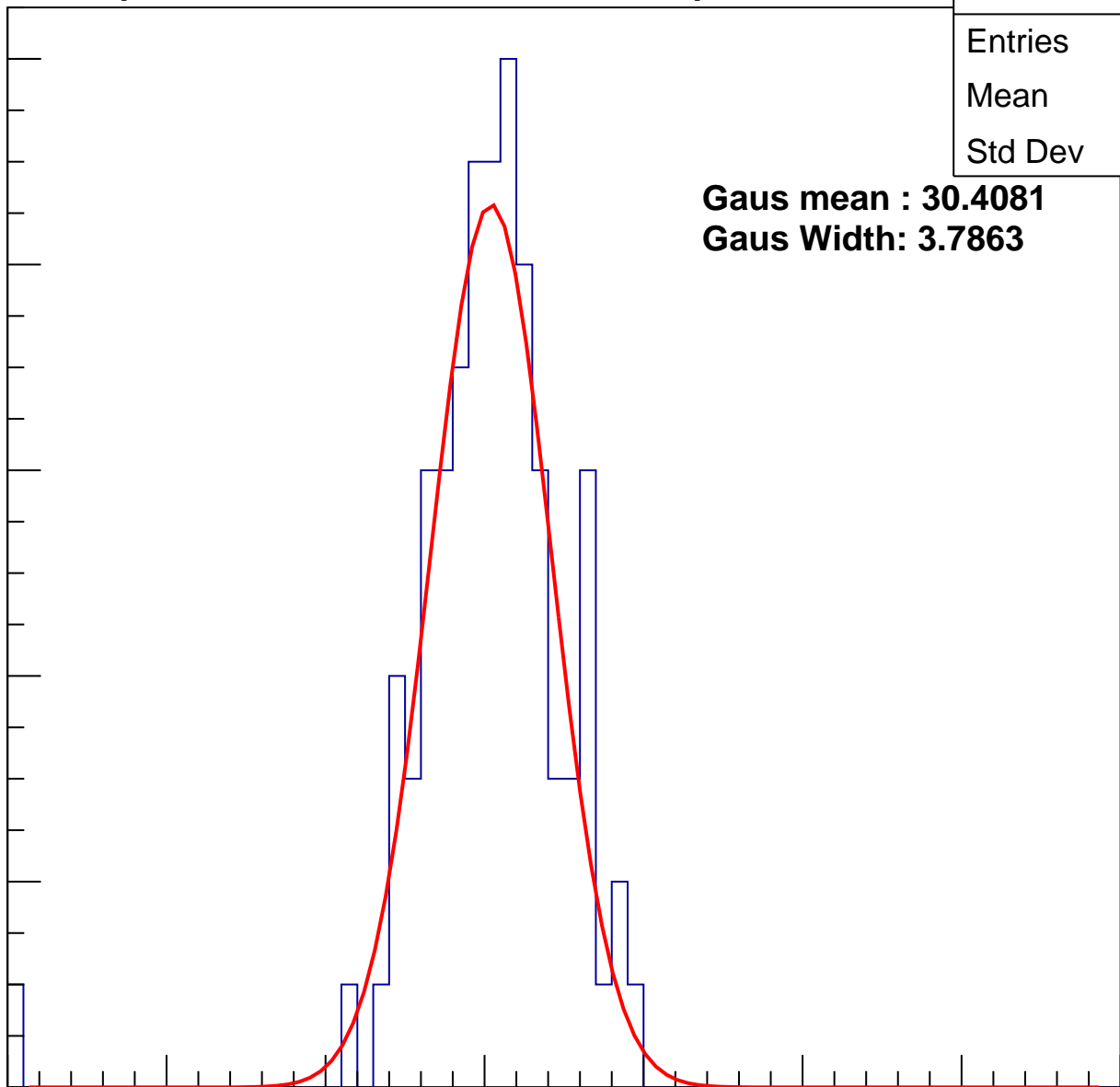
**Gaus Width: 3.7863**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch7, adc1

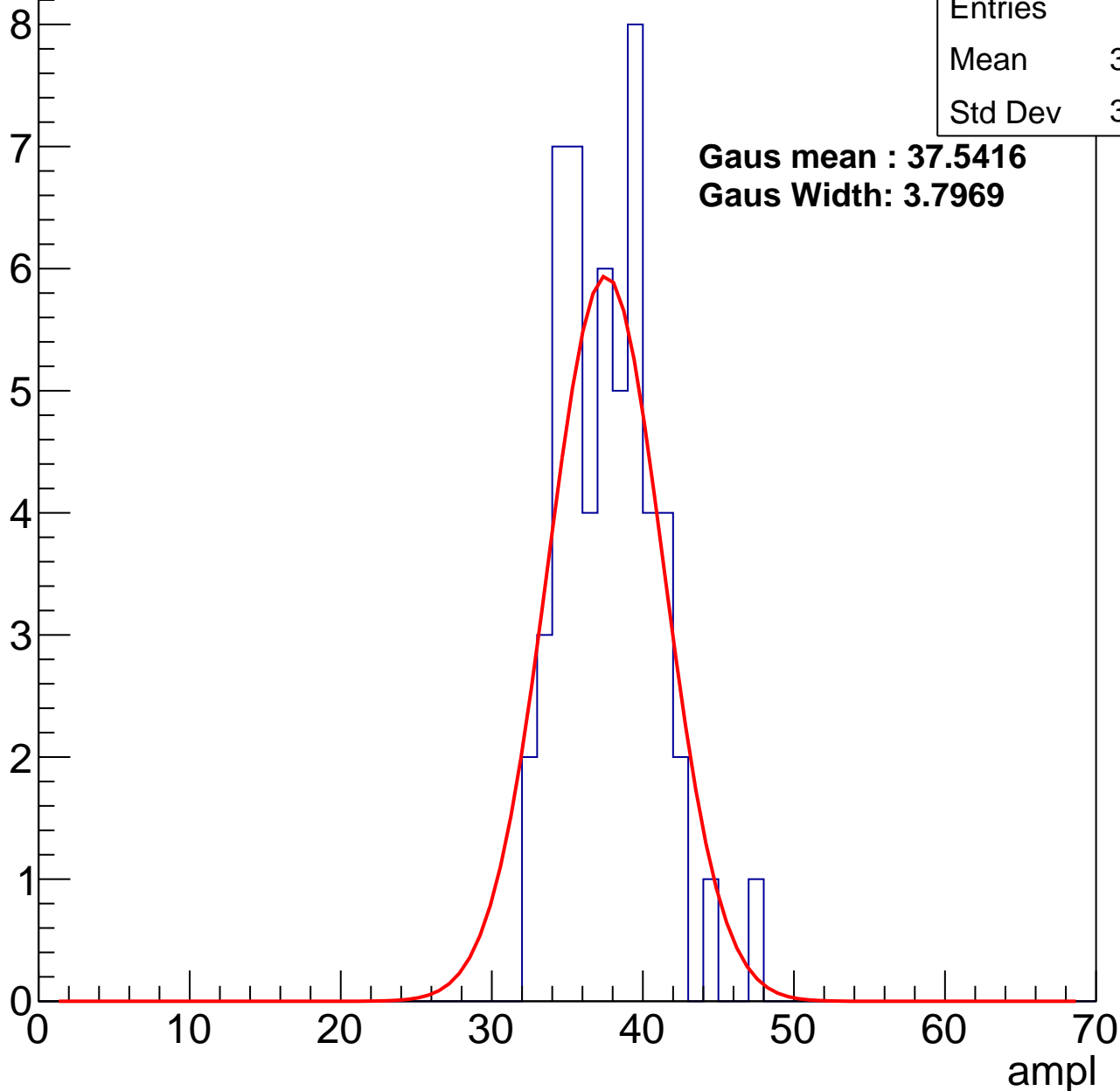
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.28
Std Dev	3.118

**Gaus mean : 37.5416**

**Gaus Width: 3.7969**



# B1L102S, U24-ch7, adc2

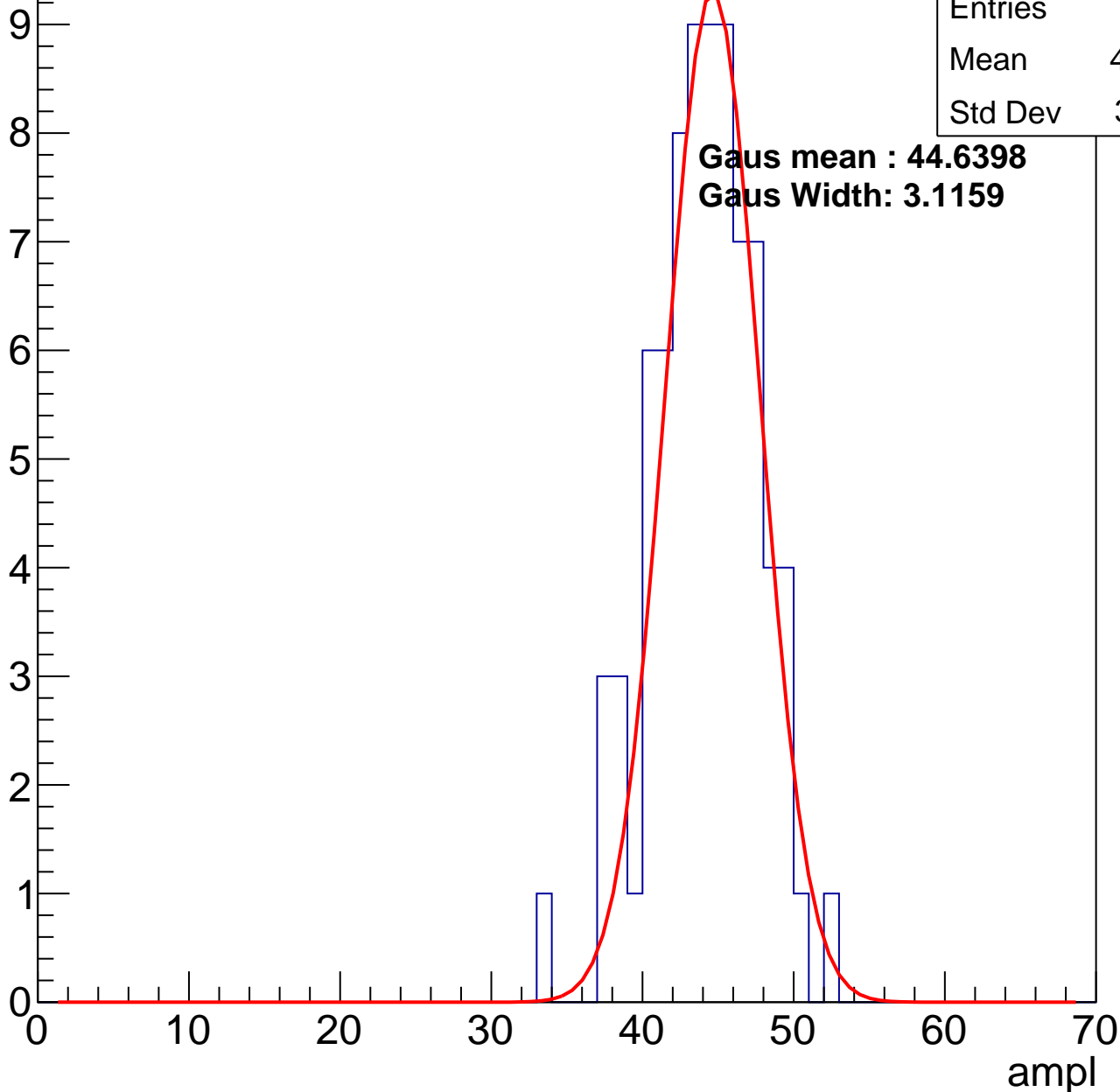
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	43.65
Std Dev	3.461

**Gaus mean : 44.6398**

**Gaus Width: 3.1159**

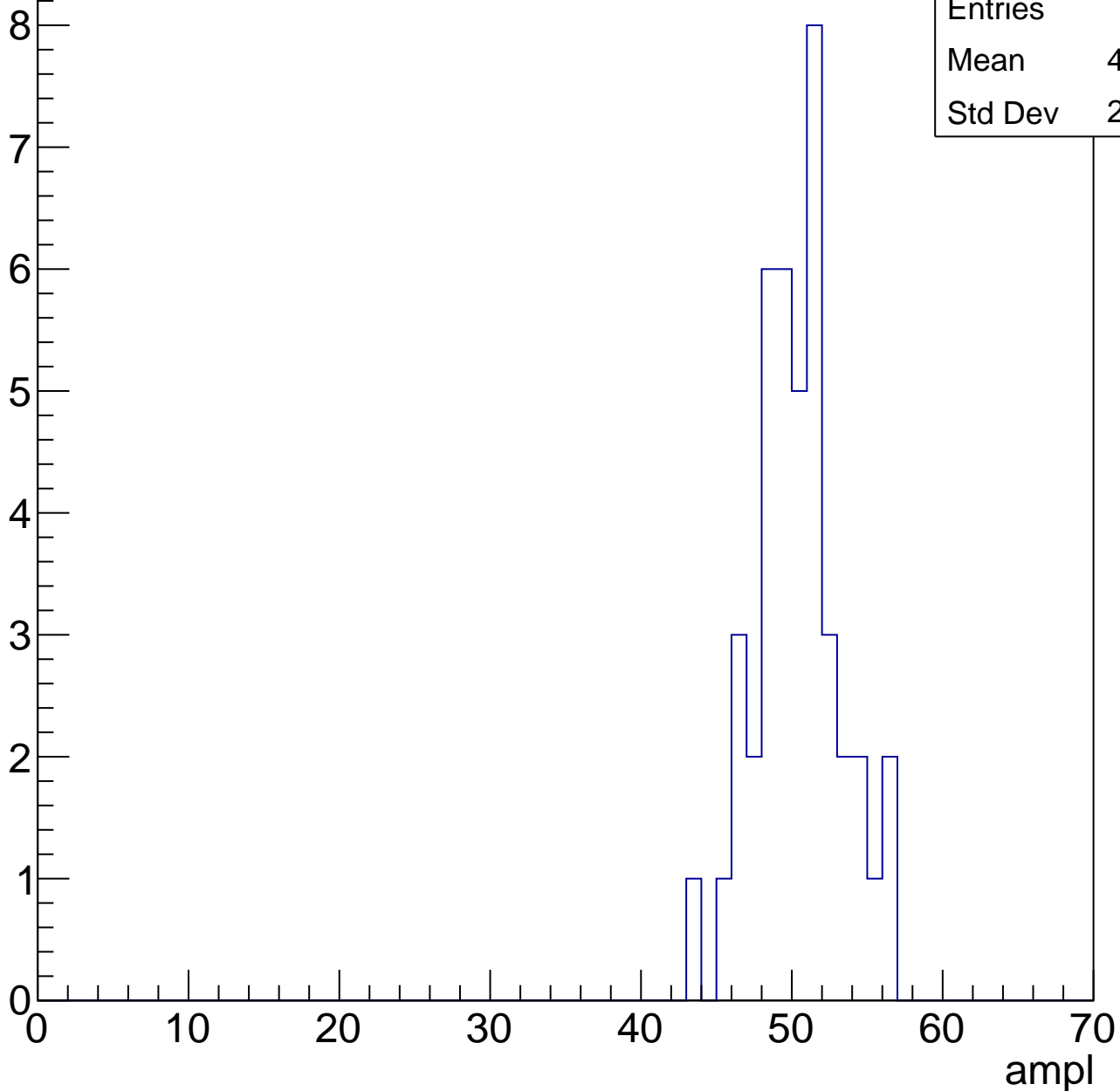


# B1L102S, U24-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	49.93
Std Dev	2.832

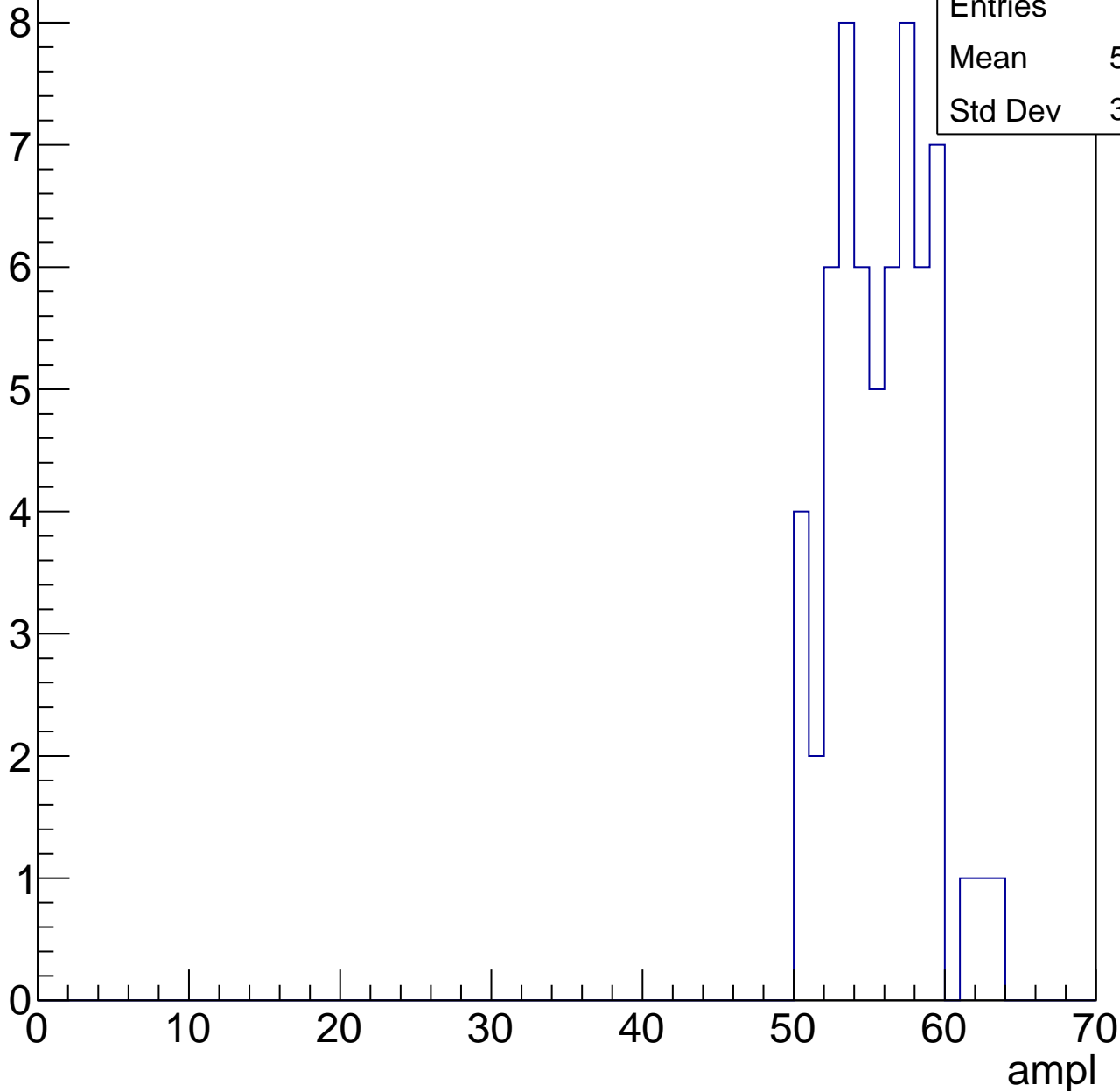


# B1L102S, U24-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.34
Std Dev	3.056



# B1L102S, U24-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

10

Entries 53

Mean 58.72

Std Dev 8.542

8

6

4

2

0

0

10

20

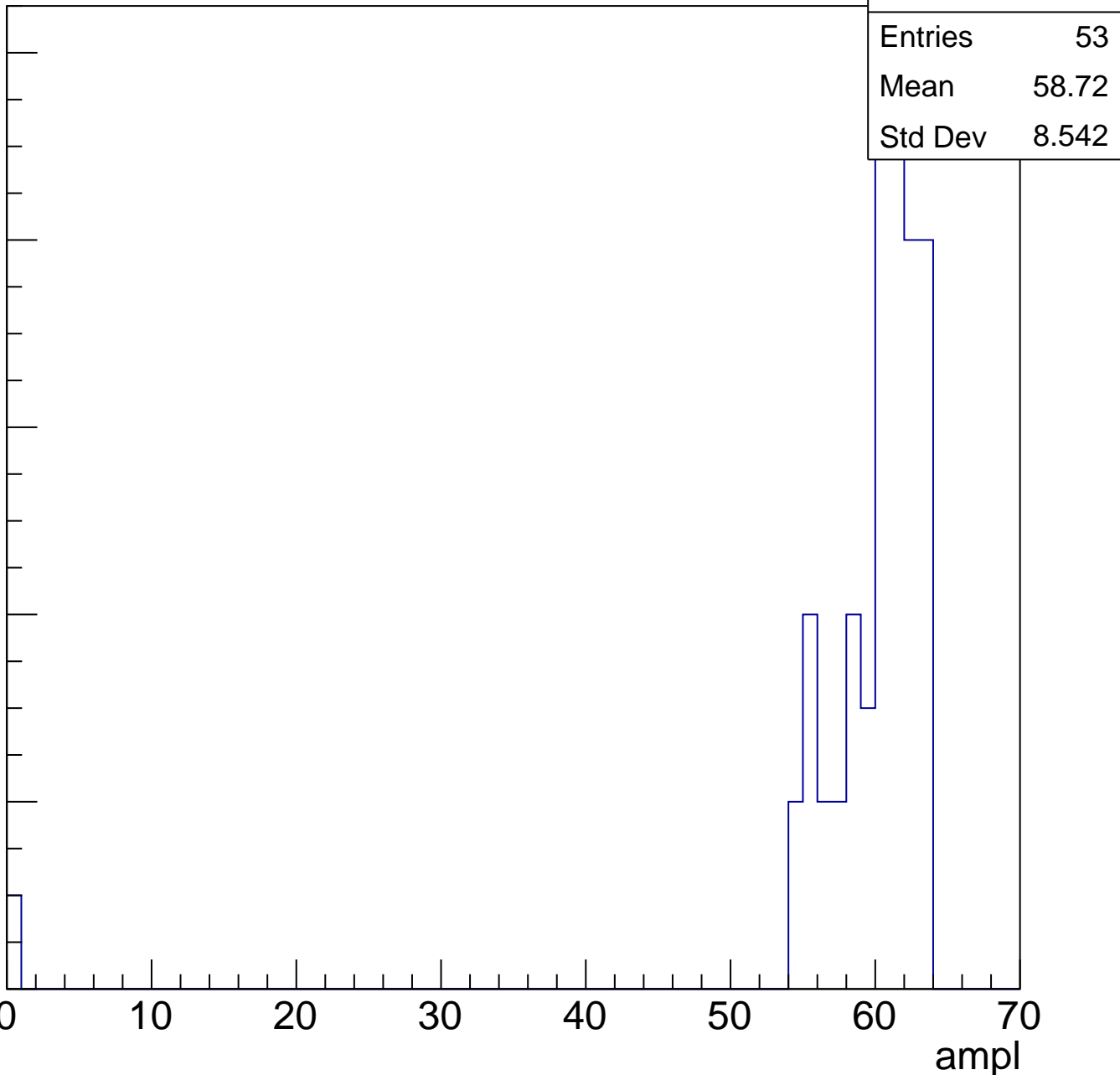
30

40

50

60

ampl



# B1L102S, U24-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch8, adc0

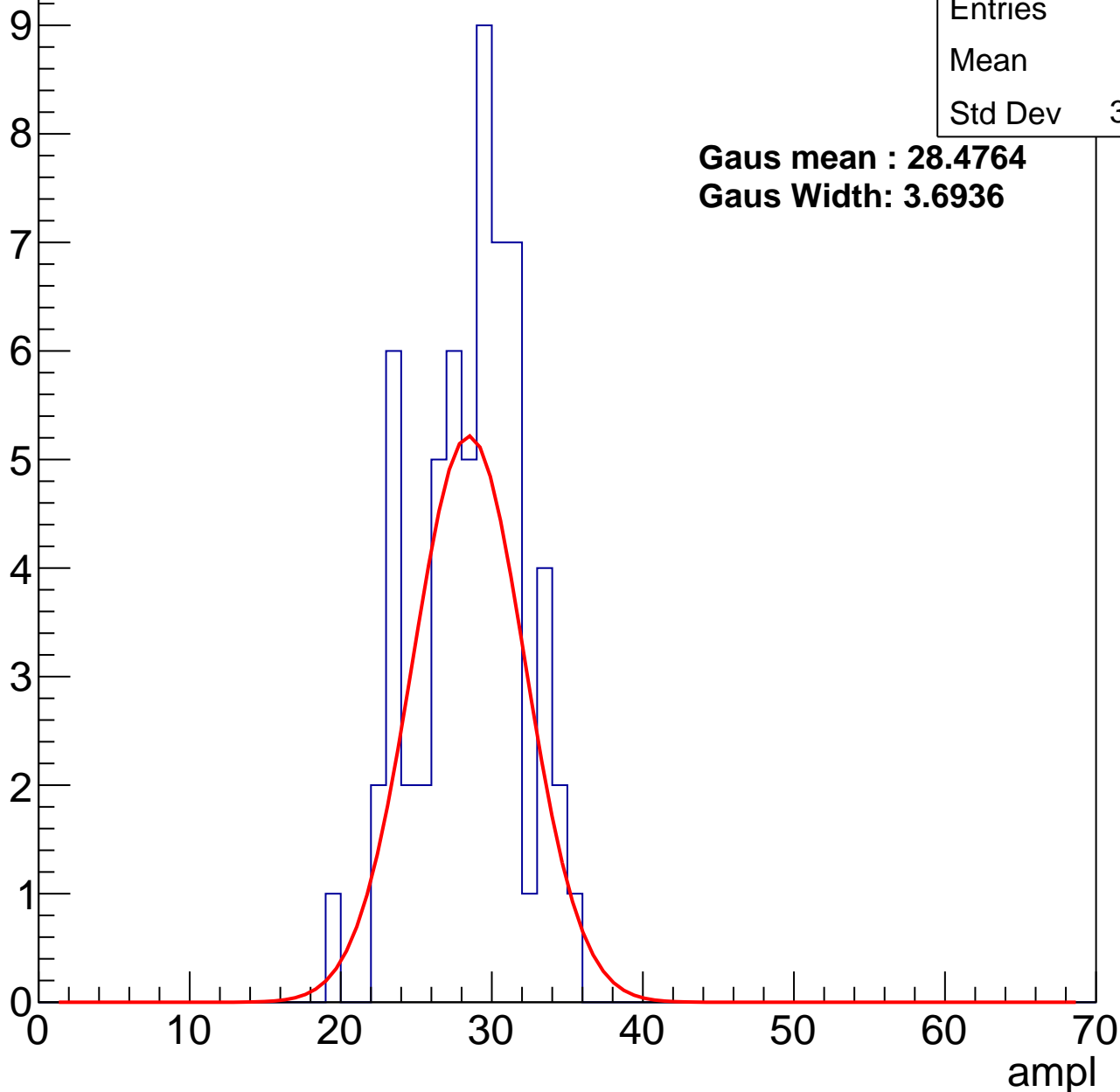
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.1
Std Dev	3.453

**Gaus mean : 28.4764**

**Gaus Width: 3.6936**



# B1L102S, U24-ch8, adc1

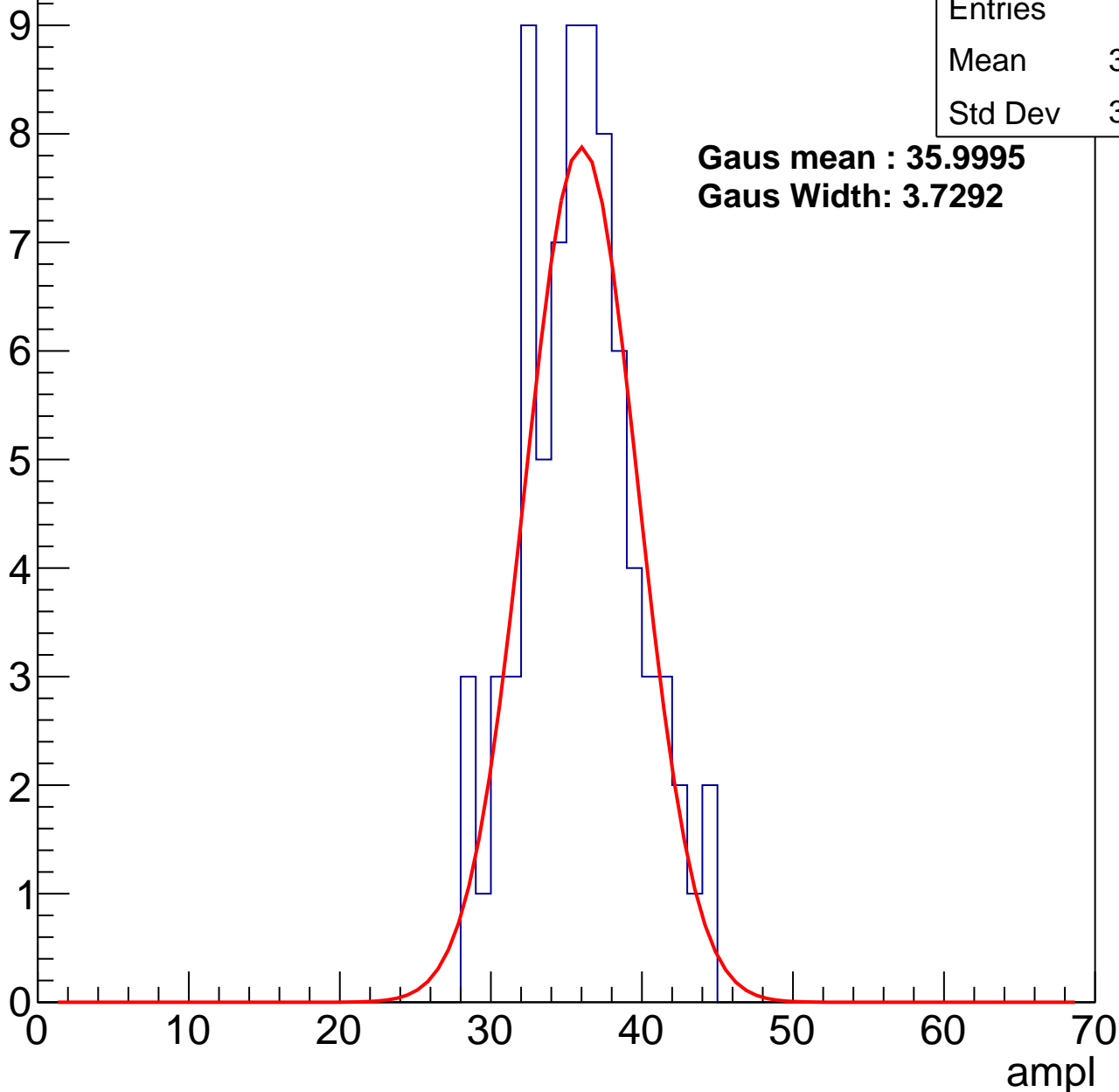
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.44
Std Dev	3.695

**Gaus mean : 35.9995**

**Gaus Width: 3.7292**



# B1L102S, U24-ch8, adc2

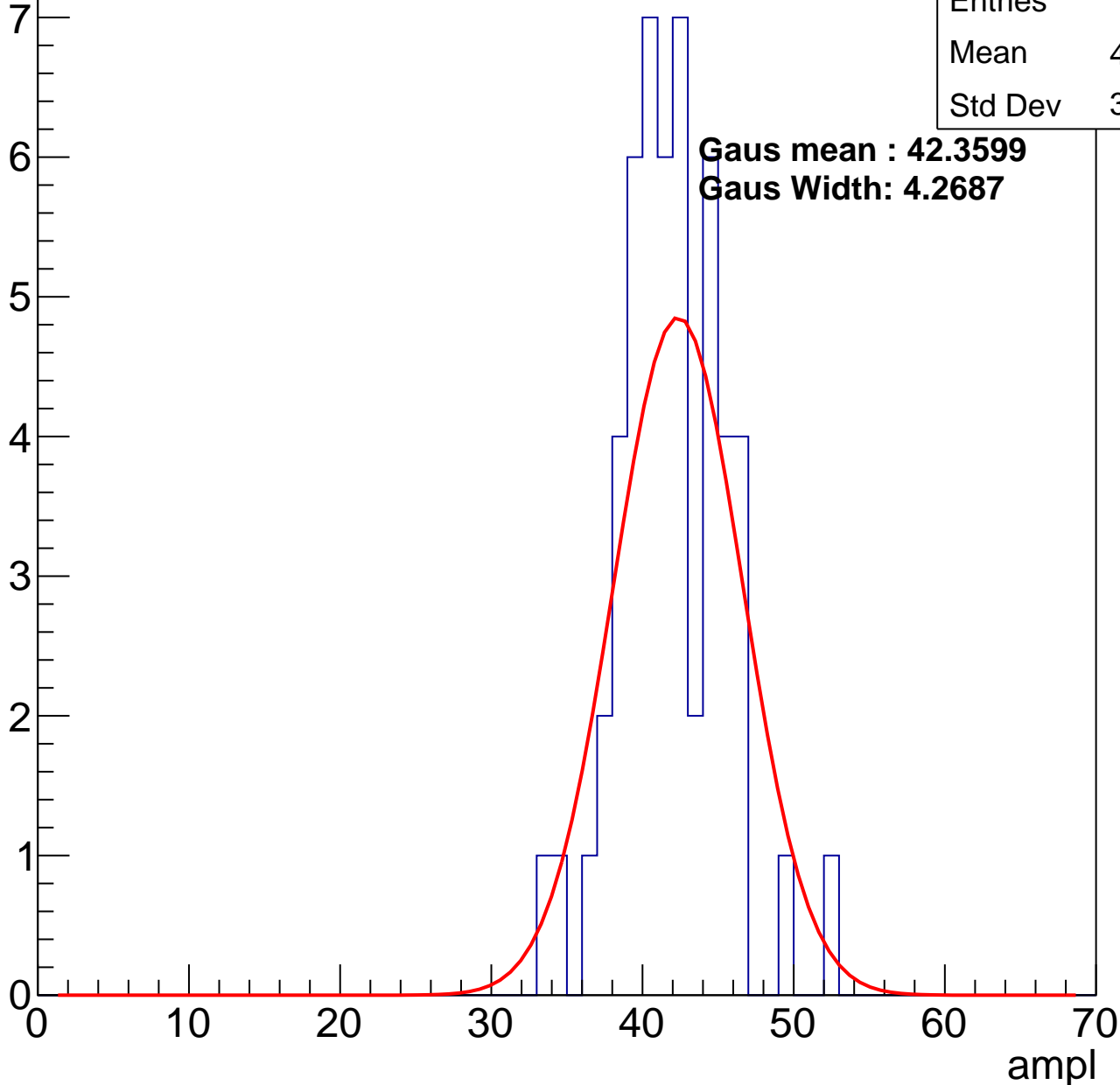
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.47
Std Dev	3.484

**Gaus mean : 42.3599**

**Gaus Width: 4.2687**



# B1L102S, U24-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

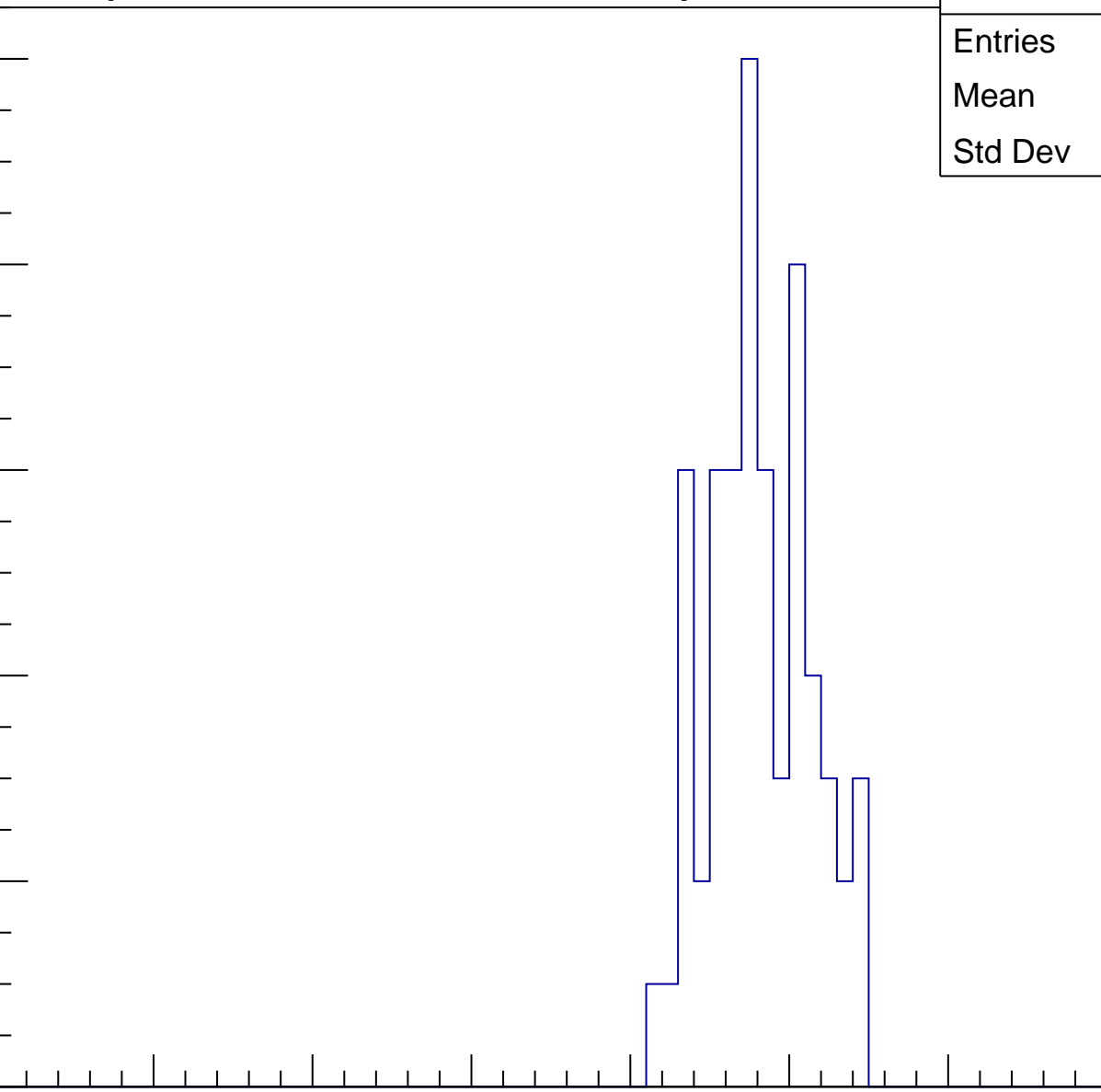
Entries	61
Mean	47.67
Std Dev	3.202

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

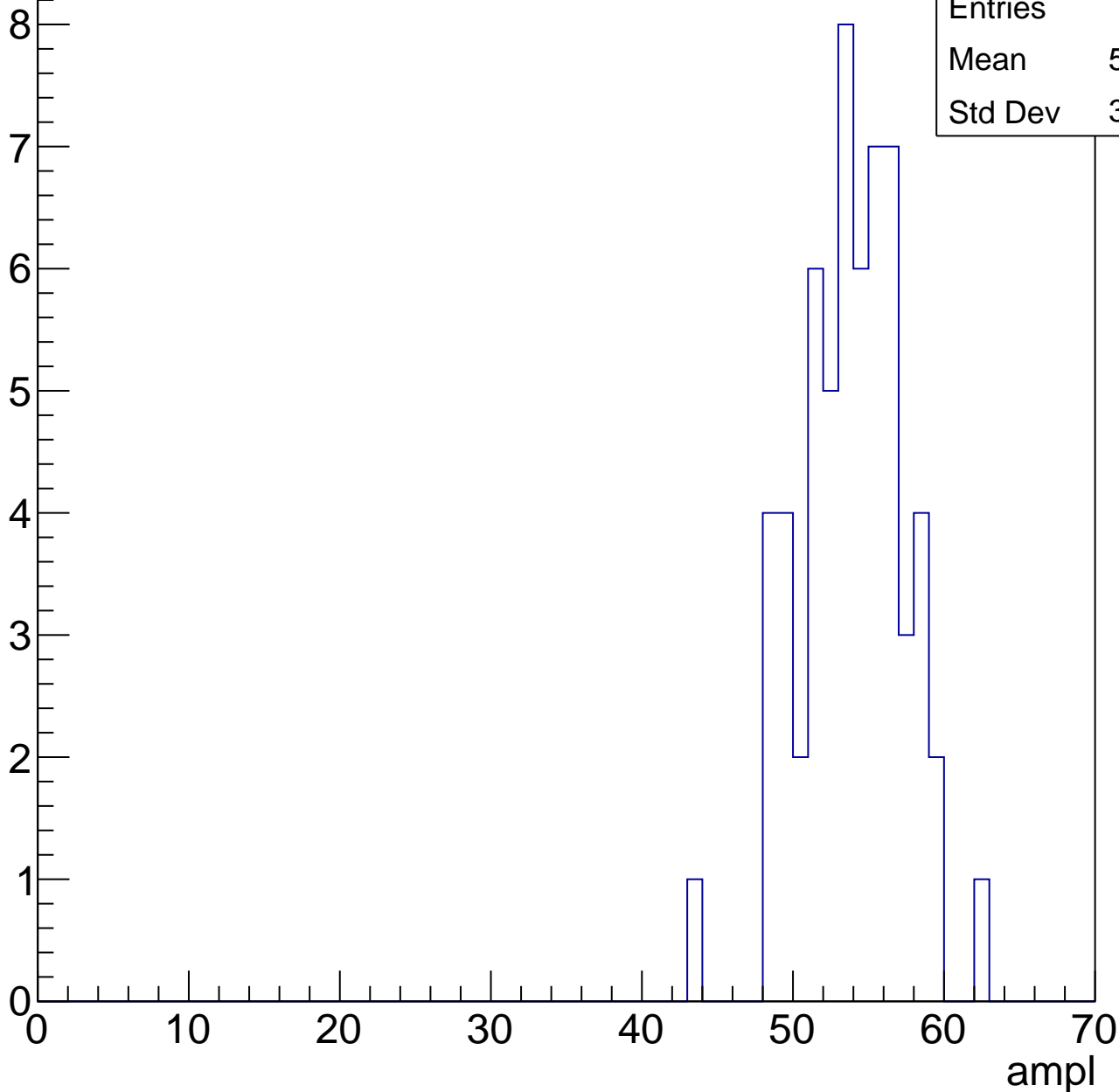


# B1L102S, U24-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.42
Std Dev	3.412

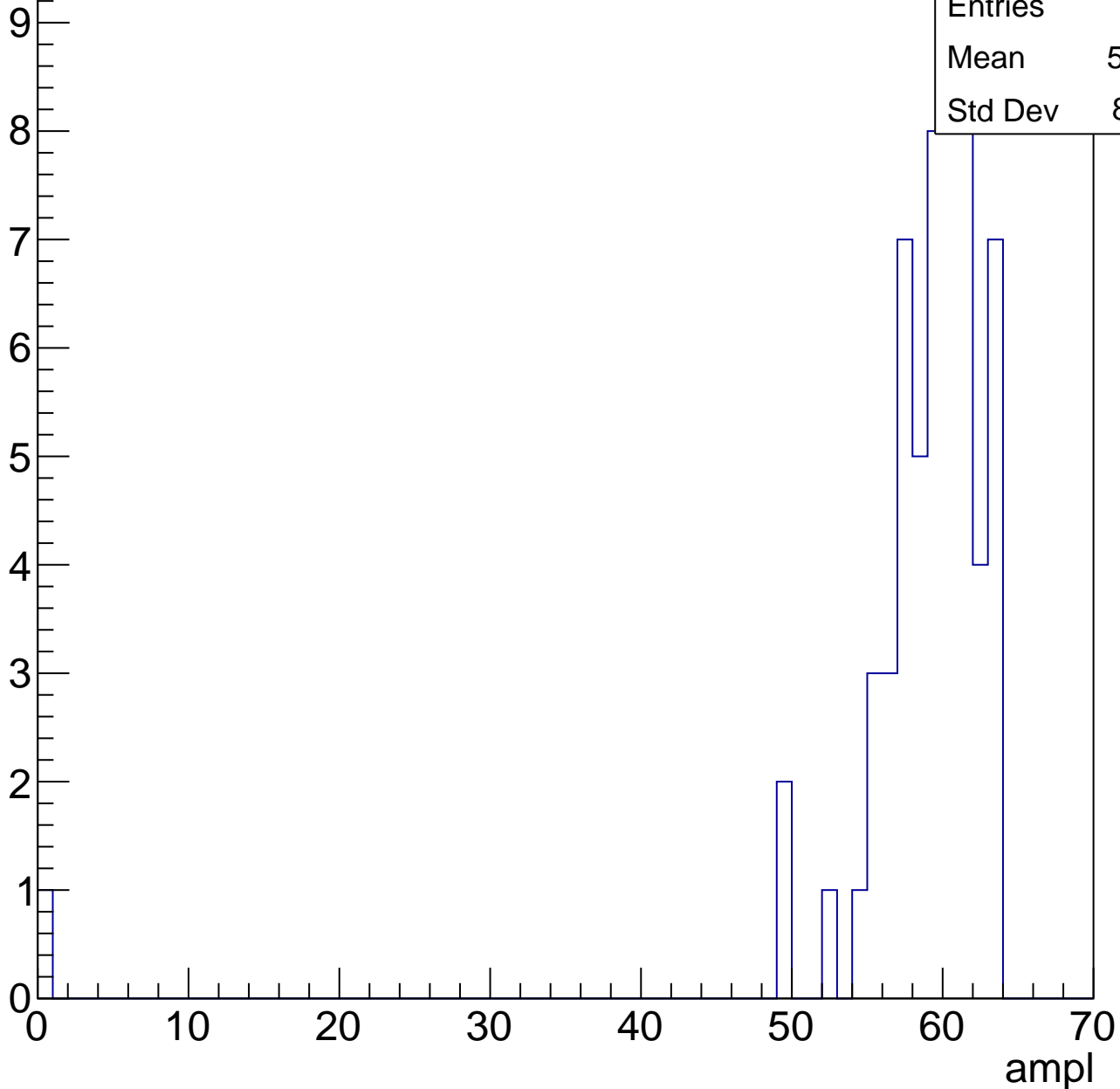


# B1L102S, U24-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.88
Std Dev	8.211

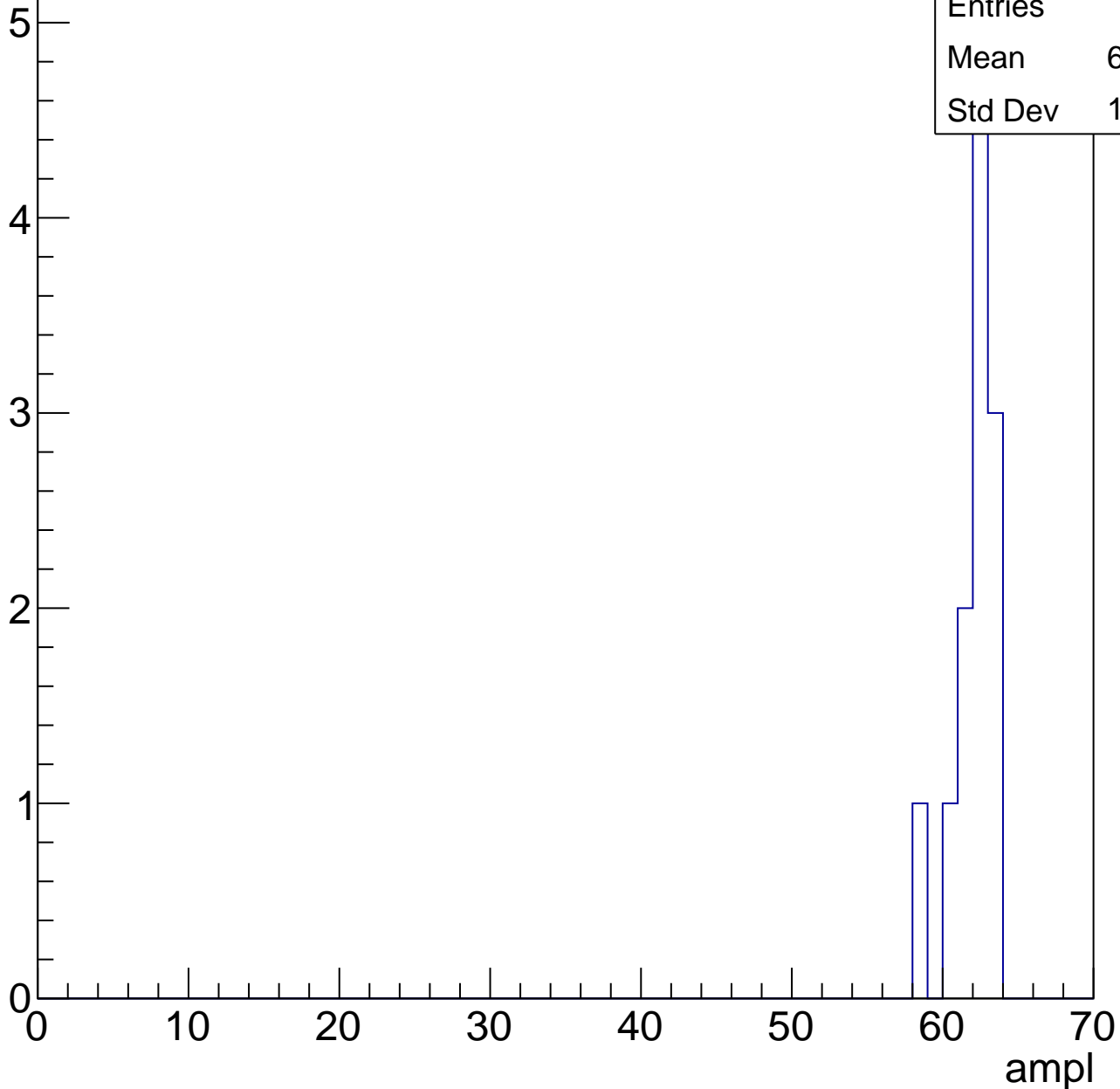


# B1L102S, U24-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.58
Std Dev	1.382





# B1L102S, U24-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch9, adc0

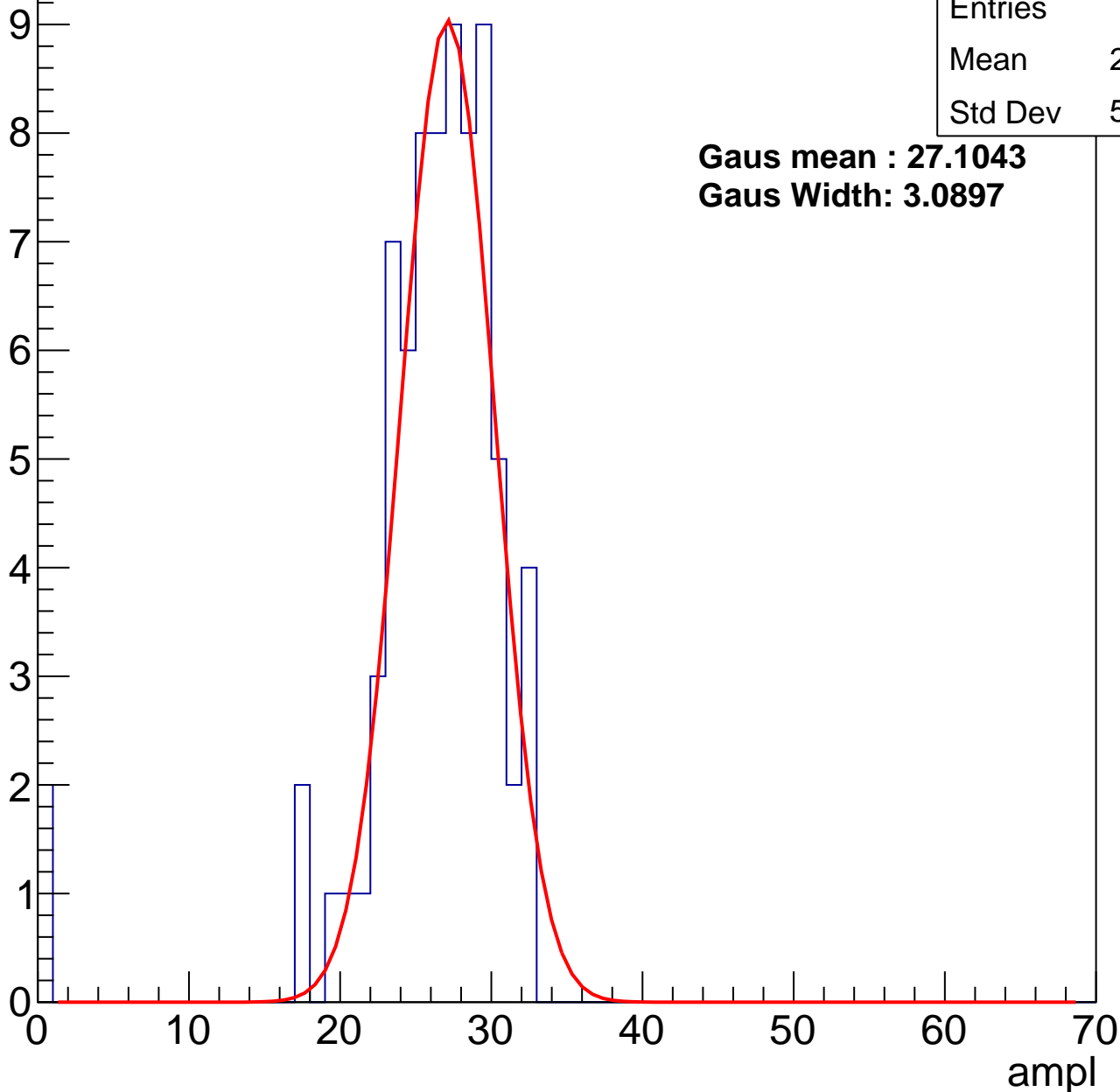
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	25.54
Std Dev	5.315

**Gaus mean : 27.1043**

**Gaus Width: 3.0897**



# B1L102S, U24-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	34.6
Std Dev	3.517

**Gaus mean : 35.1910**

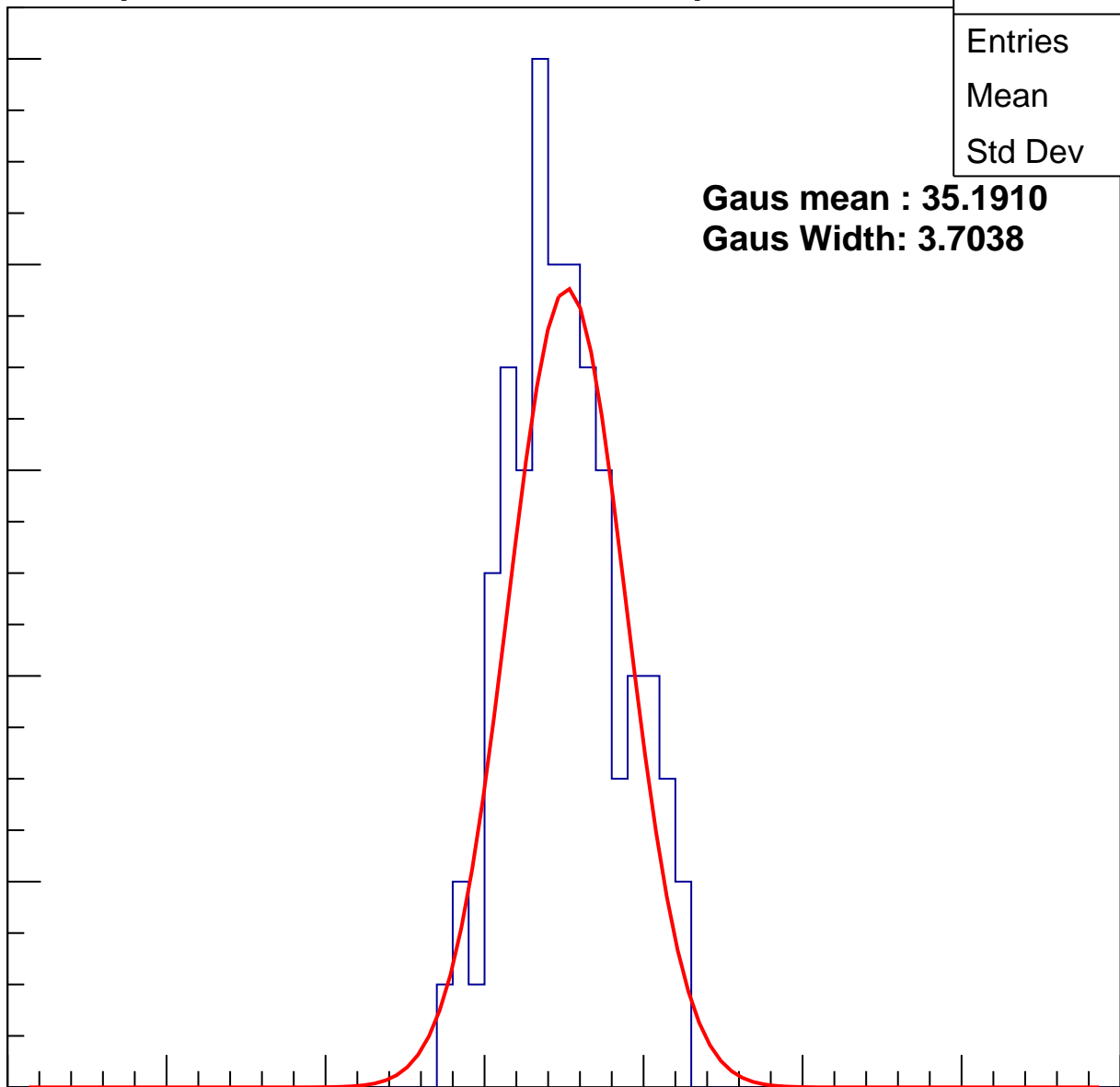
**Gaus Width: 3.7038**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch9, adc2

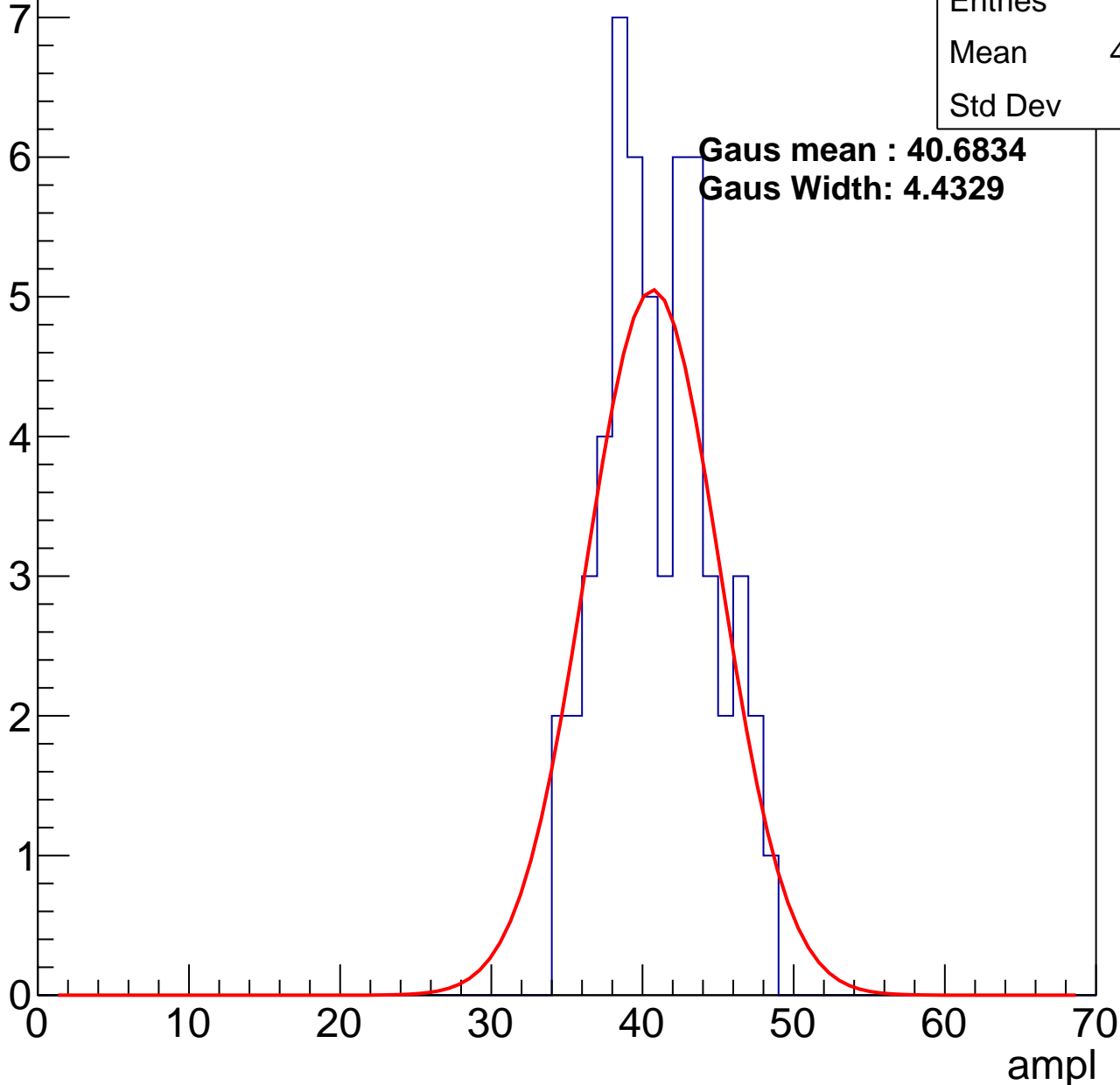
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.53
Std Dev	3.5

**Gaus mean : 40.6834**

**Gaus Width: 4.4329**

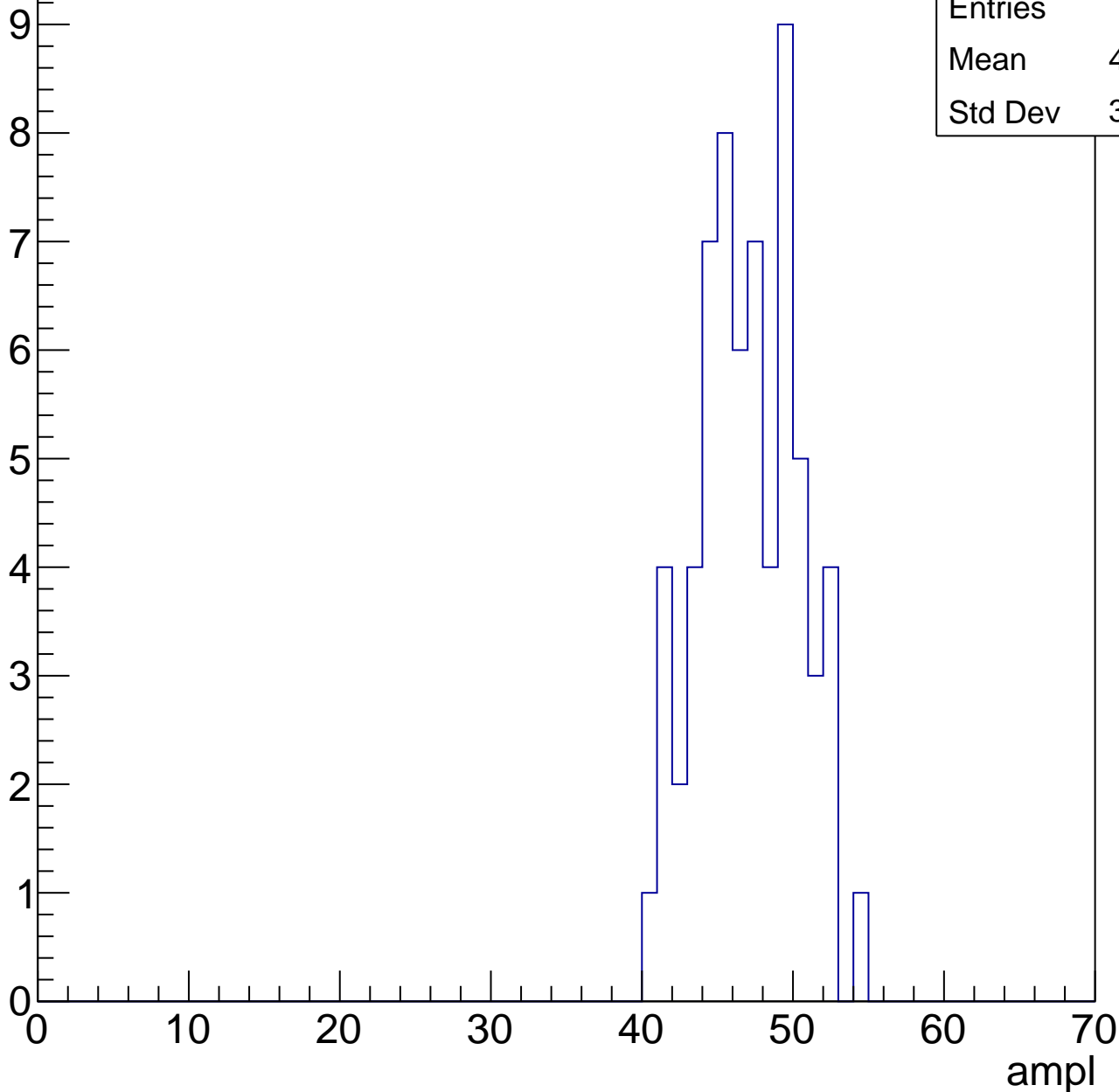


# B1L102S, U24-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	46.63
Std Dev	3.237

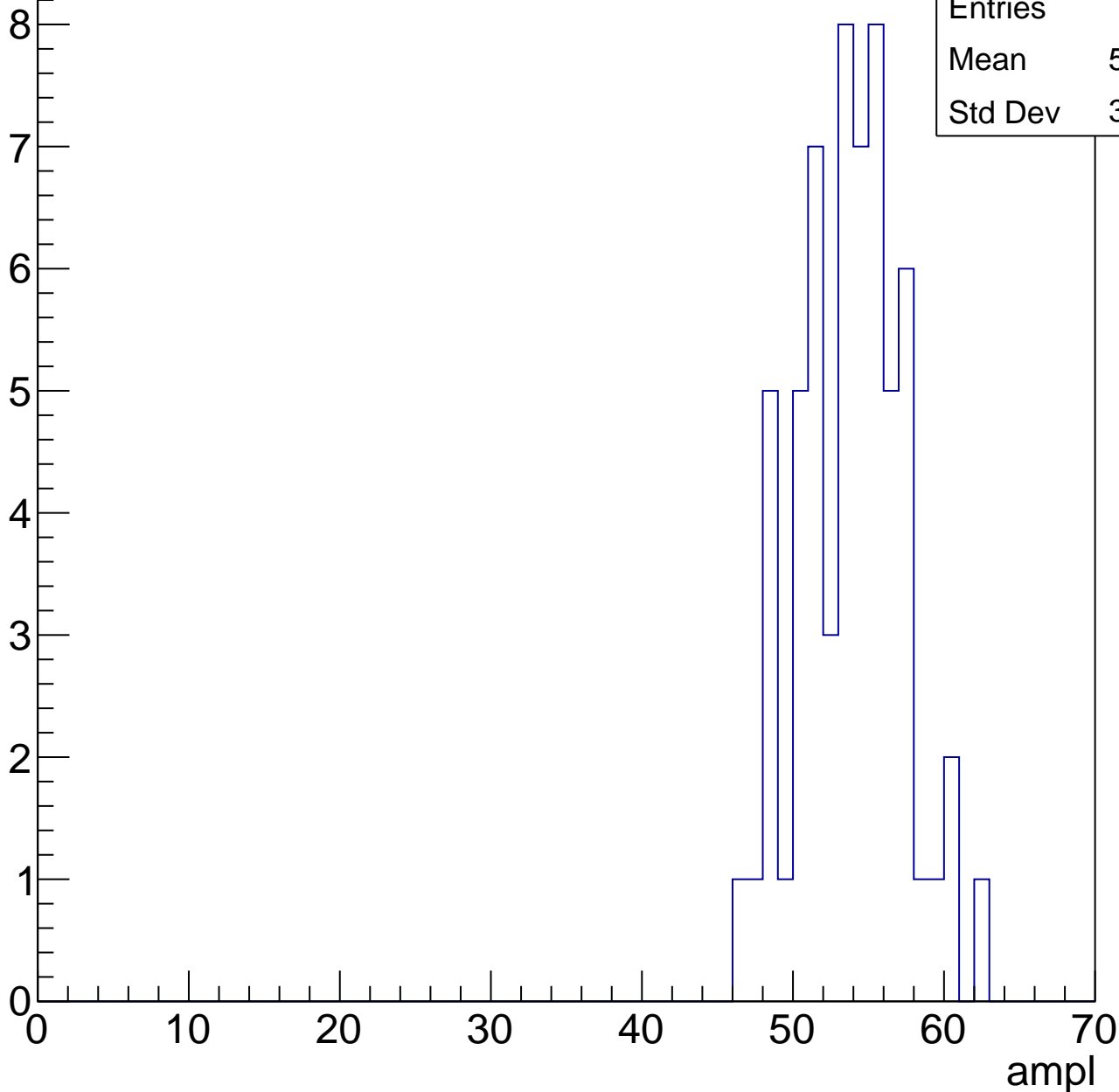


# B1L102S, U24-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.35
Std Dev	3.394

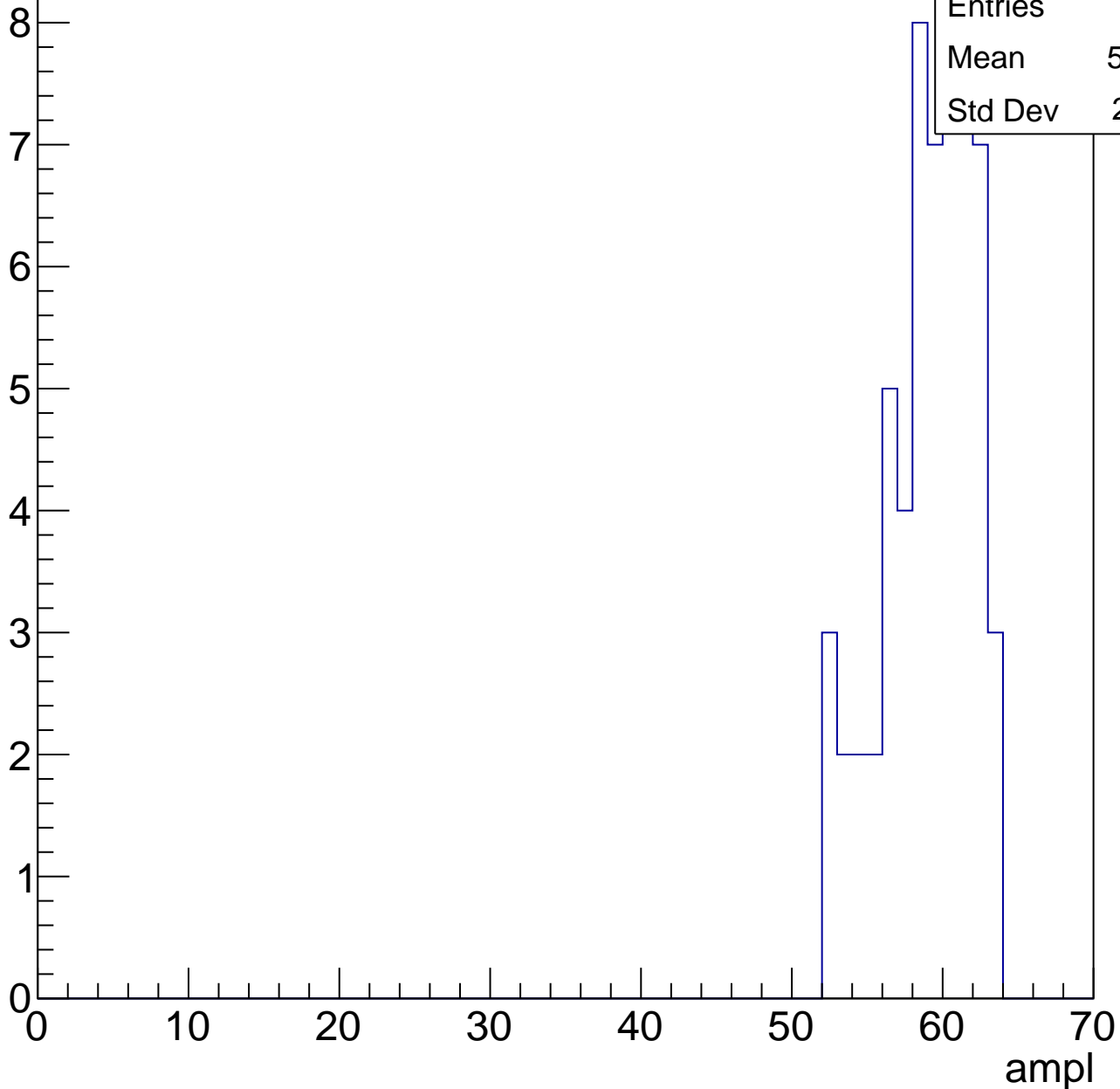


# B1L102S, U24-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.58
Std Dev	2.941

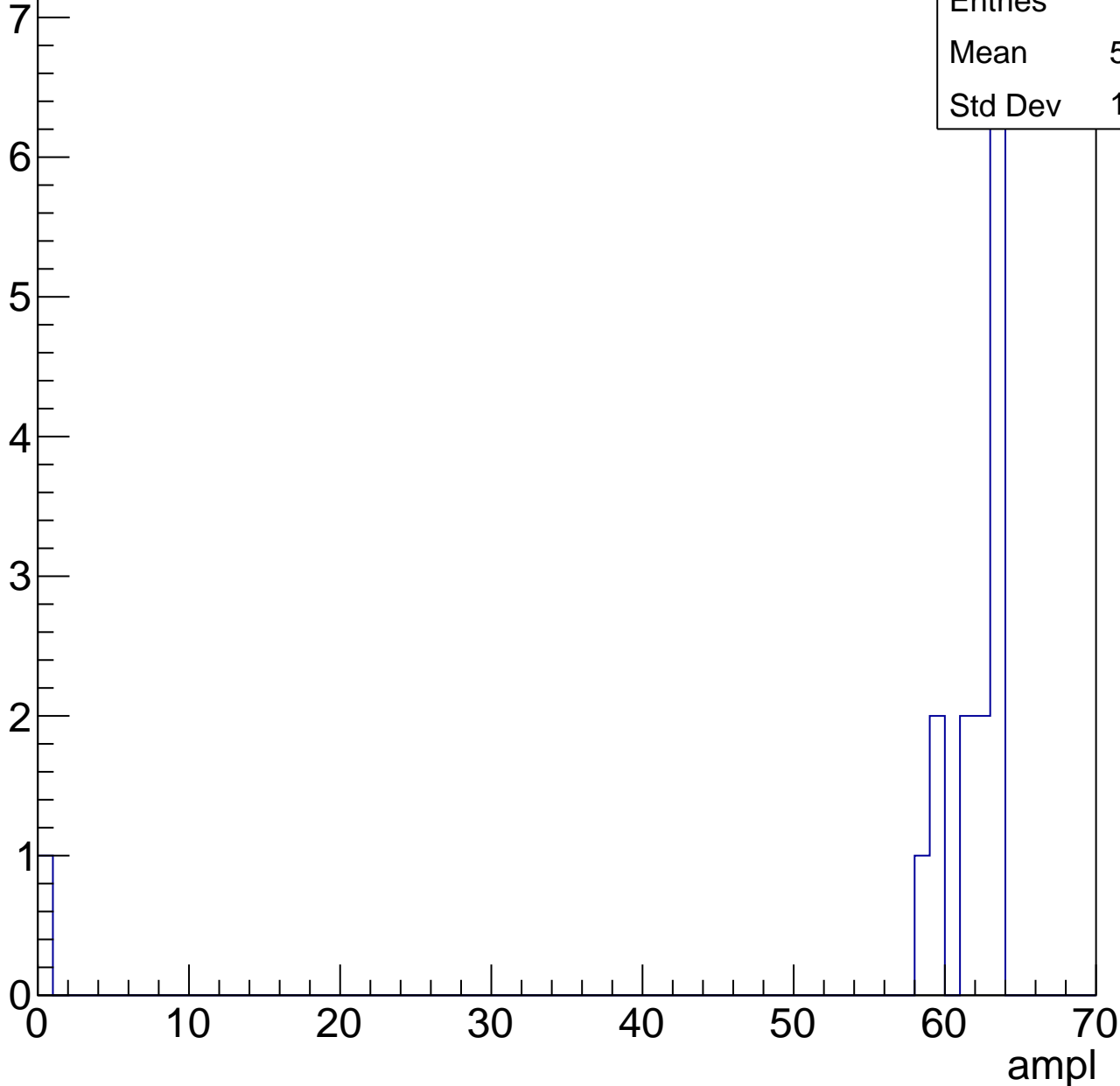


# B1L102S, U24-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.53
Std Dev	15.47





# B1L102S, U24-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch10, adc0

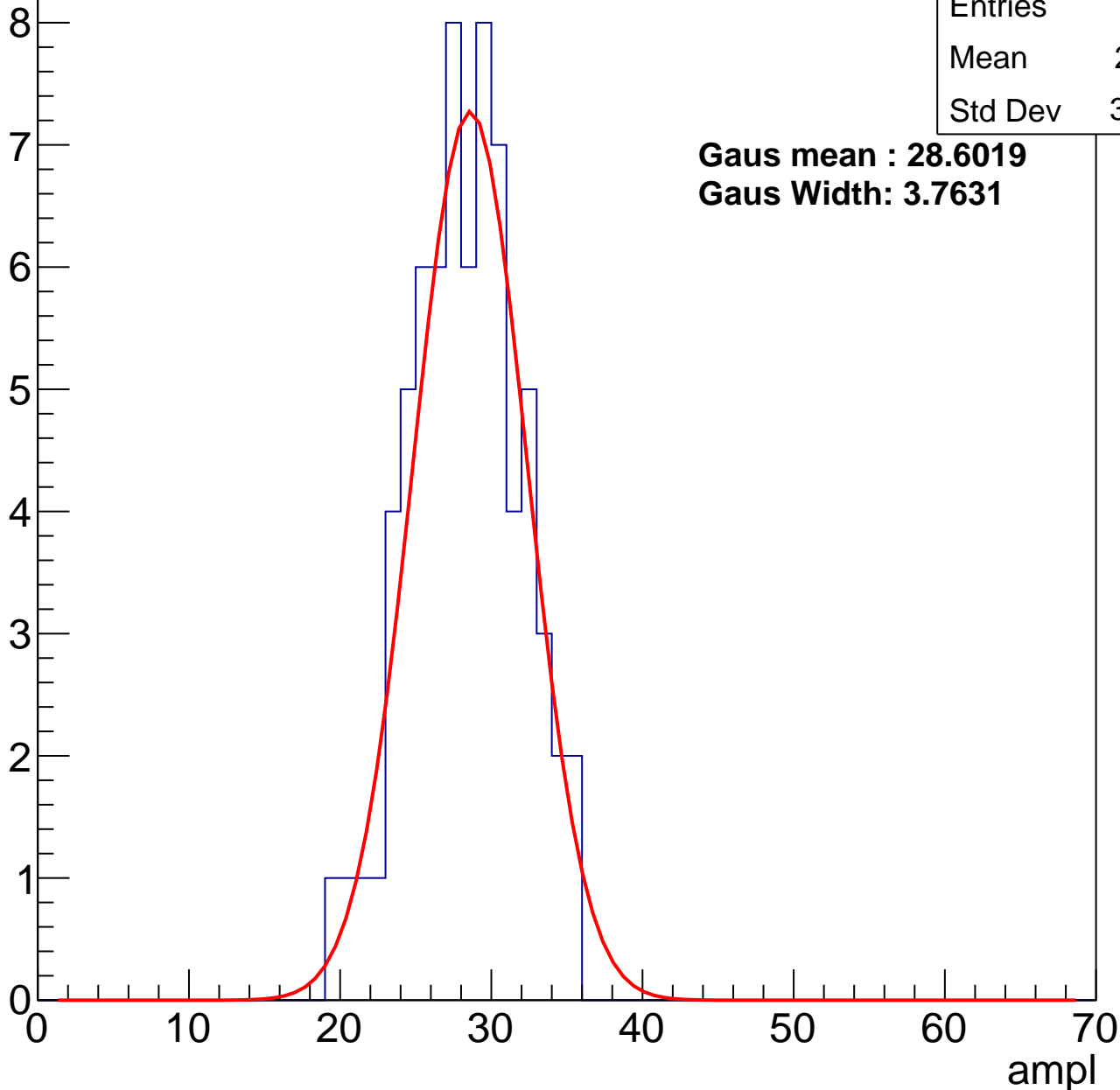
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.81
Std Dev	3.567

**Gaus mean : 28.6019**

**Gaus Width: 3.7631**



# B1L102S, U24-ch10, adc1

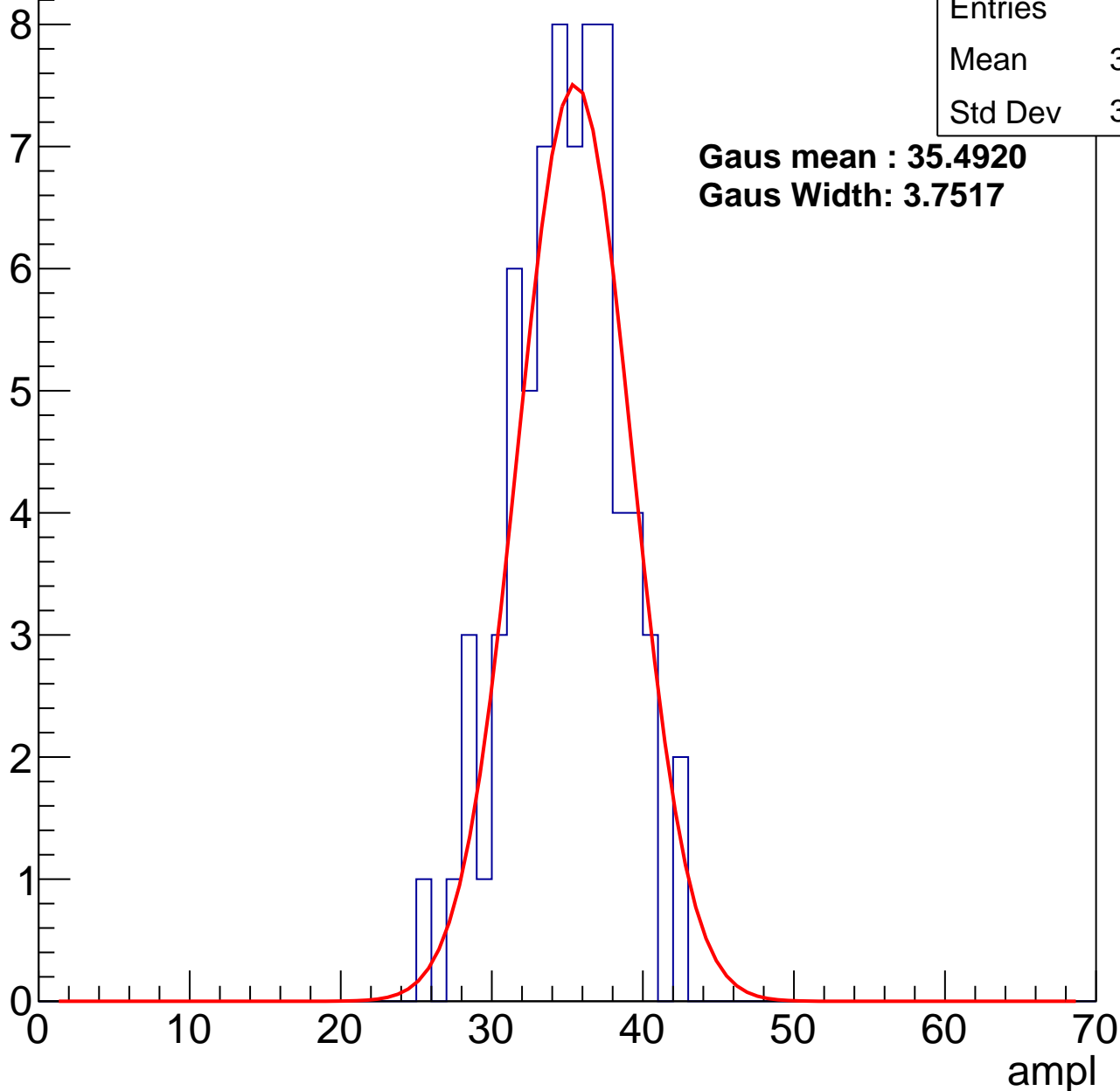
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.44
Std Dev	3.539

**Gaus mean : 35.4920**

**Gaus Width: 3.7517**



# B1L102S, U24-ch10, adc2

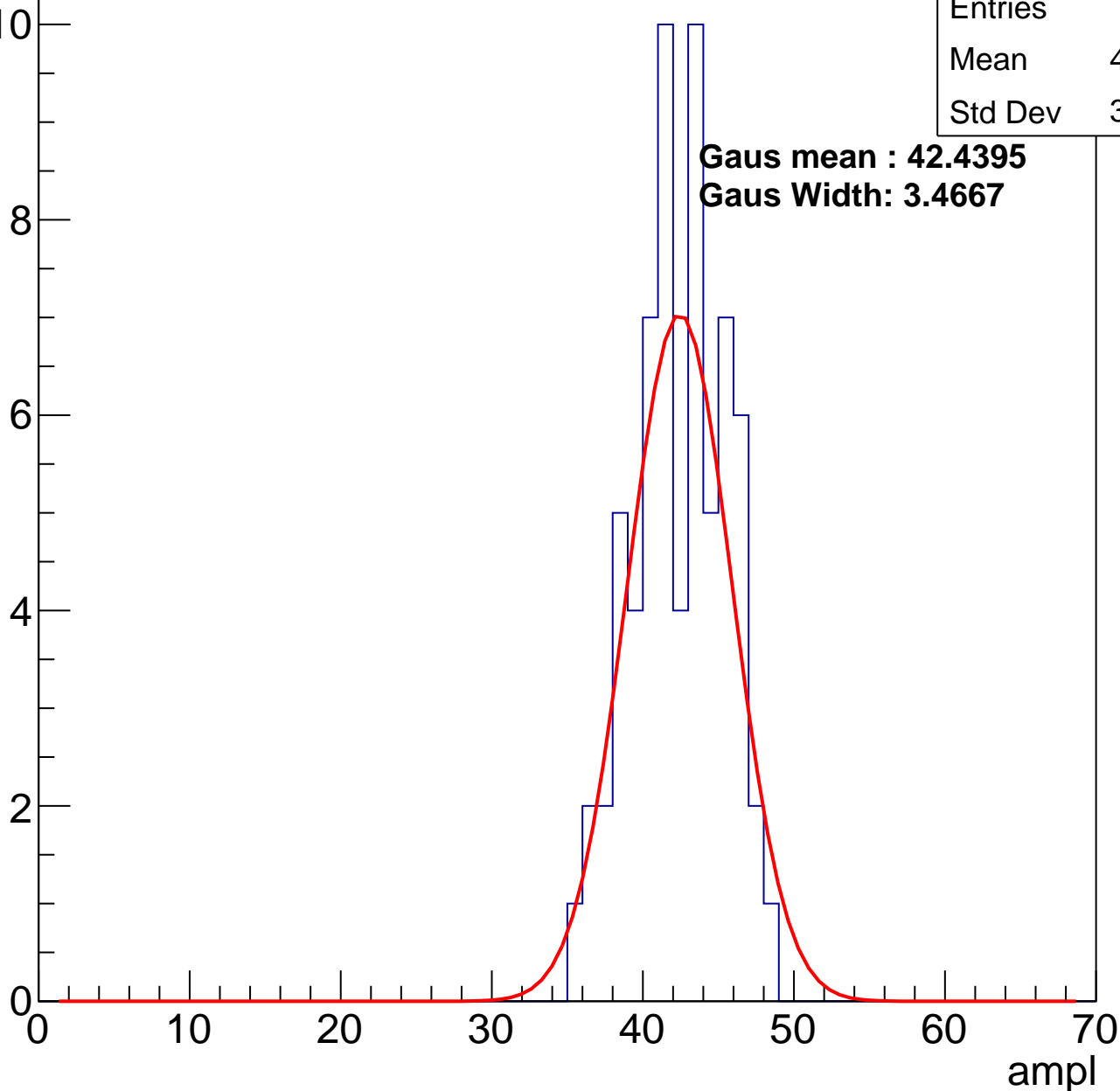
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.94
Std Dev	3.015

**Gaus mean : 42.4395**

**Gaus Width: 3.4667**

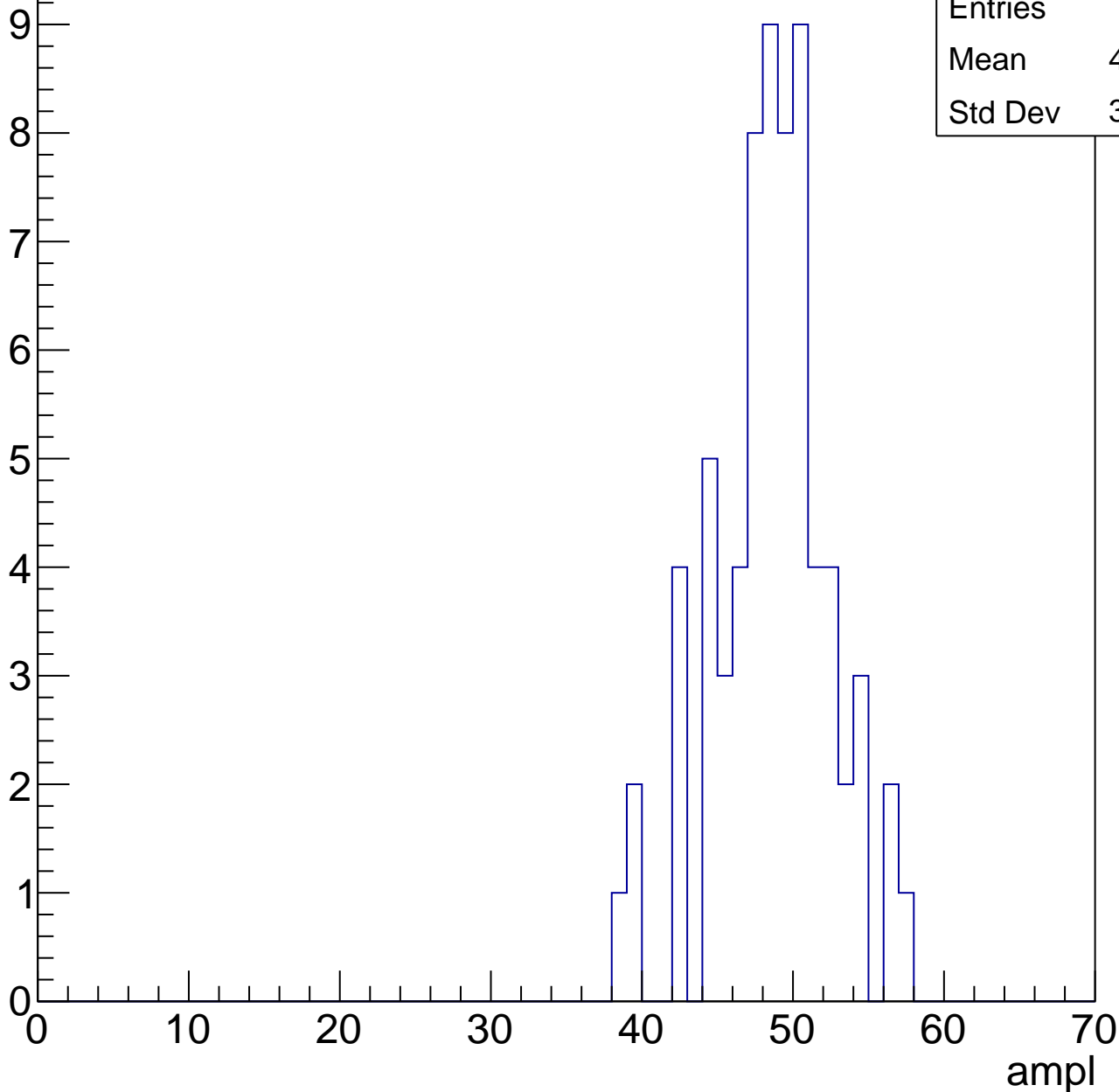


# B1L102S, U24-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

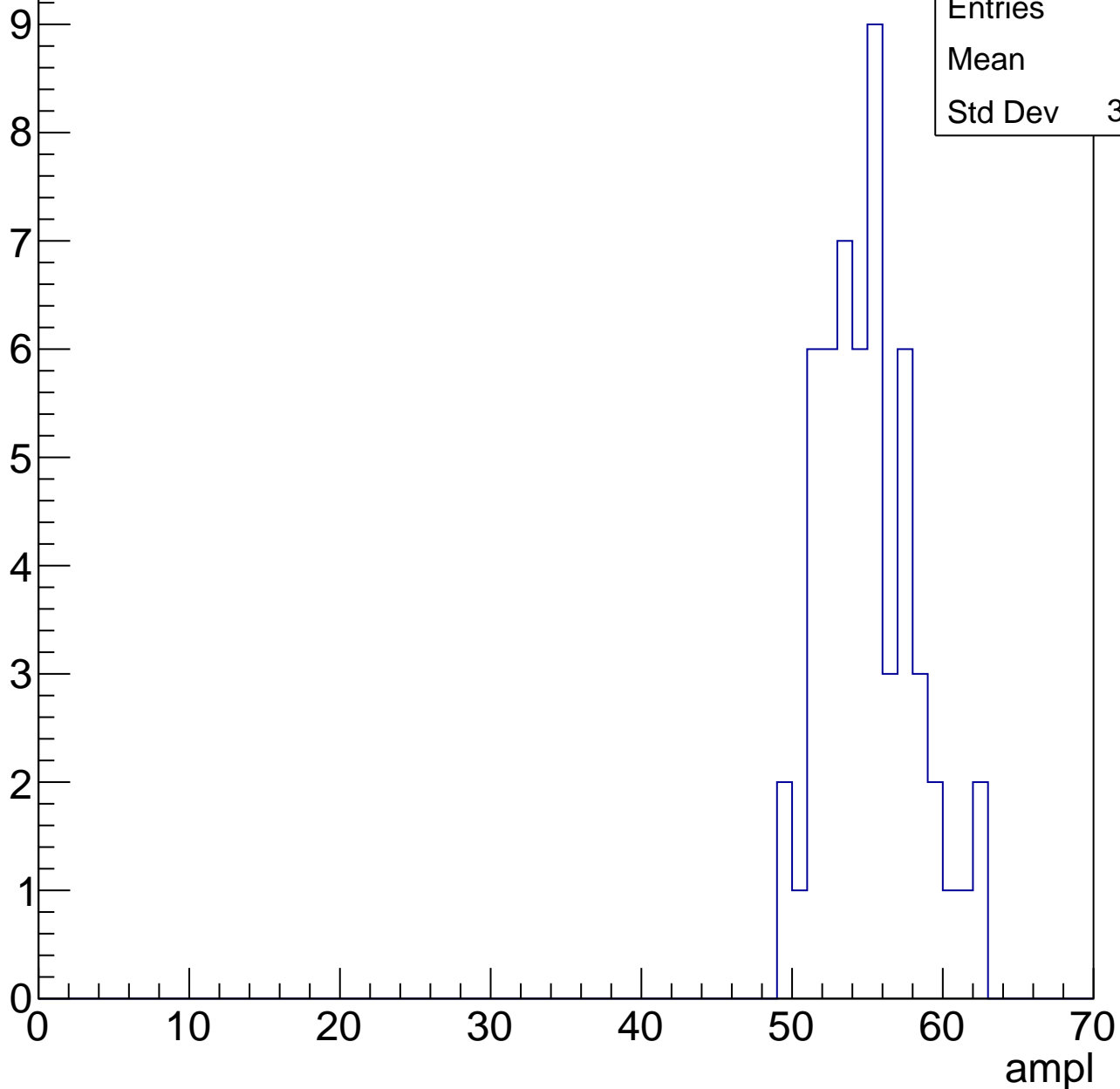
Entries	69
Mean	48.14
Std Dev	3.895



# B1L102S, U24-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

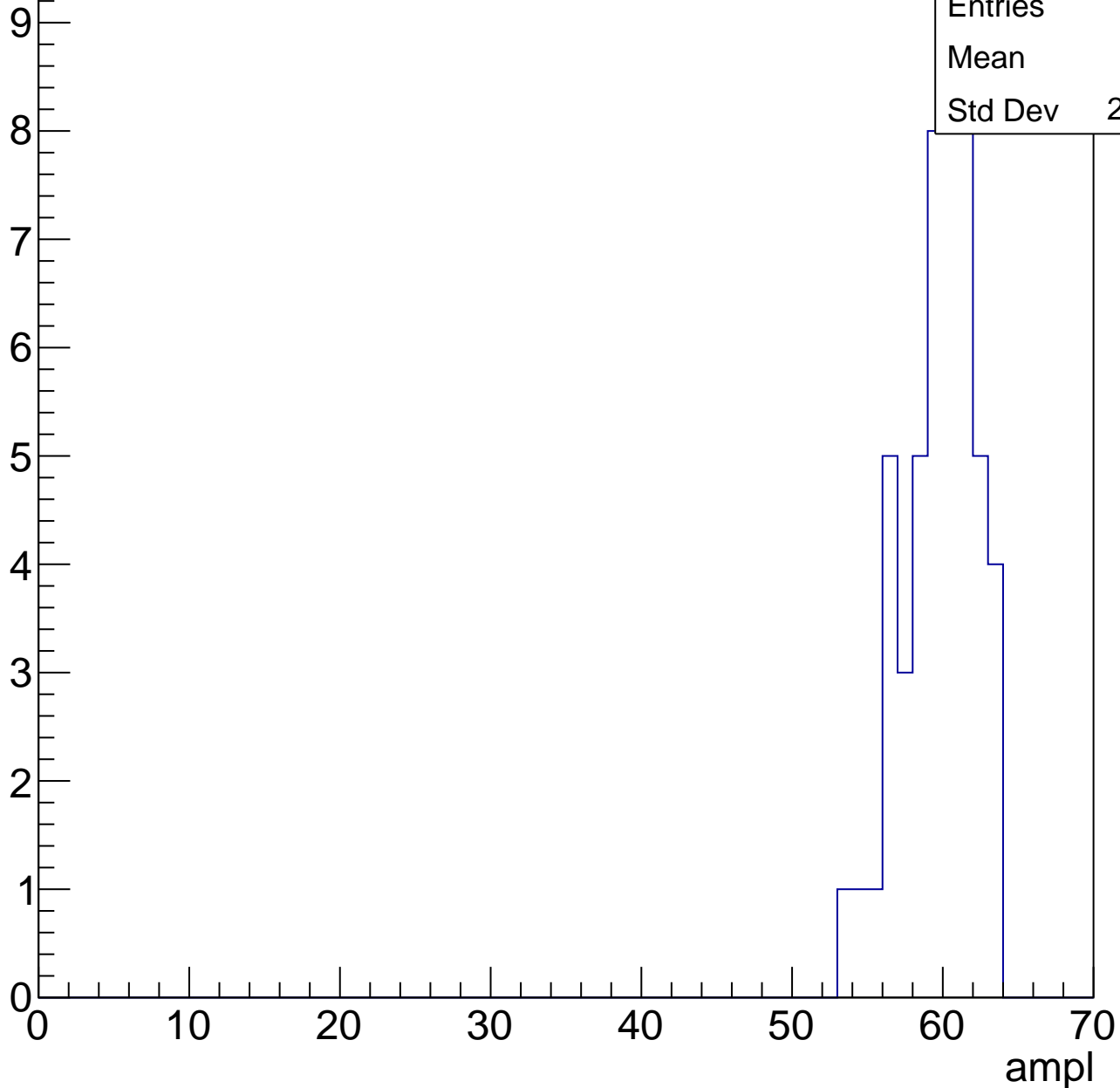


Entries	55
Mean	54.6
Std Dev	3.067

# B1L102S, U24-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



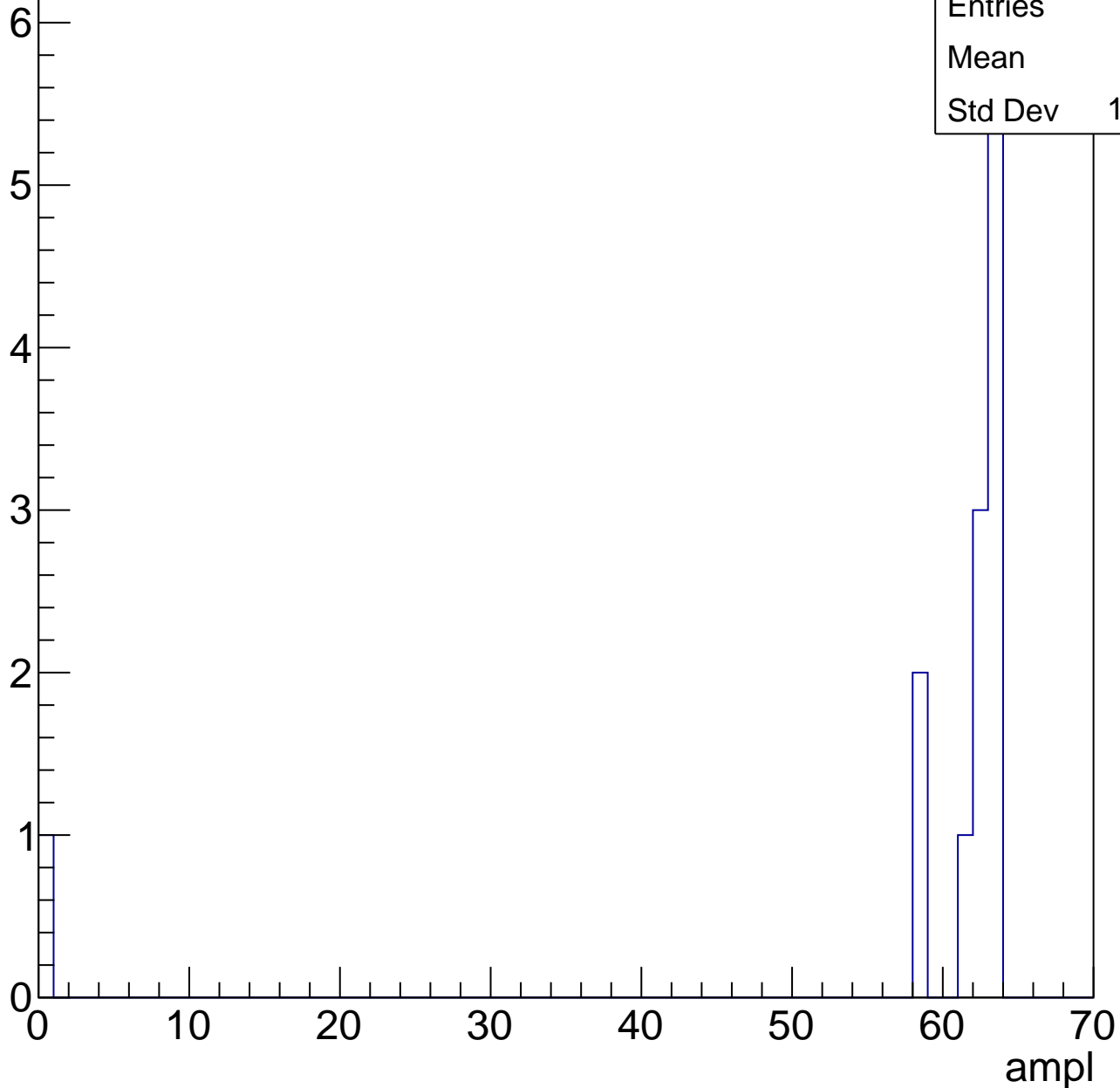
Entries	50
Mean	59.3
Std Dev	2.385

# B1L102S, U24-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57
Std Dev	16.54





# B1L102S, U24-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch11, adc0

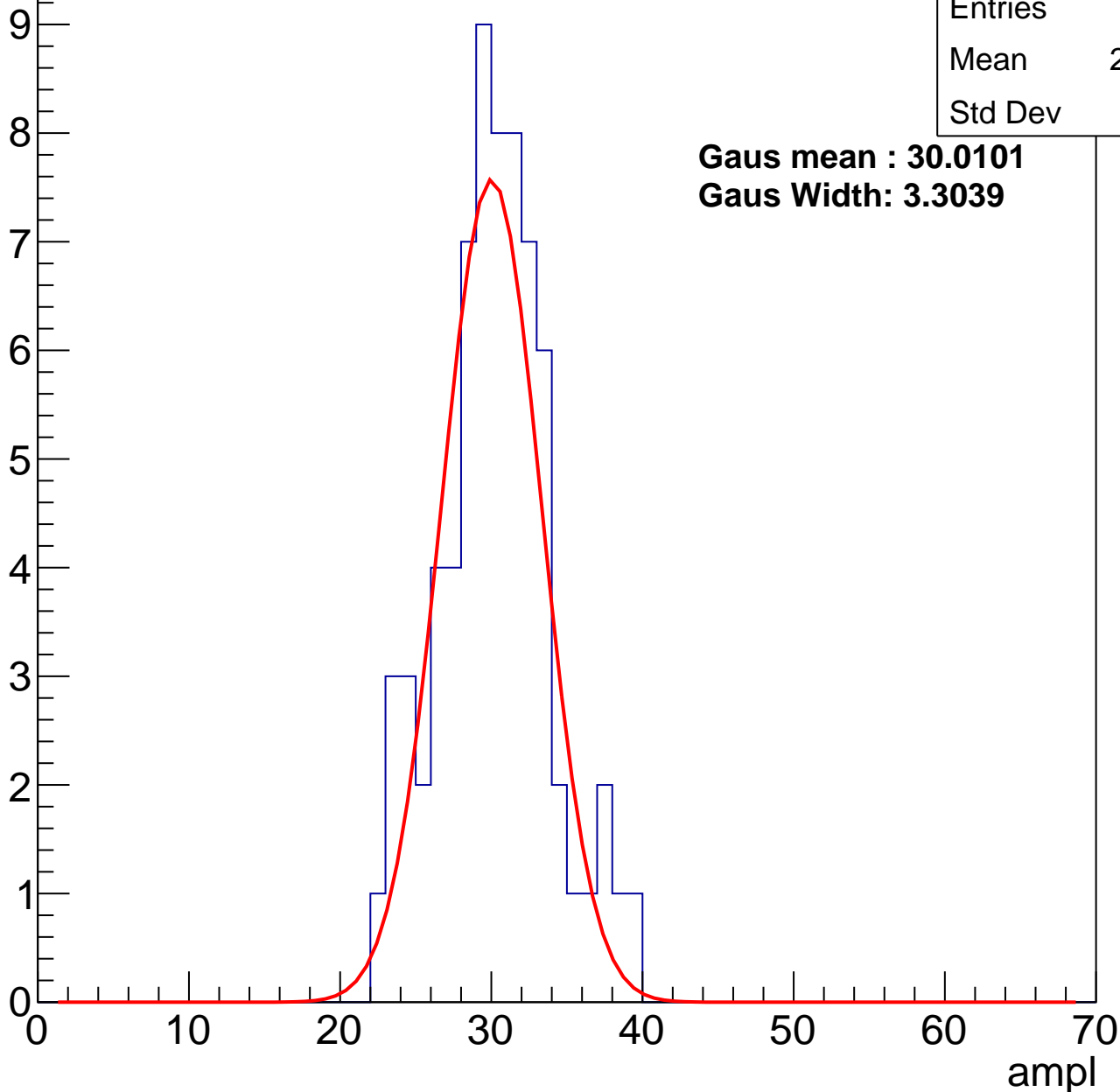
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.74
Std Dev	3.64

**Gaus mean : 30.0101**

**Gaus Width: 3.3039**



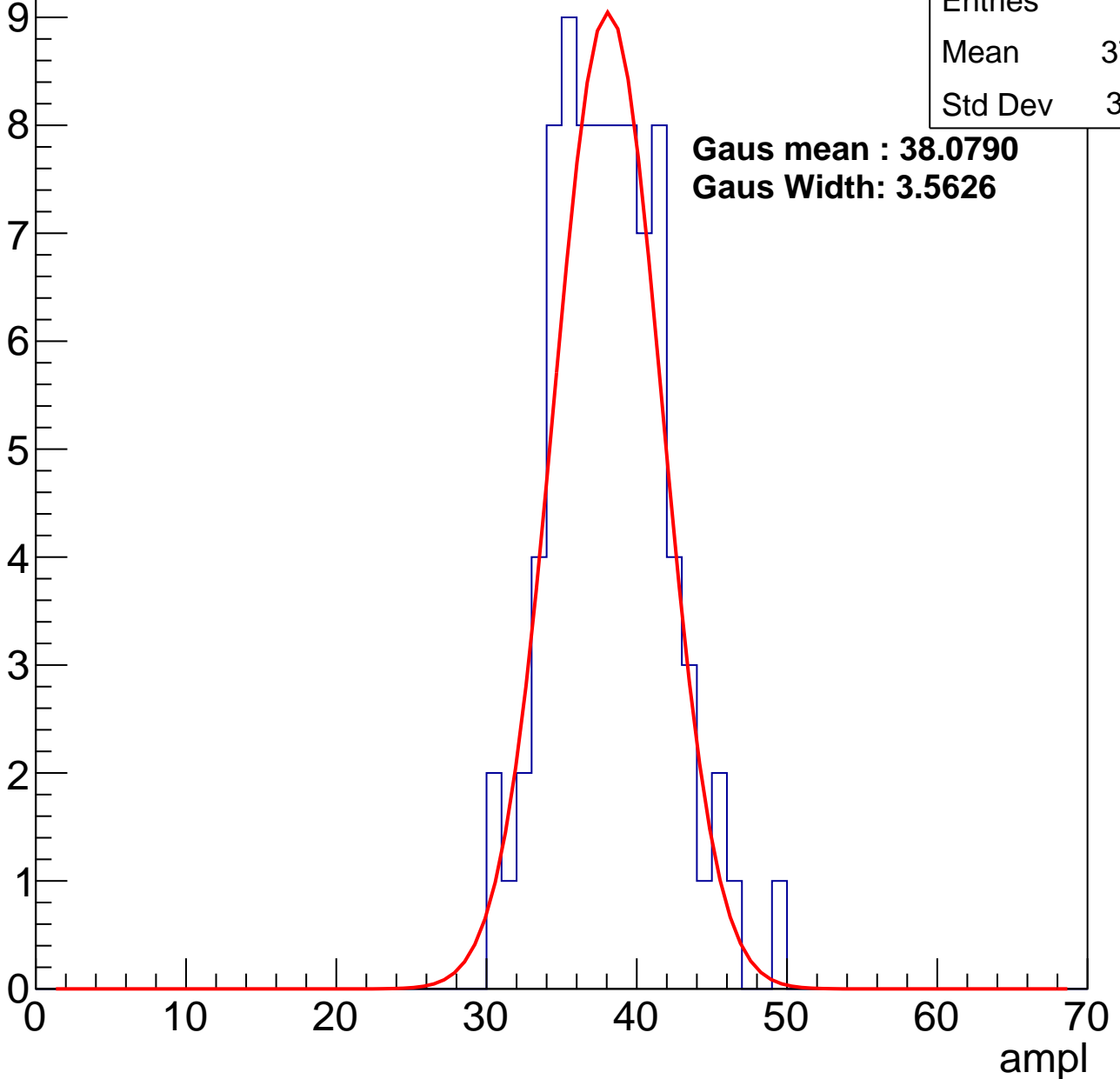
# B1L102S, U24-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	37.74
Std Dev	3.691

**Gaus mean : 38.0790**  
**Gaus Width: 3.5626**



# B1L102S, U24-ch11, adc2

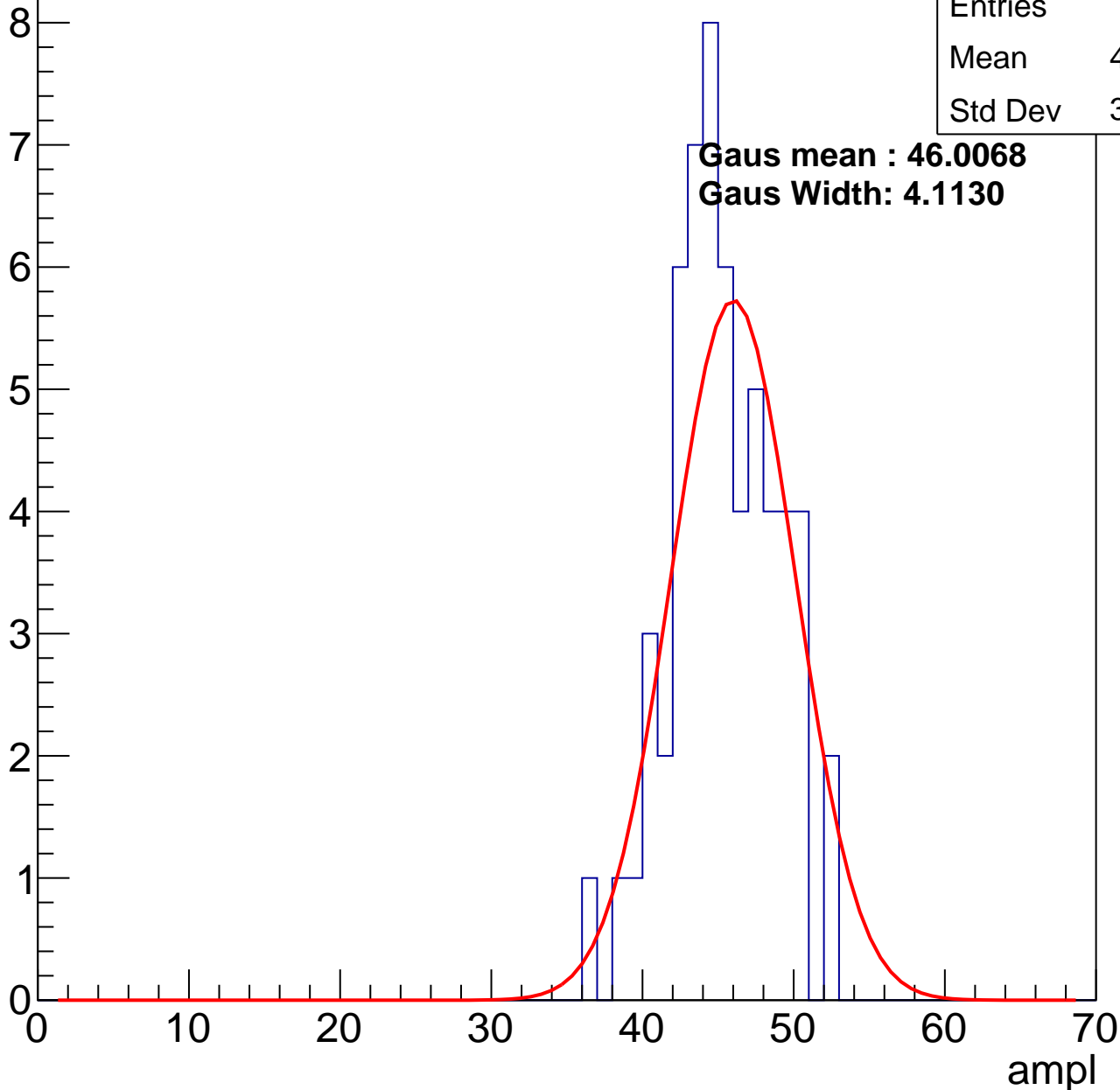
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	44.84
Std Dev	3.433

**Gaus mean : 46.0068**

**Gaus Width: 4.1130**

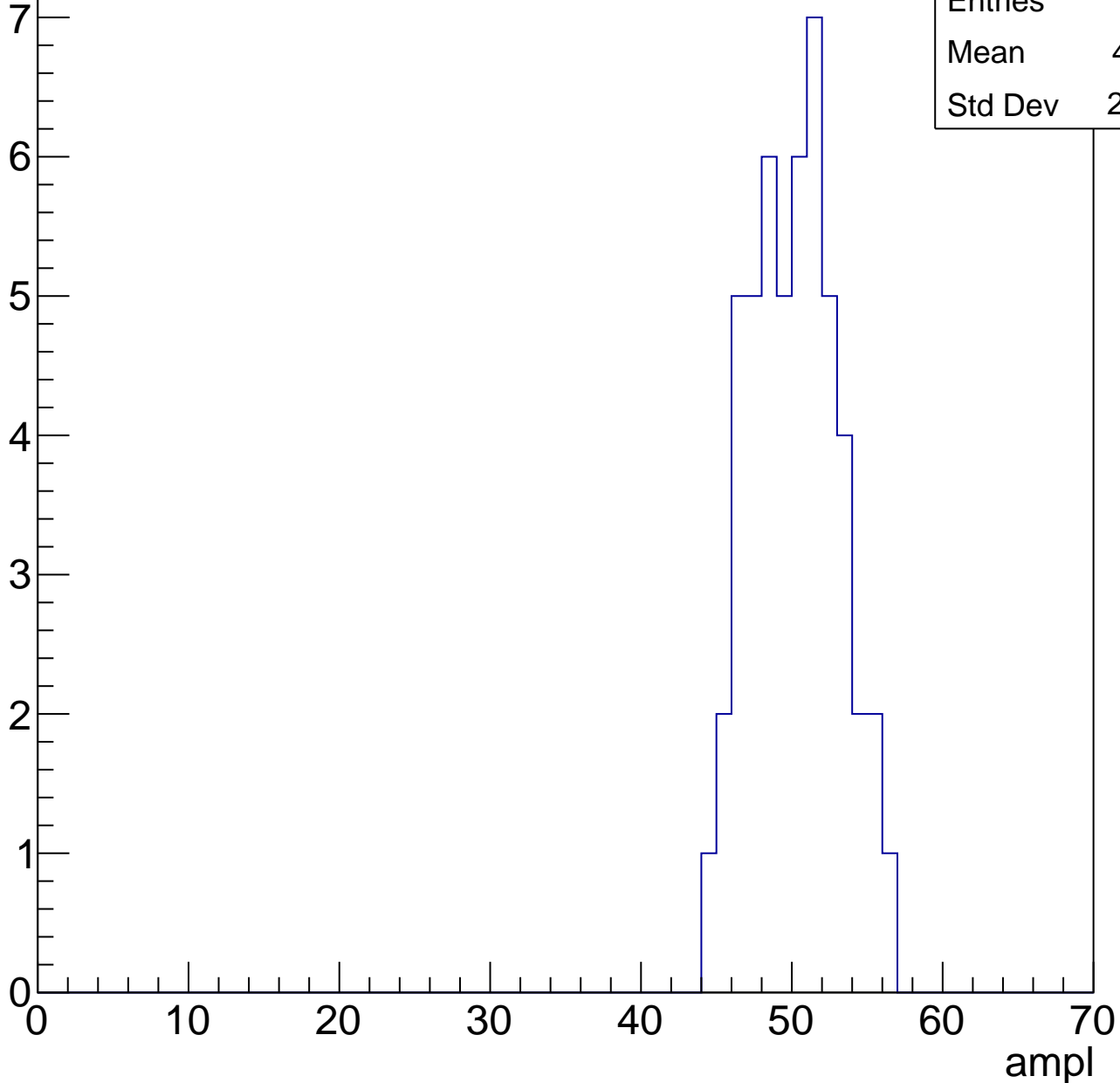


# B1L102S, U24-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.71
Std Dev	2.858

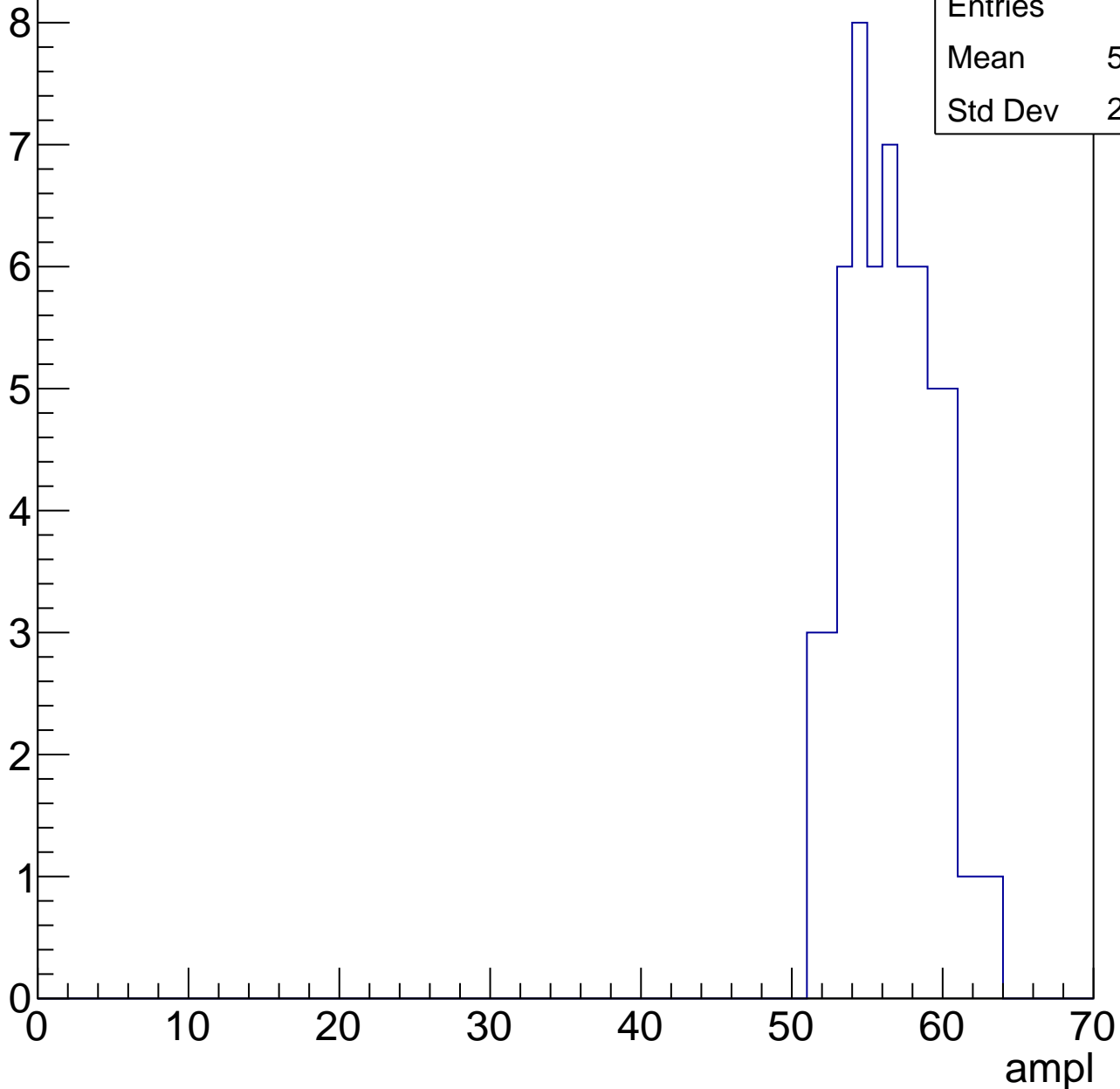


# B1L102S, U24-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.07
Std Dev	2.876

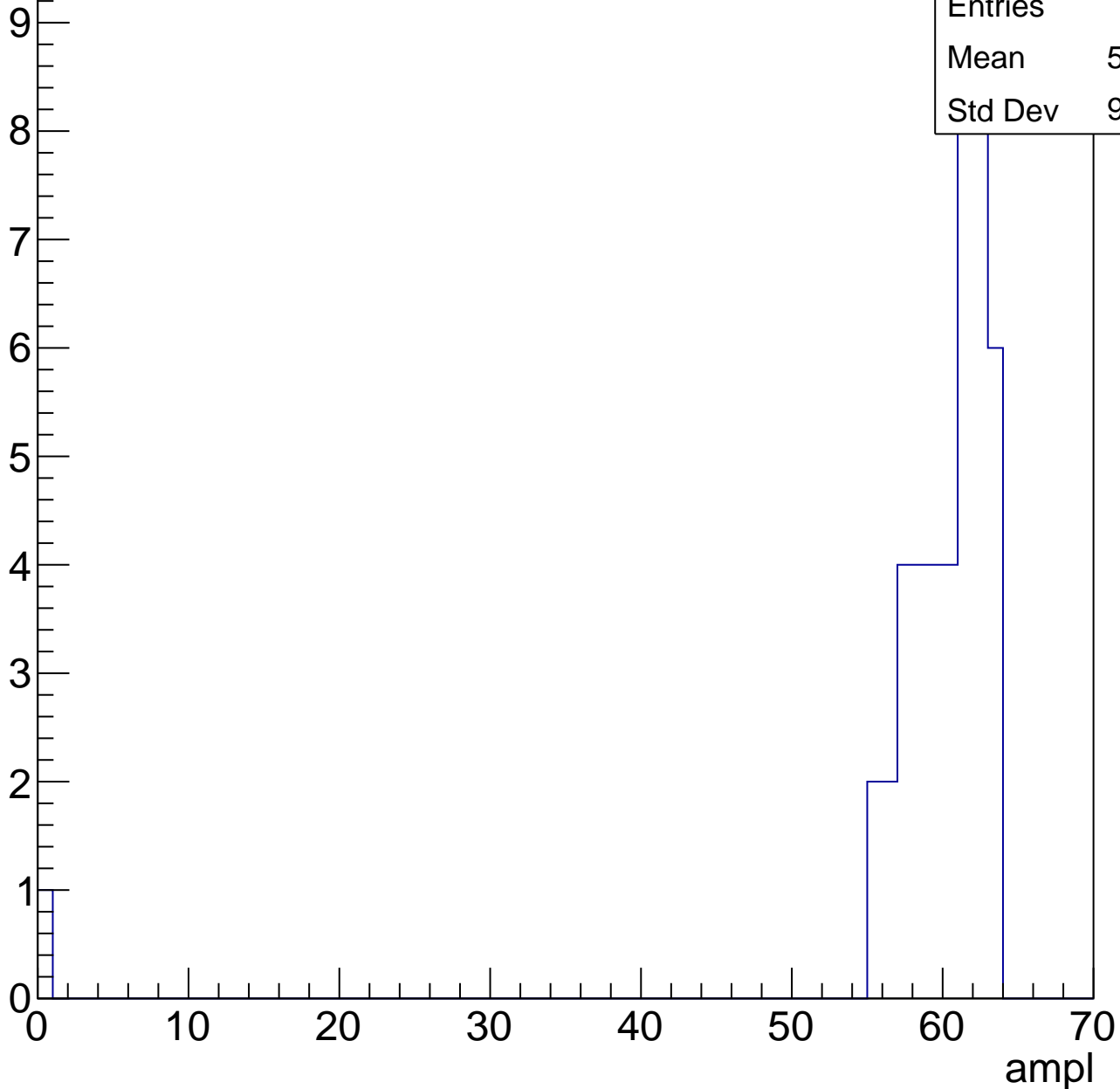


# B1L102S, U24-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

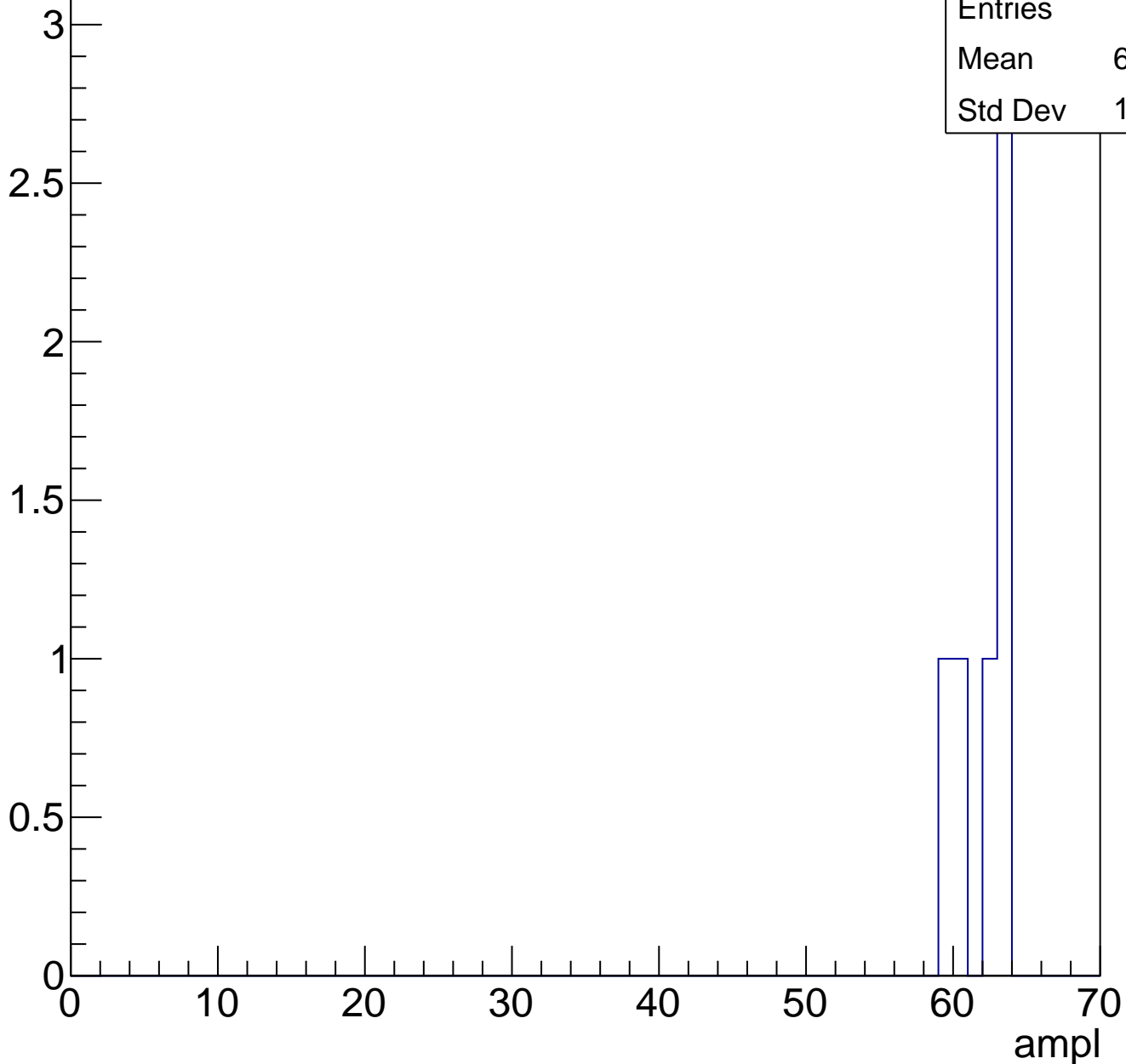
Entries	44
Mean	58.66
Std Dev	9.237



# B1L102S, U24-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	75
Mean	28.11
Std Dev	3.361

**Gaus mean : 28.4712**

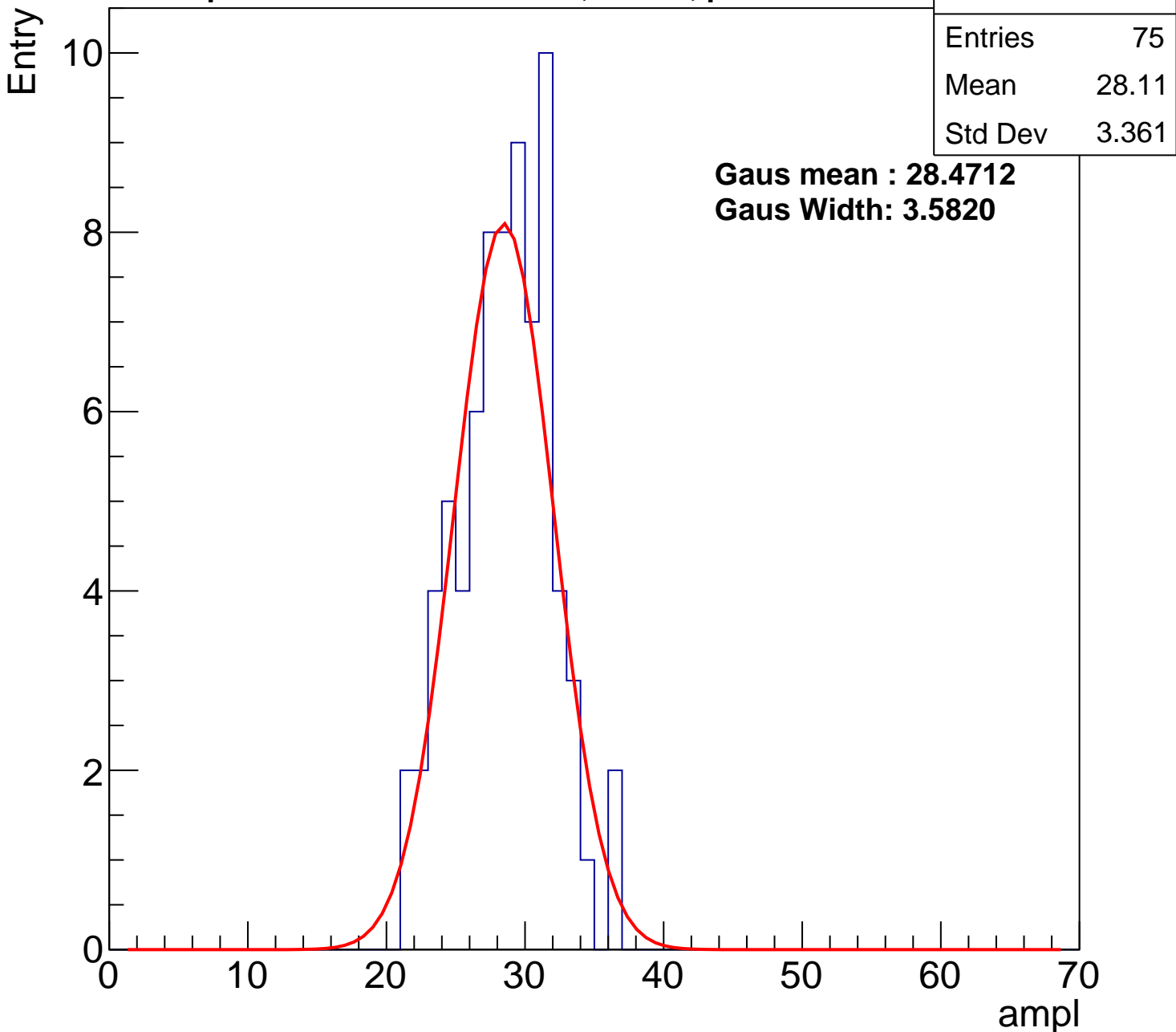
**Gaus Width: 3.5820**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch12, adc1

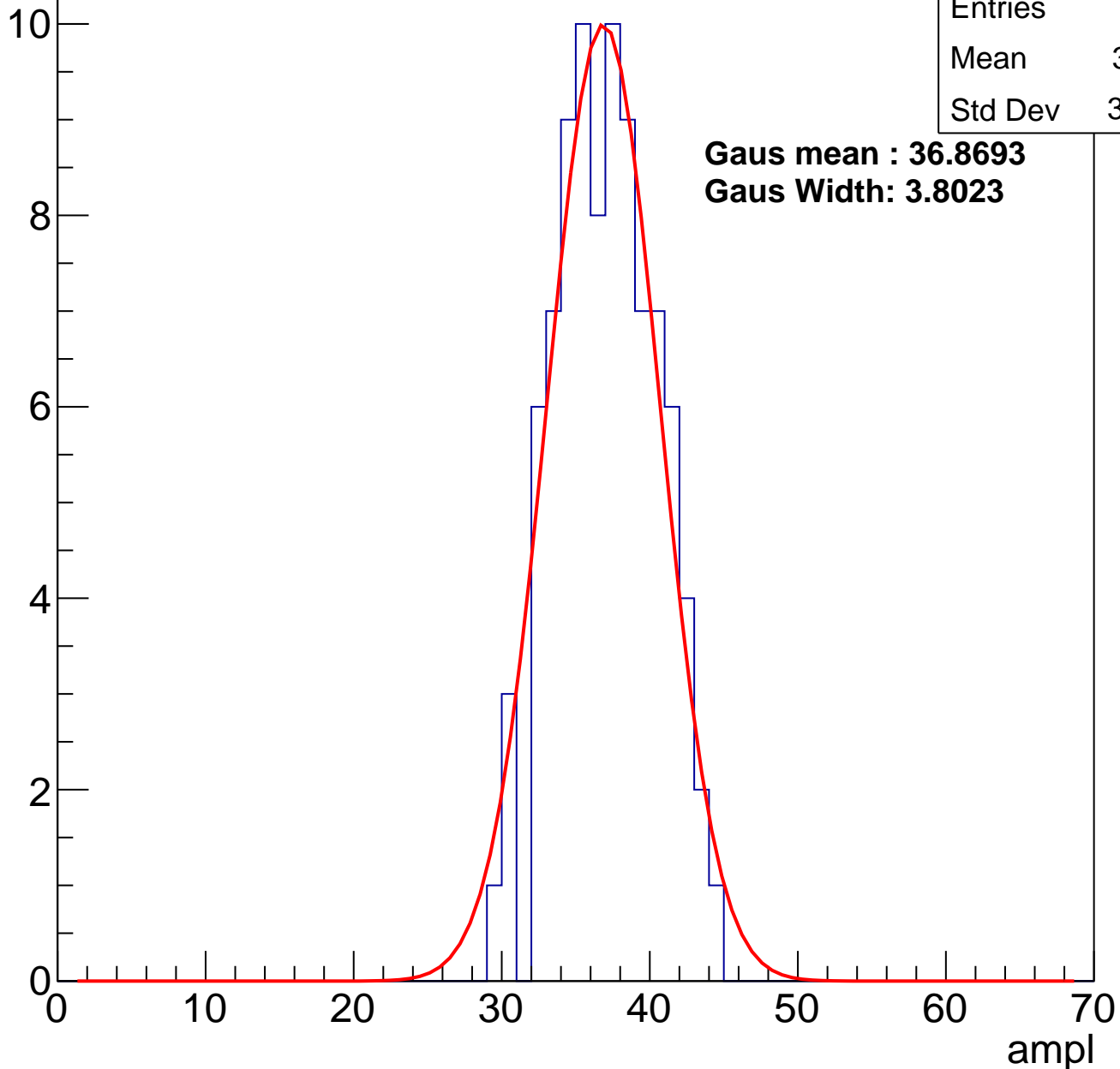
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	90
Mean	36.61
Std Dev	3.342

**Gaus mean : 36.8693**

**Gaus Width: 3.8023**

Entry

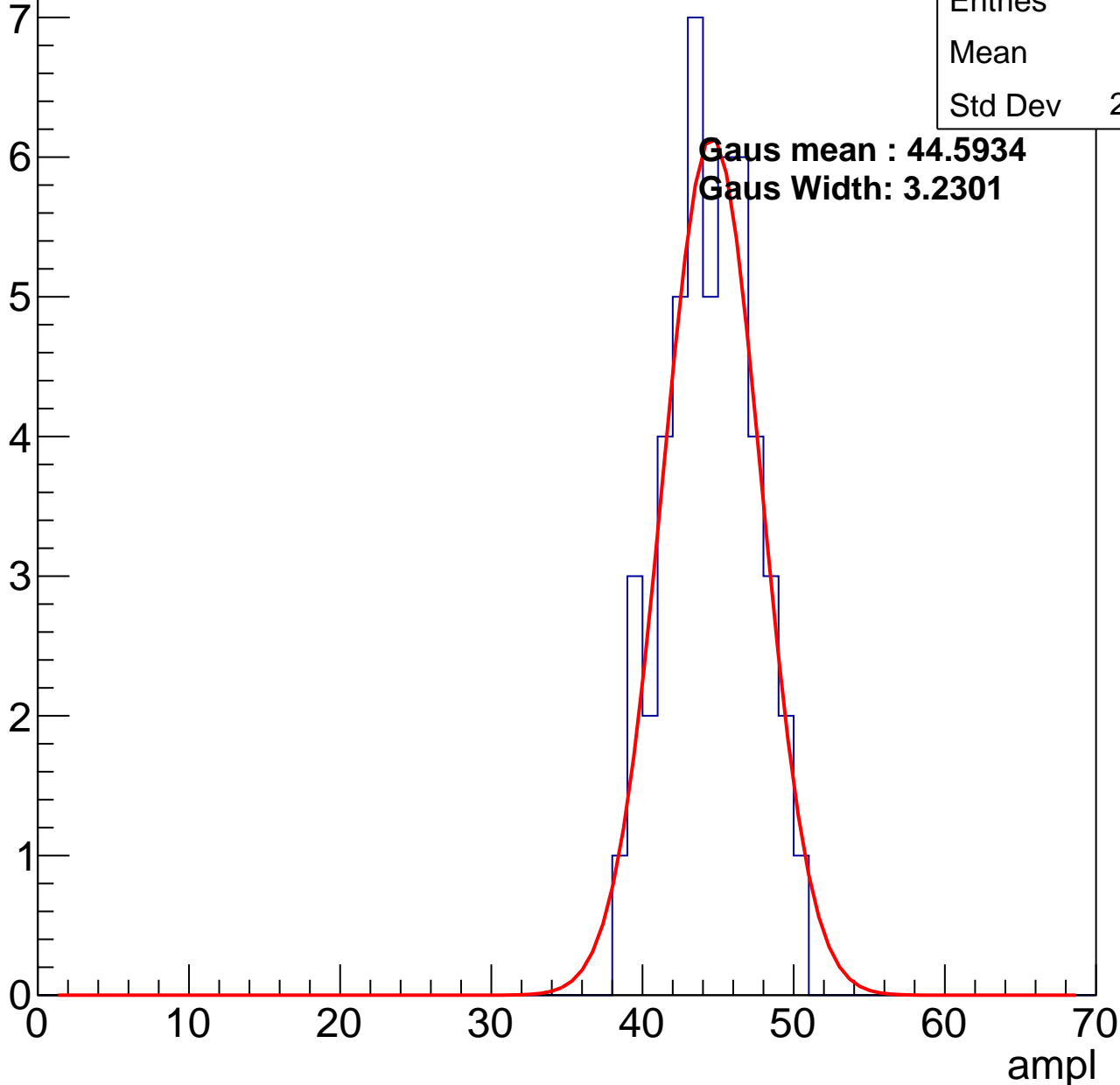


# B1L102S, U24-ch12, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	44
Std Dev	2.878

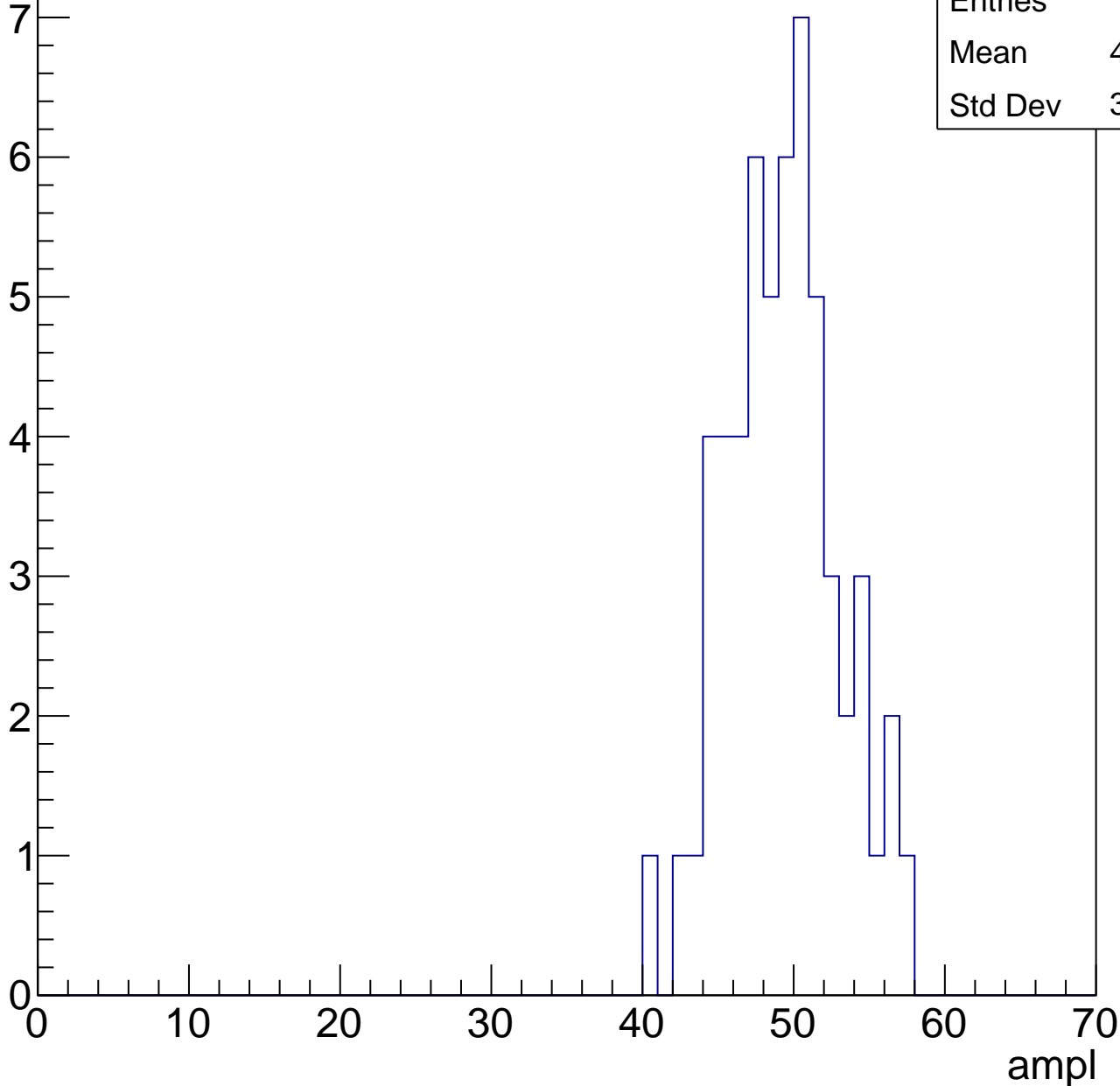


# B1L102S, U24-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.82
Std Dev	3.665

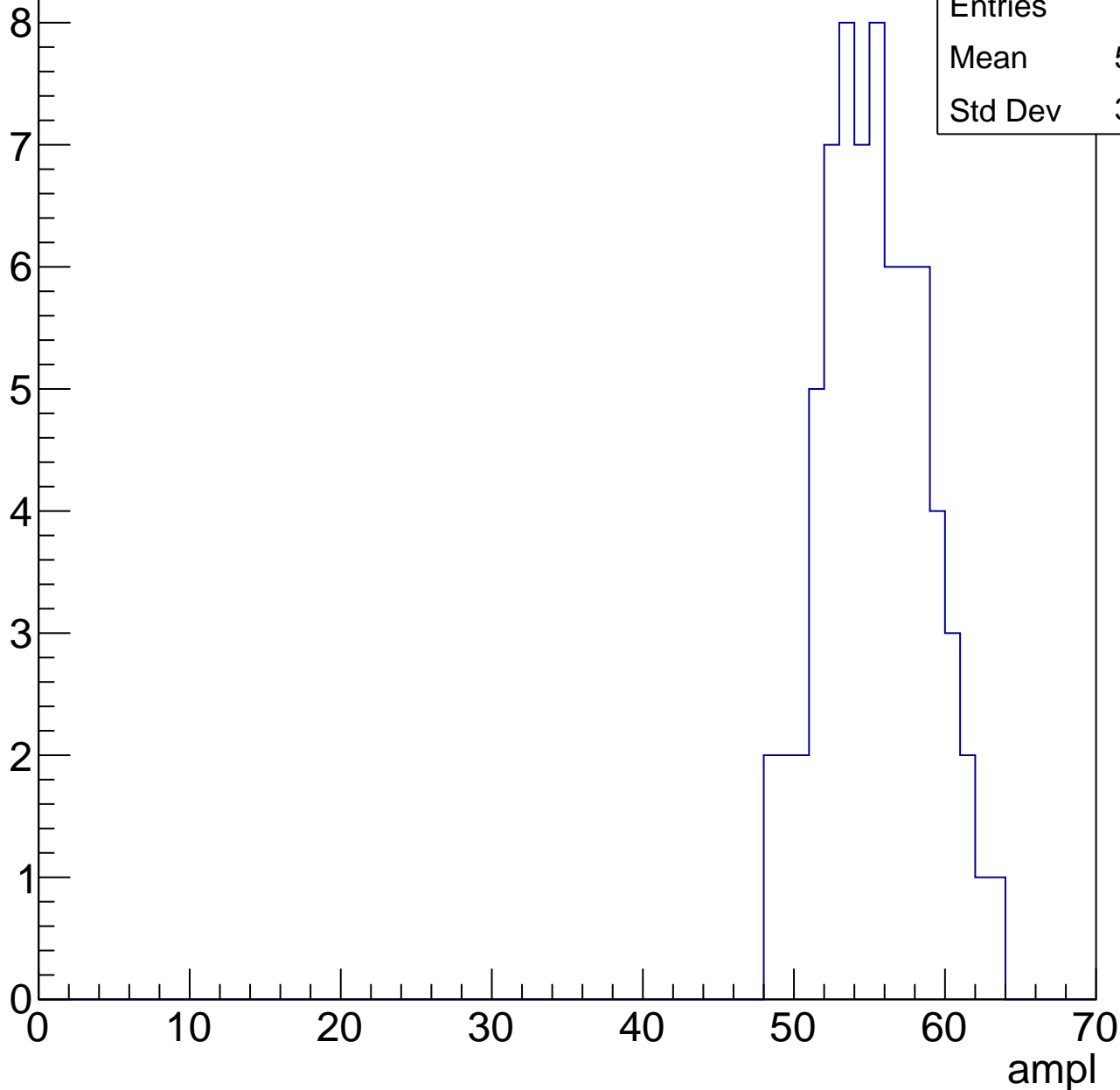


# B1L102S, U24-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.91
Std Dev	3.401



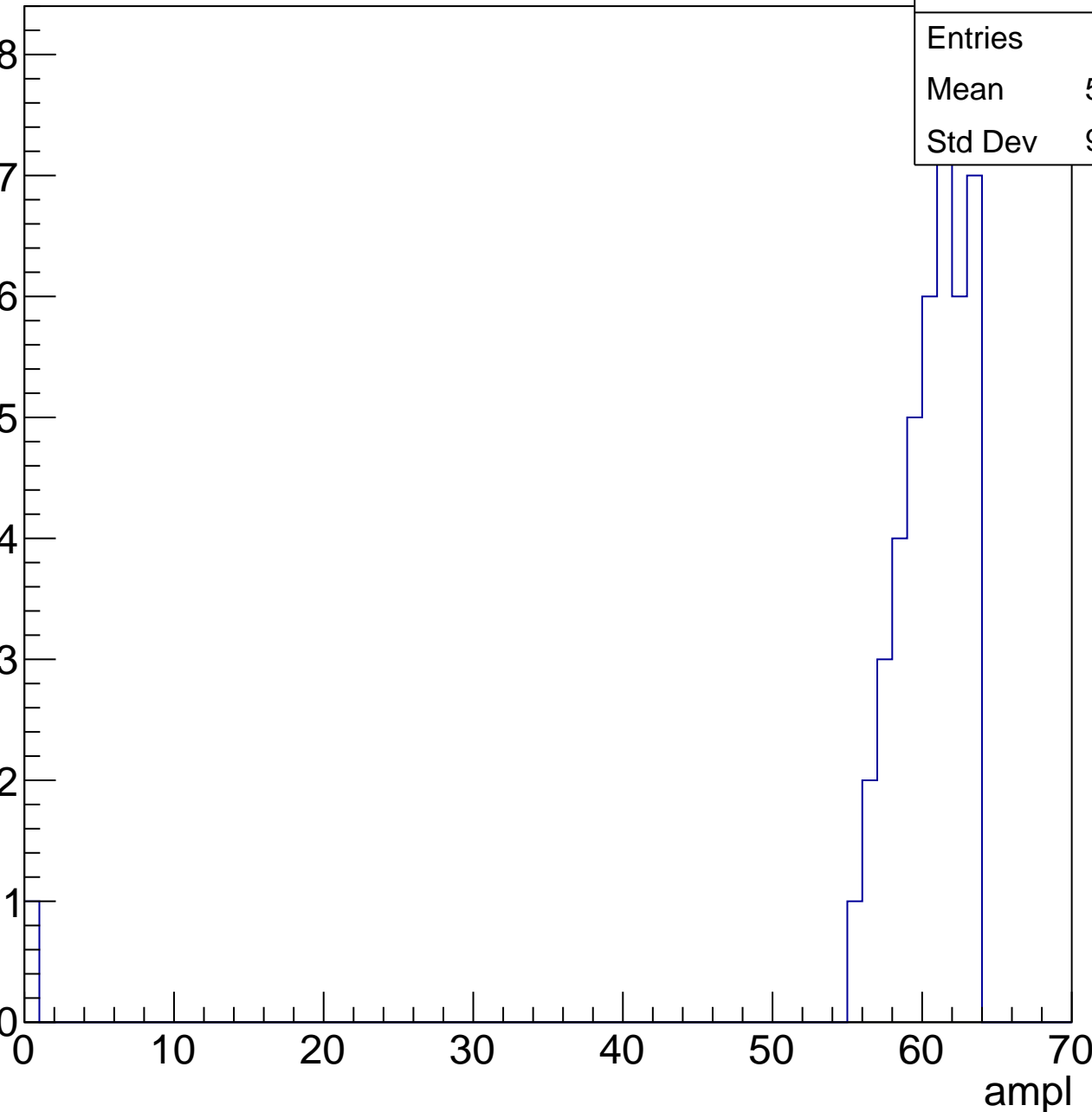
# B1L102S, U24-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.74
Std Dev	9.316

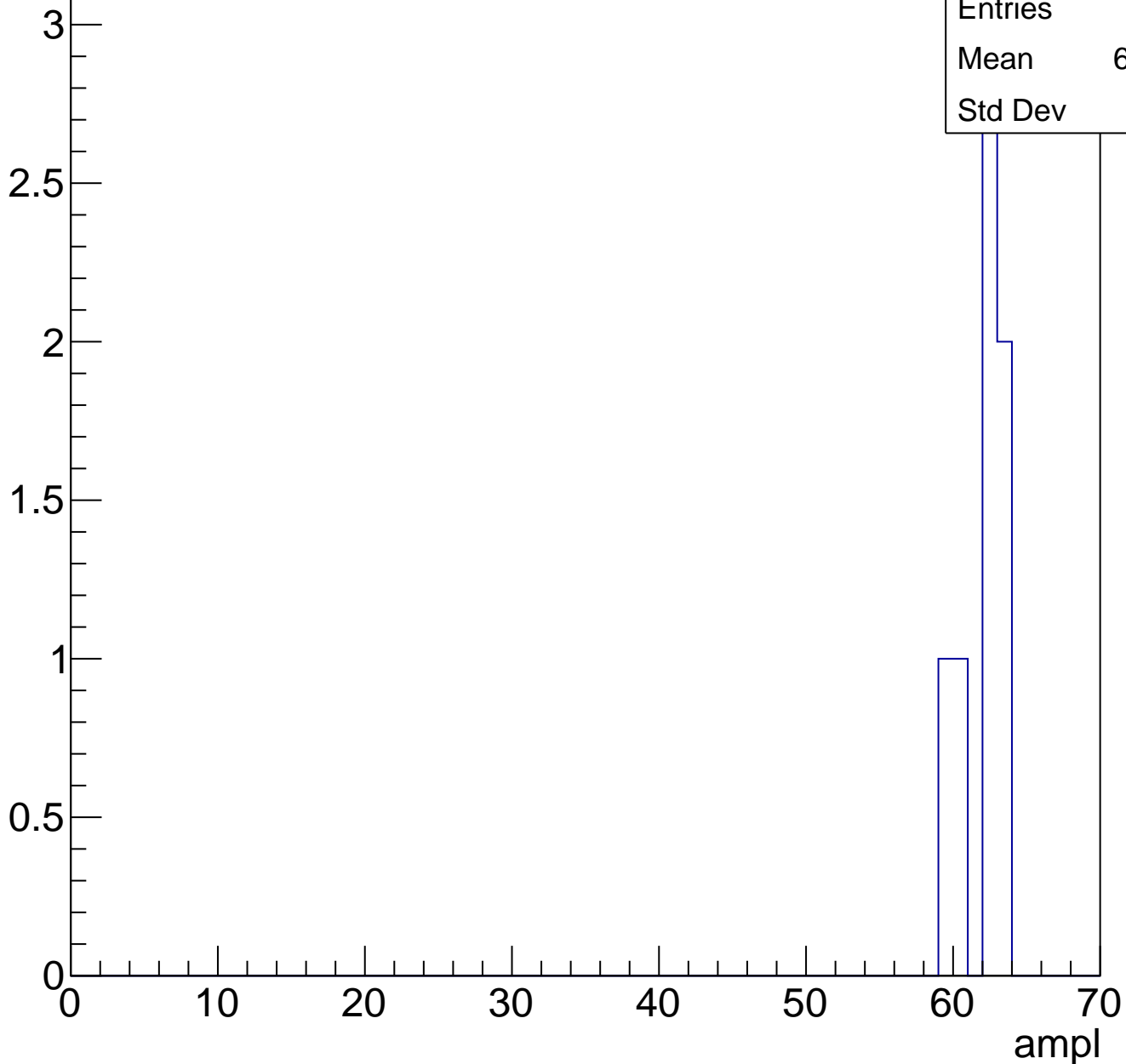
8  
7  
6  
5  
4  
3  
2  
1  
0



# B1L102S, U24-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U24-ch13, adc0

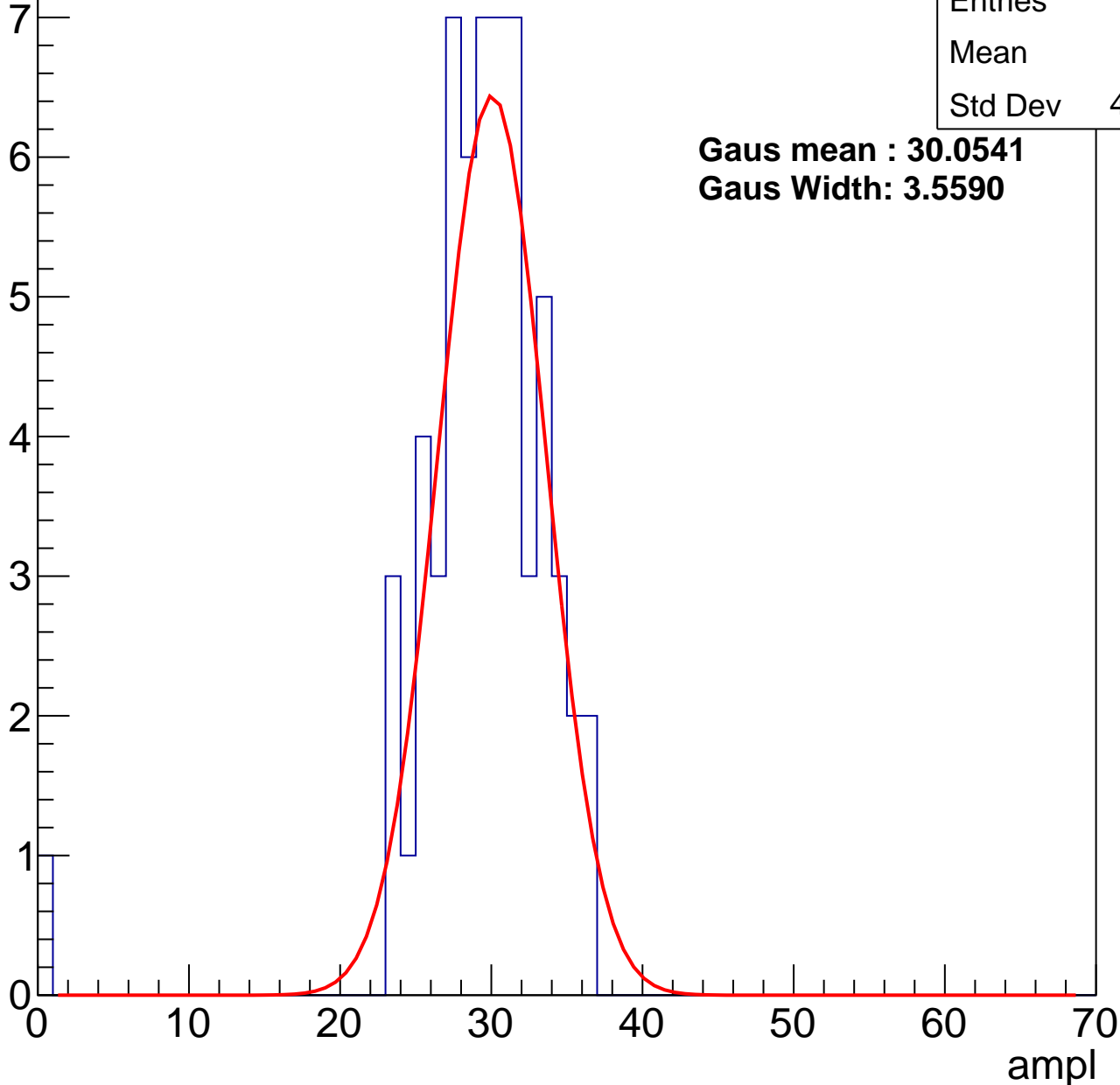
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.9
Std Dev	4.935

**Gaus mean : 30.0541**

**Gaus Width: 3.5590**



# B1L102S, U24-ch13, adc1

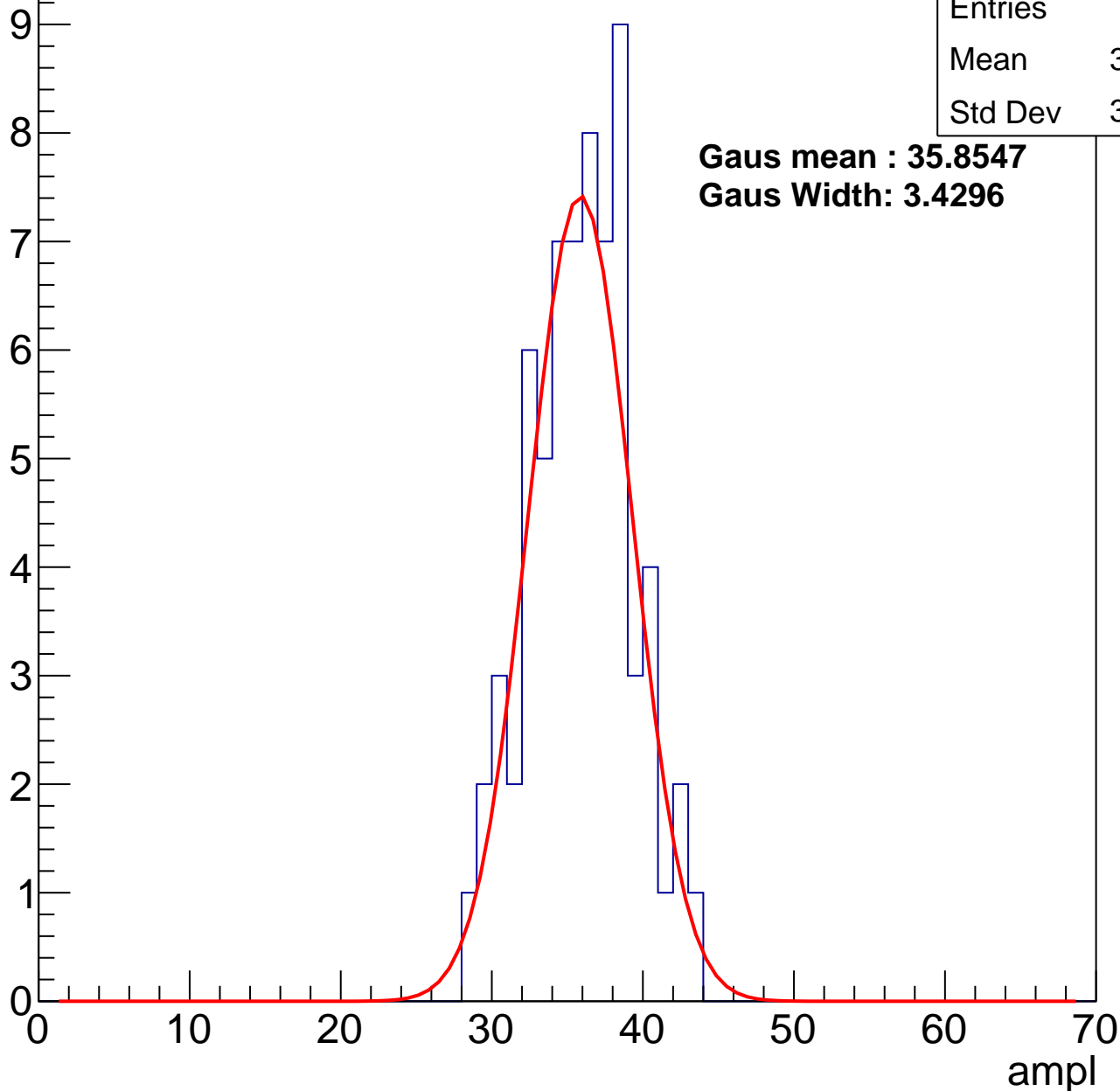
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.47
Std Dev	3.336

**Gaus mean : 35.8547**

**Gaus Width: 3.4296**



# B1L102S, U24-ch13, adc2

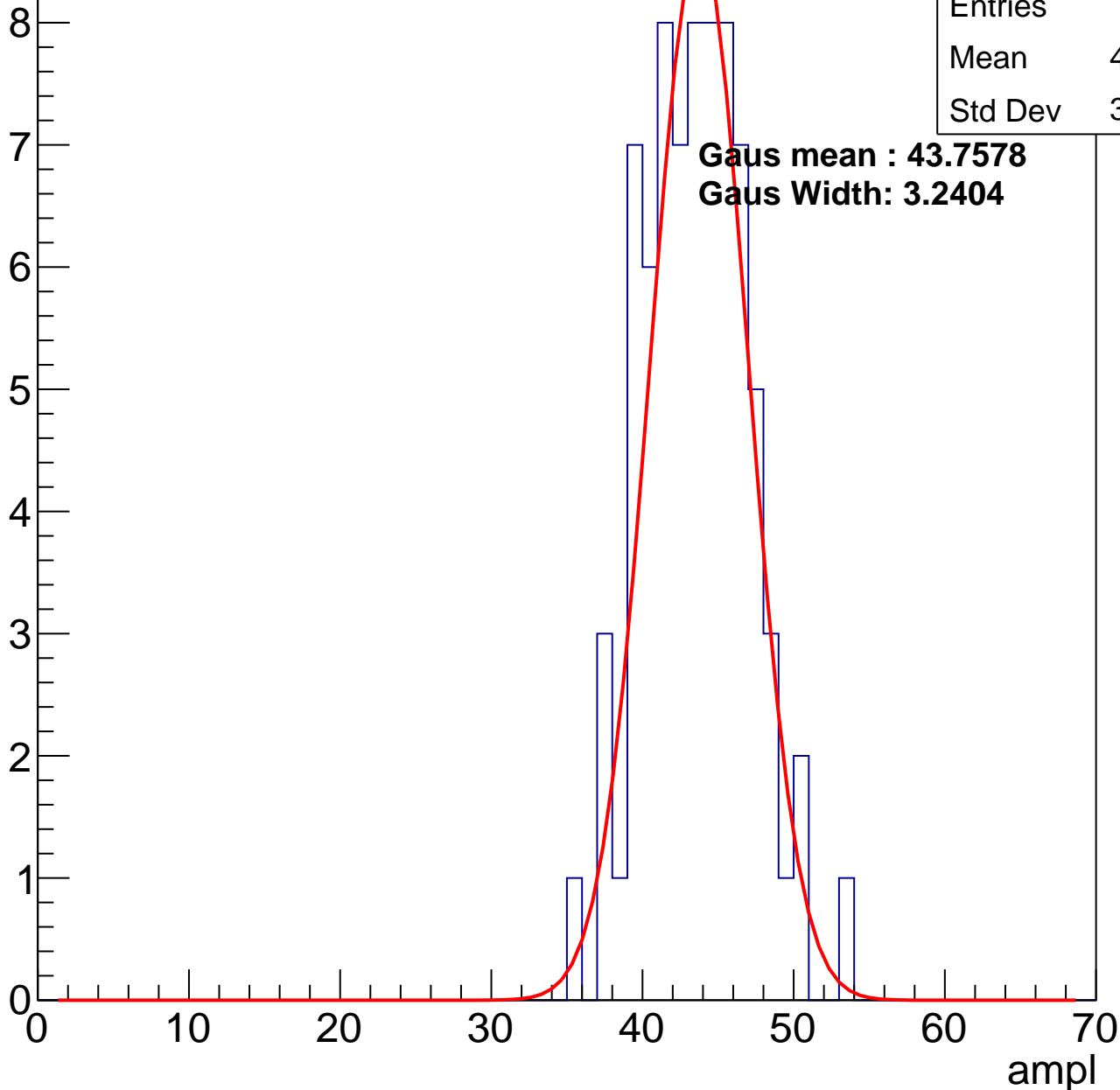
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.13
Std Dev	3.427

**Gaus mean : 43.7578**

**Gaus Width: 3.2404**

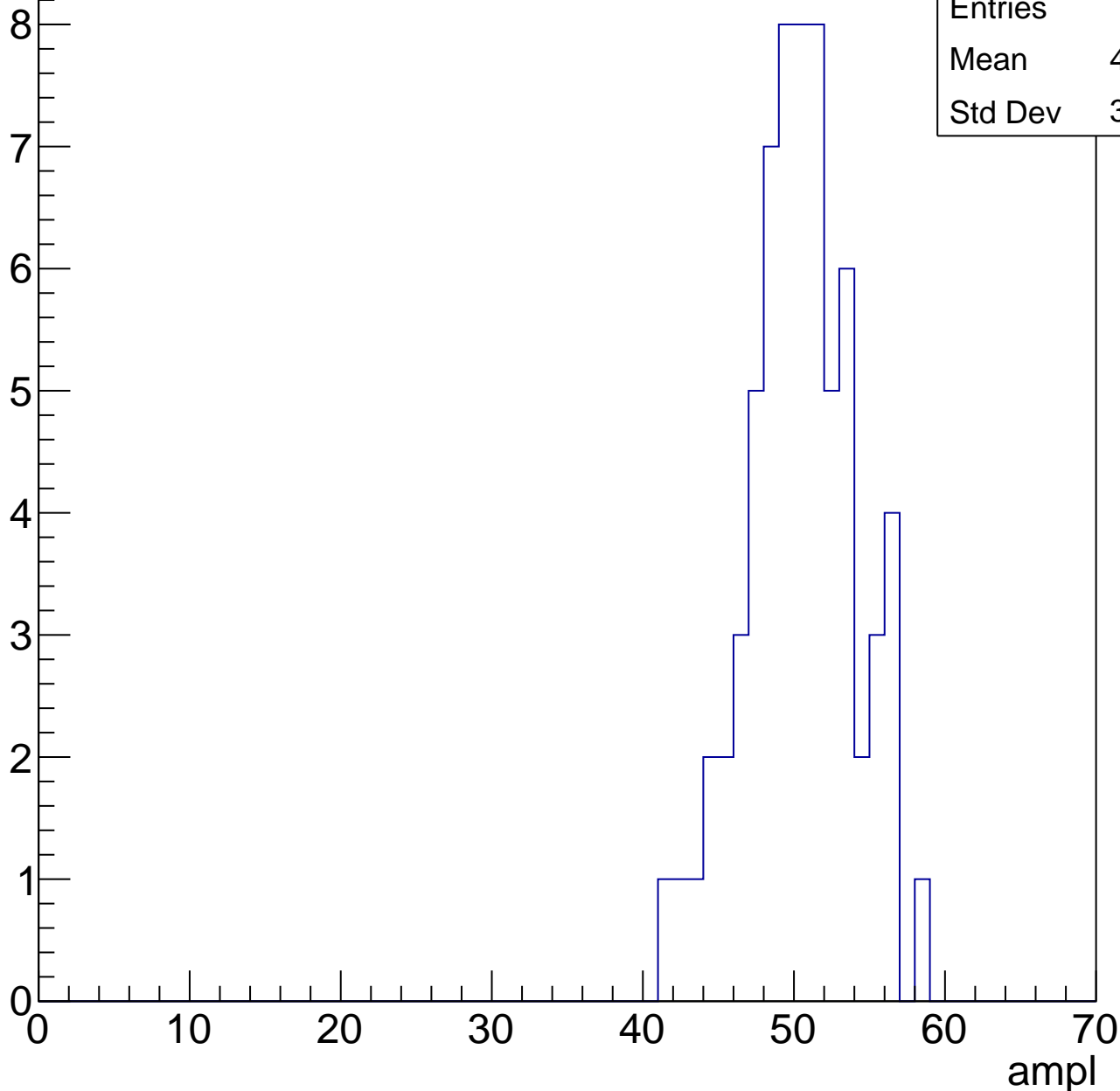


# B1L102S, U24-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.94
Std Dev	3.545

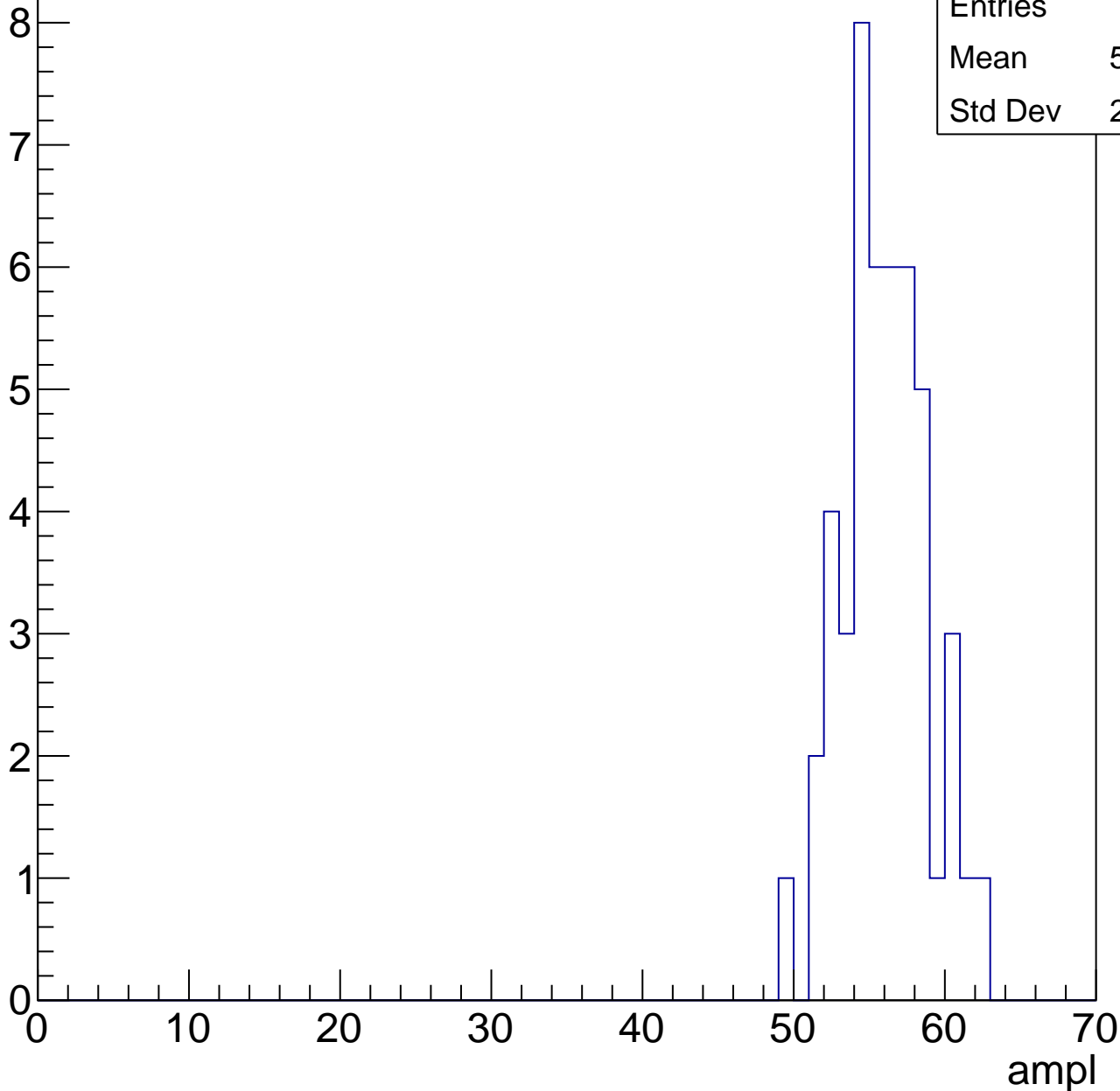


# B1L102S, U24-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

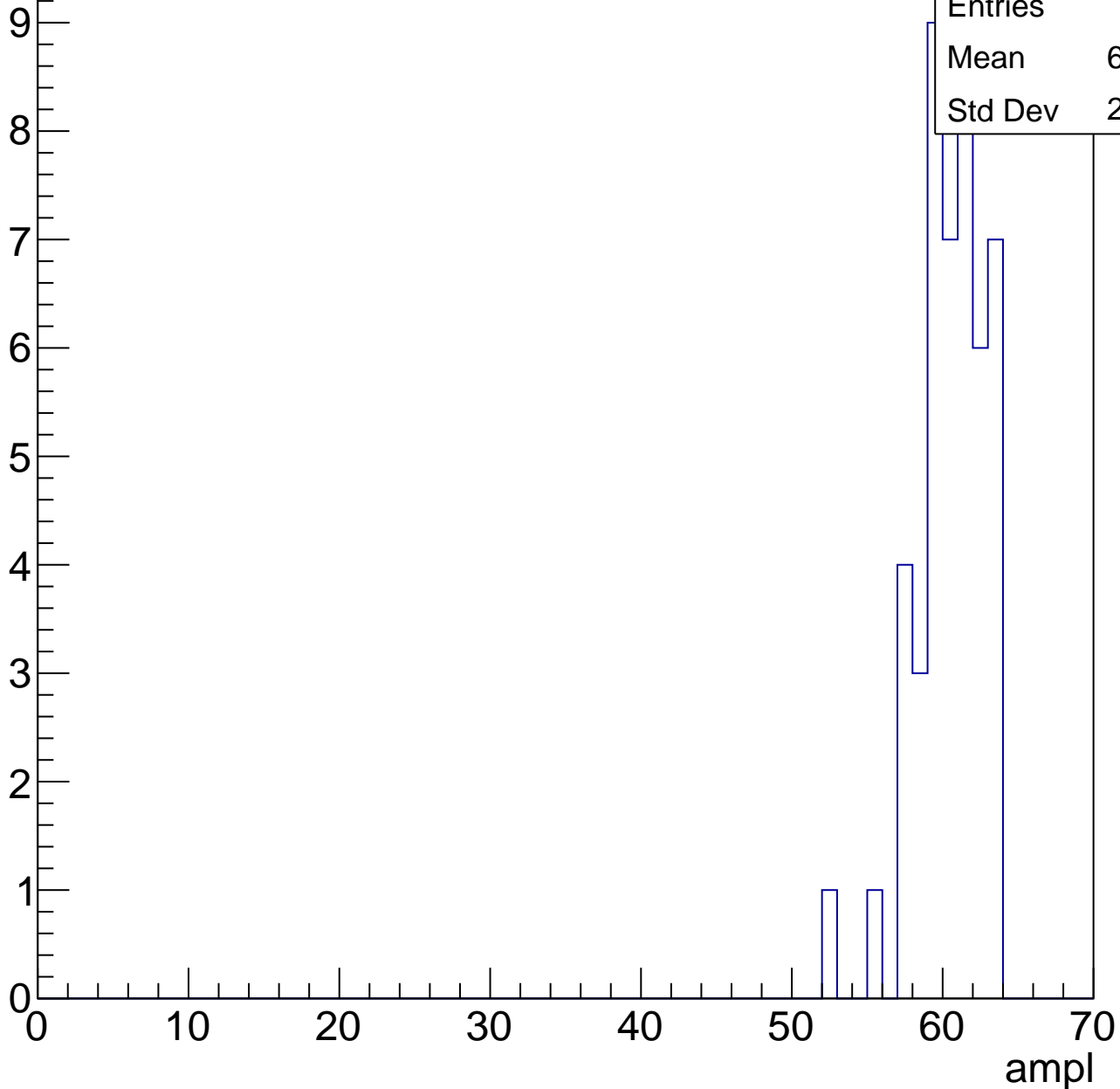
Entries	47
Mean	55.53
Std Dev	2.774



# B1L102S, U24-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

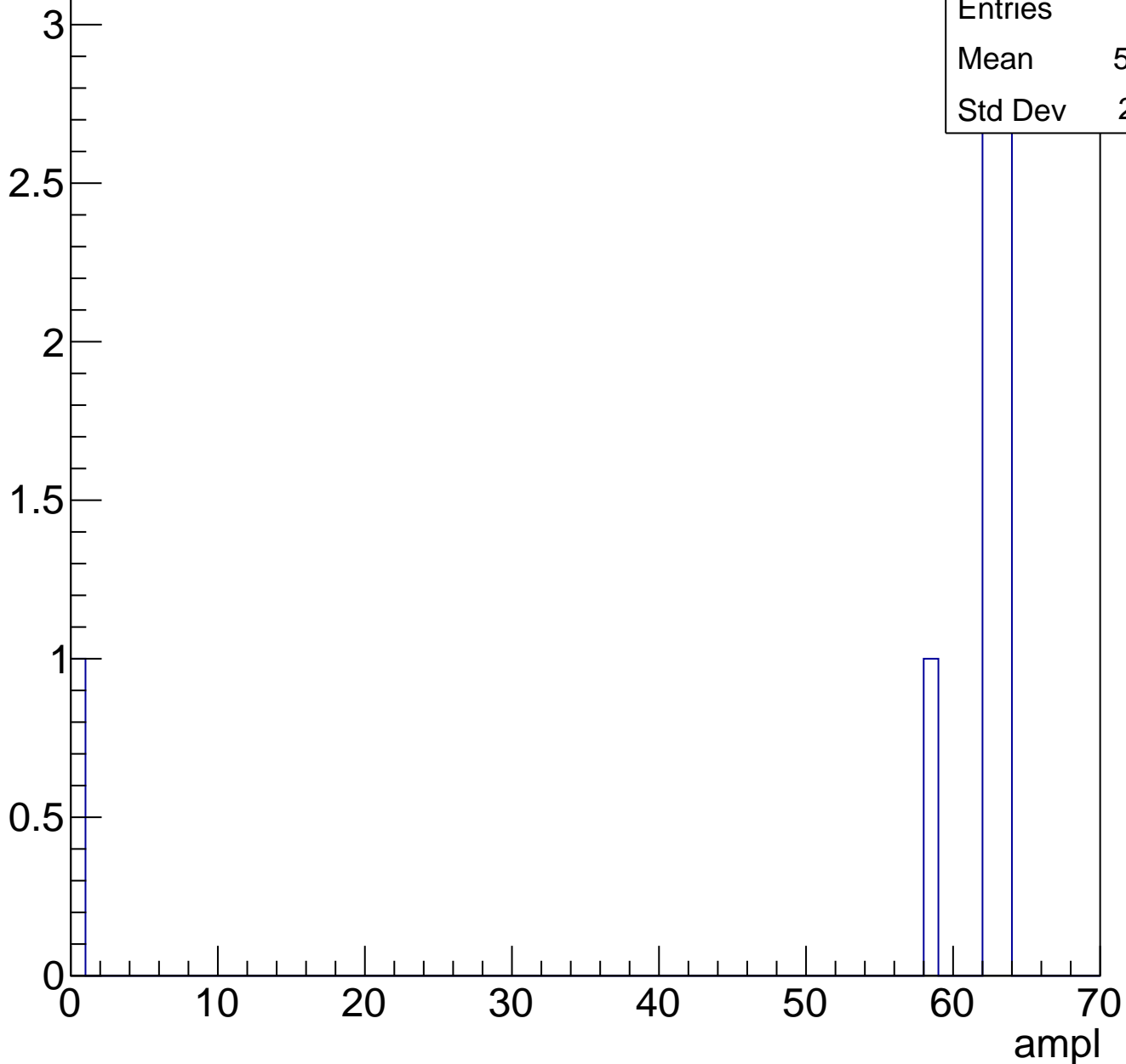
Entry



# B1L102S, U24-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch14, adc0

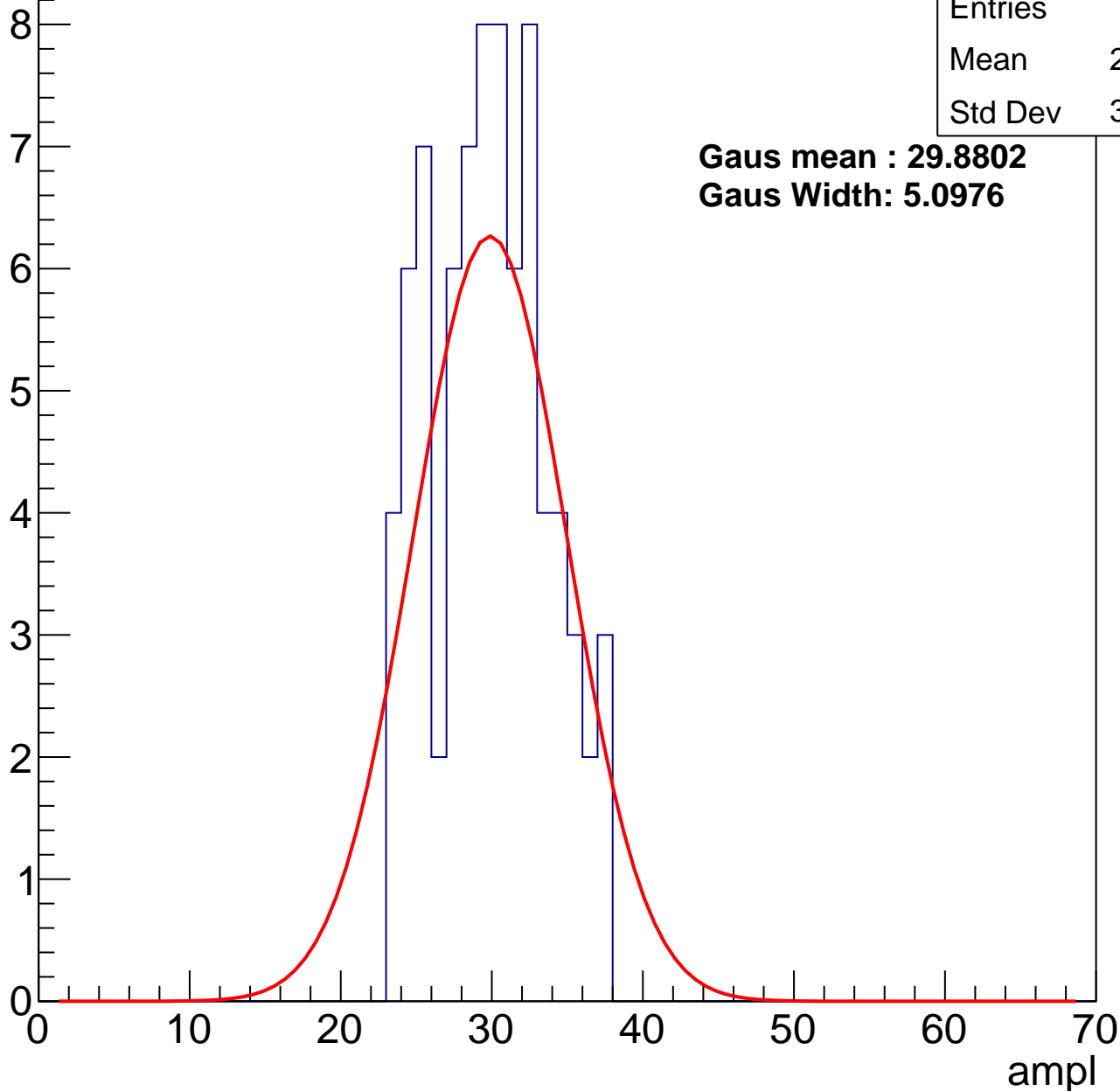
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.37
Std Dev	3.773

**Gaus mean : 29.8802**

**Gaus Width: 5.0976**



# B1L102S, U24-ch14, adc1

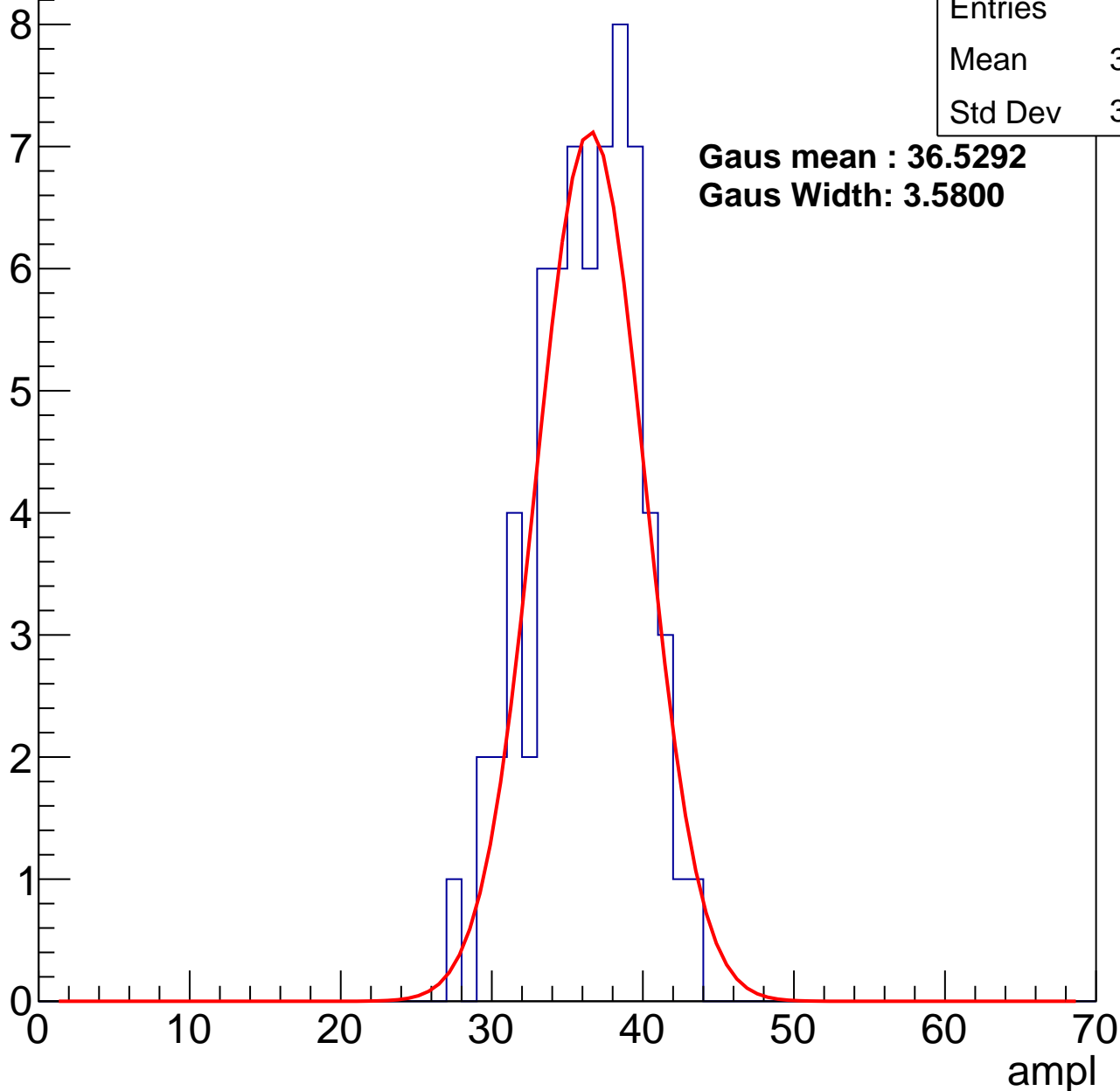
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.82
Std Dev	3.438

**Gaus mean : 36.5292**

**Gaus Width: 3.5800**



# B1L102S, U24-ch14, adc2

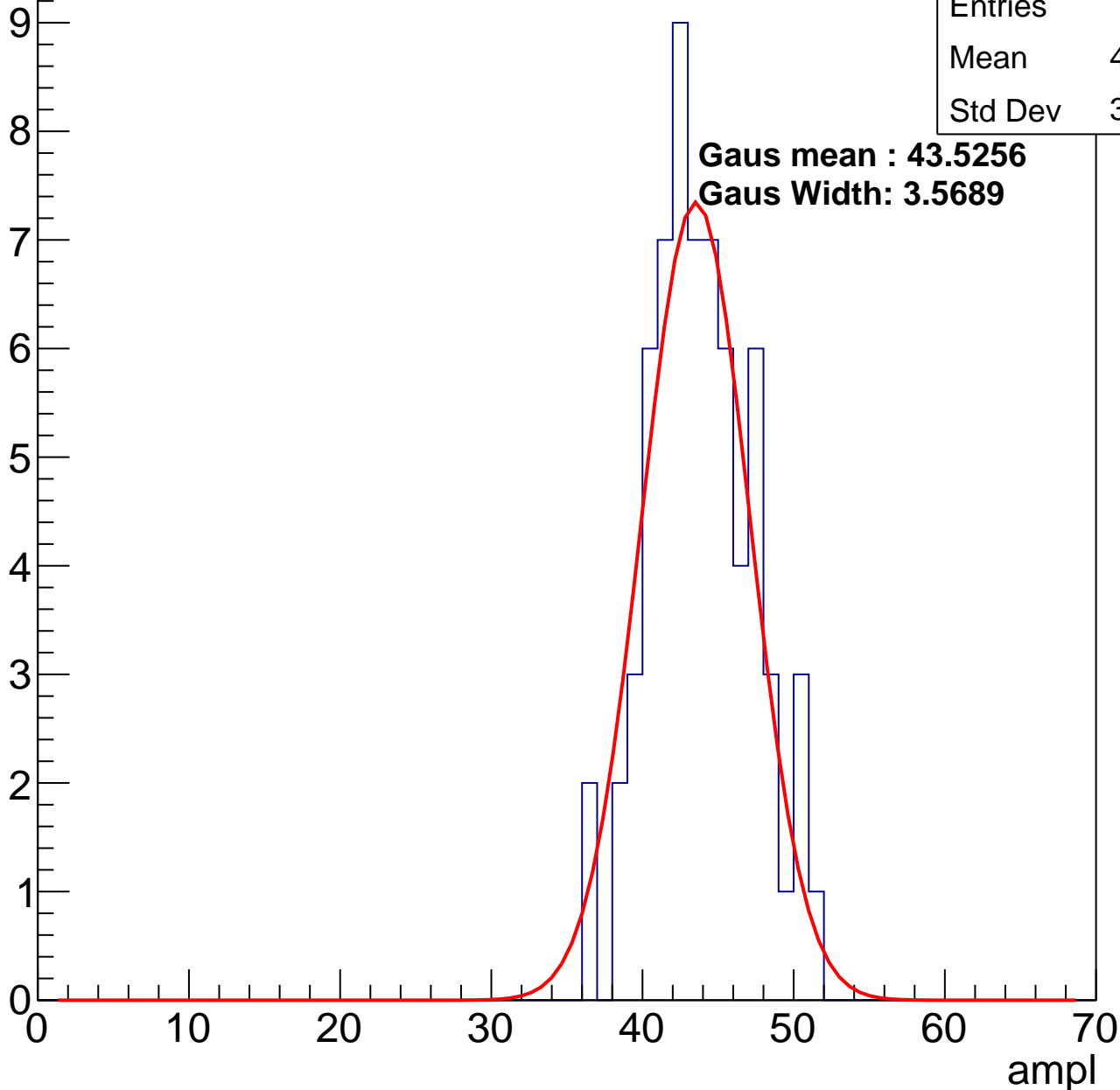
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.42
Std Dev	3.369

**Gaus mean : 43.5256**

**Gaus Width: 3.5689**

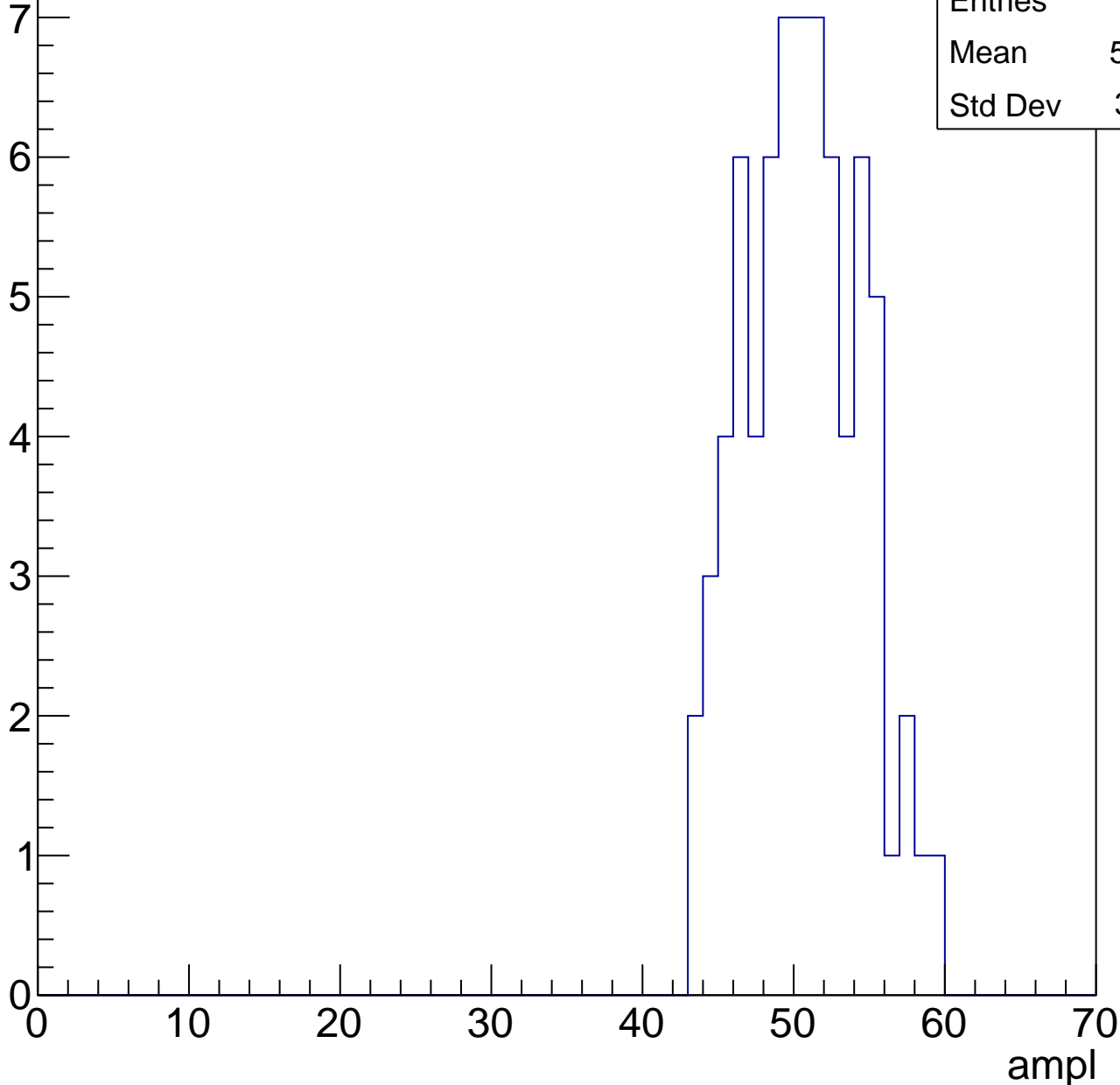


# B1L102S, U24-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	50.14
Std Dev	3.791

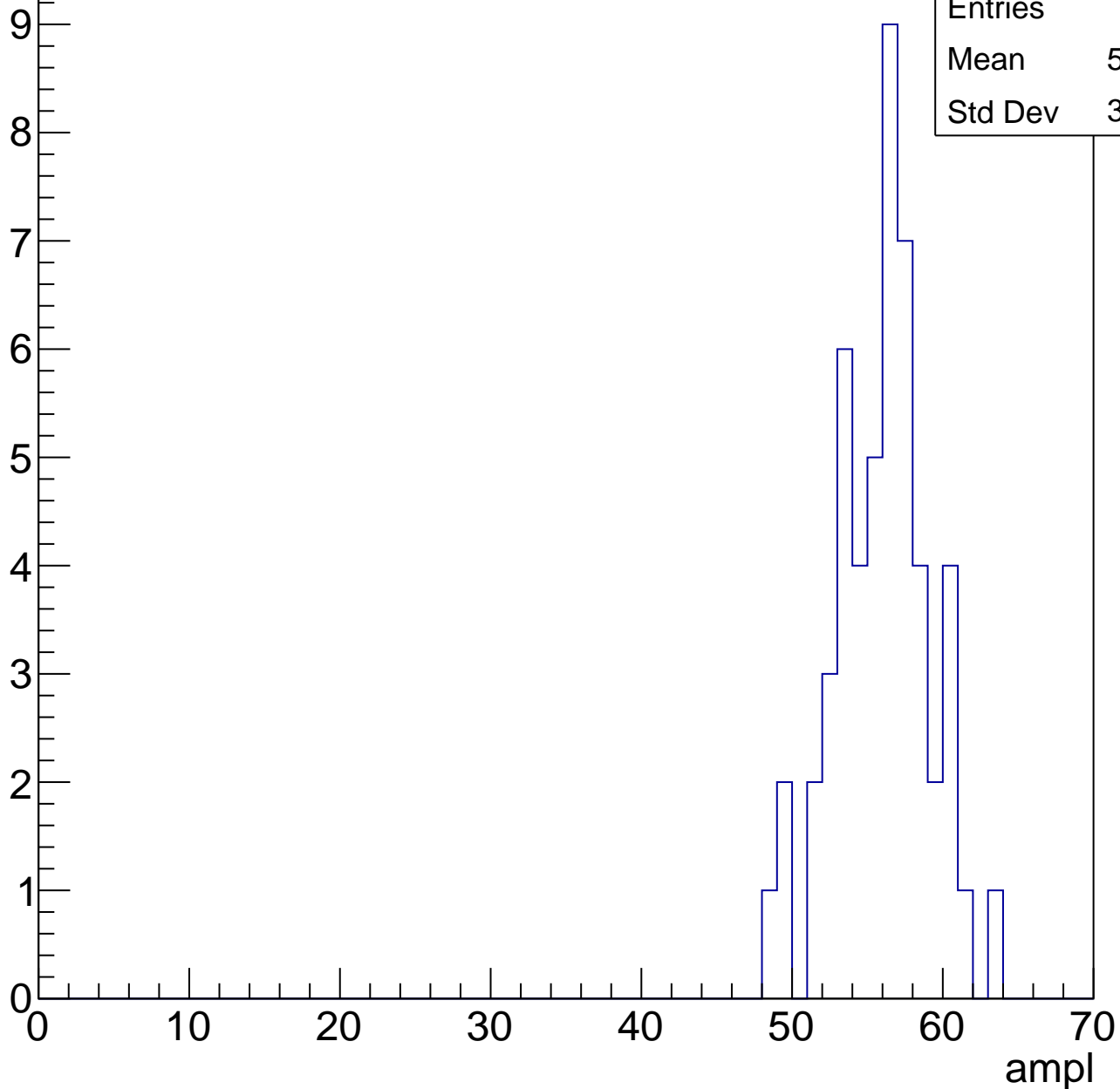


# B1L102S, U24-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.49
Std Dev	3.133

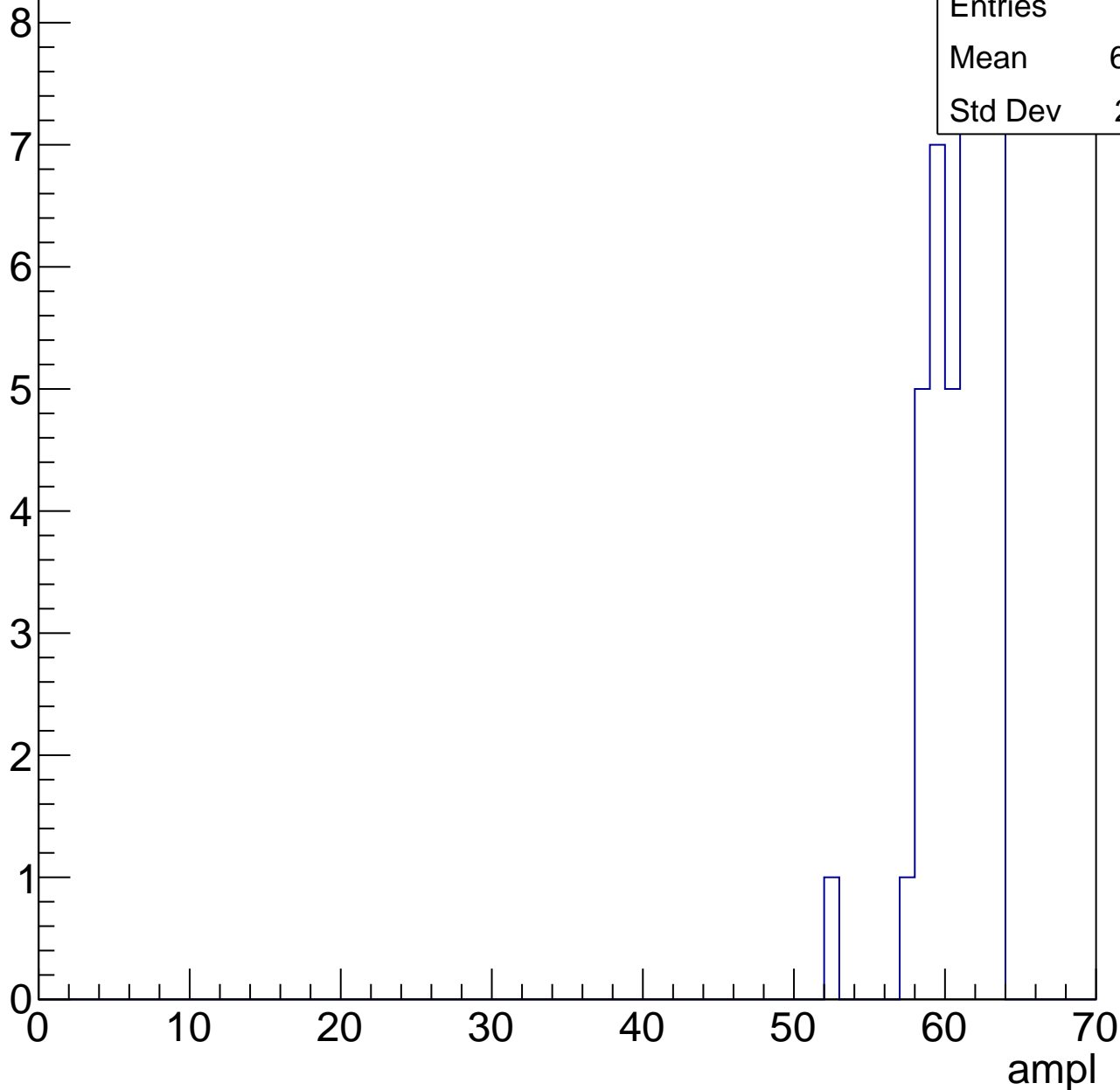


# B1L102S, U24-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	60.47
Std Dev	2.171



# B1L102S, U24-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

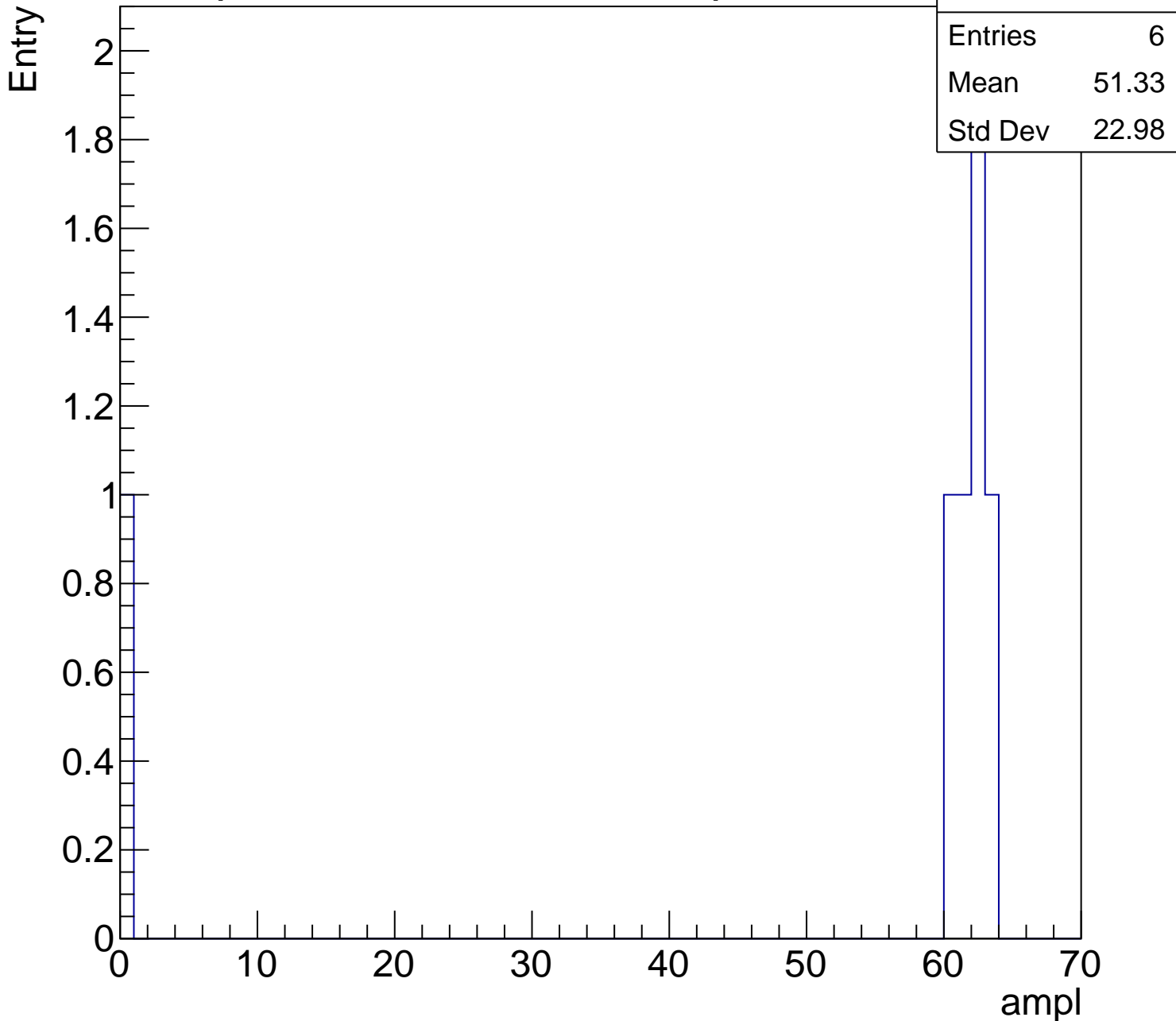
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L102S, U24-ch15, adc0

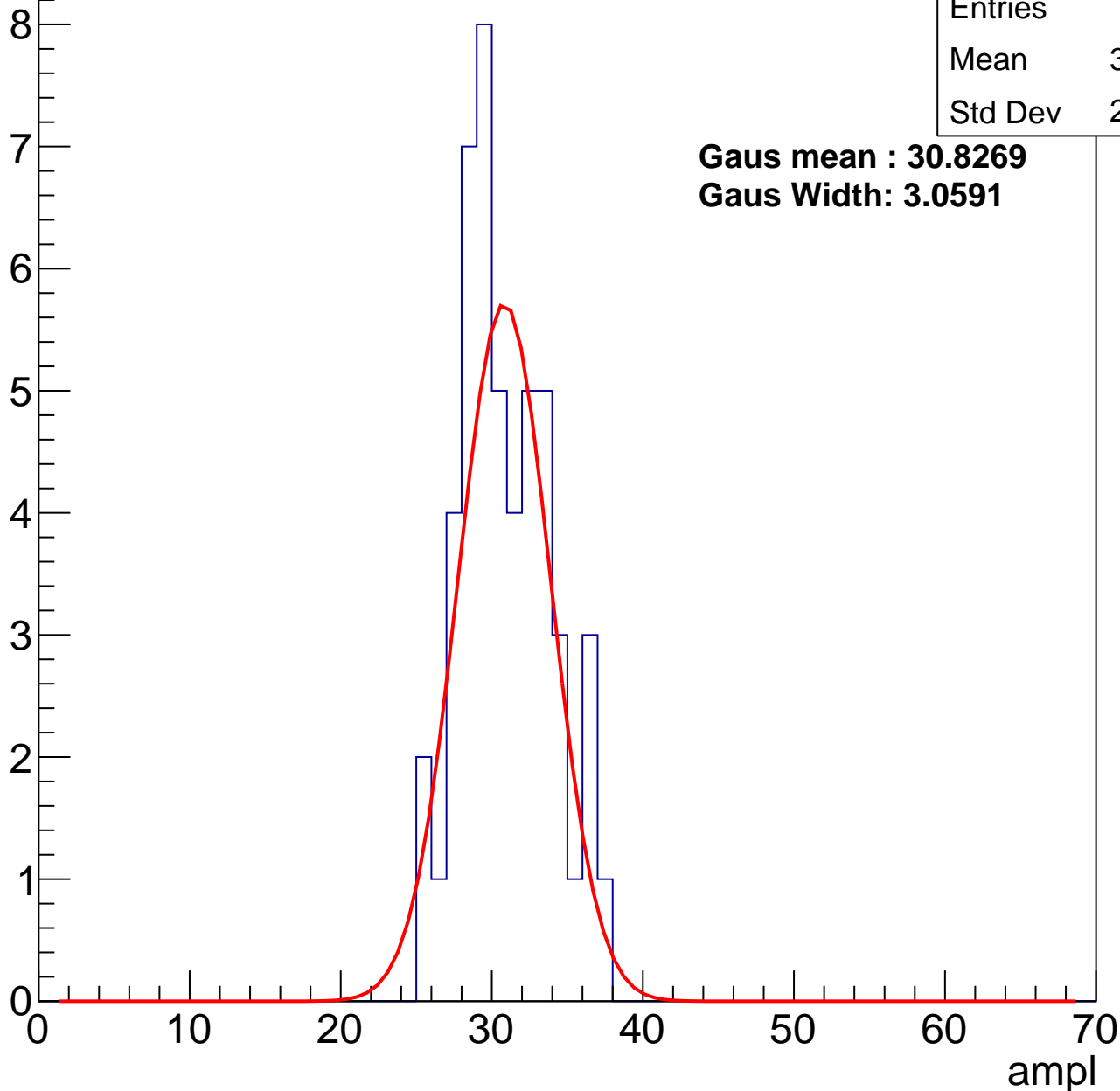
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	30.47
Std Dev	2.949

**Gaus mean : 30.8269**

**Gaus Width: 3.0591**



# B1L102S, U24-ch15, adc1

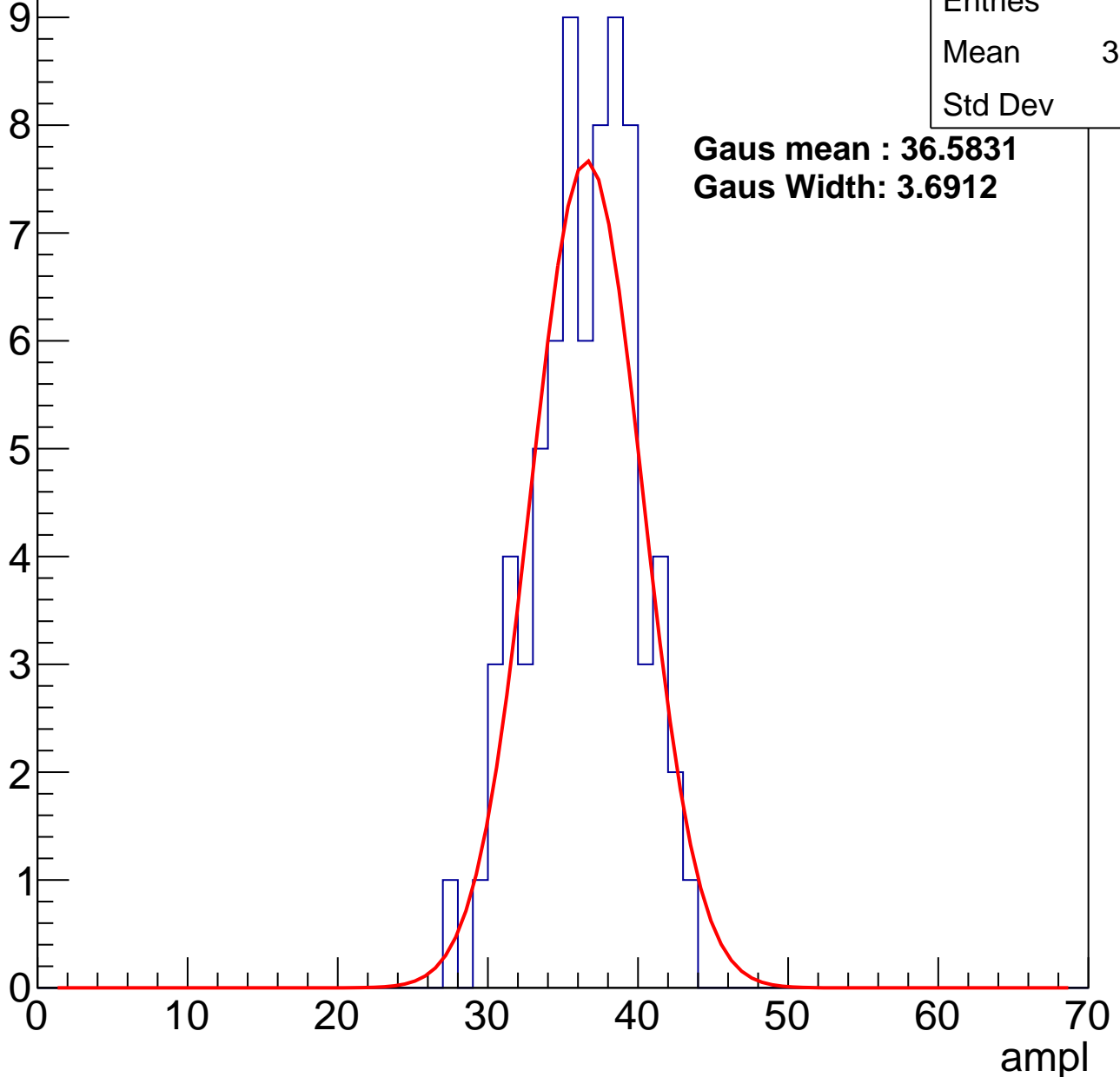
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.99
Std Dev	3.41

**Gaus mean : 36.5831**

**Gaus Width: 3.6912**



# B1L102S, U24-ch15, adc2

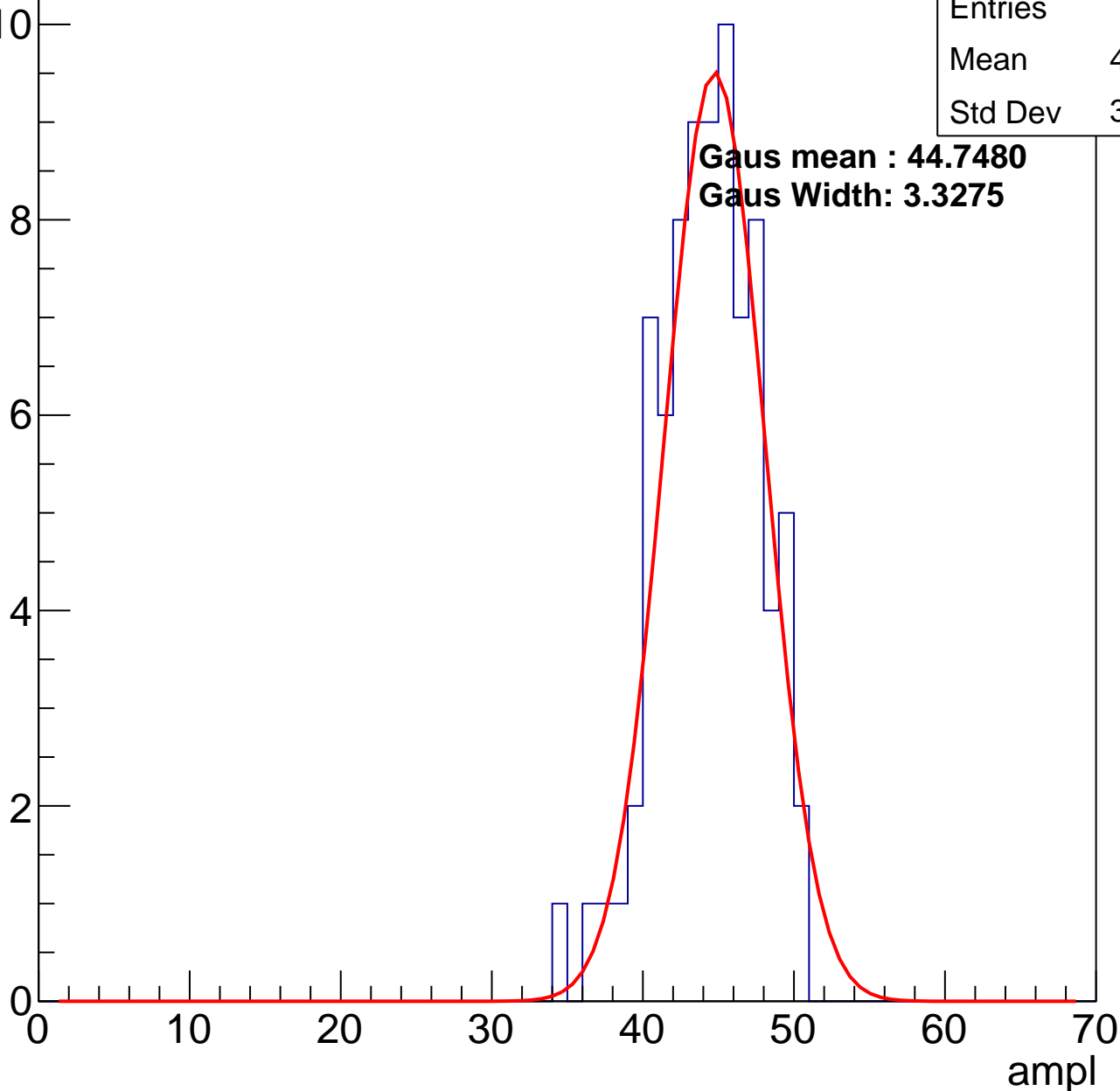
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	43.86
Std Dev	3.288

**Gaus mean : 44.7480**

**Gaus Width: 3.3275**

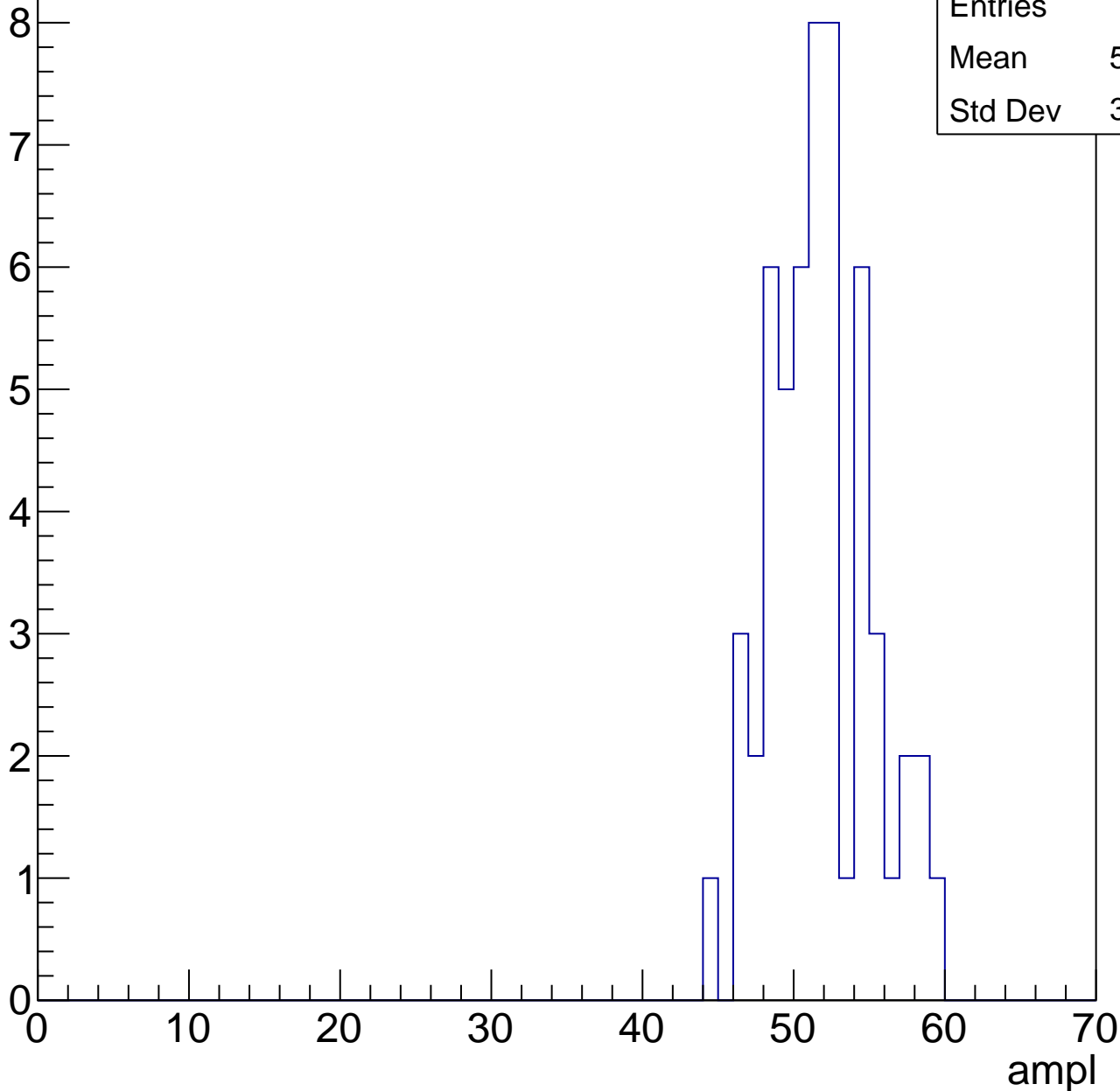


# B1L102S, U24-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

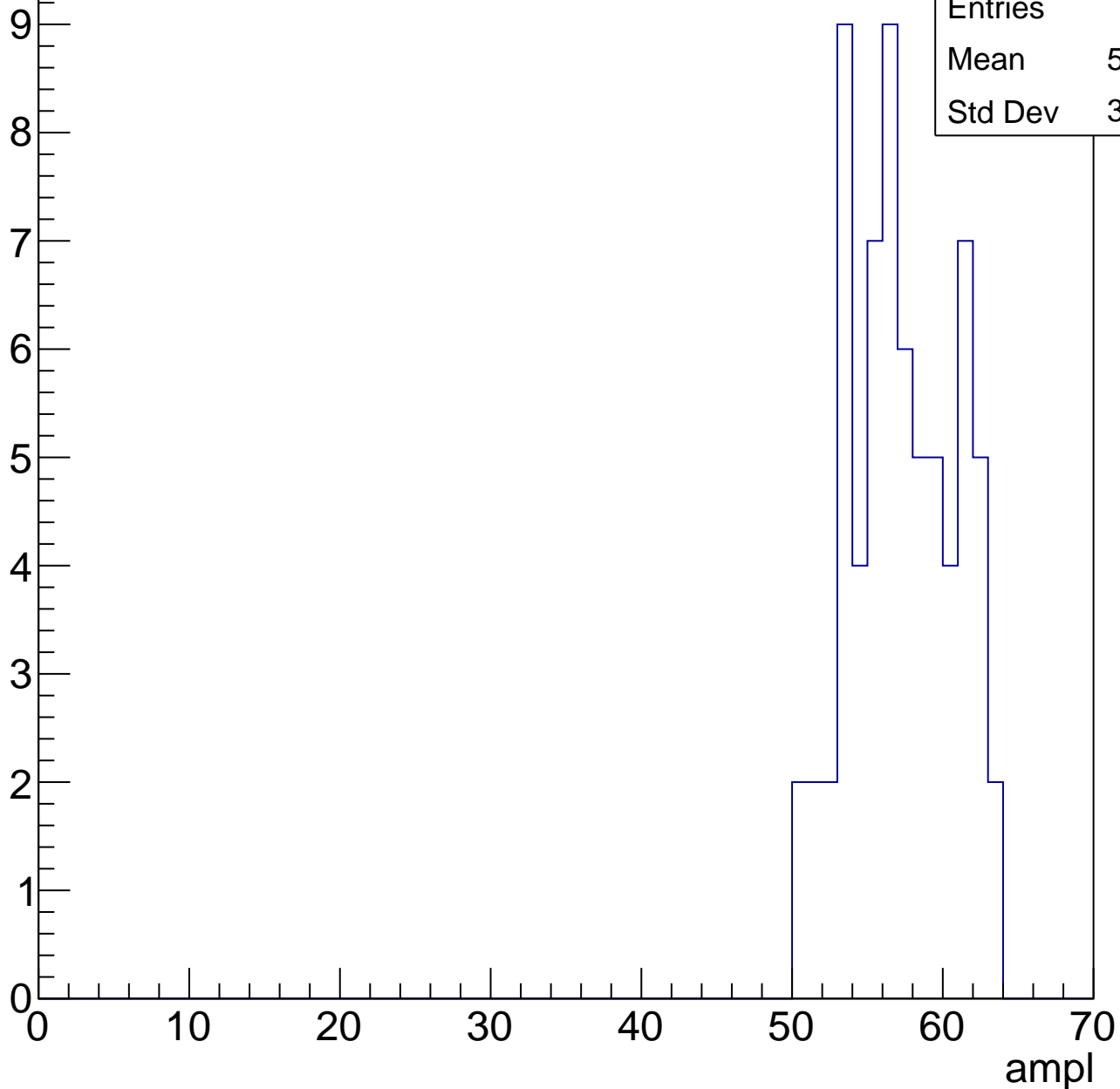
Entries	55
Mean	51.27
Std Dev	3.316



# B1L102S, U24-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



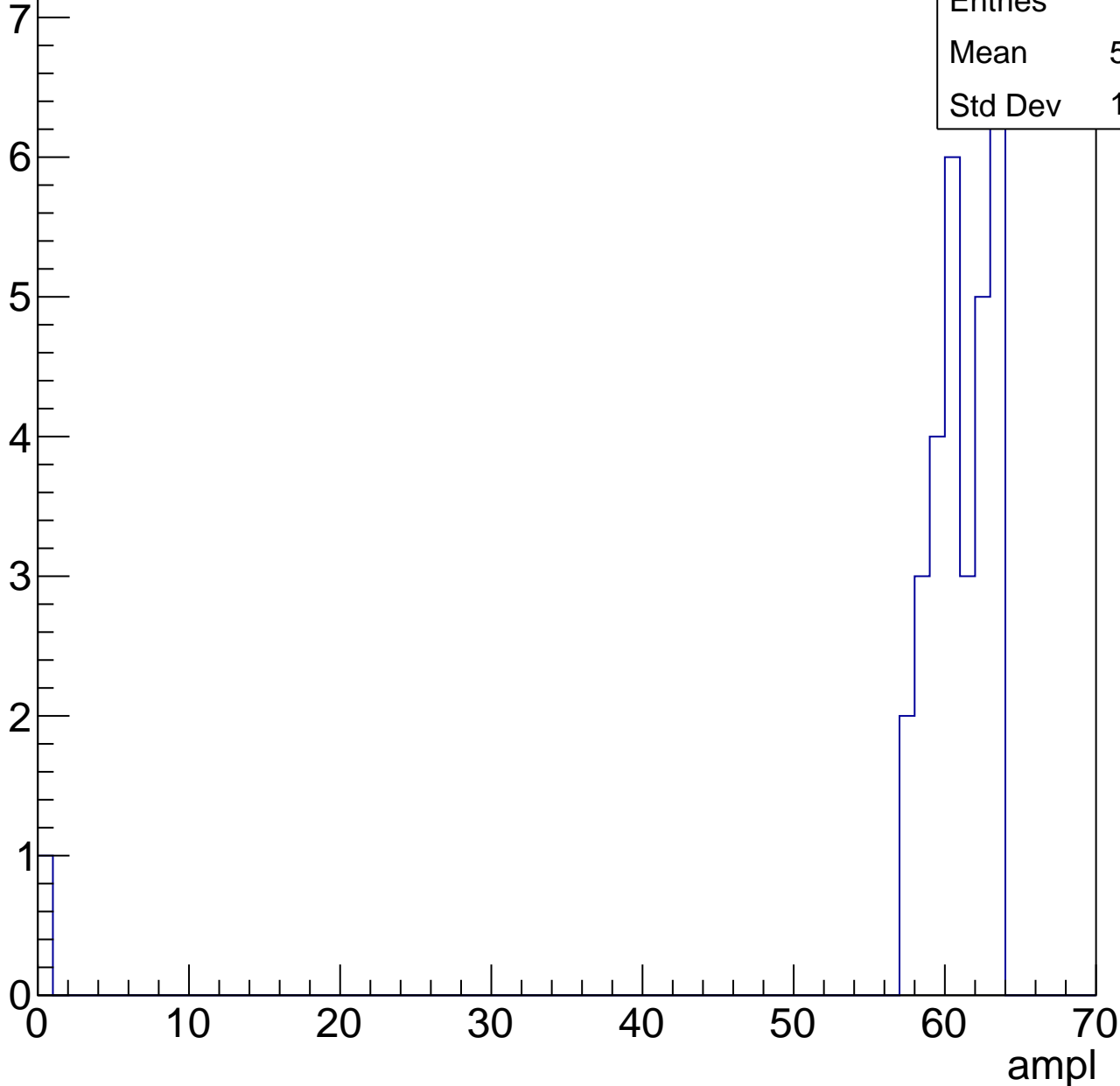
Entries	69
Mean	56.78
Std Dev	3.413

# B1L102S, U24-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	31
Mean	58.65
Std Dev	10.87



# B1L102S, U24-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	23
Std Dev	0

# B1L102S, U24-ch16, adc0

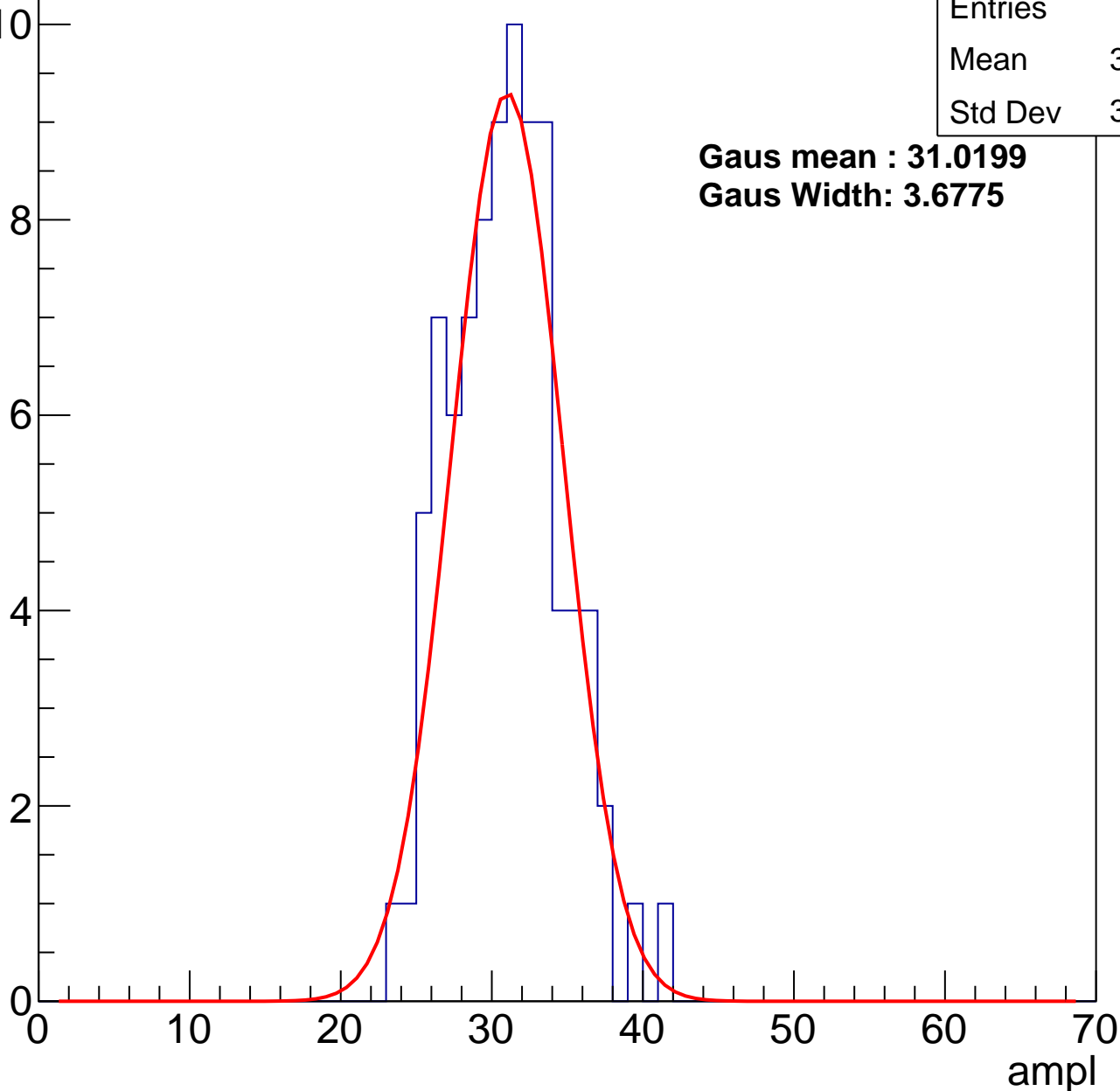
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	30.49
Std Dev	3.577

**Gaus mean : 31.0199**

**Gaus Width: 3.6775**



# B1L102S, U24-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	62
Mean	38.08
Std Dev	3.007

**Gaus mean : 38.1888**

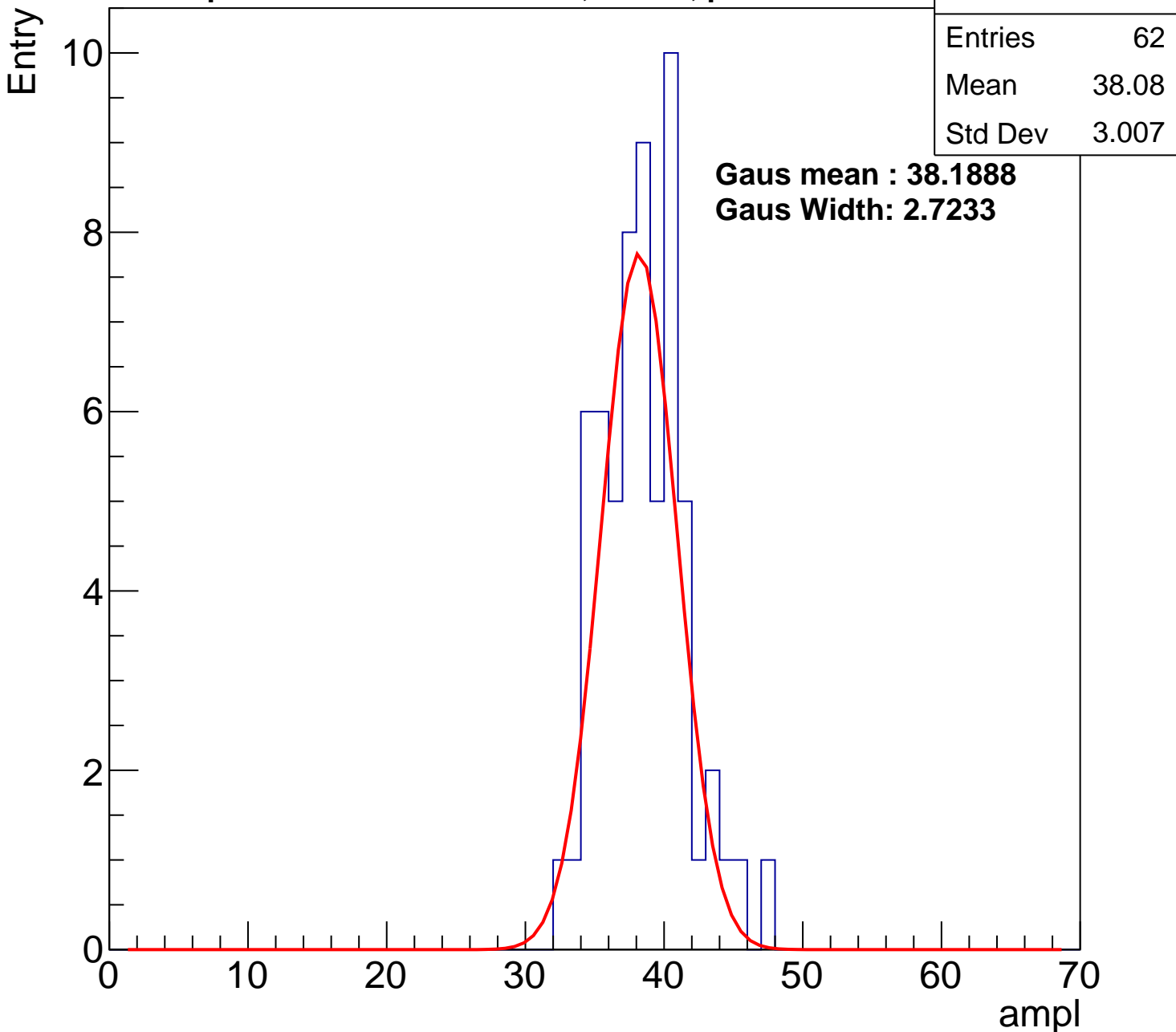
**Gaus Width: 2.7233**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U24-ch16, adc2

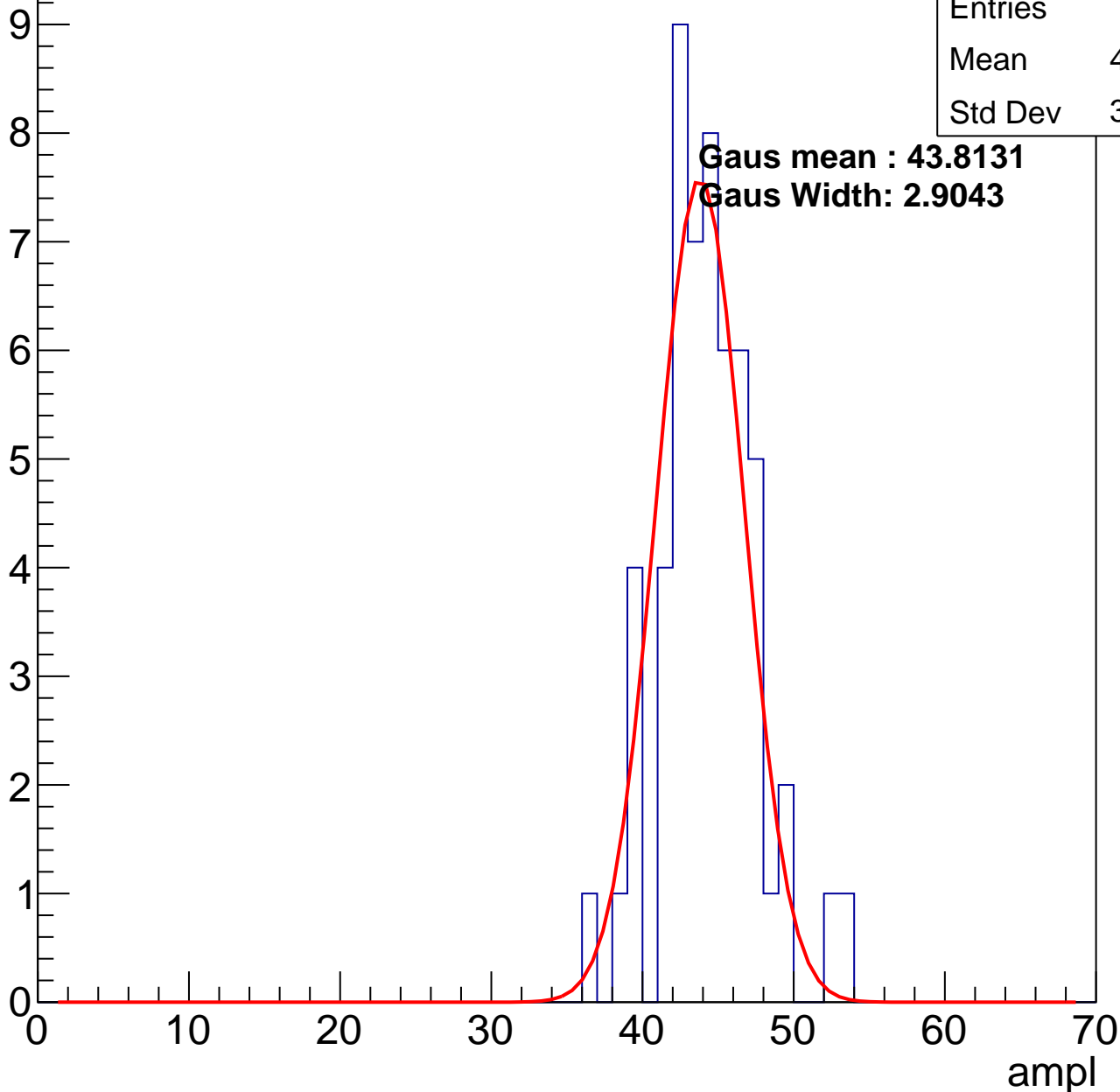
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.88
Std Dev	3.174

**Gaus mean : 43.8131**

**Gaus Width: 2.9043**

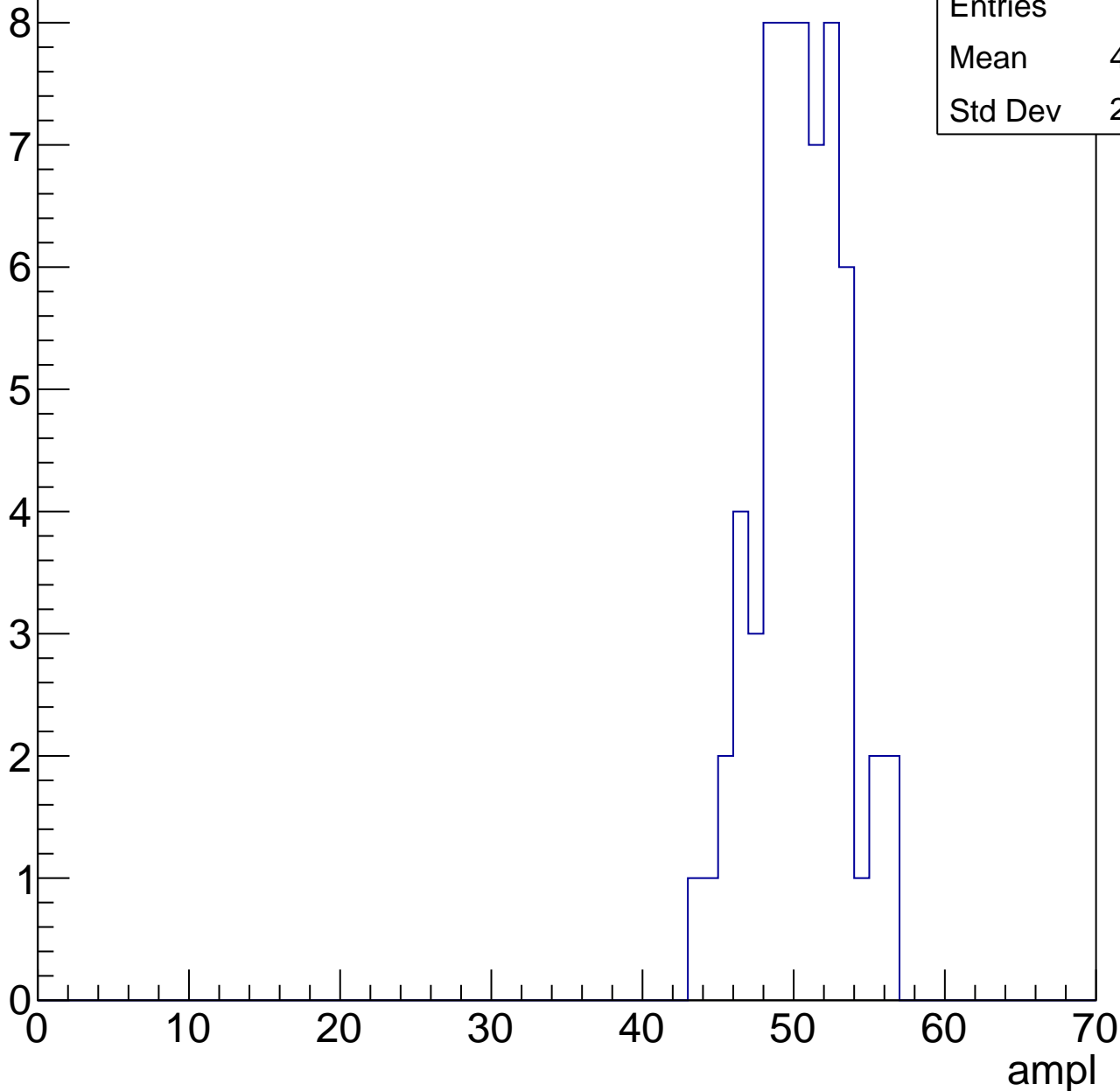


# B1L102S, U24-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.92
Std Dev	2.853

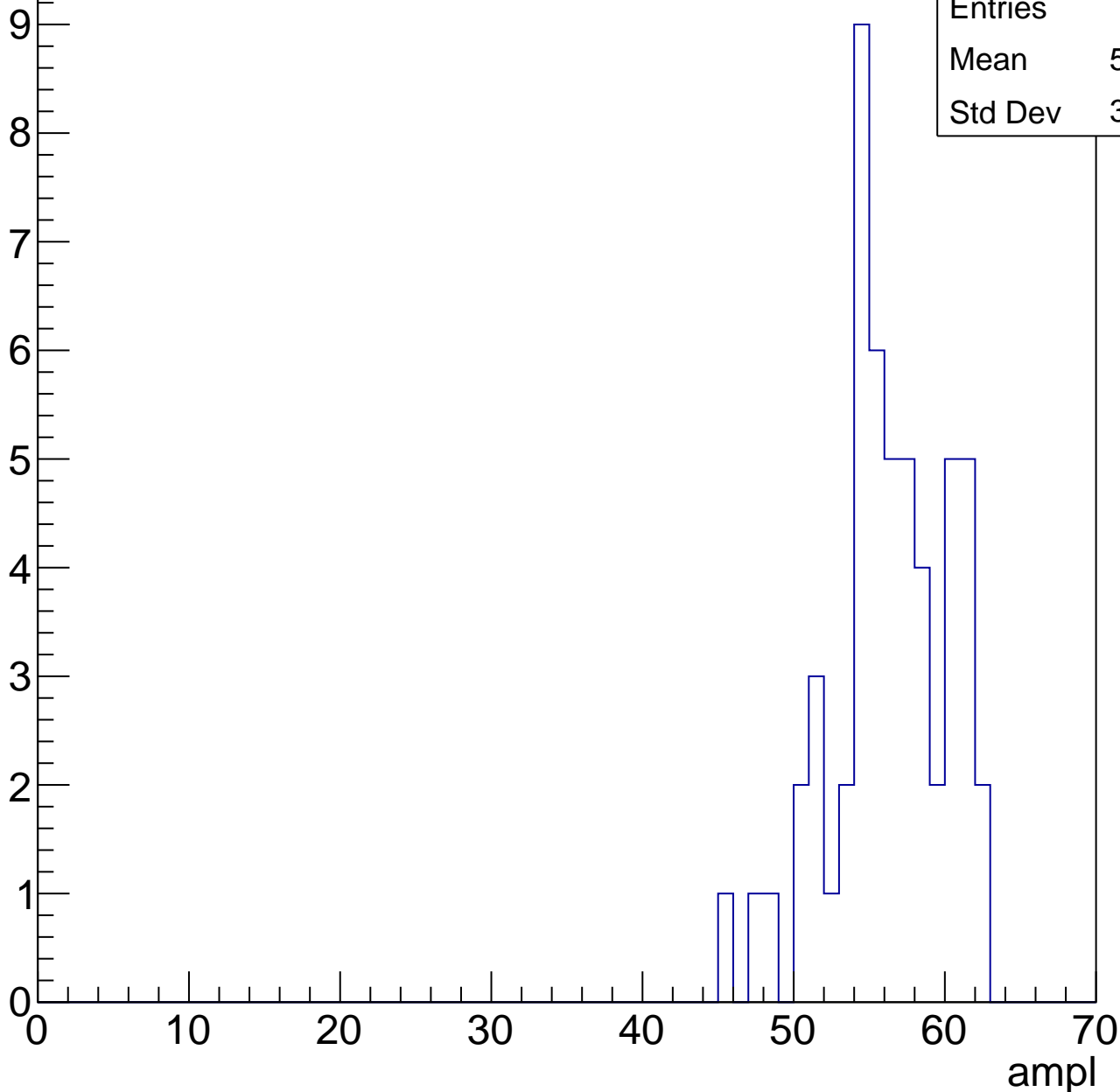


# B1L102S, U24-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.76
Std Dev	3.853

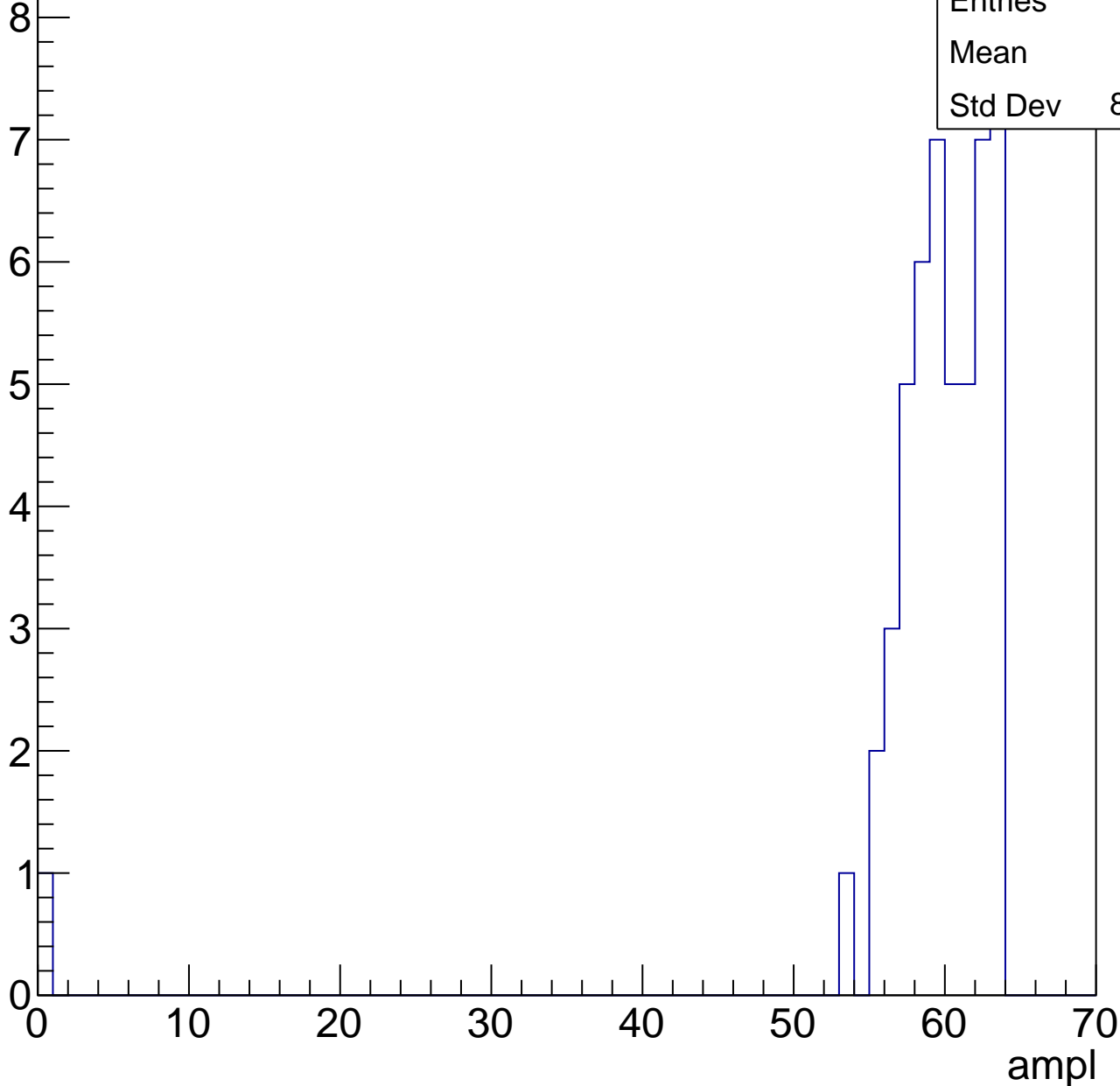


# B1L102S, U24-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.4
Std Dev	8.718



# B1L102S, U24-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch17, adc0

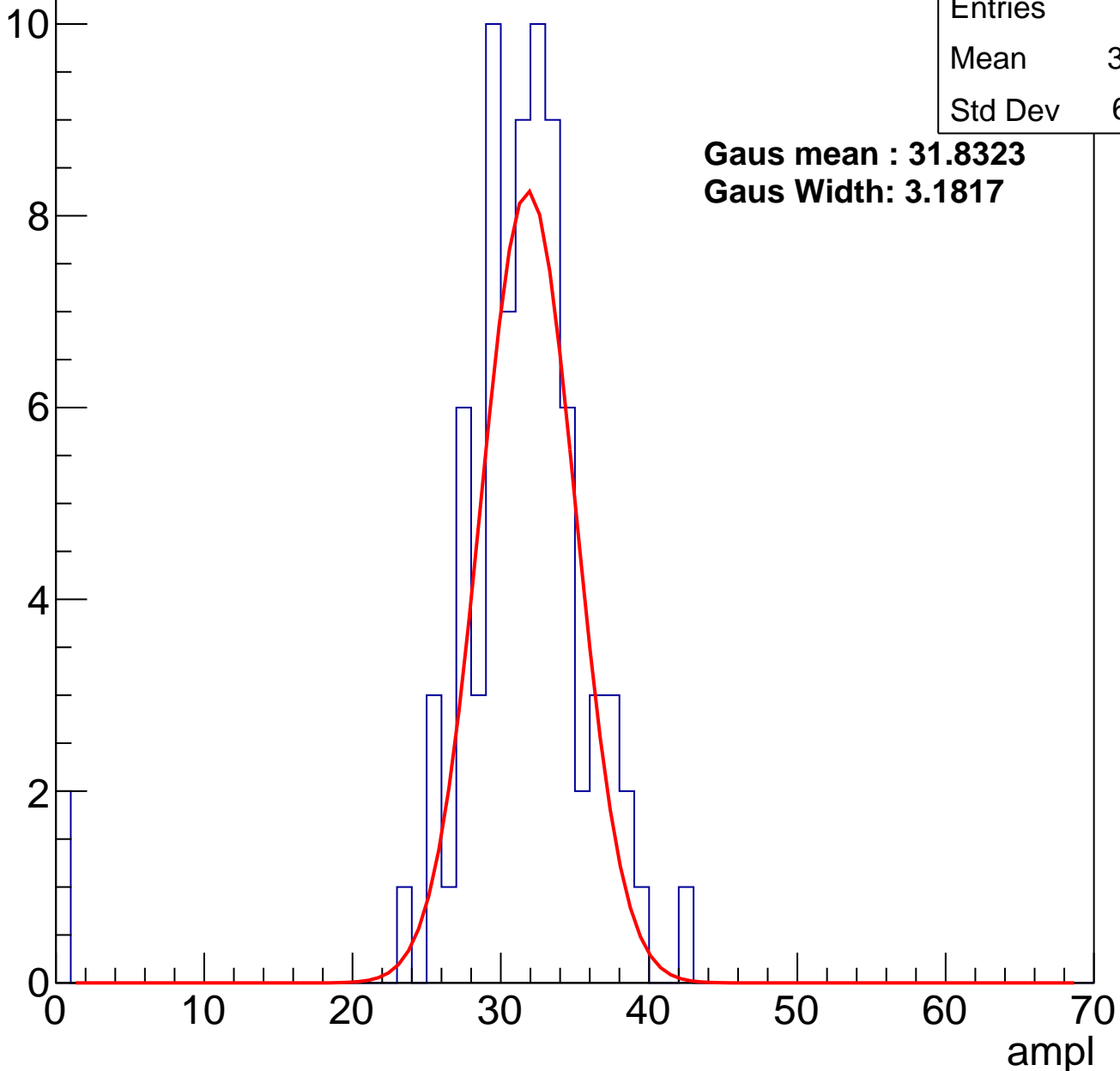
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	79
Mean	30.58
Std Dev	6.031

**Gaus mean : 31.8323**

**Gaus Width: 3.1817**

Entry



# B1L102S, U24-ch17, adc1

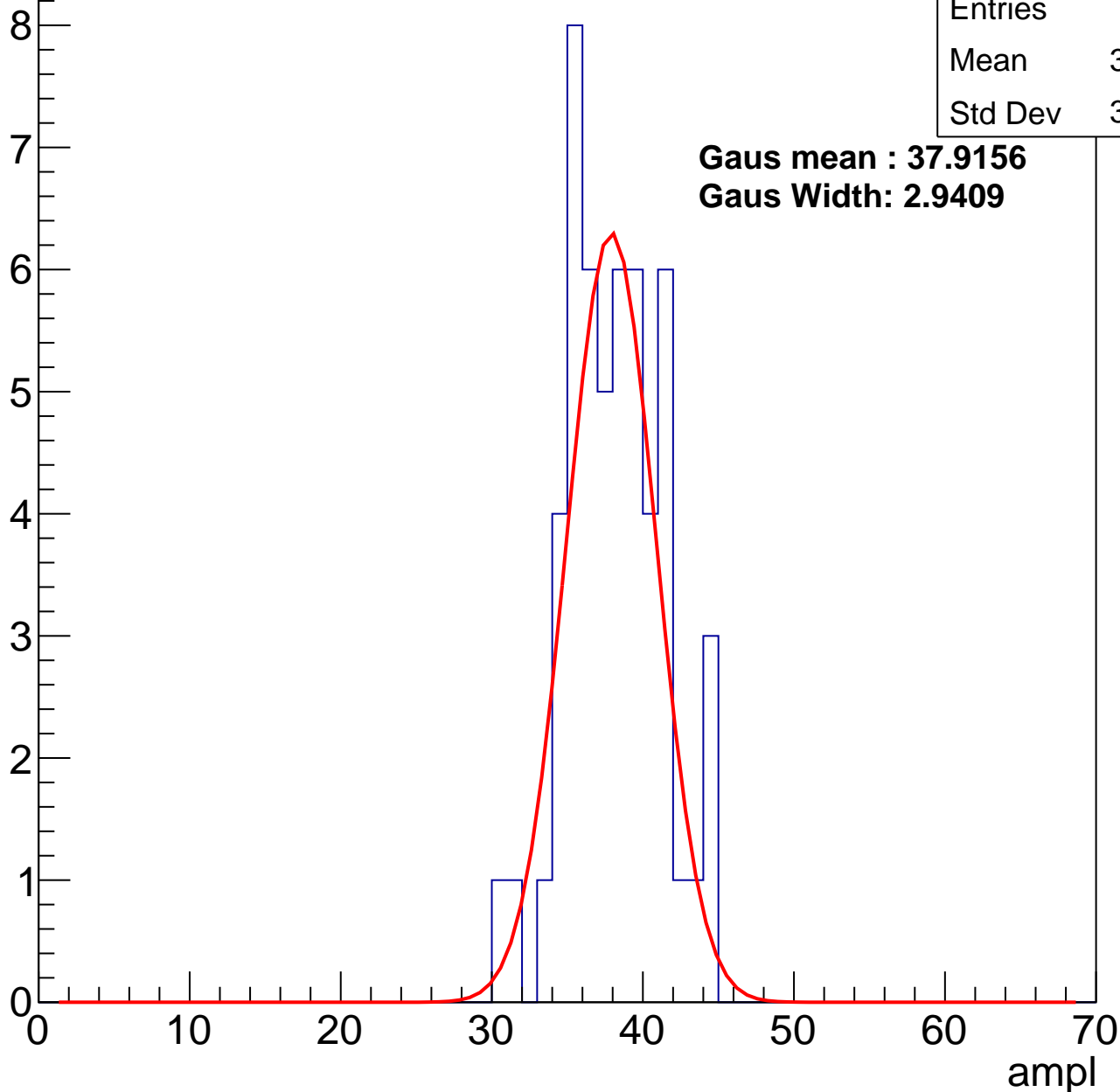
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	37.66
Std Dev	3.138

**Gaus mean : 37.9156**

**Gaus Width: 2.9409**



# B1L102S, U24-ch17, adc2

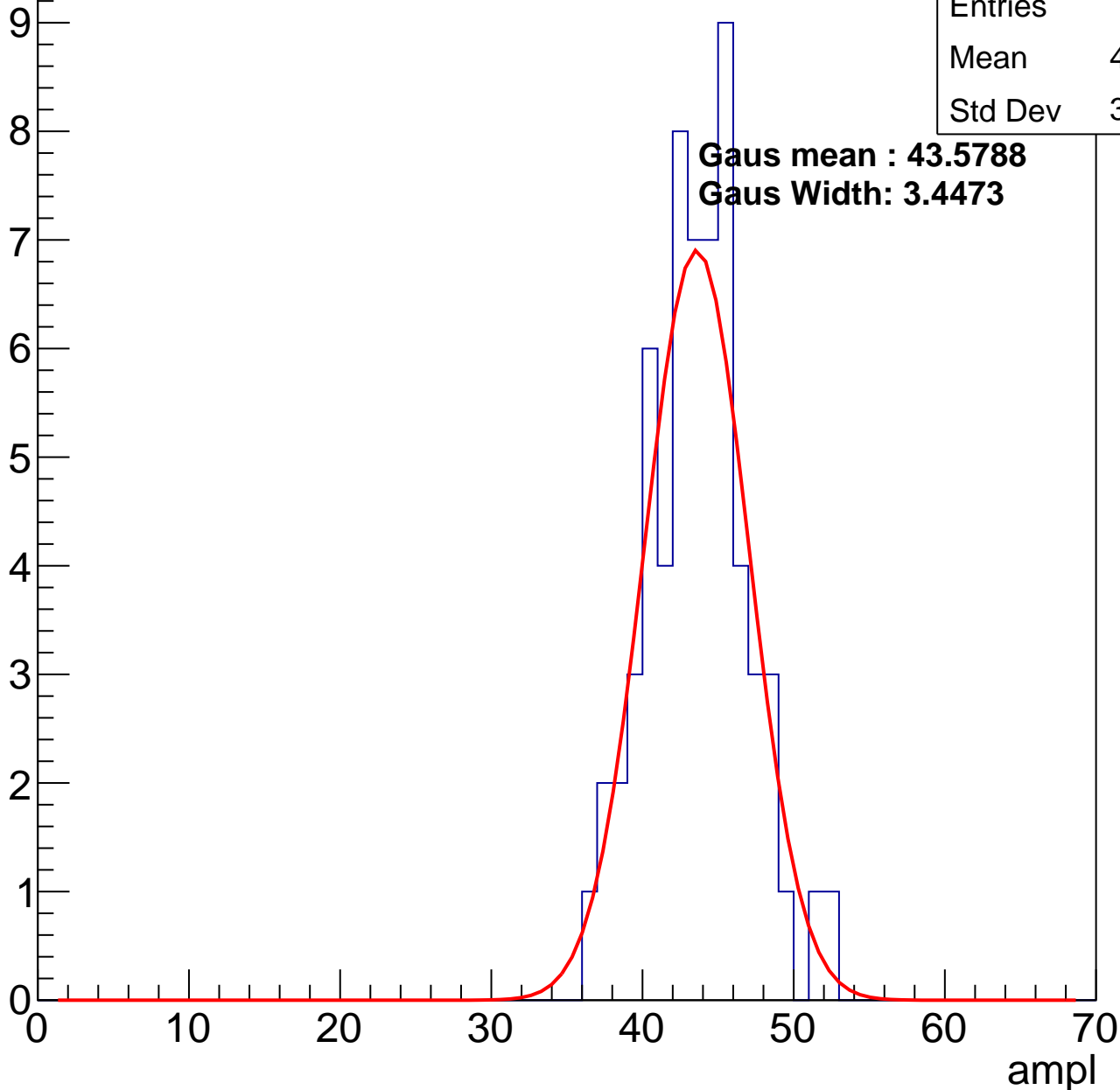
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.19
Std Dev	3.306

**Gaus mean : 43.5788**

**Gaus Width: 3.4473**

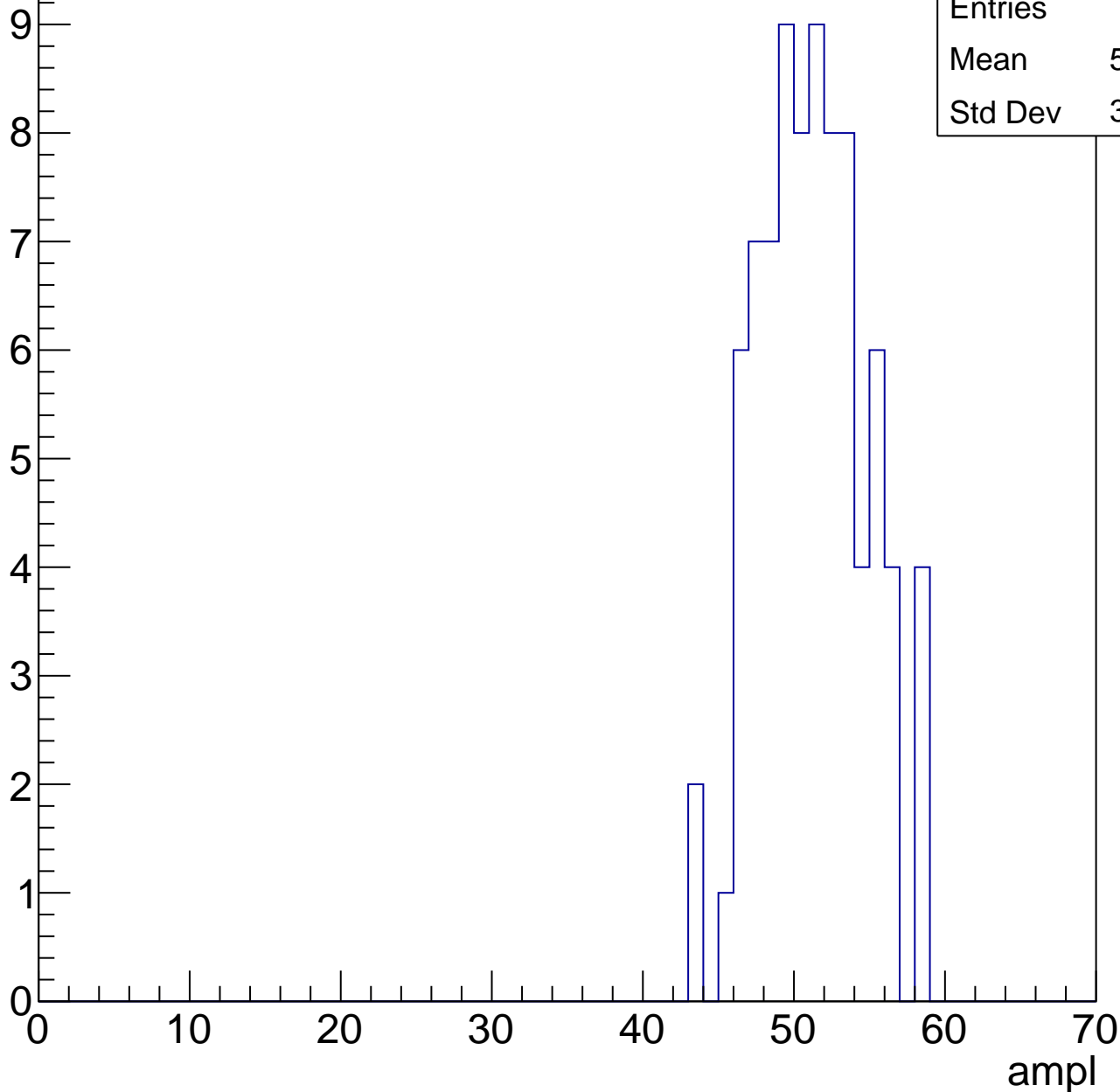


# B1L102S, U24-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	50.77
Std Dev	3.465



# B1L102S, U24-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	53
Mean	56.11
Std Dev	8.352

Entry

10

8

6

4

2

0

0

10

20

30

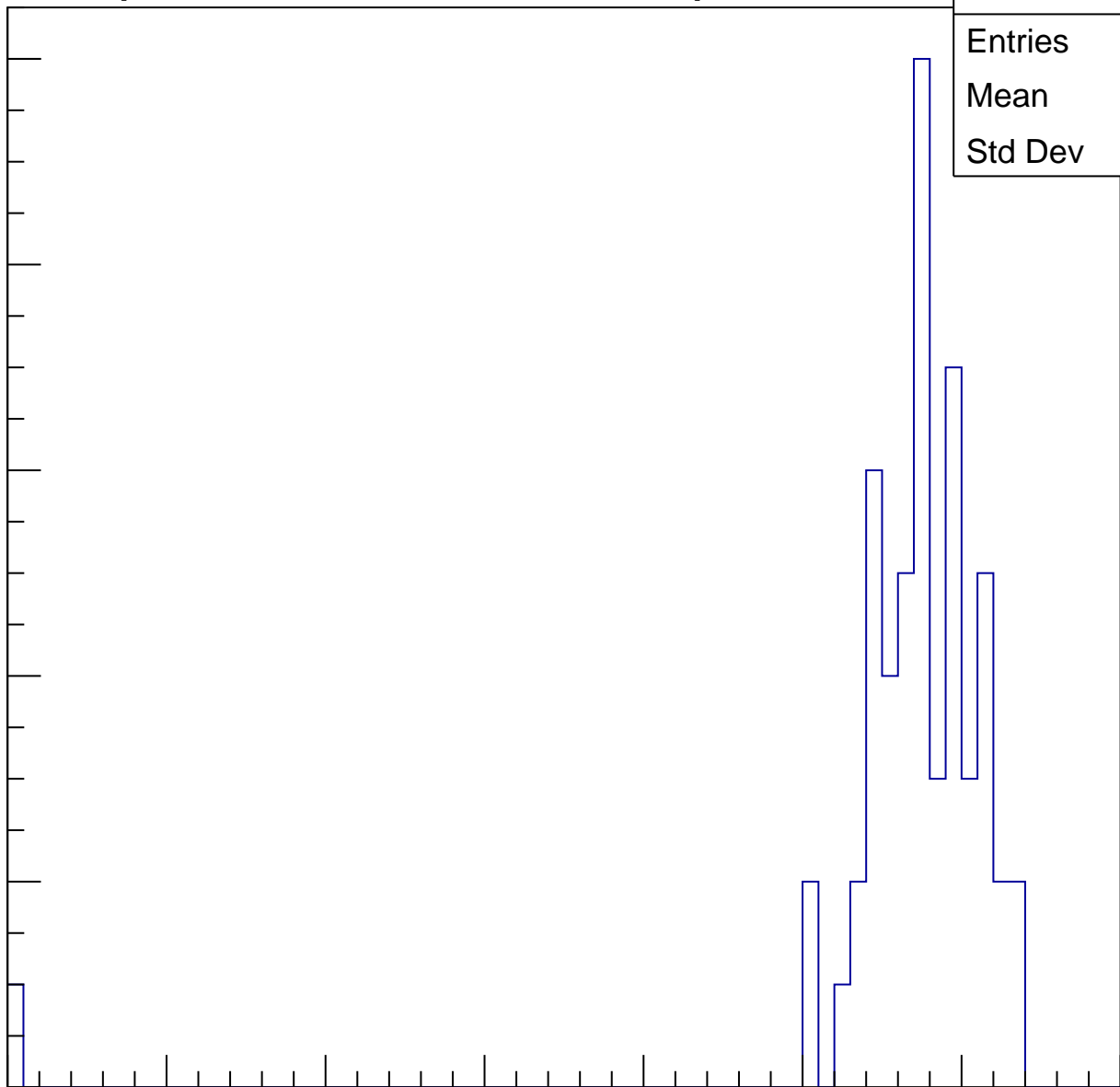
40

50

60

70

ampl

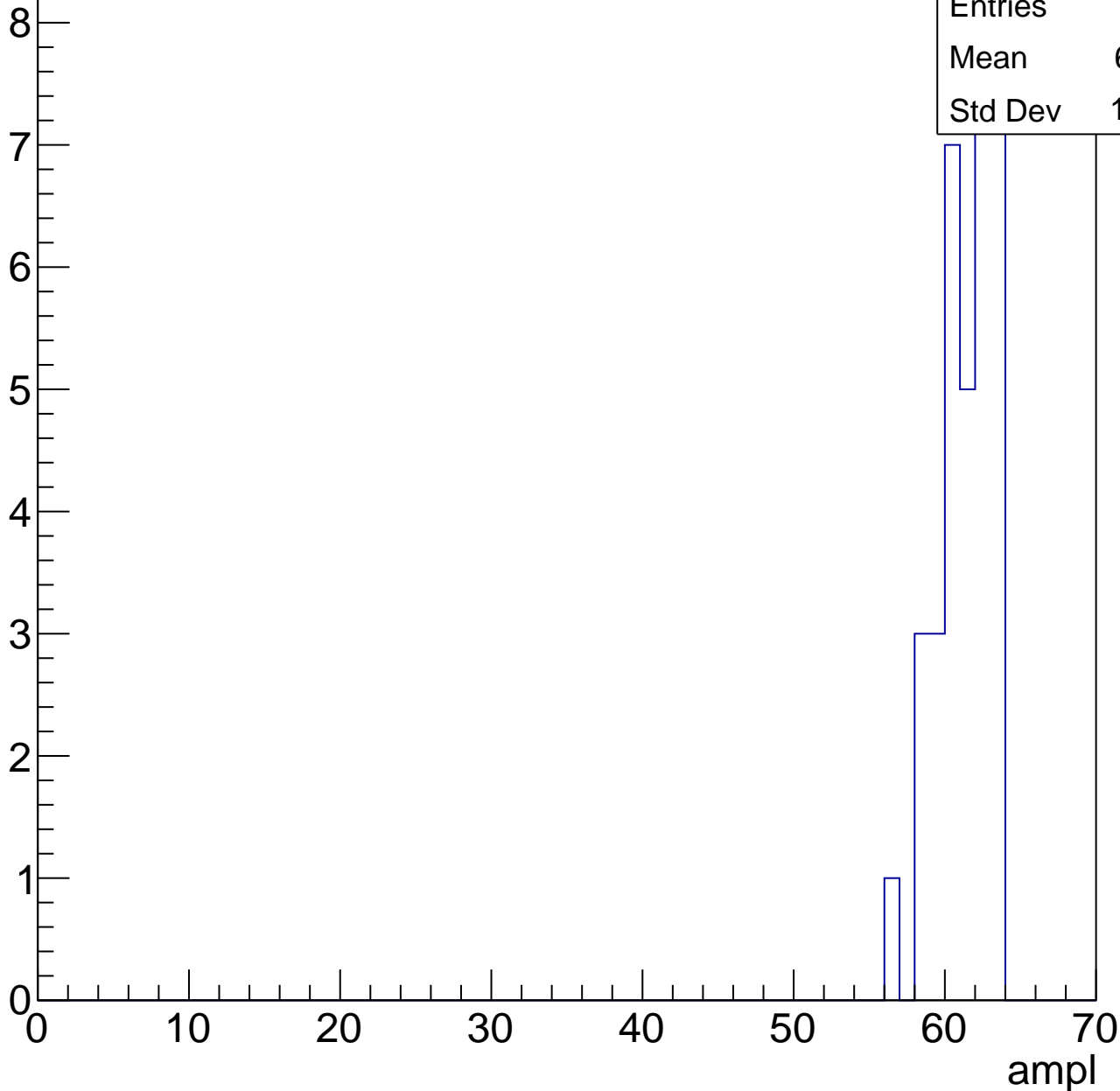


# B1L102S, U24-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.91
Std Dev	1.779



# B1L102S, U24-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch18, adc0

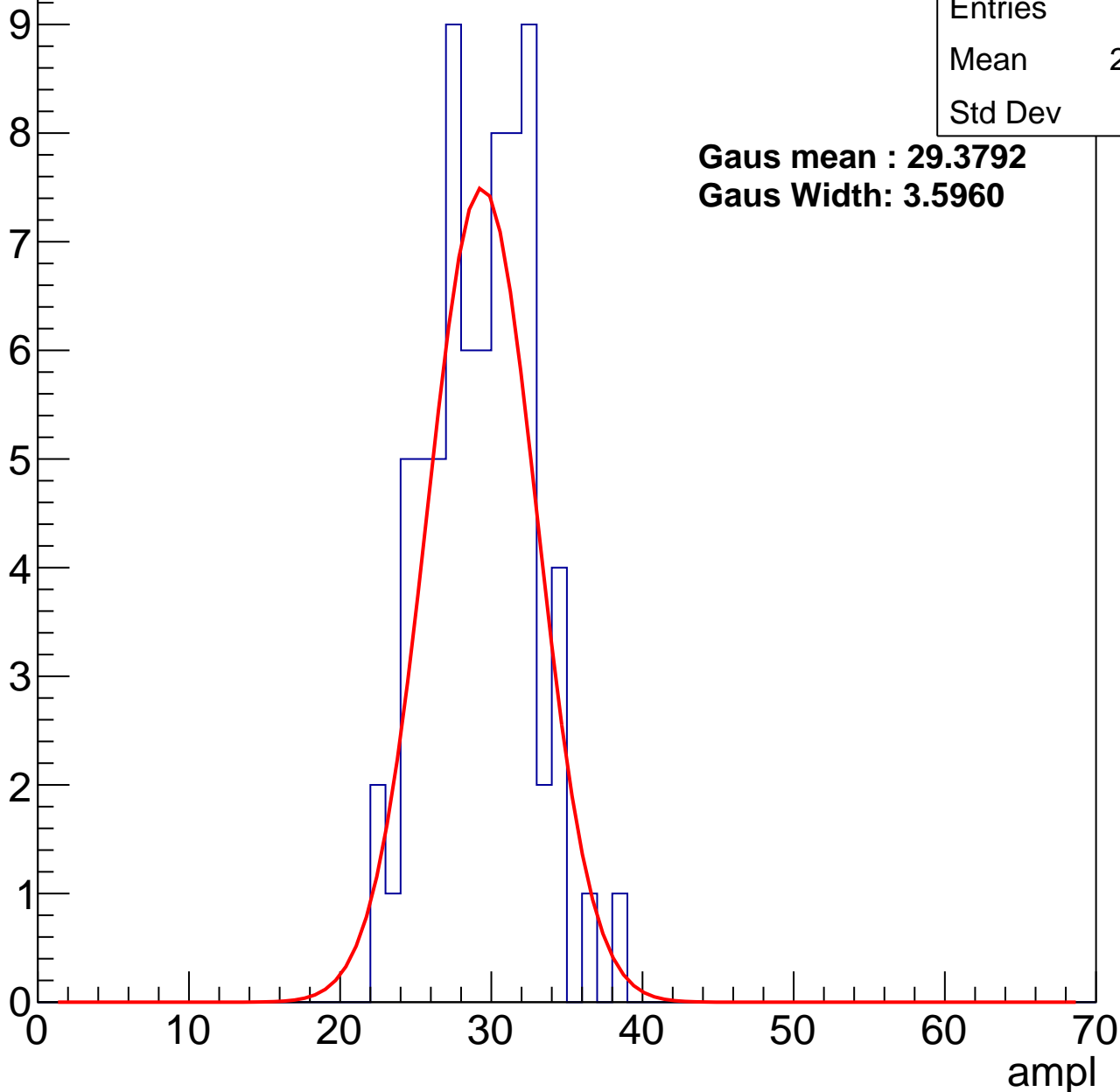
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.88
Std Dev	3.35

**Gaus mean : 29.3792**

**Gaus Width: 3.5960**



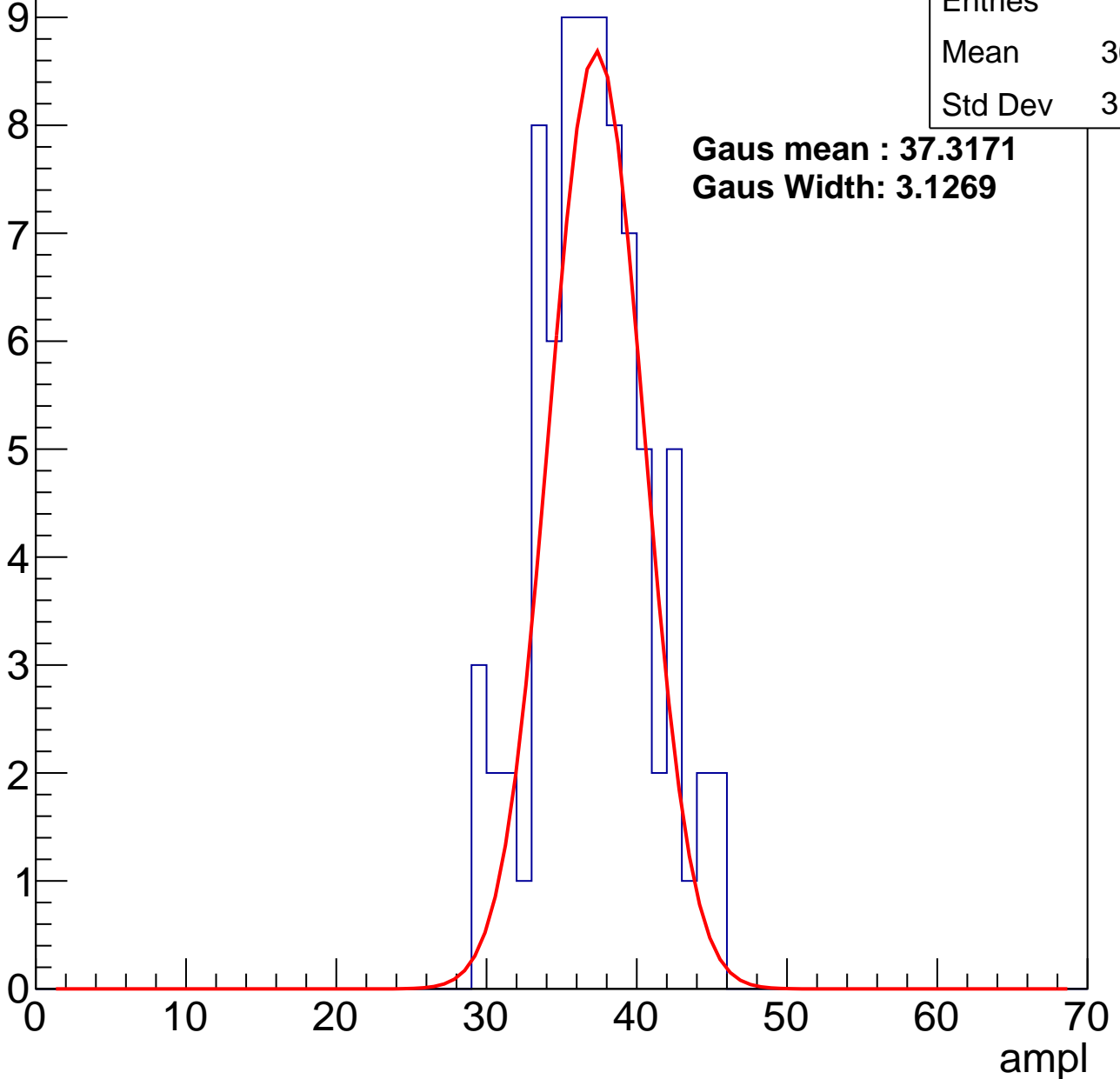
# B1L102S, U24-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.68
Std Dev	3.695

**Gaus mean : 37.3171**  
**Gaus Width: 3.1269**



# B1L102S, U24-ch18, adc2

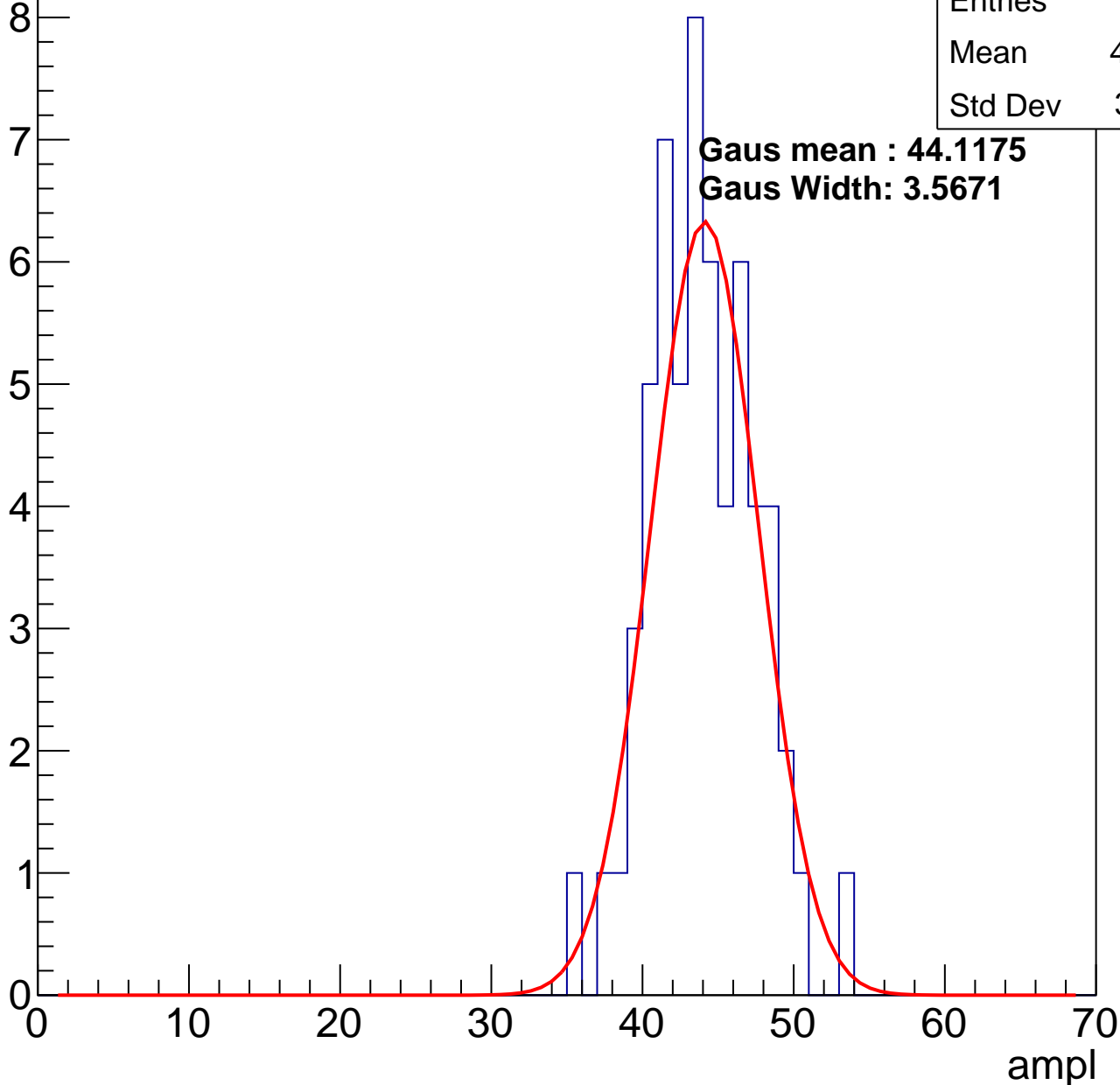
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	43.54
Std Dev	3.431

**Gaus mean : 44.1175**

**Gaus Width: 3.5671**

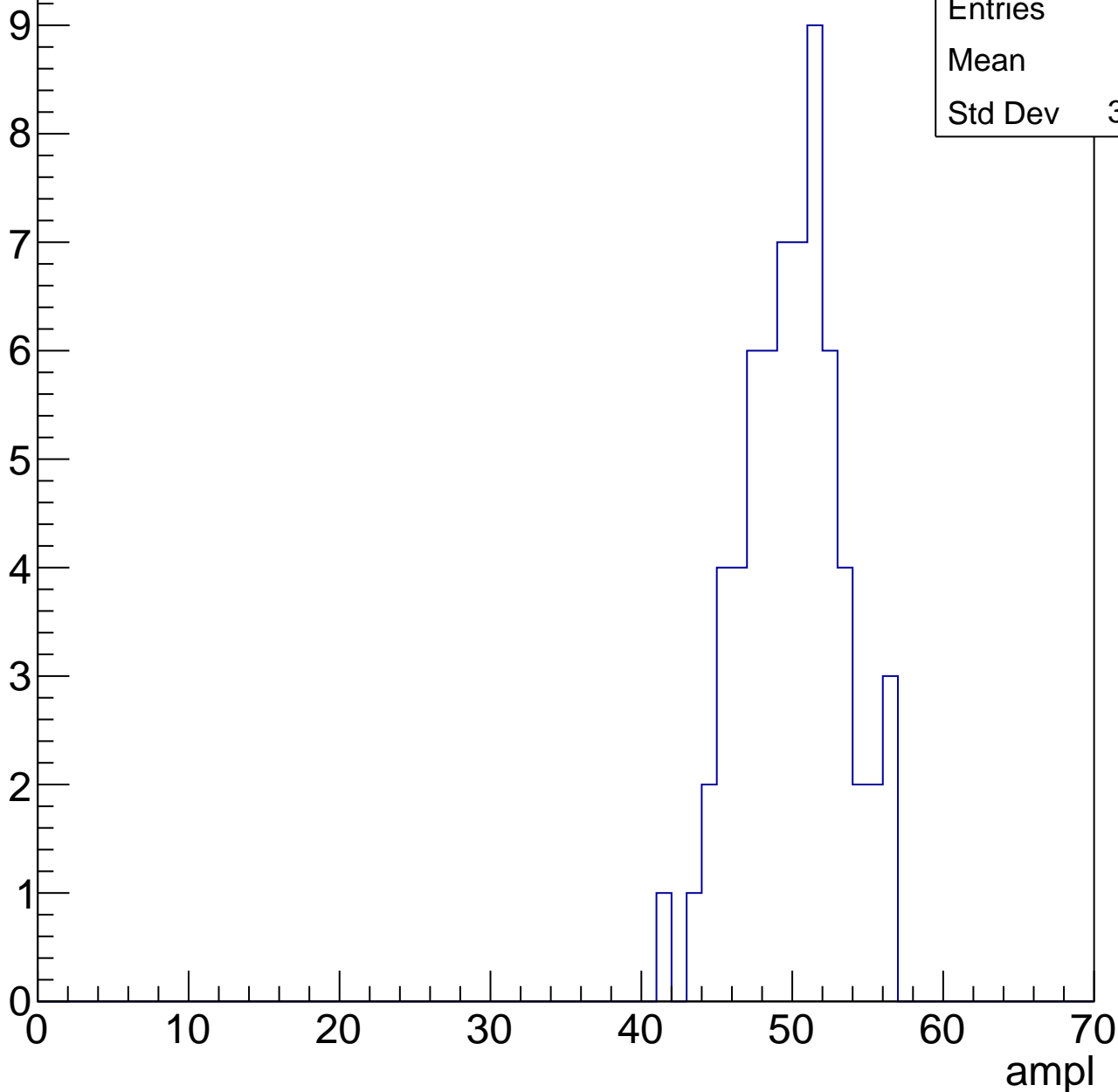


# B1L102S, U24-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.5
Std Dev	3.293

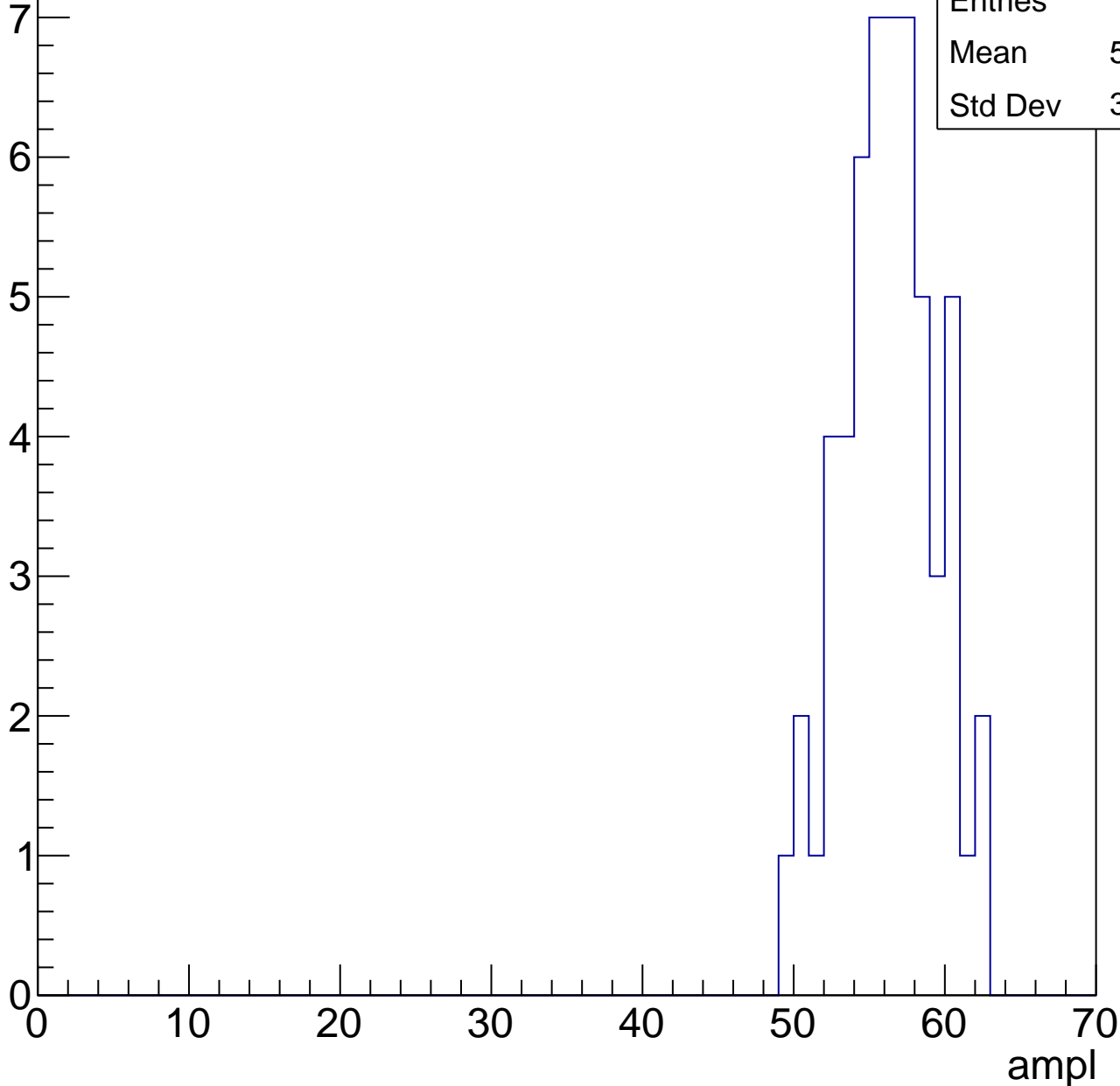


# B1L102S, U24-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.85
Std Dev	3.036

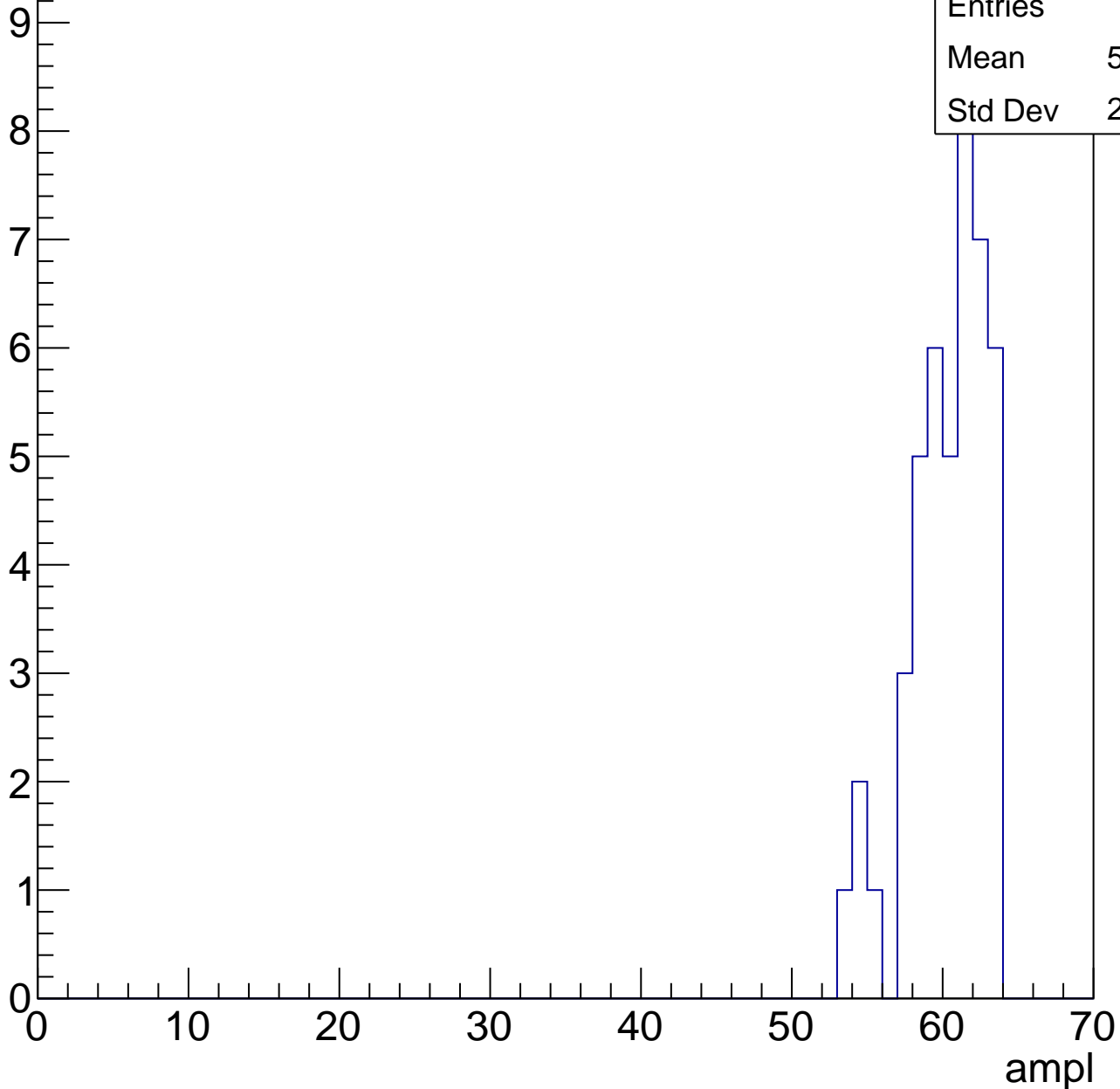


# B1L102S, U24-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

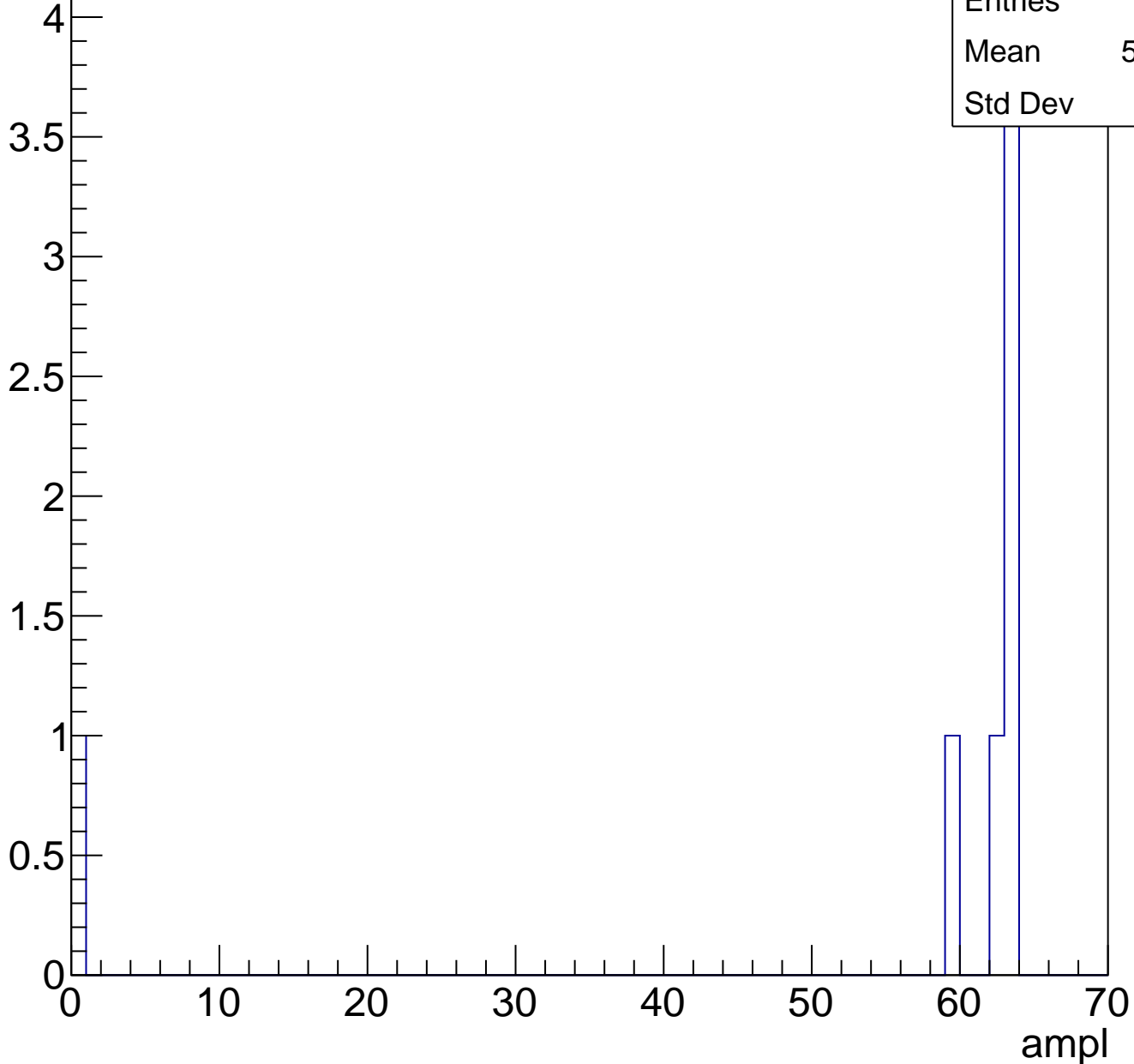
Entries	45
Mean	59.82
Std Dev	2.532



# B1L102S, U24-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch19, adc0

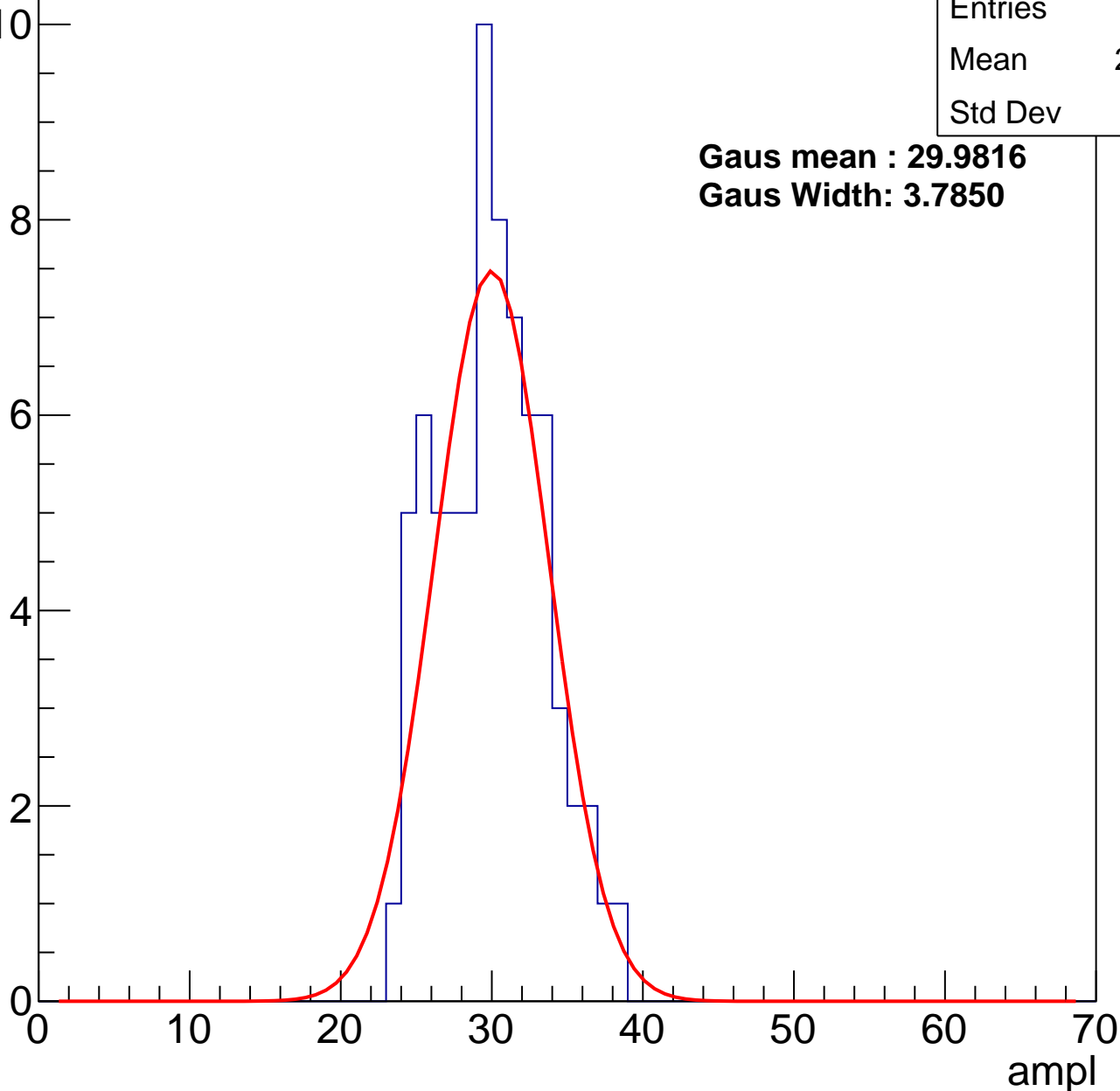
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.51
Std Dev	3.48

**Gaus mean : 29.9816**

**Gaus Width: 3.7850**



# B1L102S, U24-ch19, adc1

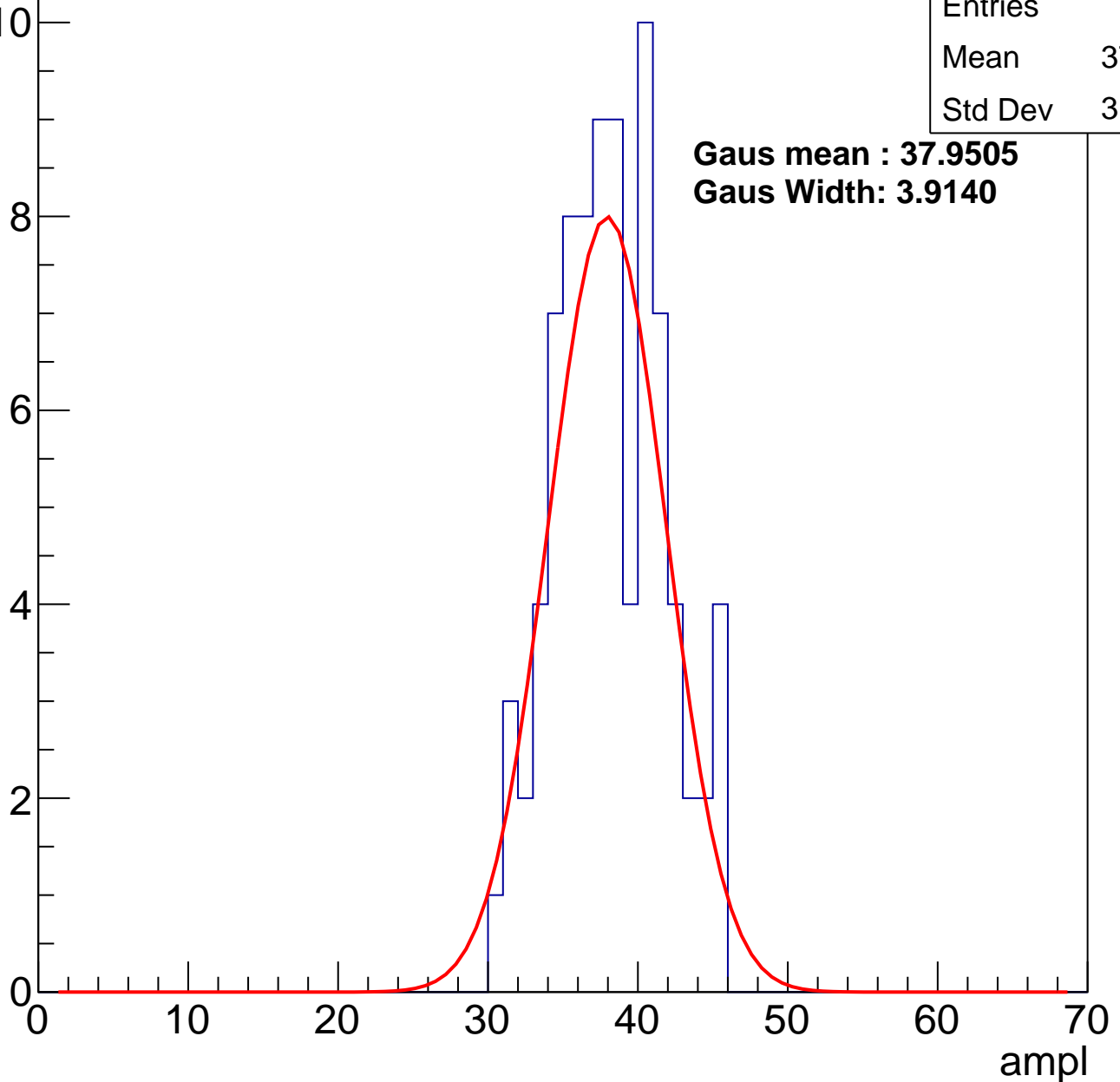
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	37.68
Std Dev	3.599

**Gaus mean : 37.9505**

**Gaus Width: 3.9140**



# B1L102S, U24-ch19, adc2

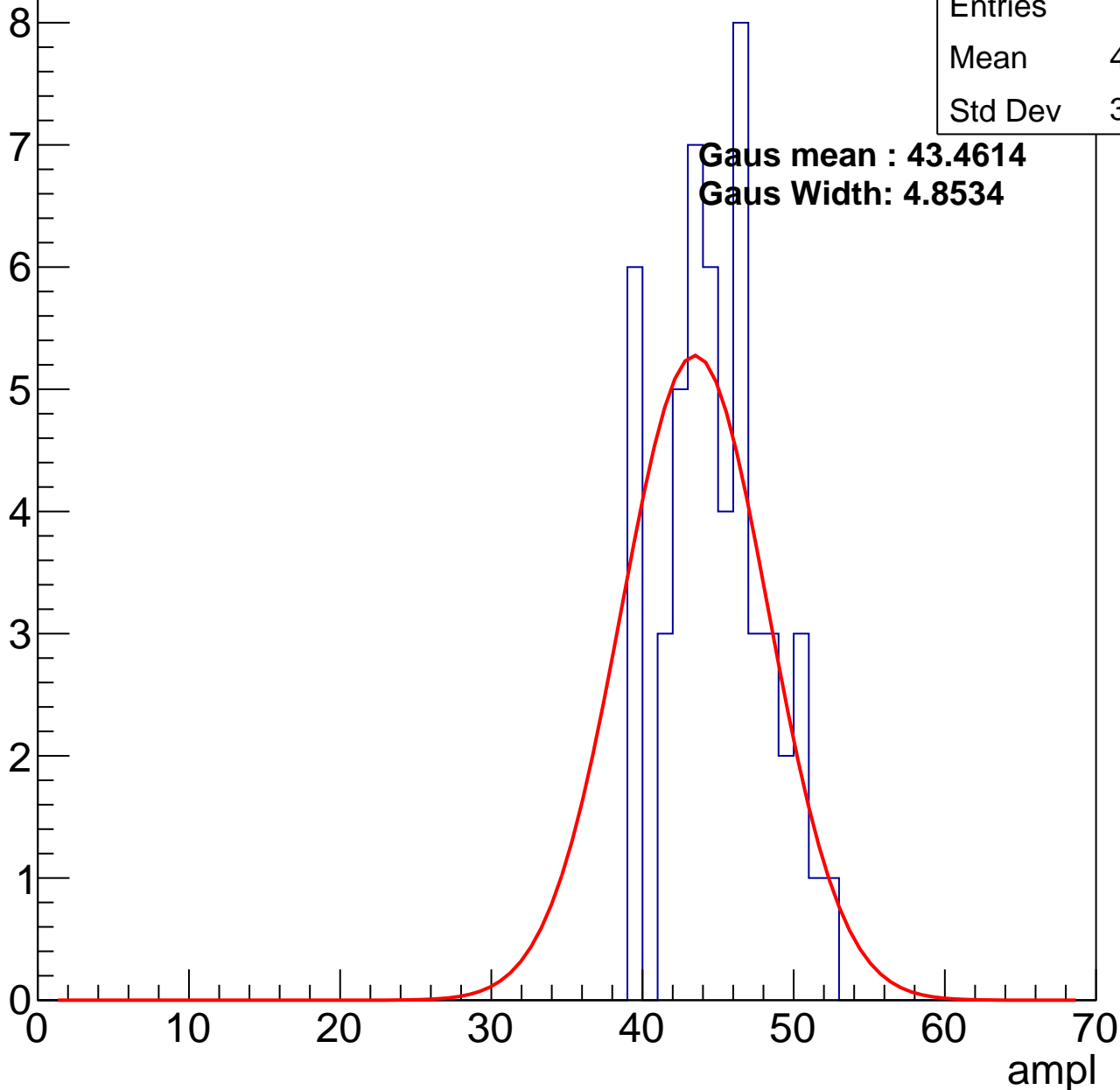
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	44.54
Std Dev	3.314

**Gaus mean : 43.4614**

**Gaus Width: 4.8534**

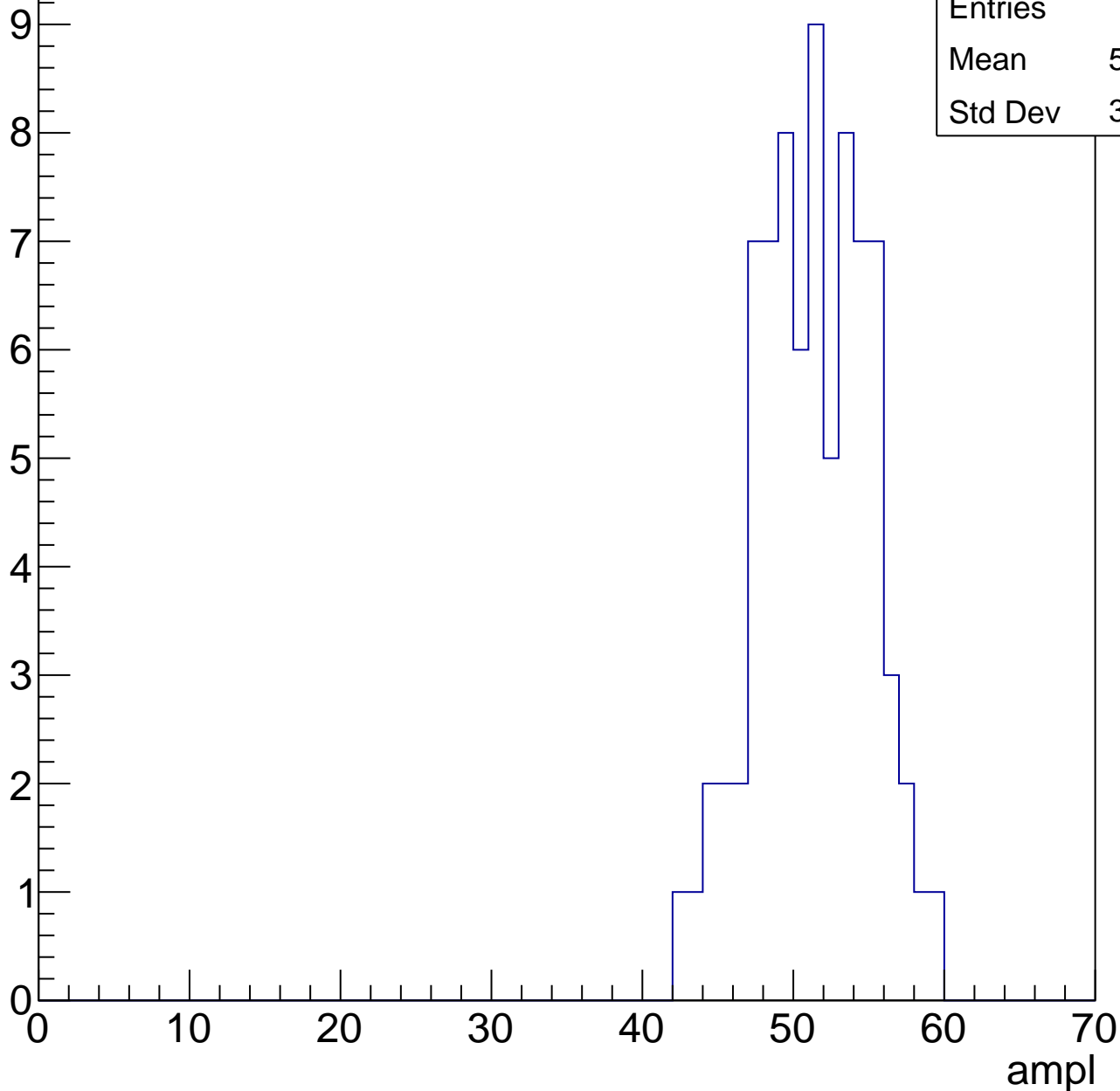


# B1L102S, U24-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50.85
Std Dev	3.643

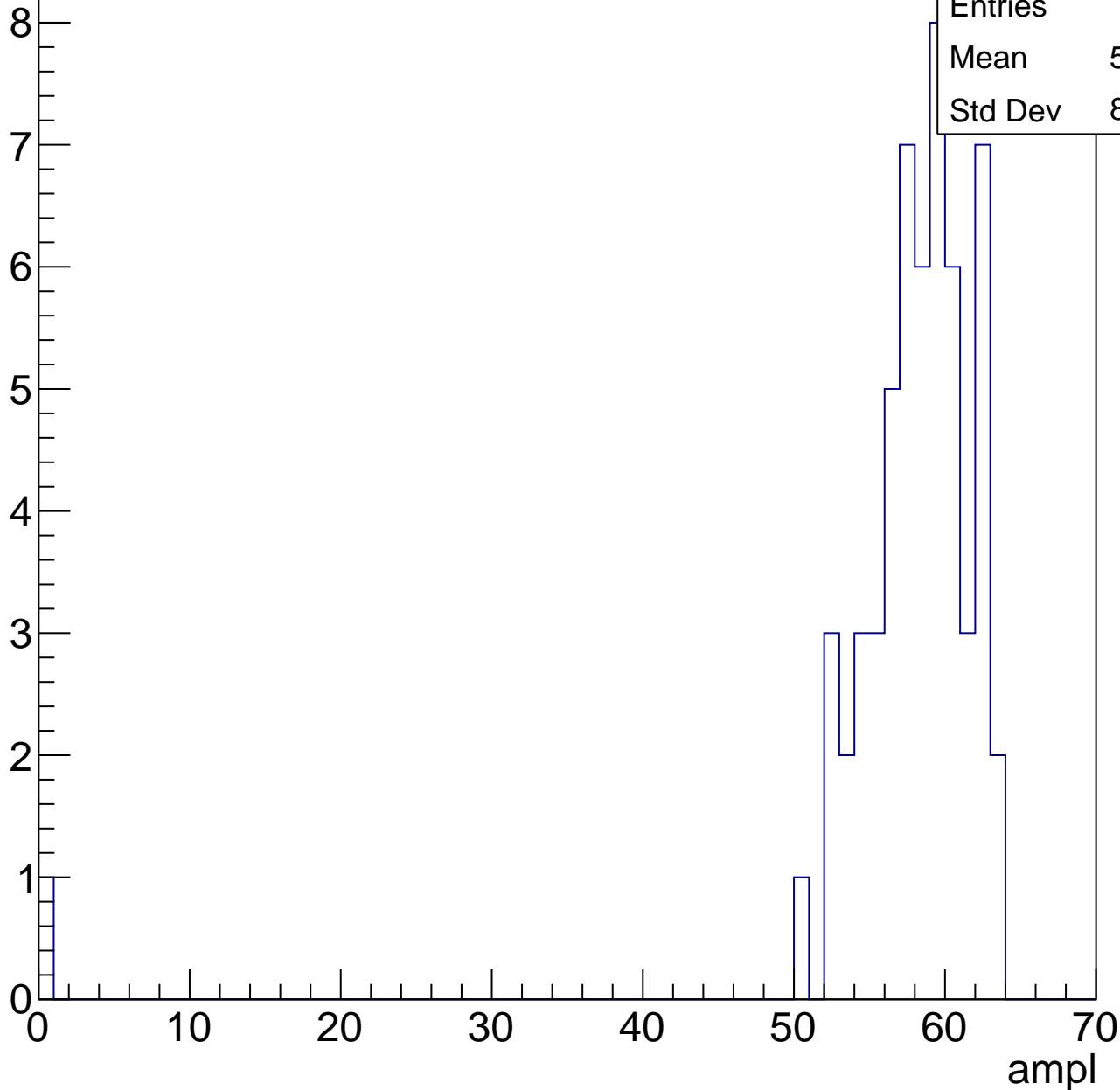


# B1L102S, U24-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.86
Std Dev	8.202

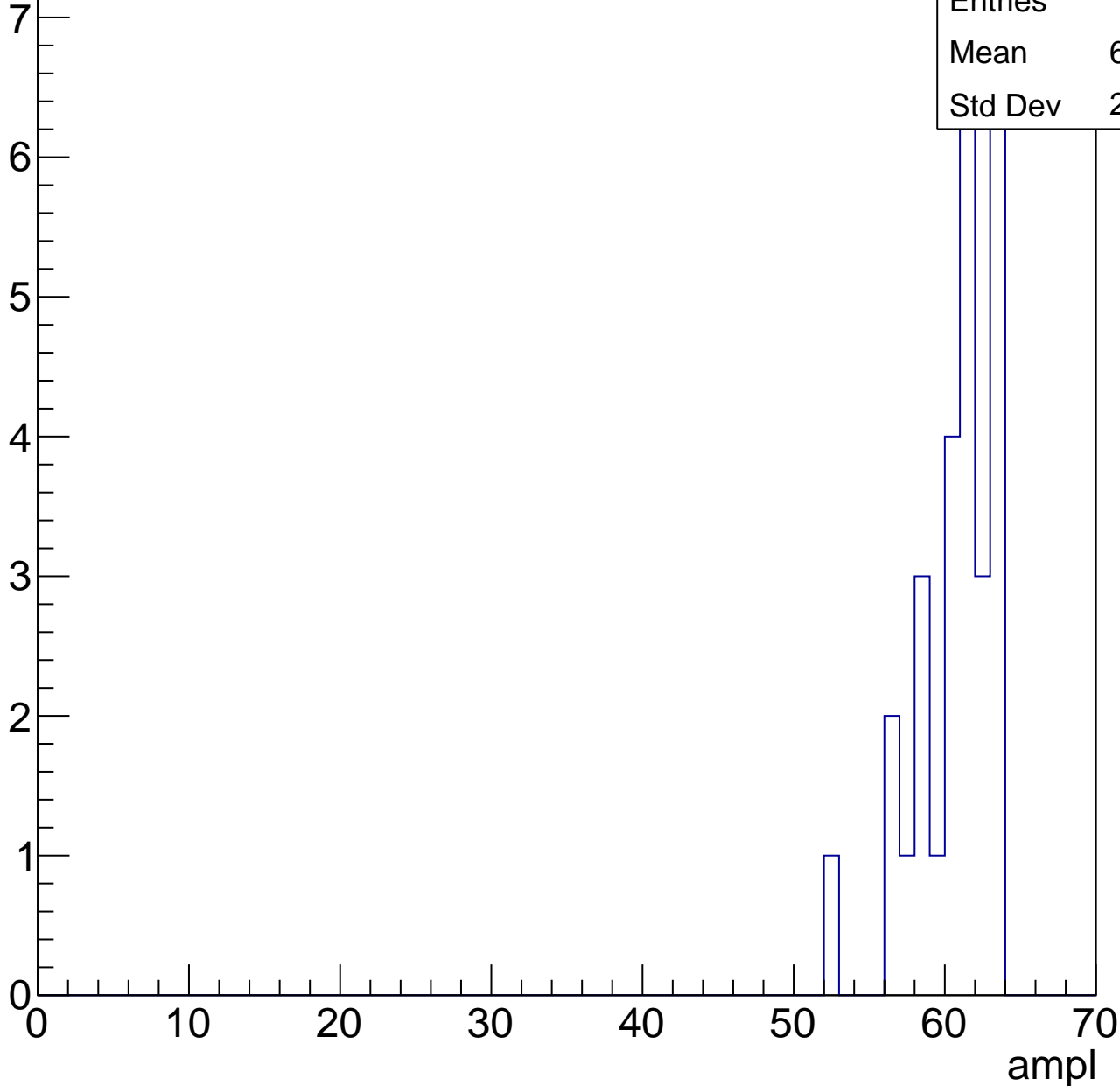


# B1L102S, U24-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	29
Mean	60.28
Std Dev	2.612



# B1L102S, U24-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

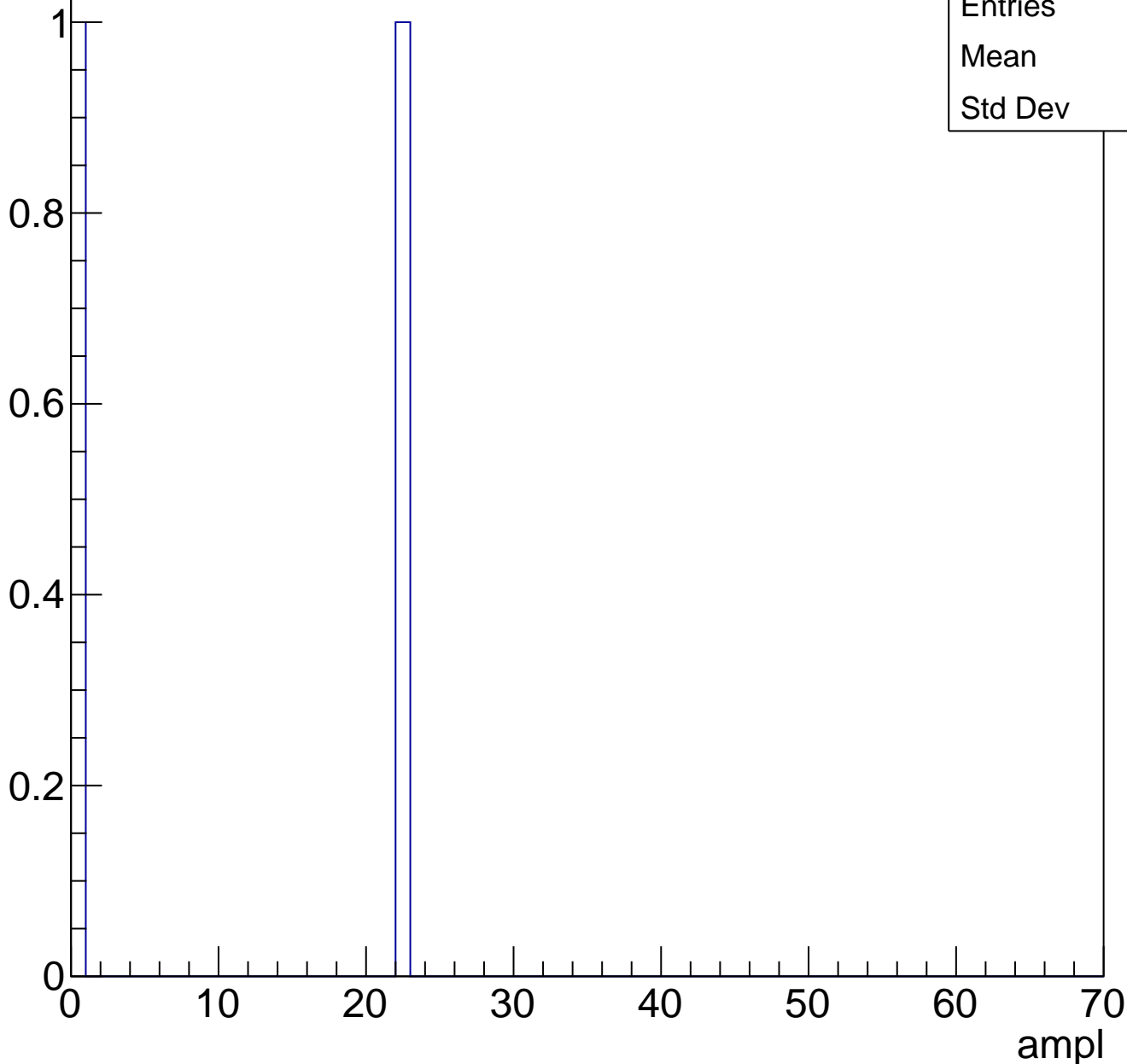




# B1L102S, U24-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U24-ch20, adc0

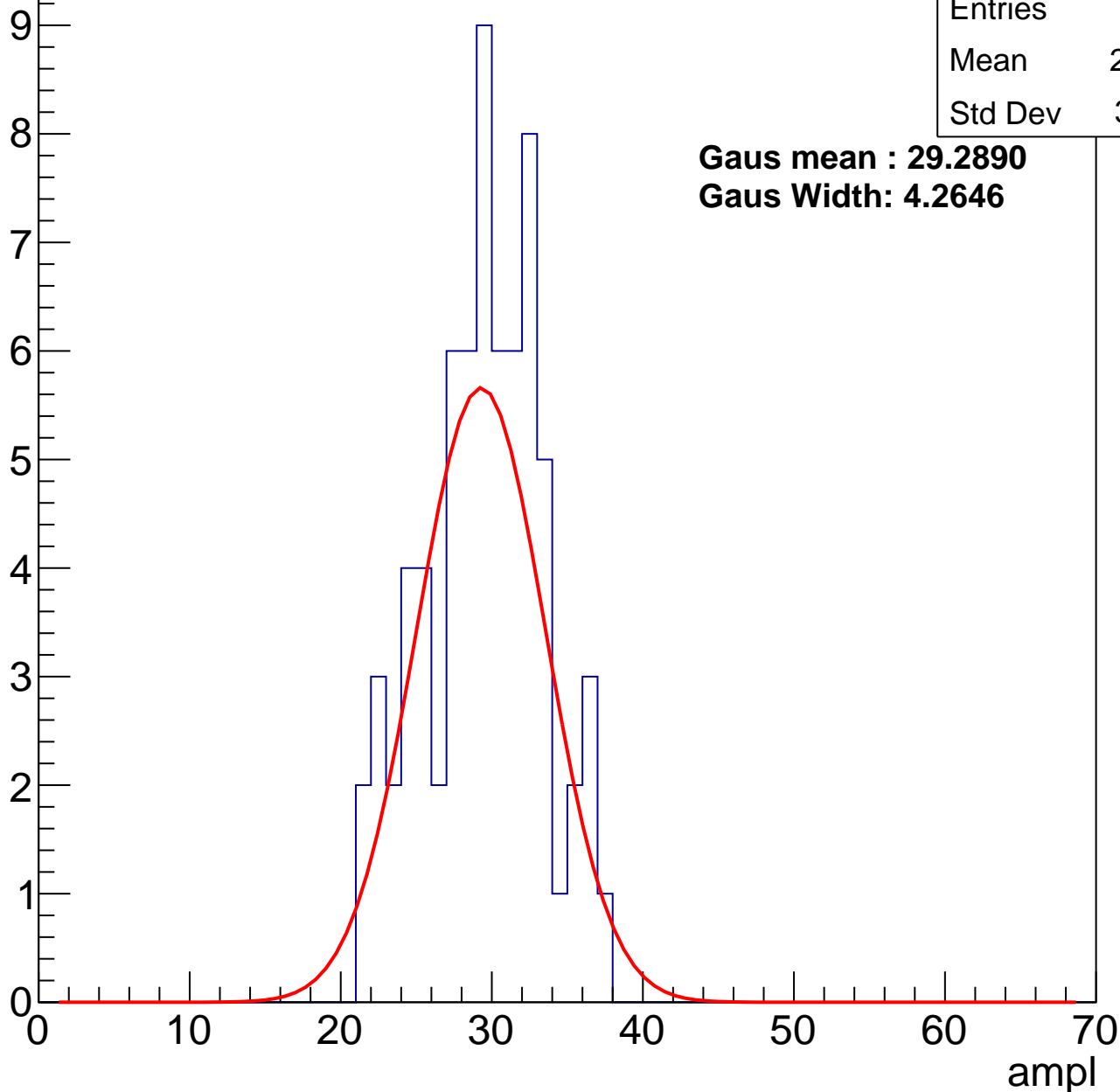
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.99
Std Dev	3.871

**Gaus mean : 29.2890**

**Gaus Width: 4.2646**



# B1L102S, U24-ch20, adc1

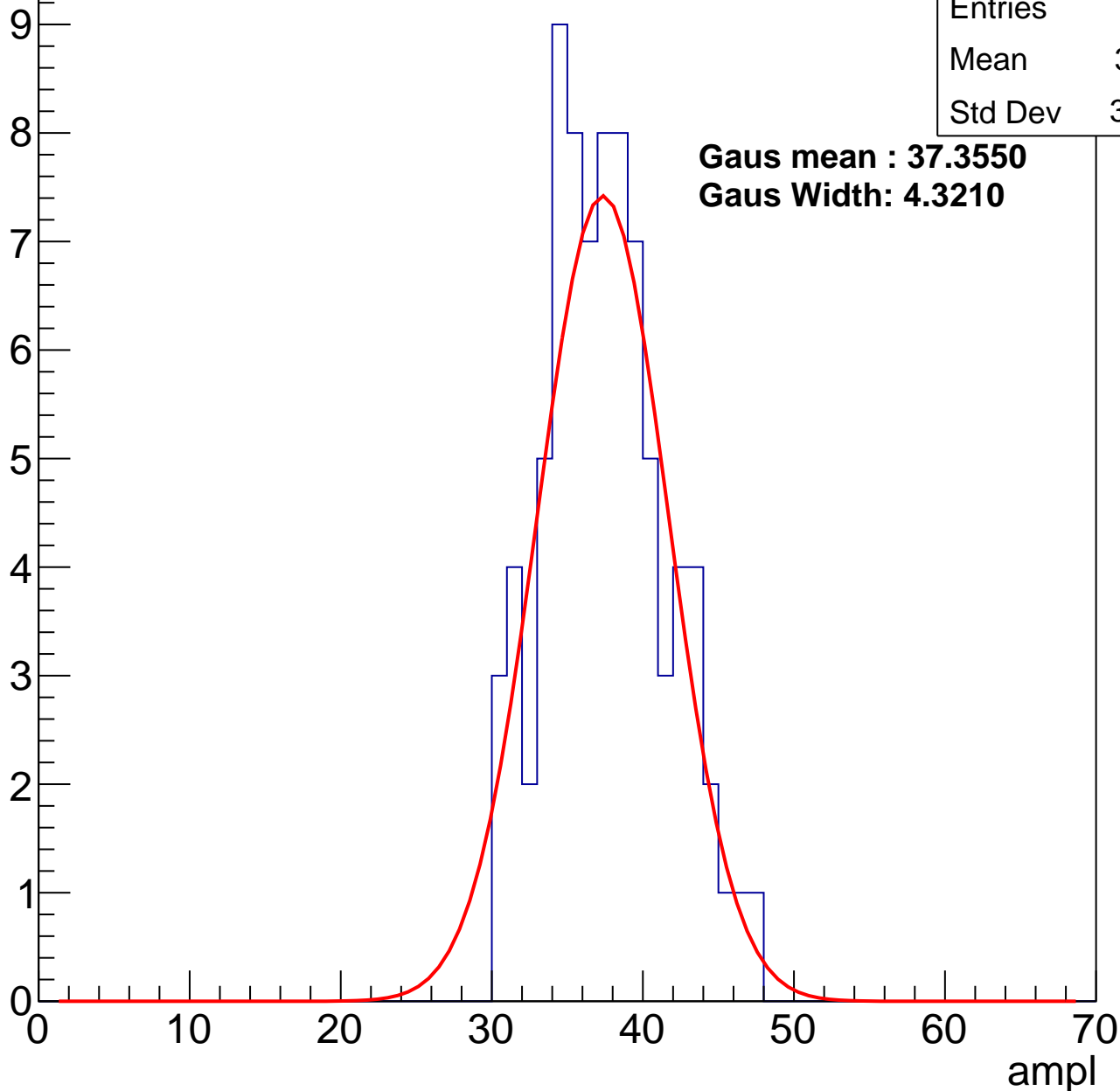
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	37.11
Std Dev	3.914

**Gaus mean : 37.3550**

**Gaus Width: 4.3210**



# B1L102S, U24-ch20, adc2

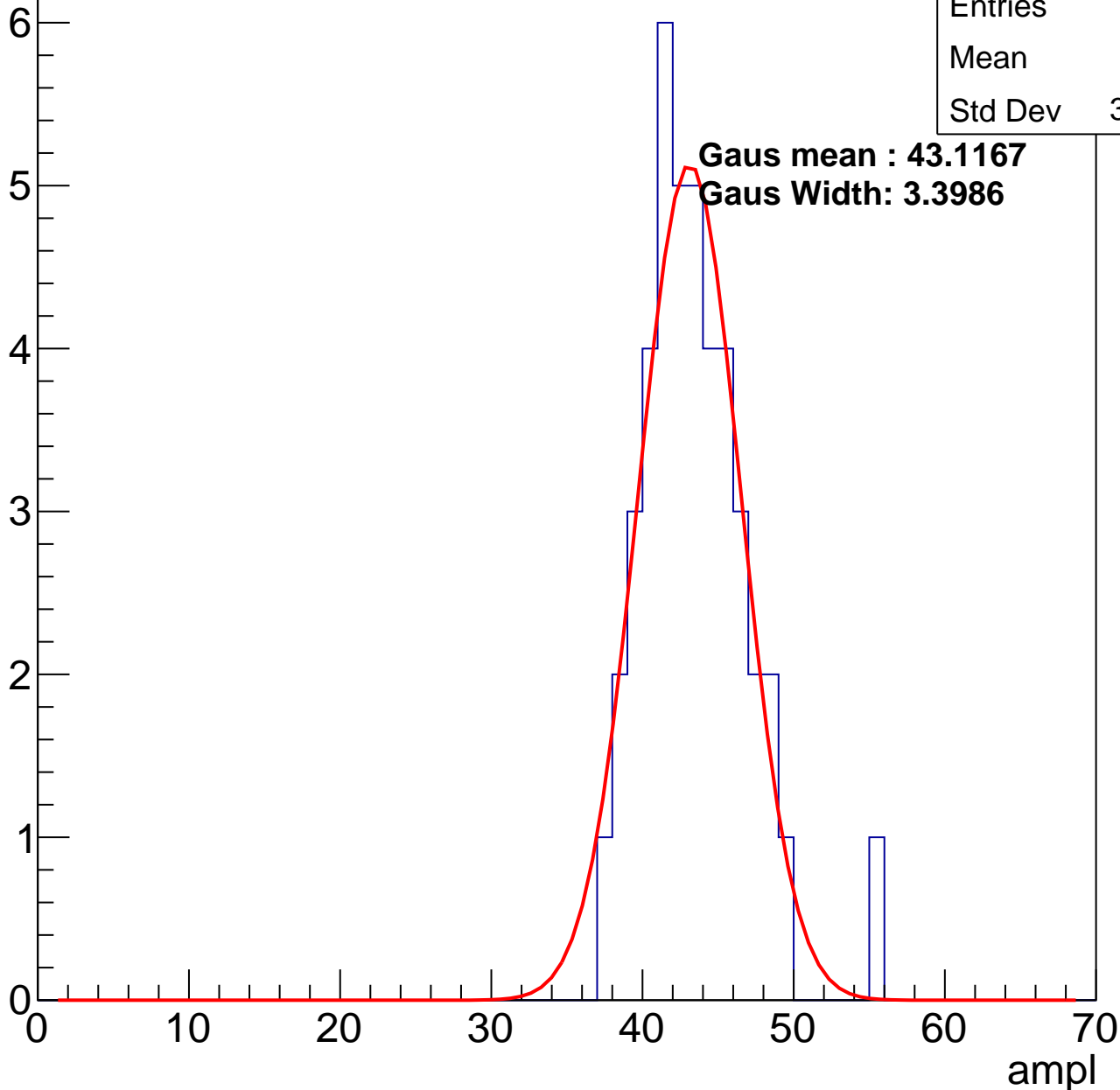
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	43
Std Dev	3.437

**Gaus mean : 43.1167**

**Gaus Width: 3.3986**

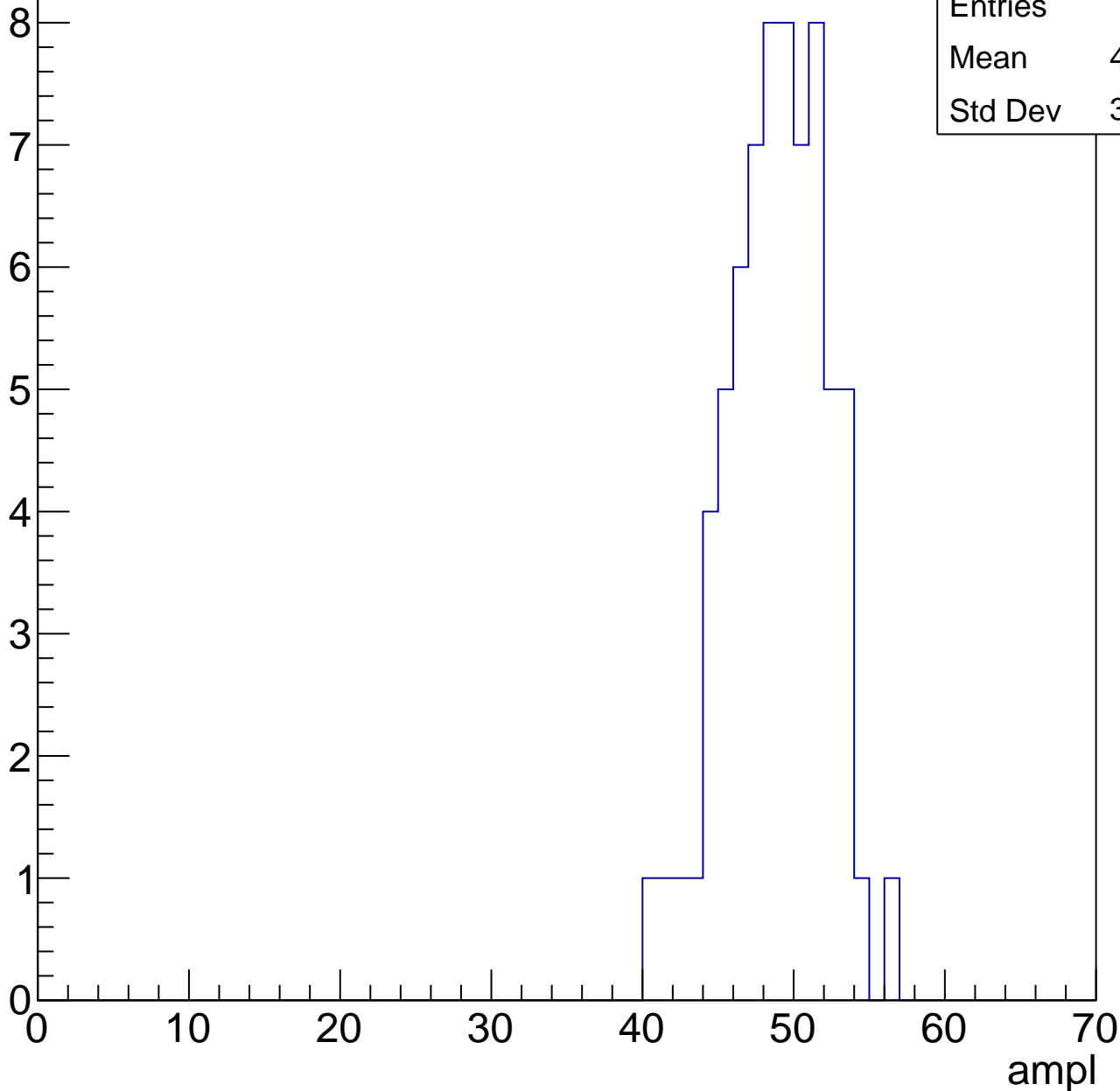


# B1L102S, U24-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.42
Std Dev	3.219

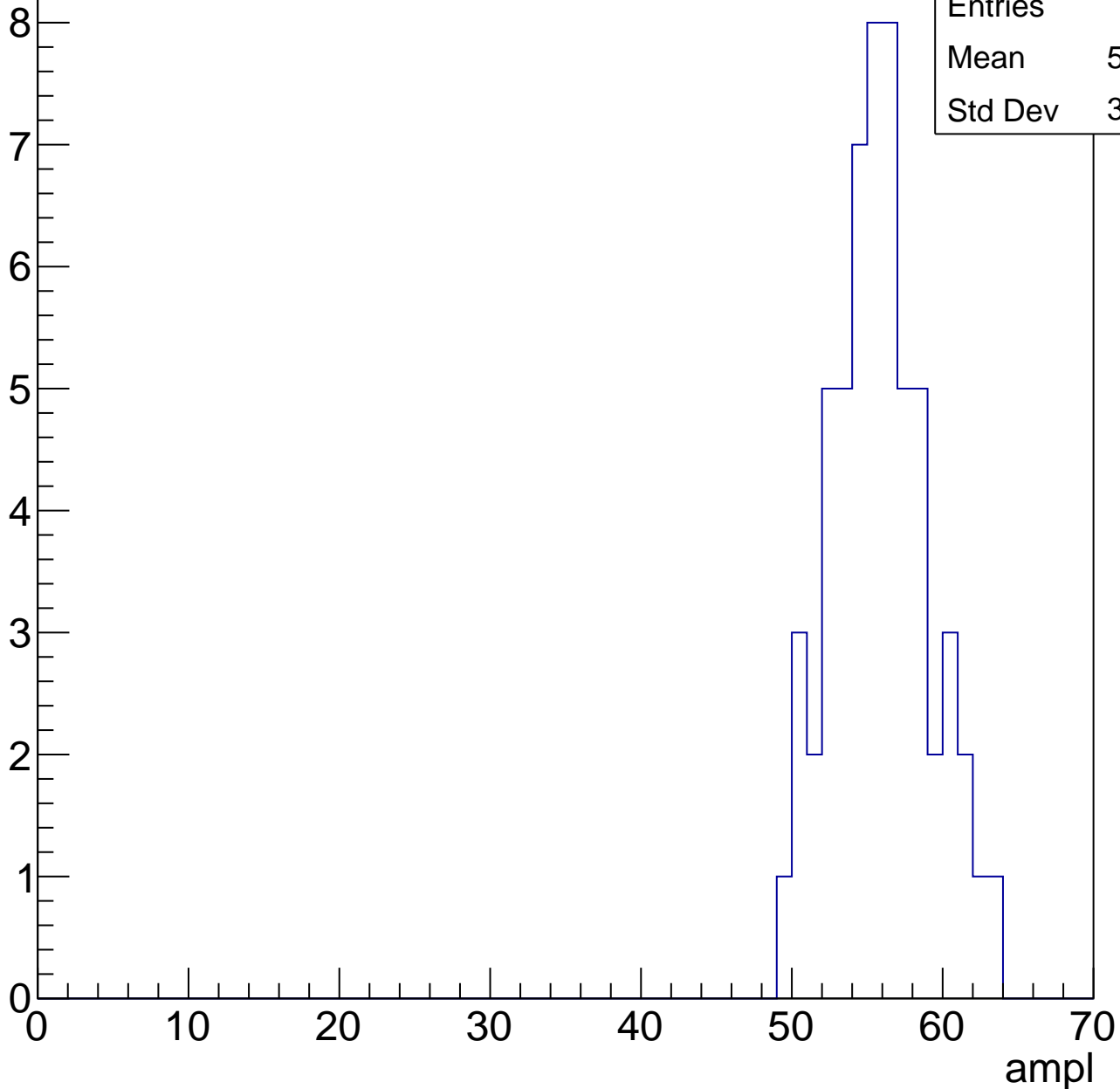


# B1L102S, U24-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

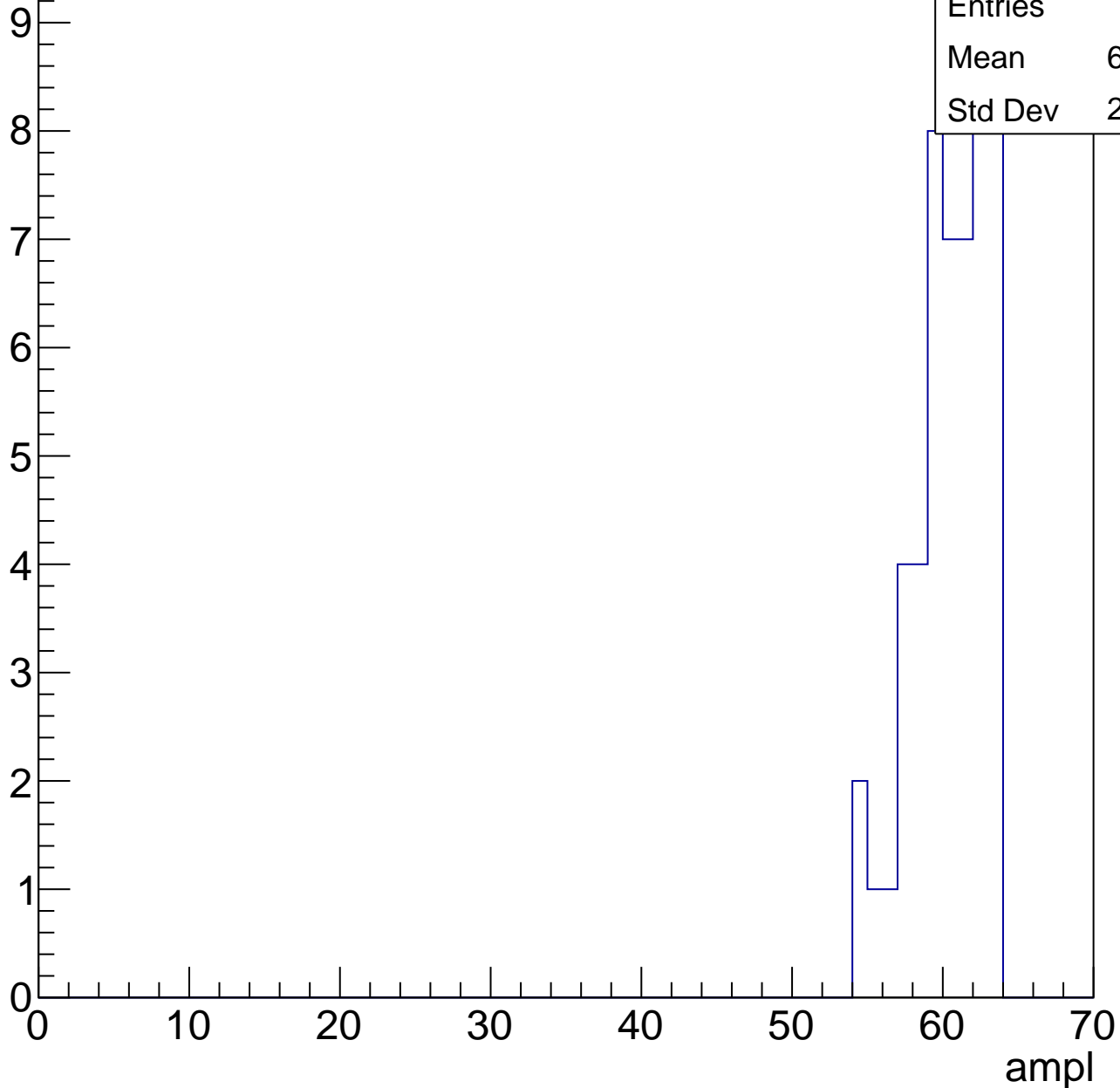
Entries	58
Mean	55.38
Std Dev	3.139



# B1L102S, U24-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

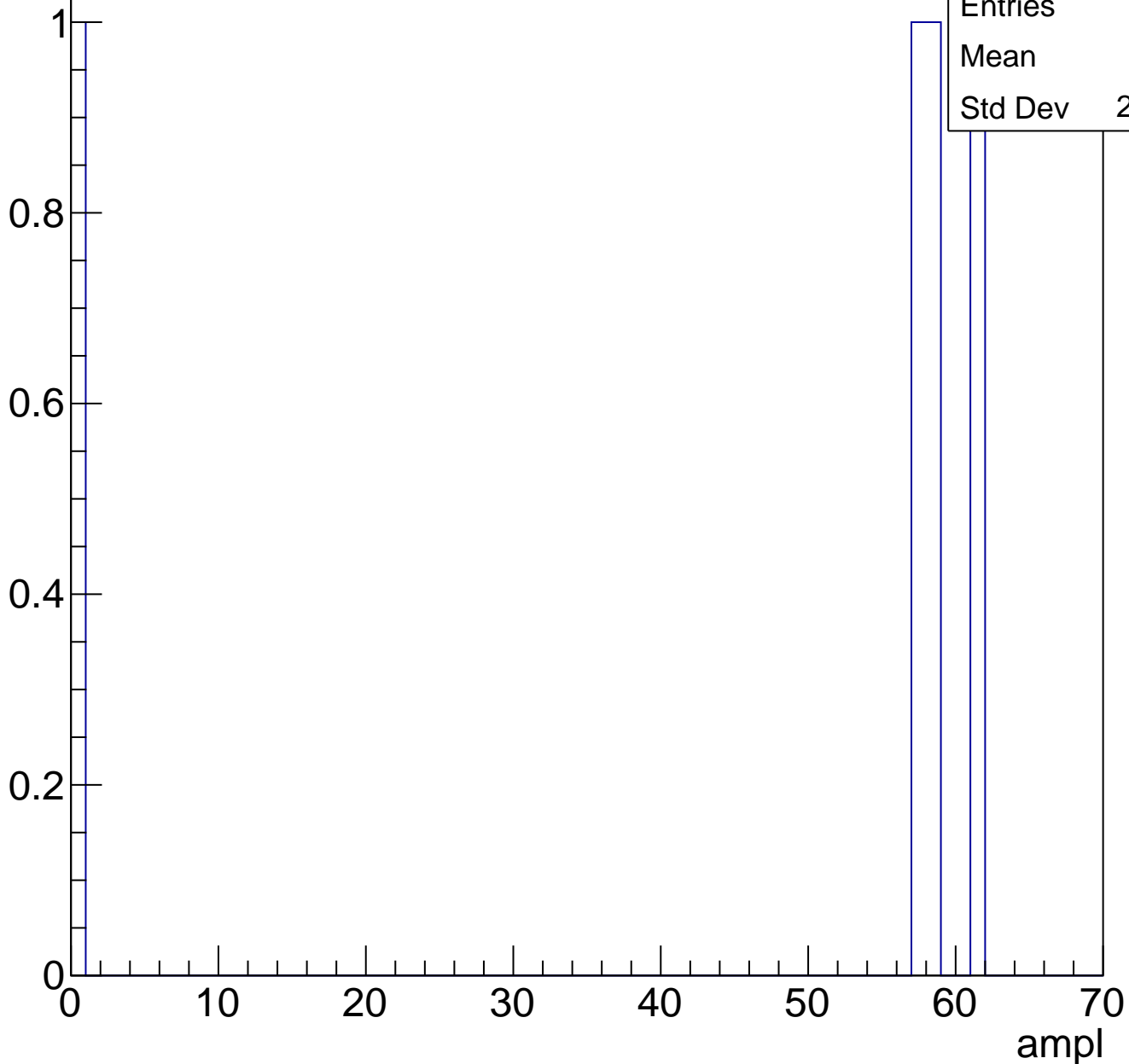
Entry



# B1L102S, U24-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch21, adc0

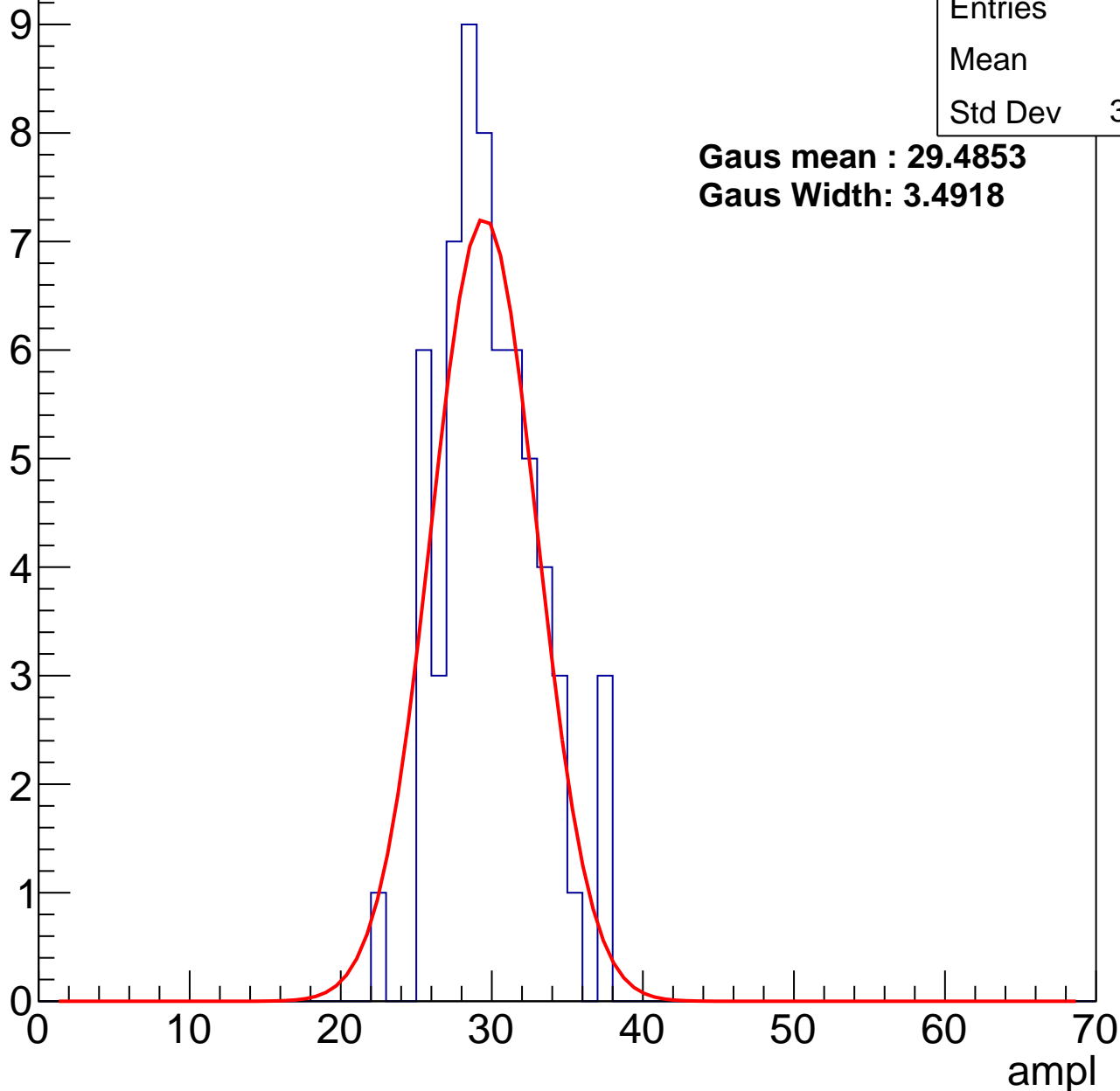
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.5
Std Dev	3.202

**Gaus mean : 29.4853**

**Gaus Width: 3.4918**



# B1L102S, U24-ch21, adc1

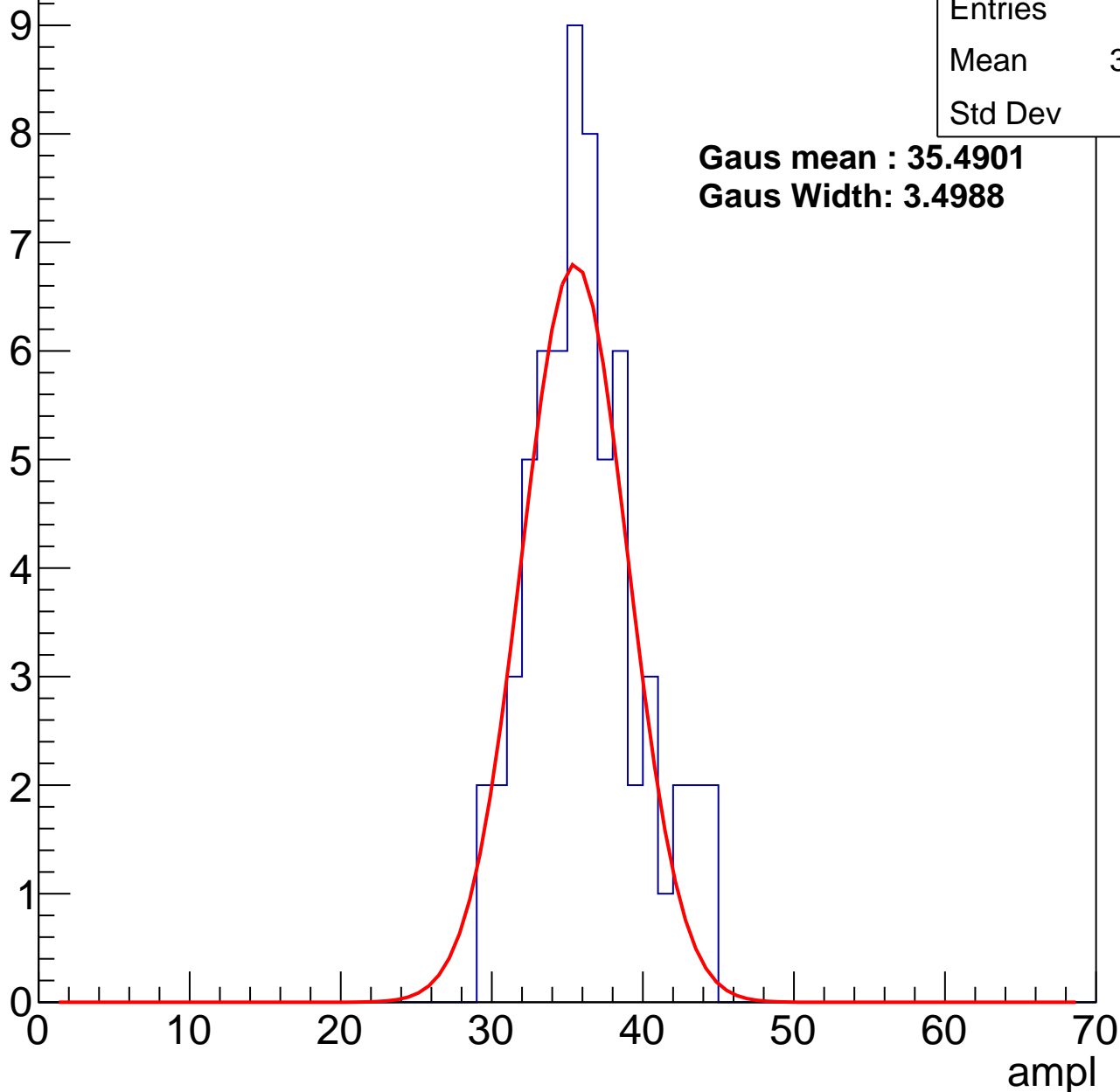
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.72
Std Dev	3.59

**Gaus mean : 35.4901**

**Gaus Width: 3.4988**



# B1L102S, U24-ch21, adc2

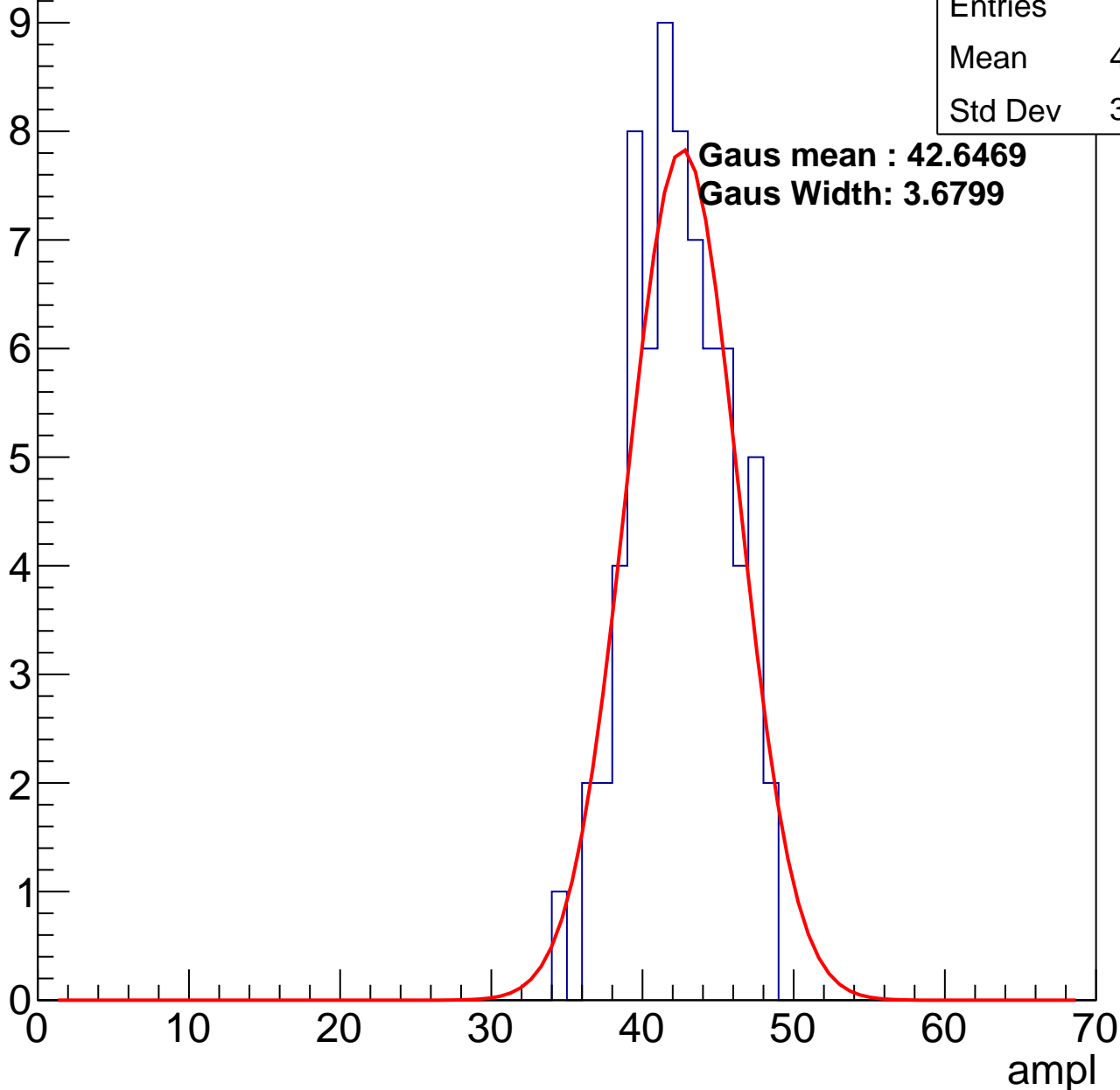
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.99
Std Dev	3.165

**Gaus mean : 42.6469**

**Gaus Width: 3.6799**

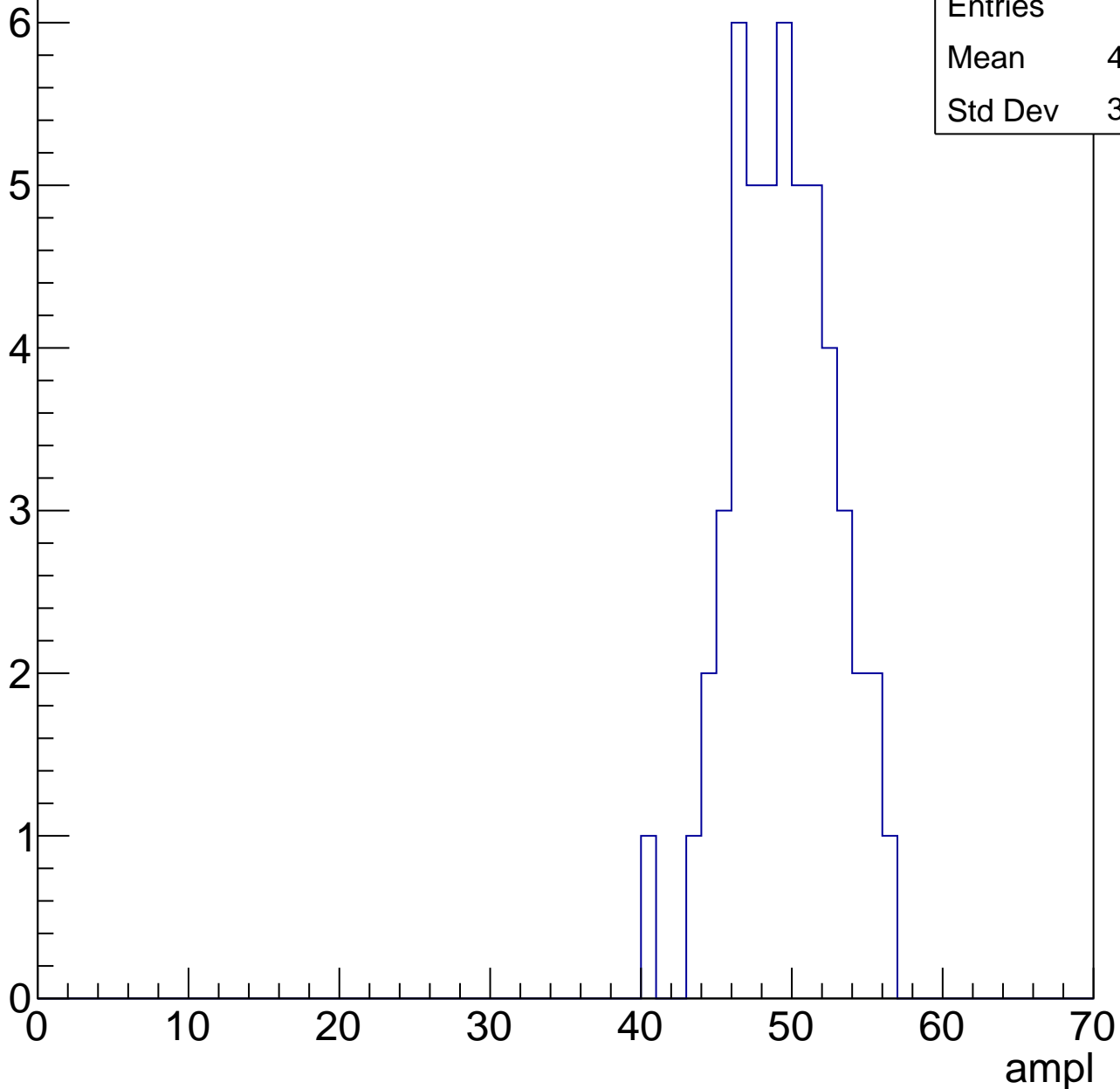


# B1L102S, U24-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	48.96
Std Dev	3.355

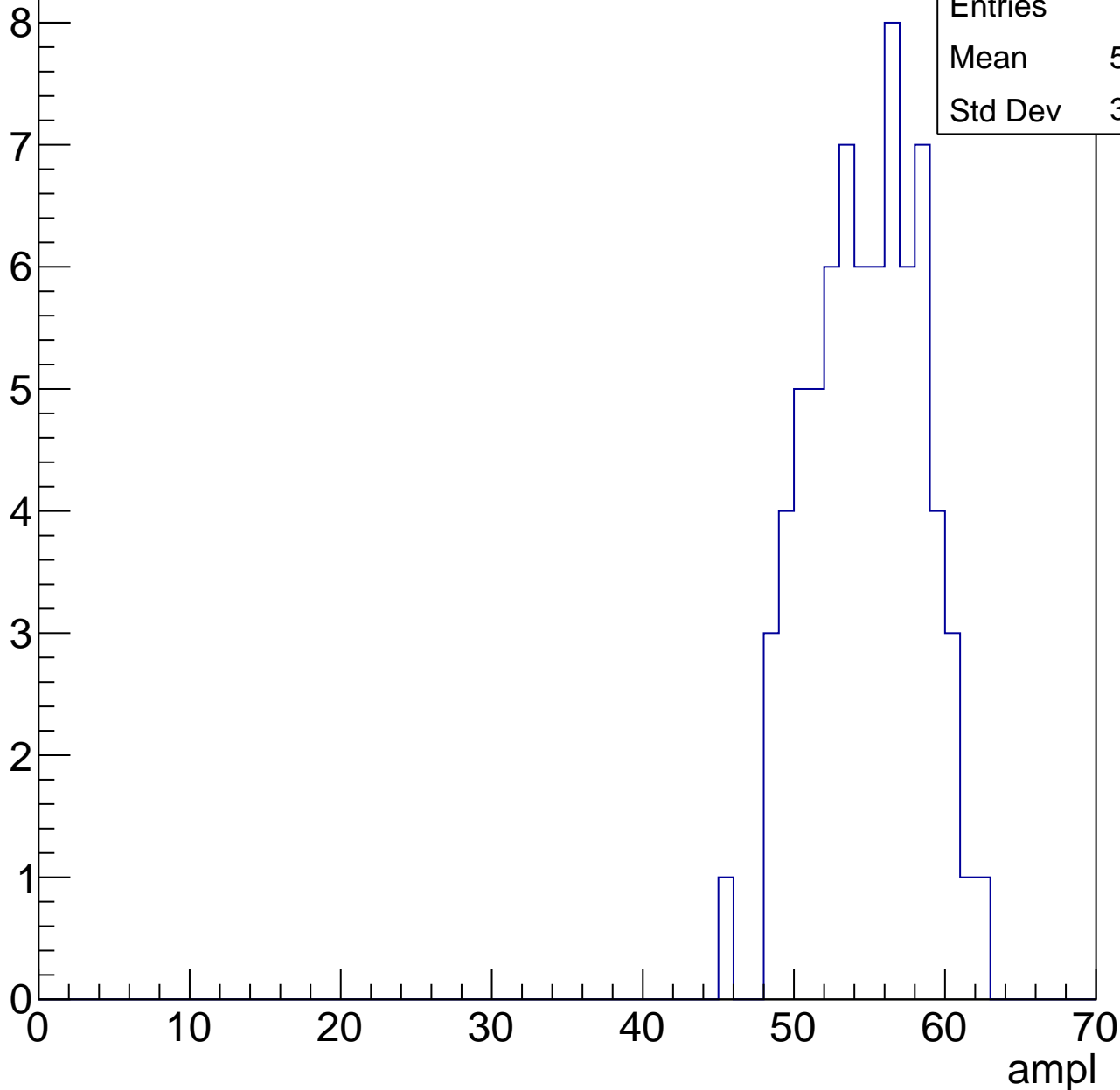


# B1L102S, U24-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	54.27
Std Dev	3.635

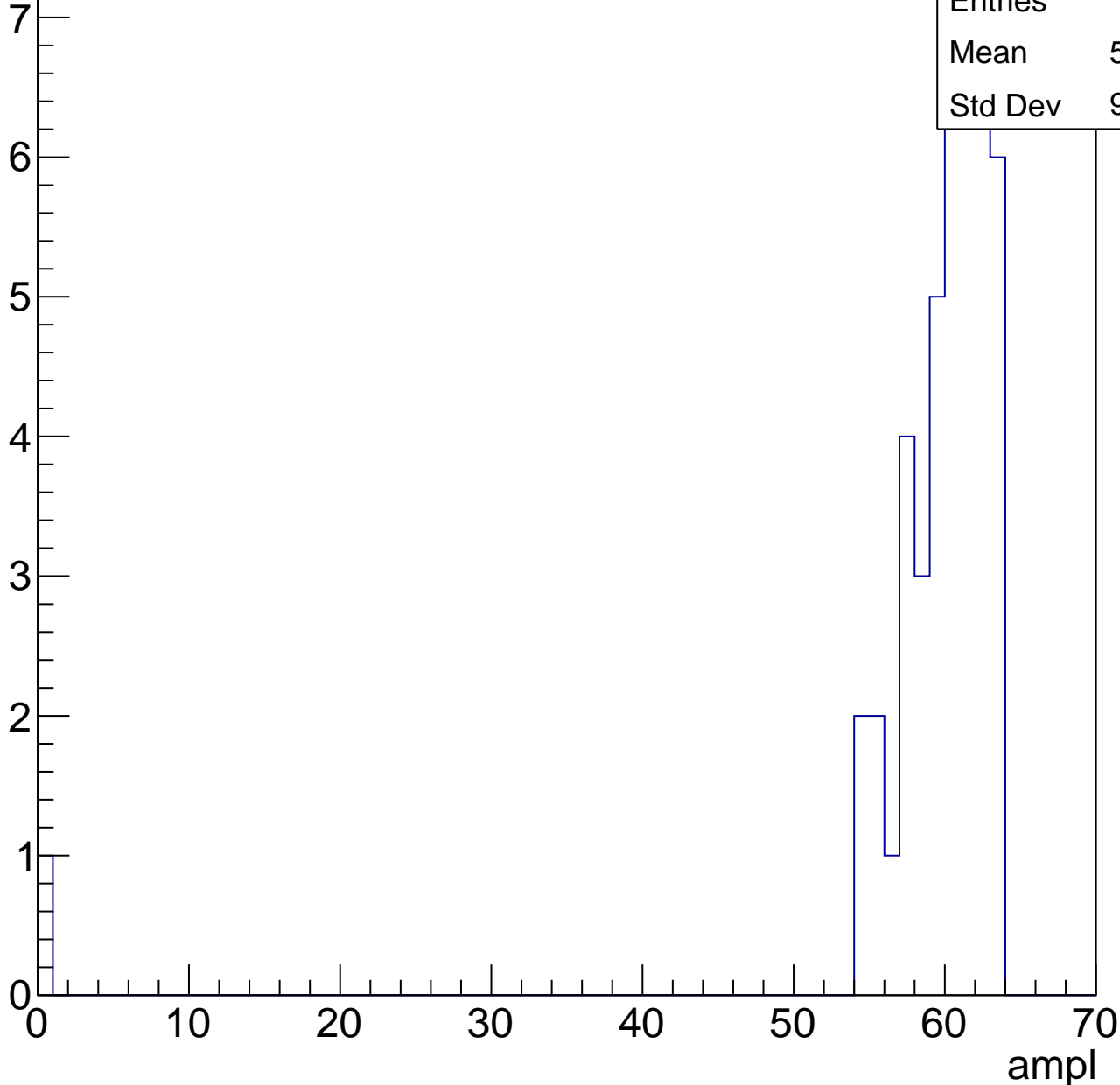


# B1L102S, U24-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

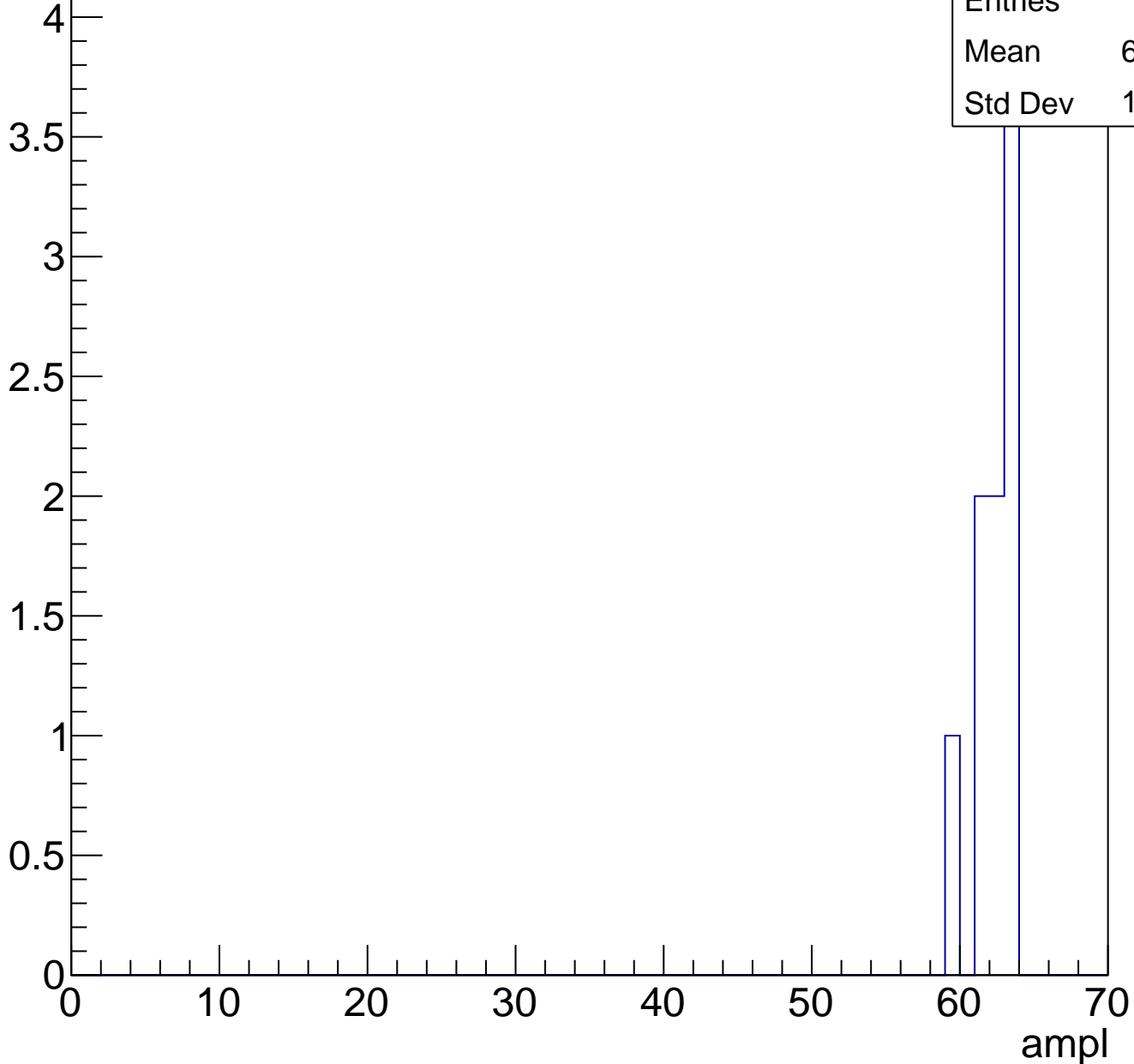
Entries	45
Mean	58.44
Std Dev	9.154



# B1L102S, U24-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch22, adc0

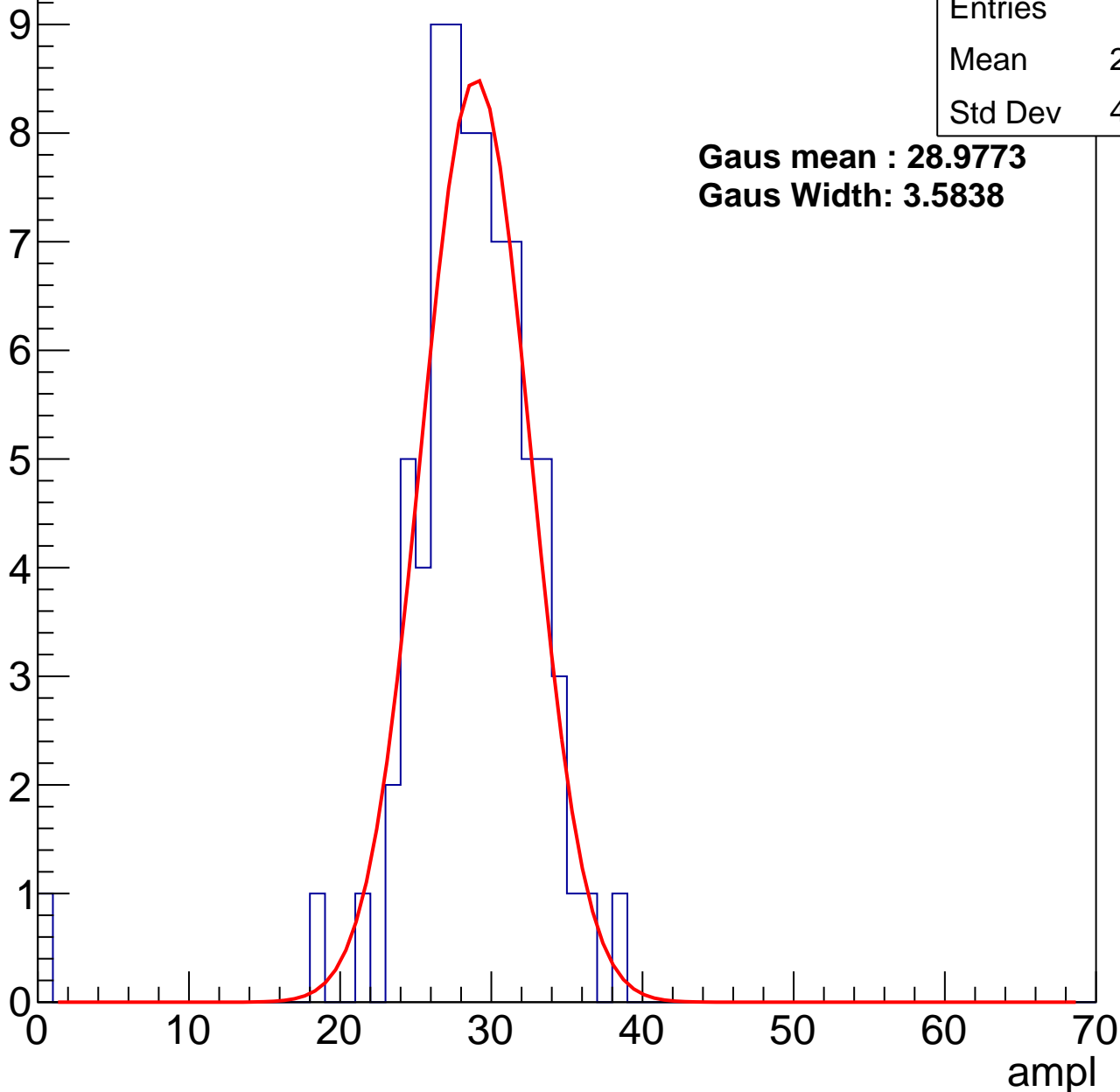
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.22
Std Dev	4.757

**Gaus mean : 28.9773**

**Gaus Width: 3.5838**



# B1L102S, U24-ch22, adc1

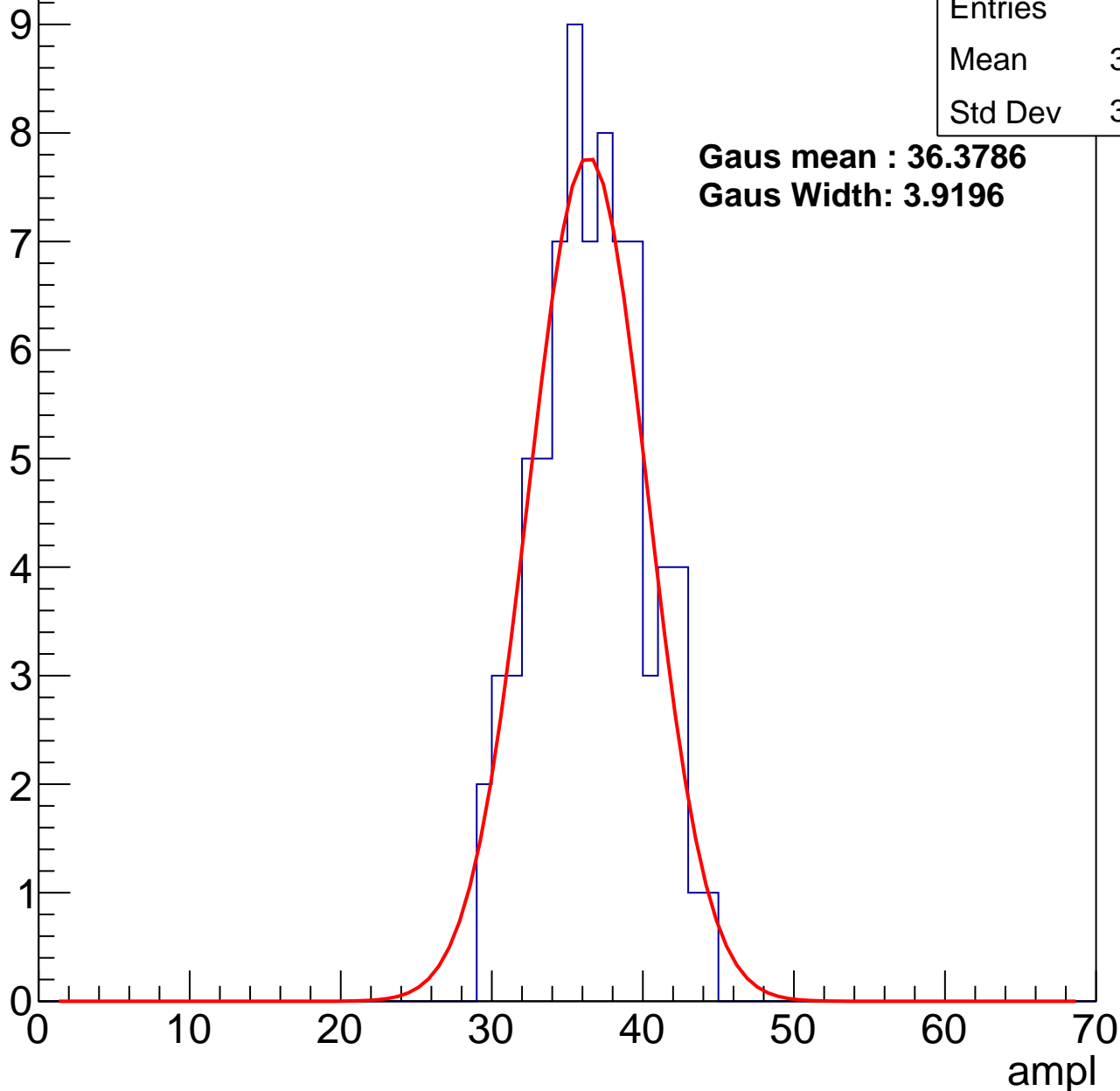
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.12
Std Dev	3.528

**Gaus mean : 36.3786**

**Gaus Width: 3.9196**



# B1L102S, U24-ch22, adc2

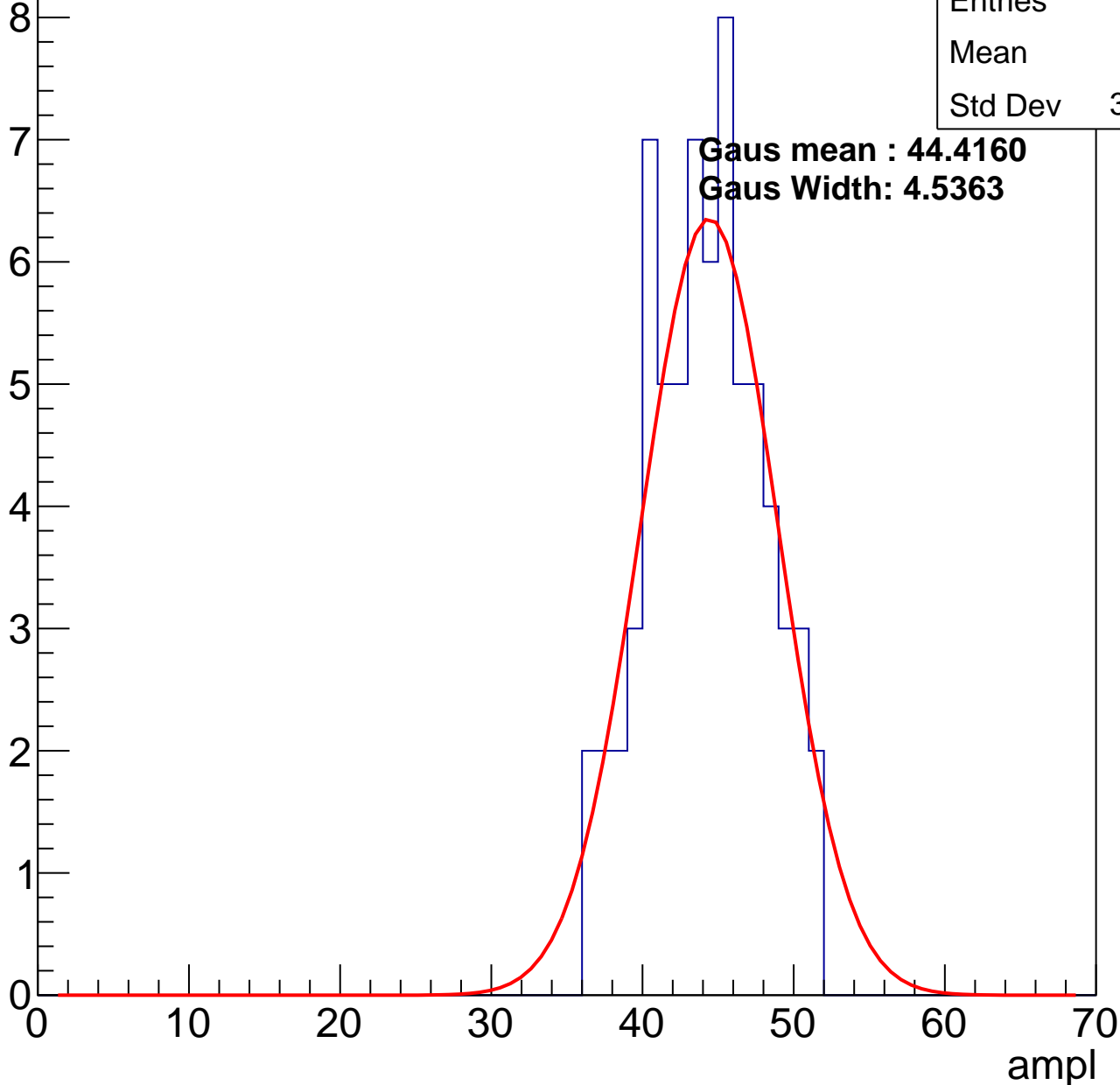
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.7
Std Dev	3.747

**Gaus mean : 44.4160**

**Gaus Width: 4.5363**

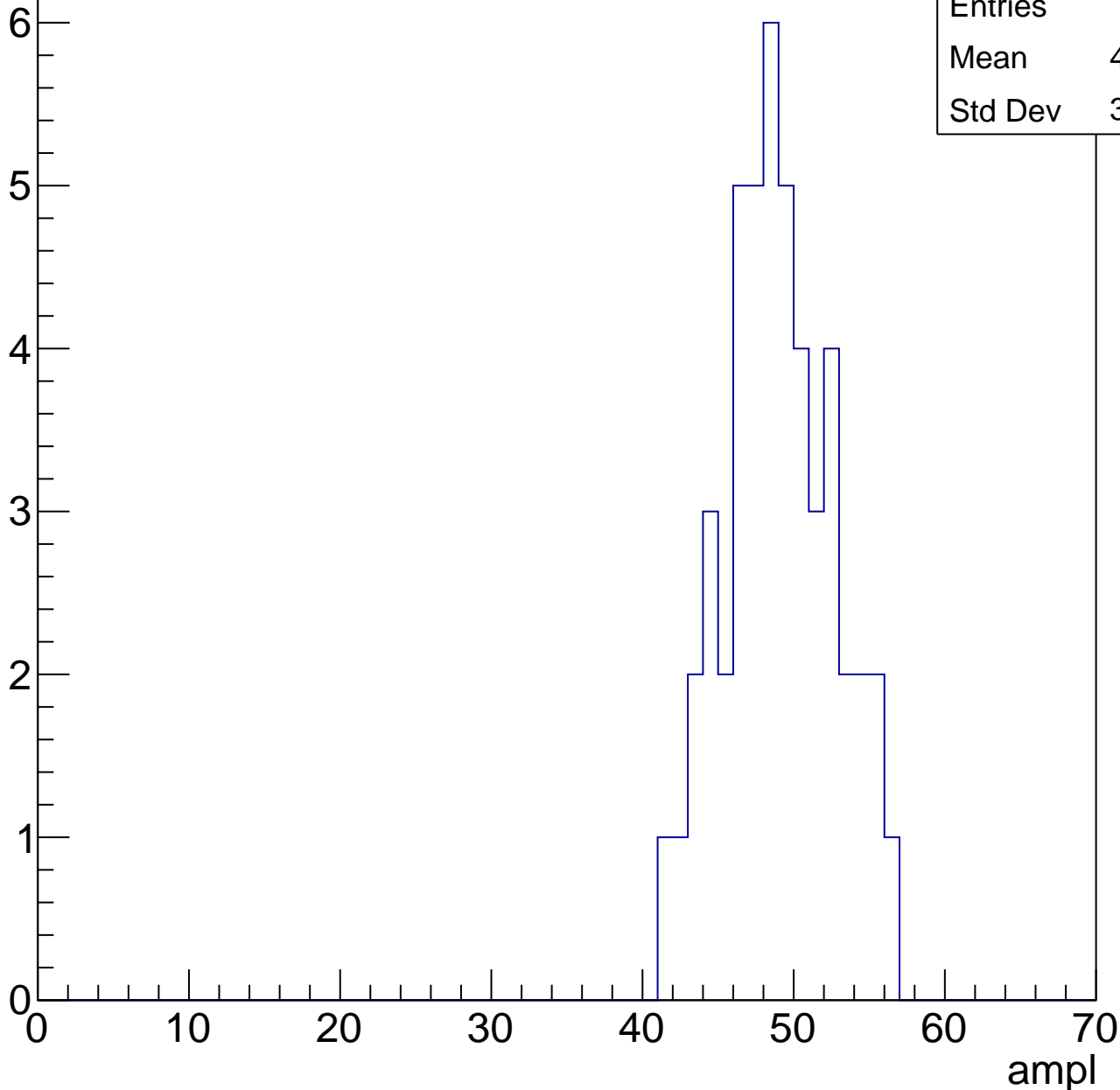


# B1L102S, U24-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	48.54
Std Dev	3.559

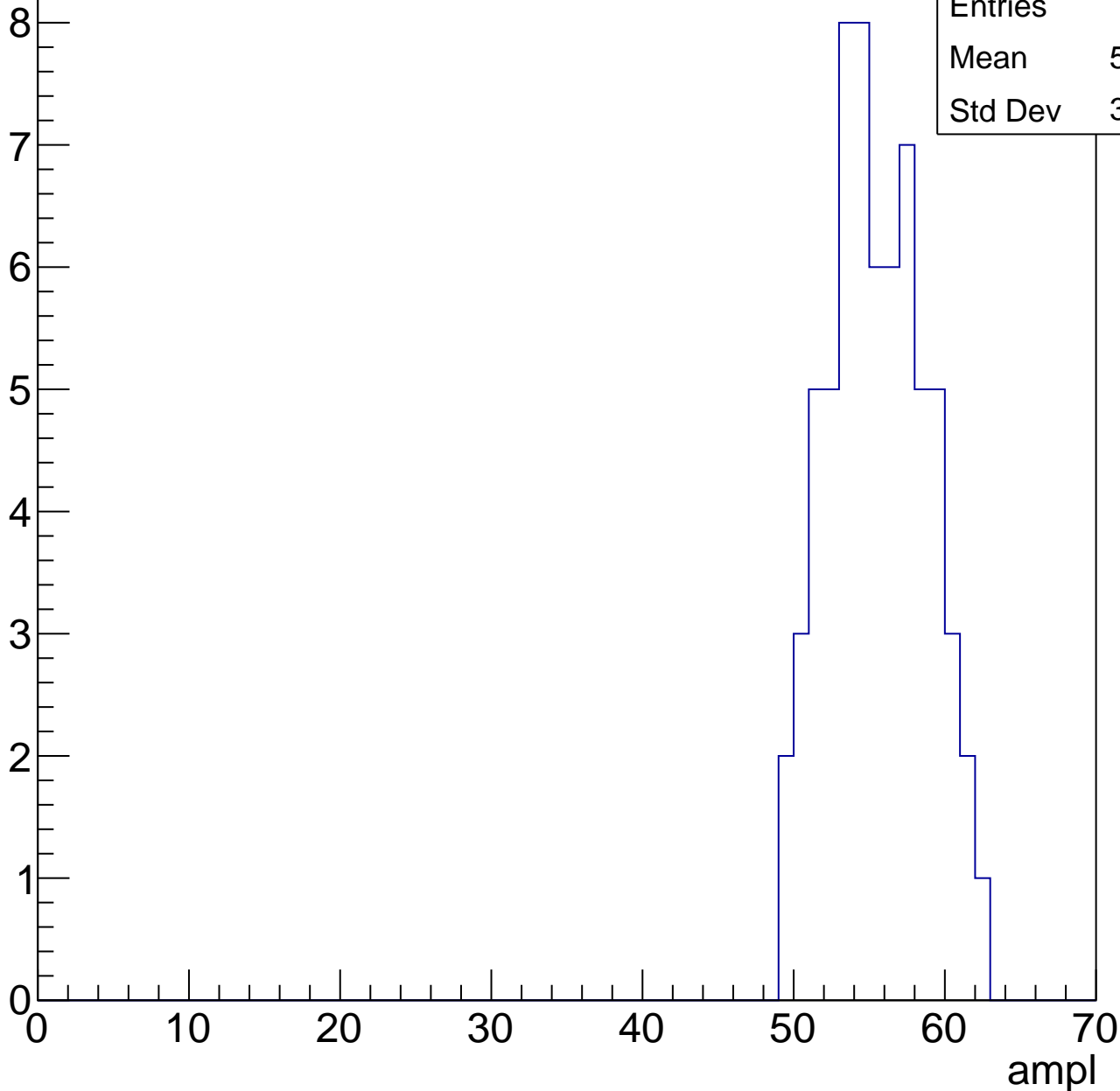


# B1L102S, U24-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.05
Std Dev	3.179

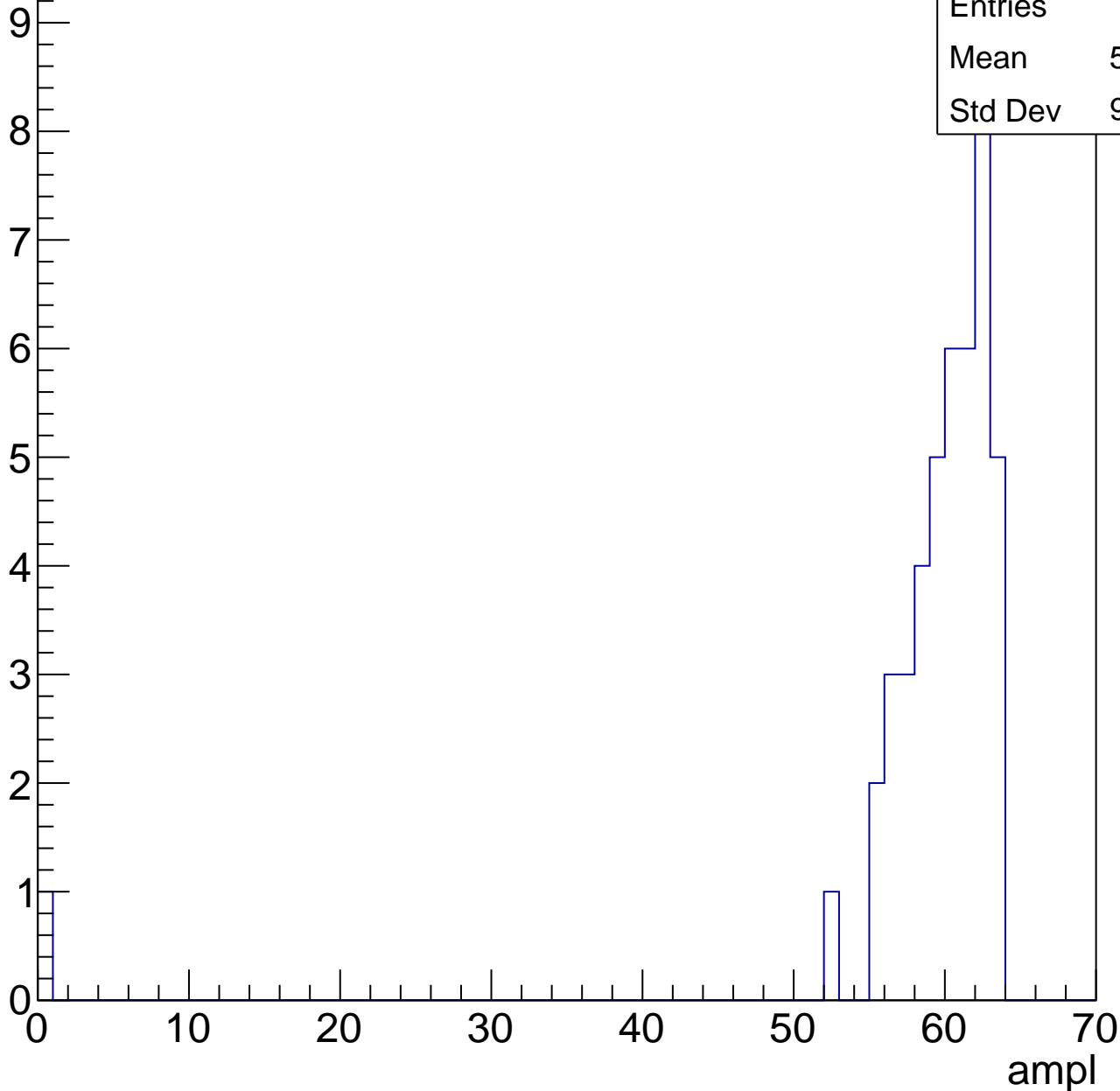


# B1L102S, U24-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.38
Std Dev	9.163

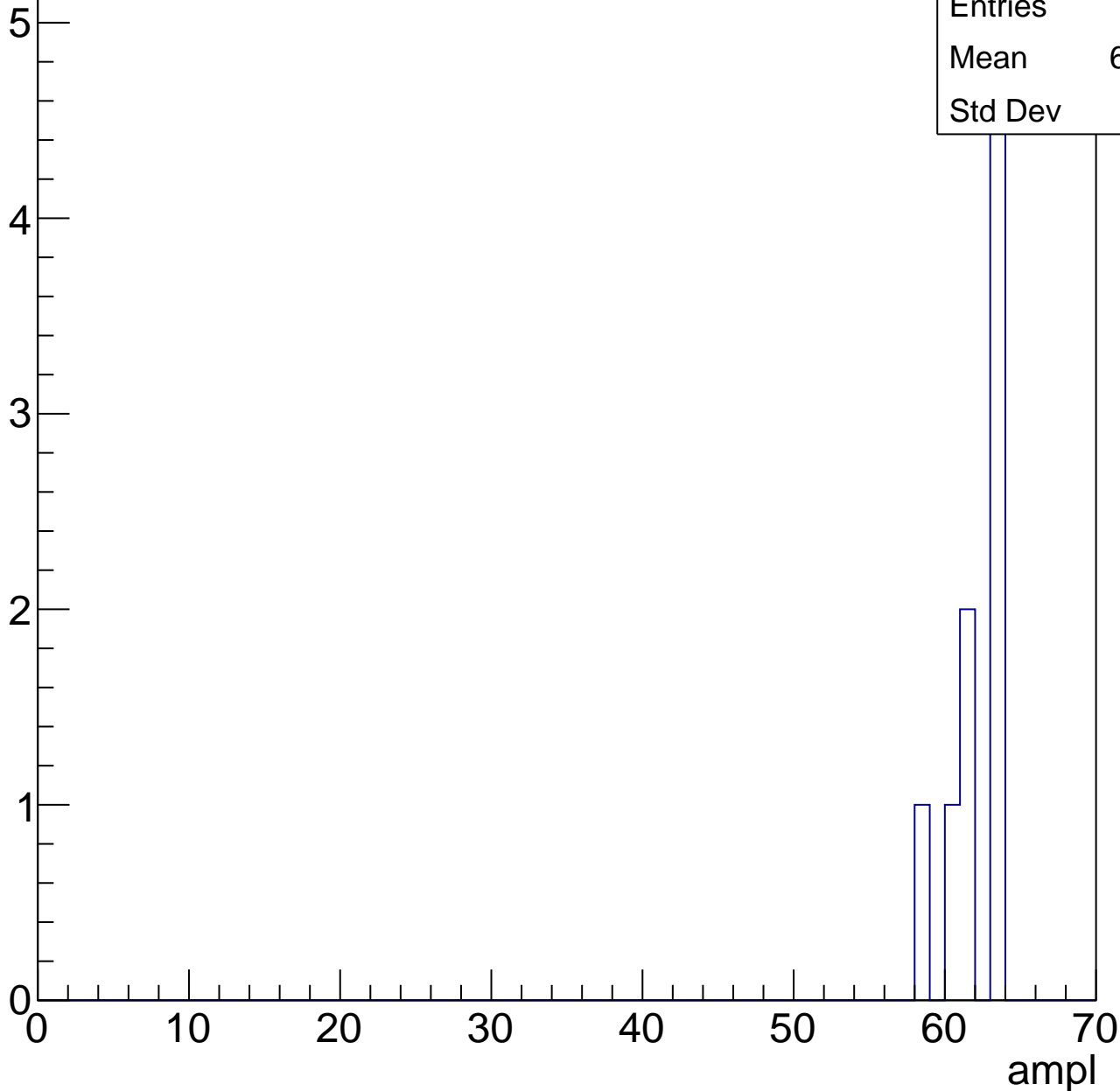


# B1L102S, U24-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.67
Std Dev	1.7





# B1L102S, U24-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch23, adc0

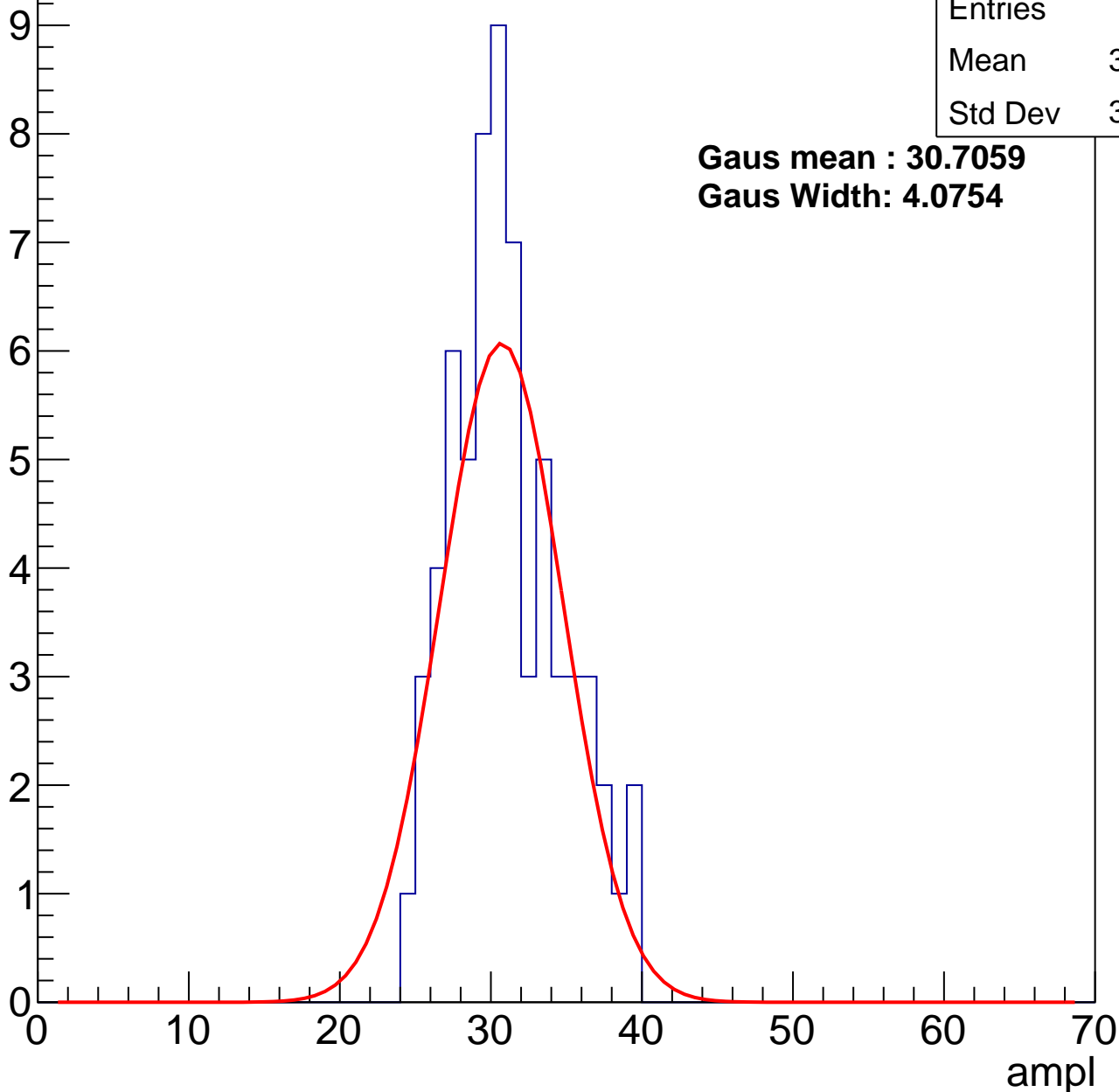
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.62
Std Dev	3.619

**Gaus mean : 30.7059**

**Gaus Width: 4.0754**



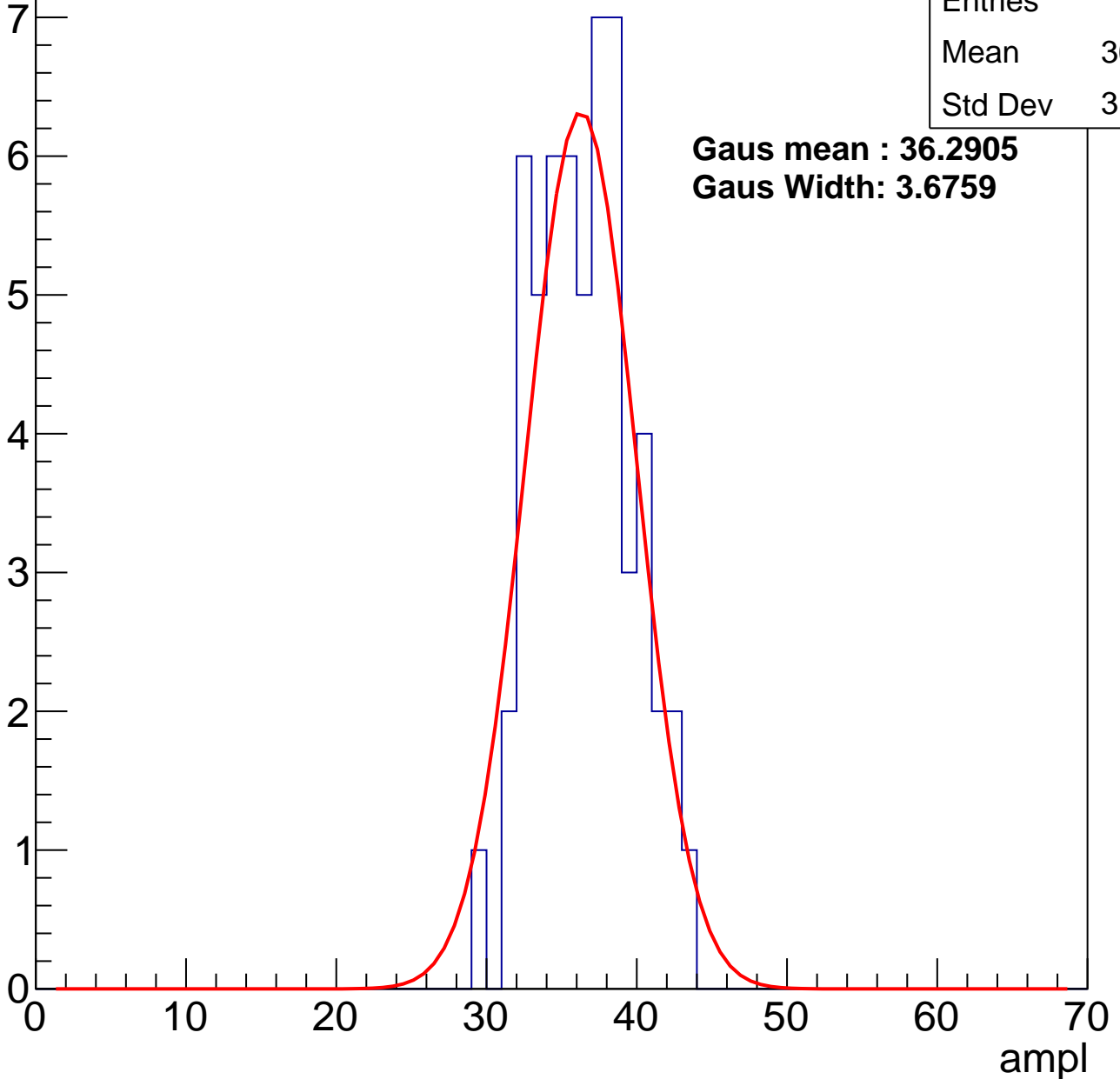
# B1L102S, U24-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	36.02
Std Dev	3.154

**Gaus mean : 36.2905**  
**Gaus Width: 3.6759**



# B1L102S, U24-ch23, adc2

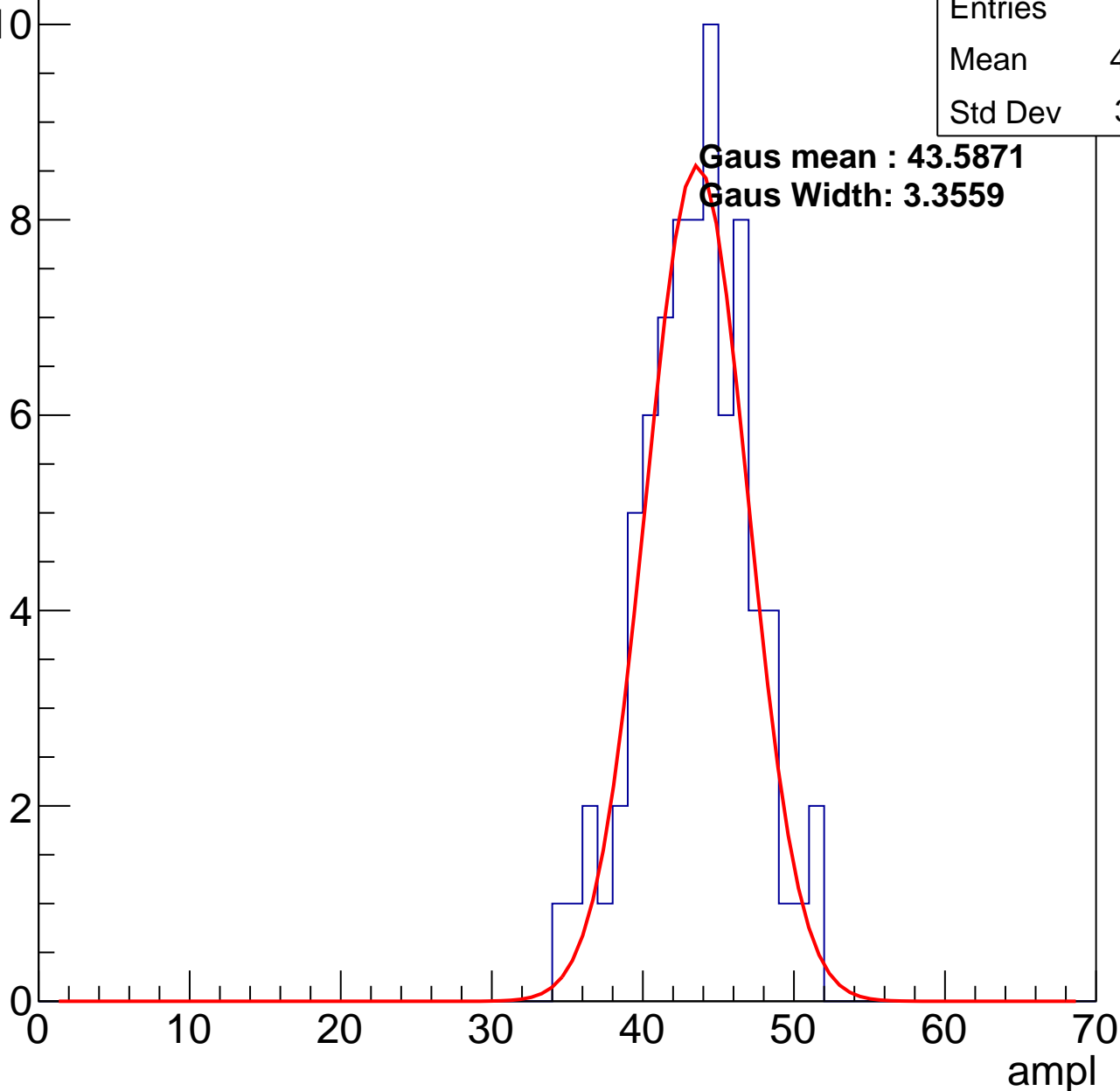
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	43.05
Std Dev	3.571

**Gaus mean : 43.5871**

**Gaus Width: 3.3559**

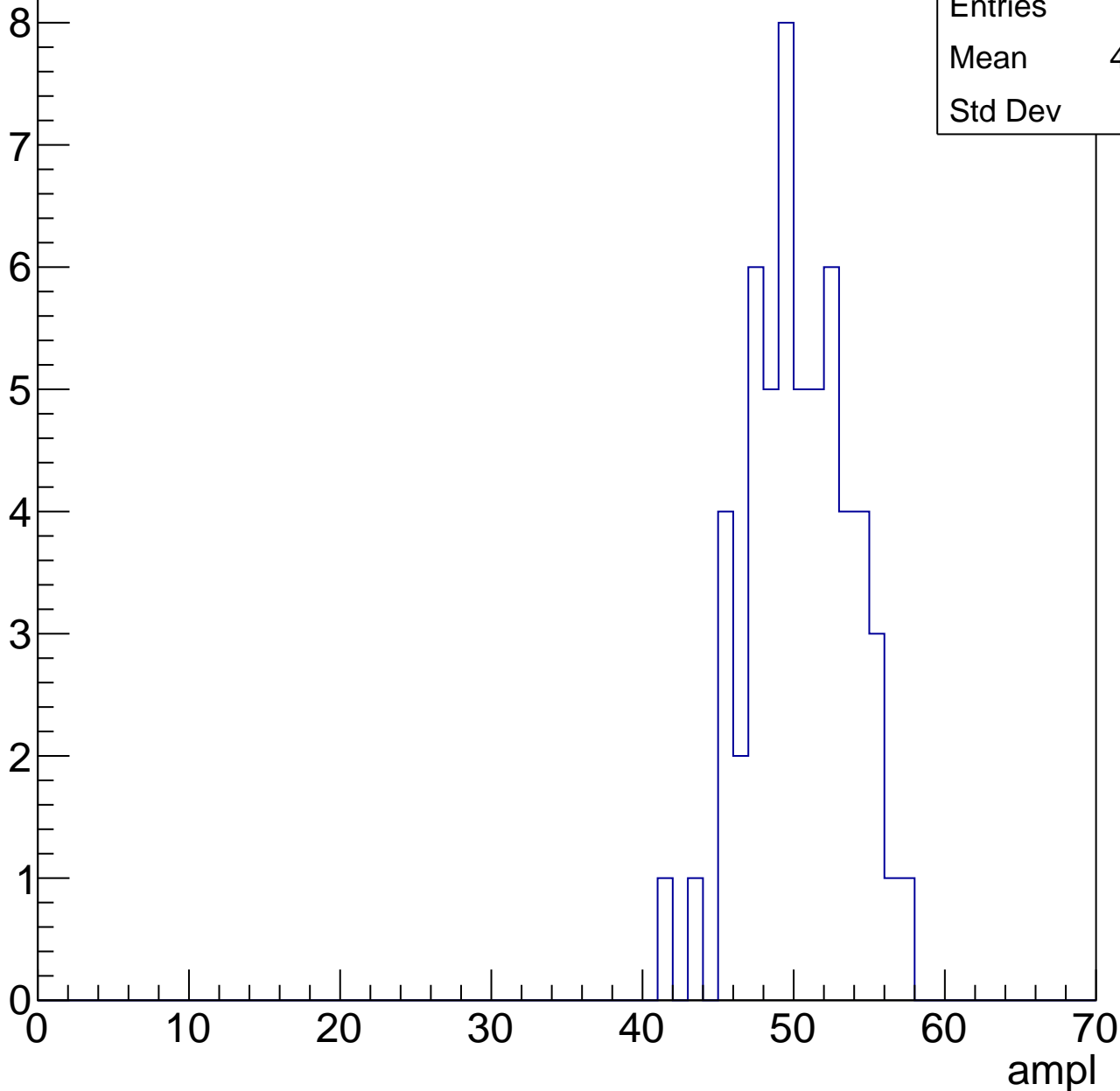


# B1L102S, U24-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

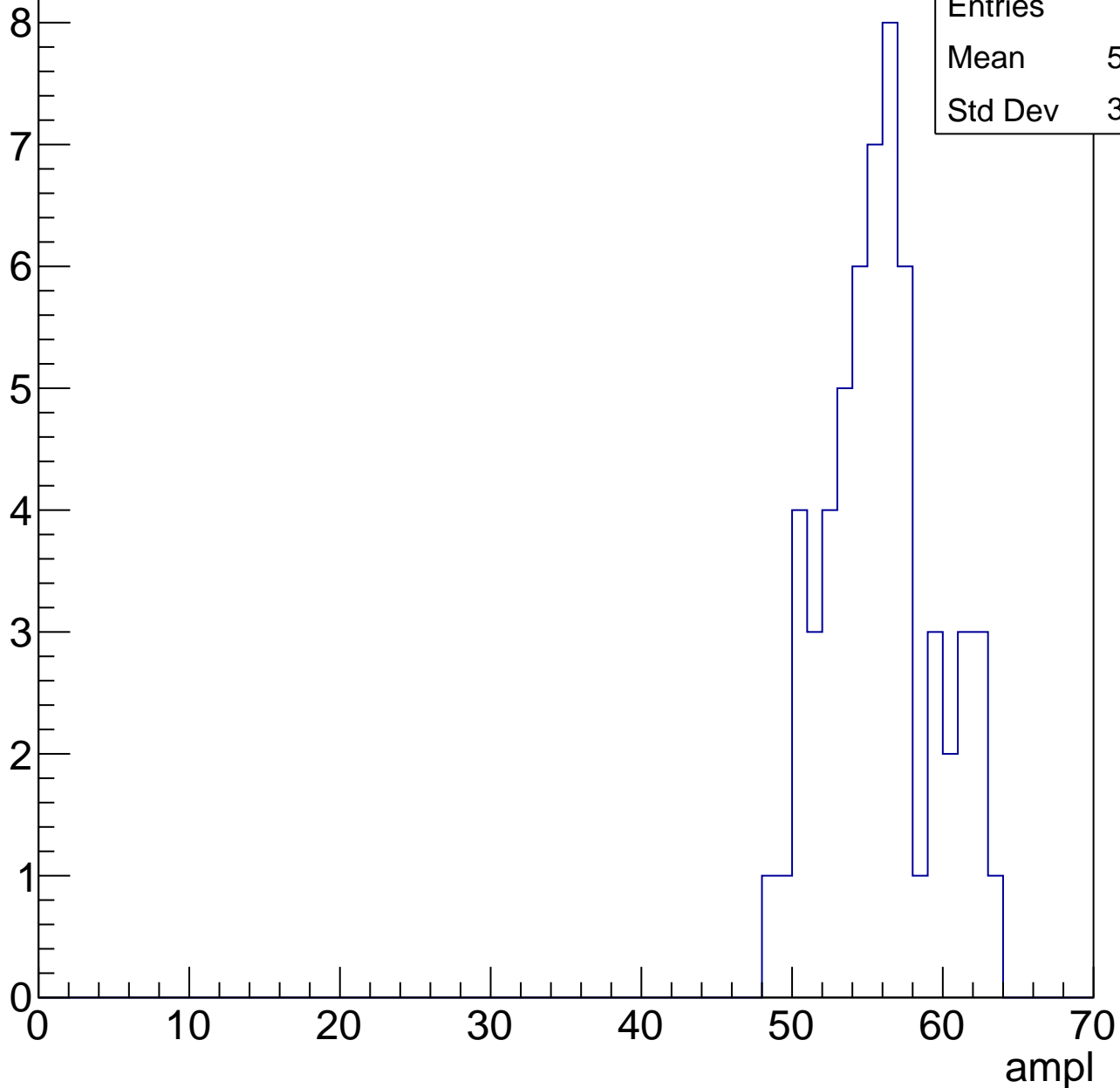
Entries	56
Mean	49.88
Std Dev	3.36



# B1L102S, U24-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

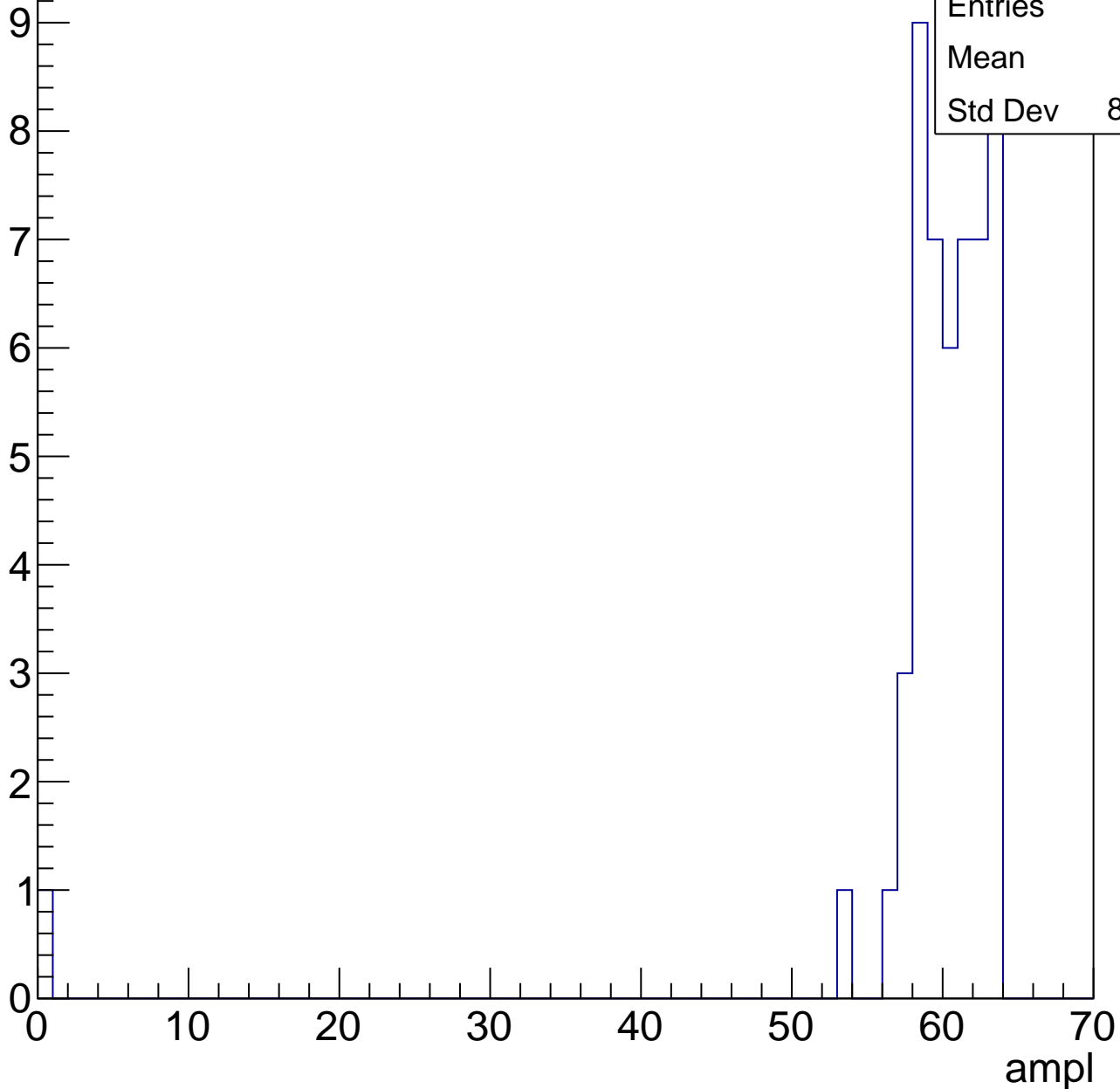
Entry



# B1L102S, U24-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

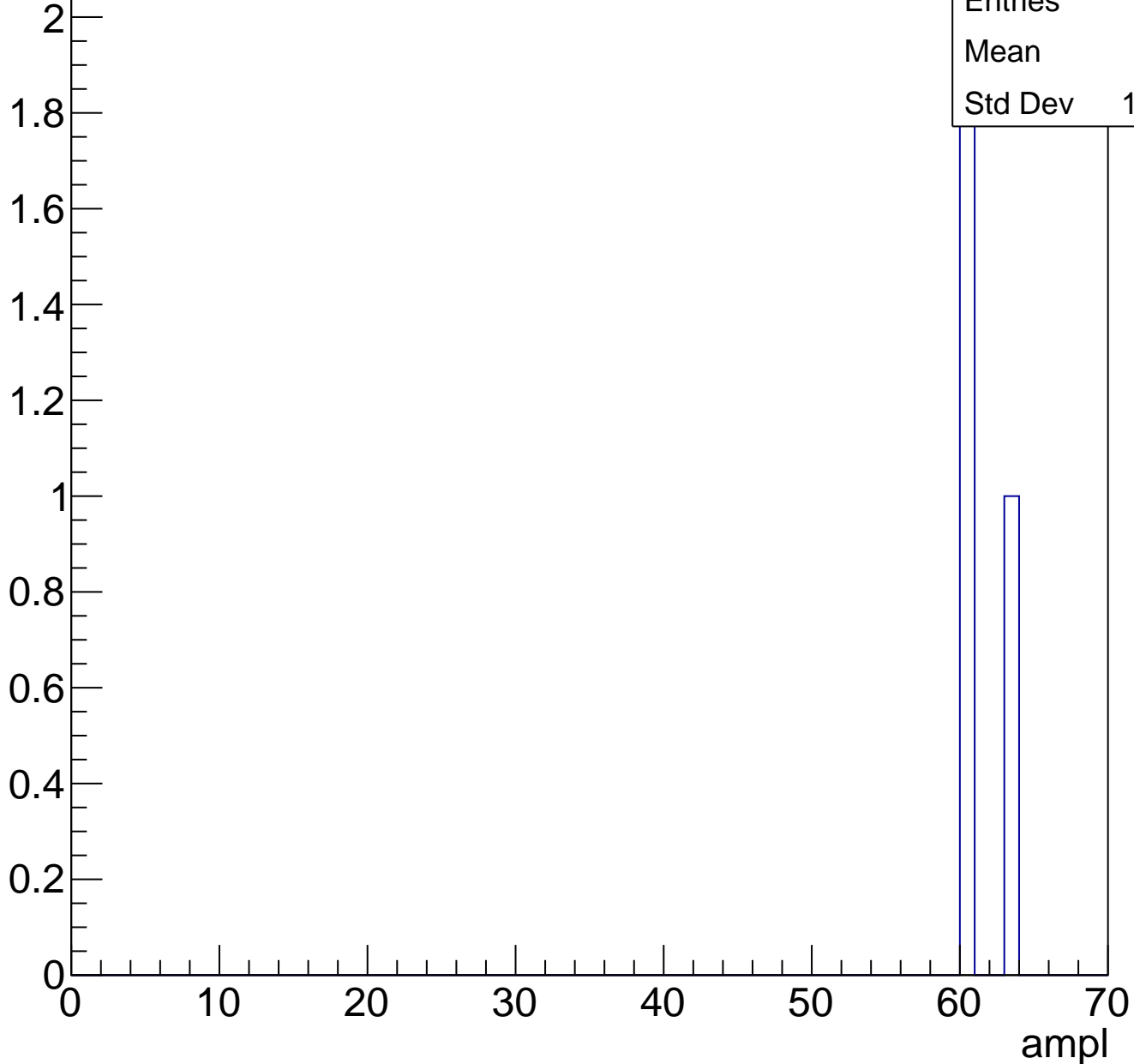
Entry



# B1L102S, U24-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

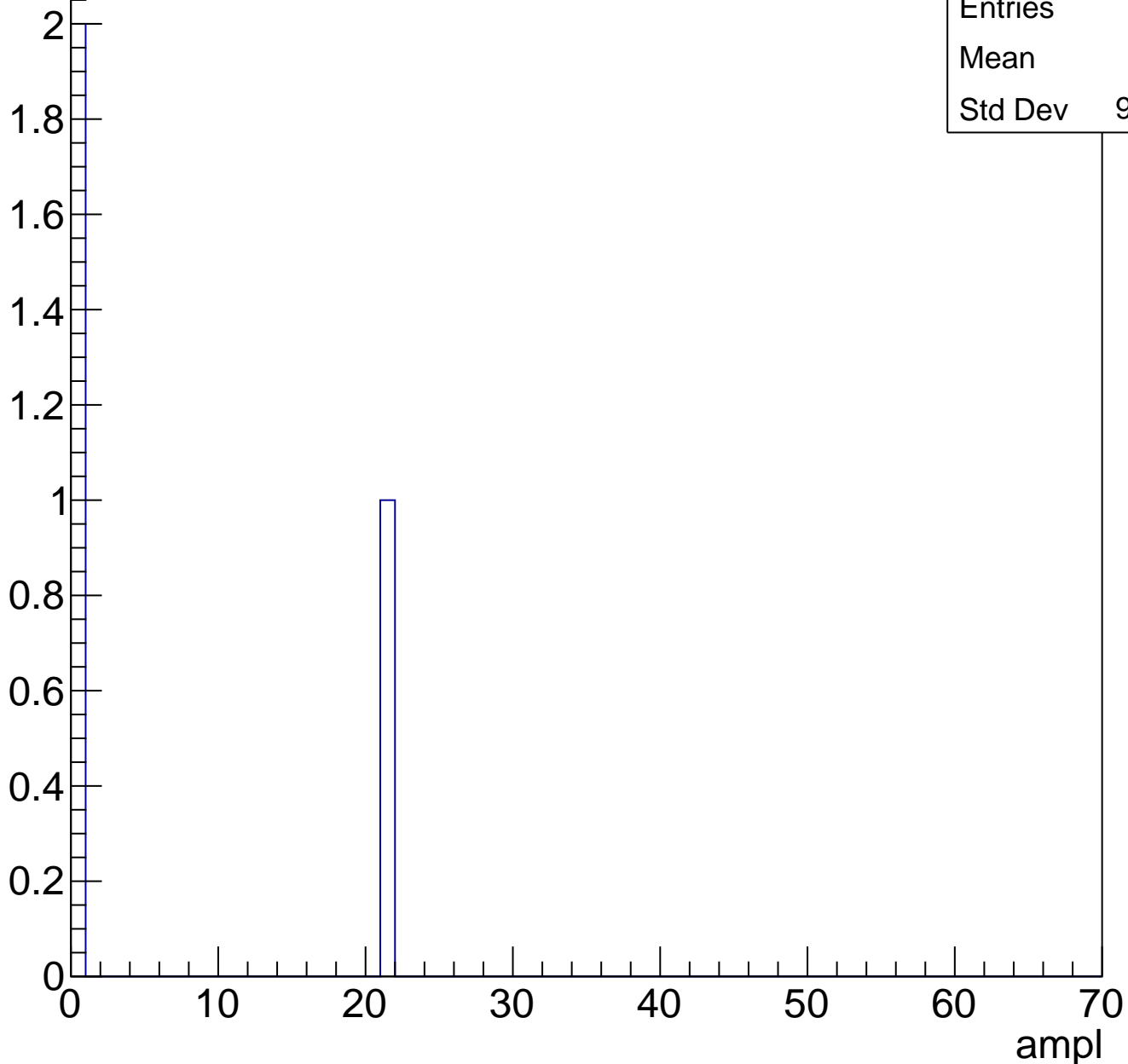




# B1L102S, U24-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L102S, U24-ch24, adc0

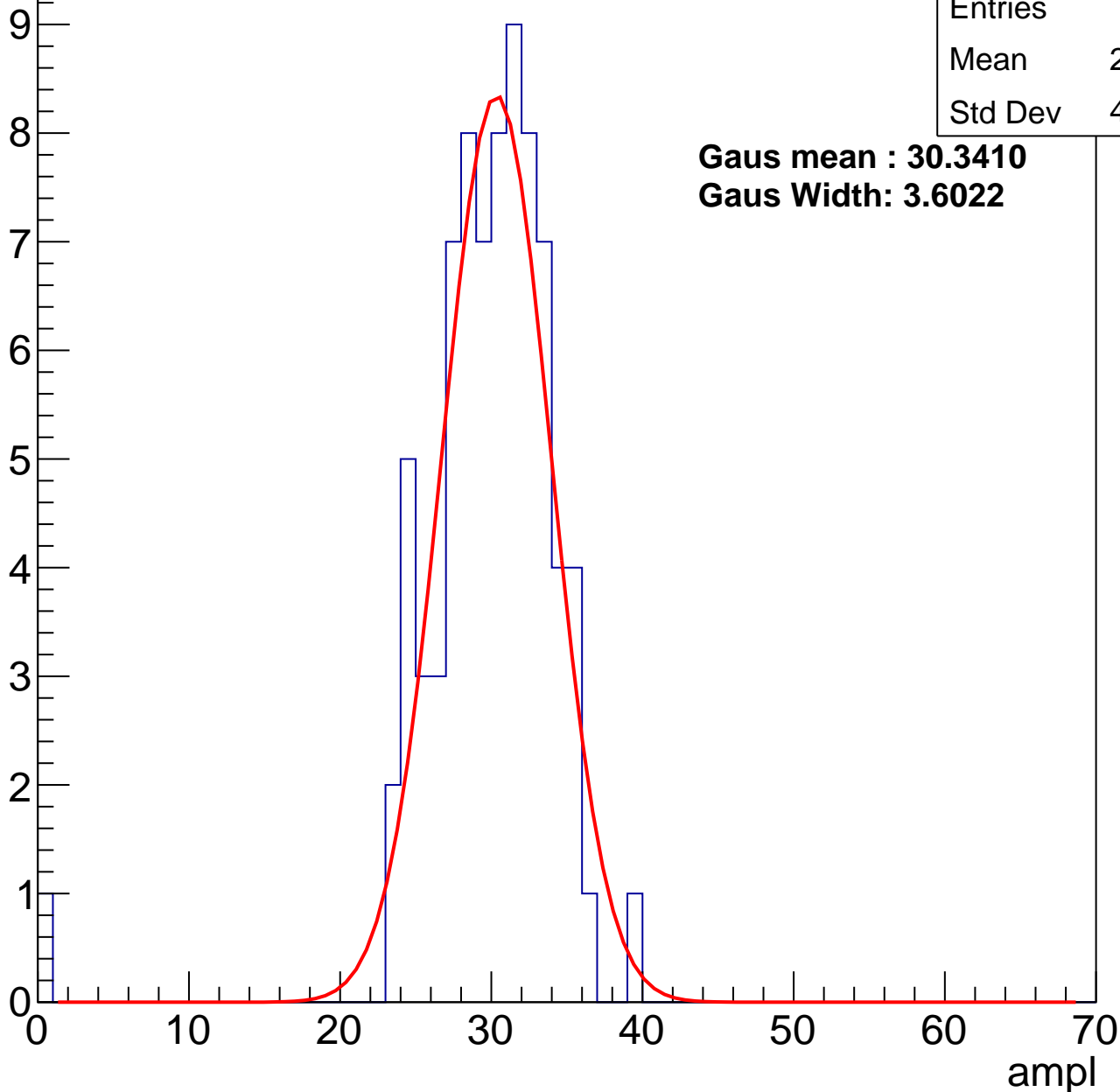
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.38
Std Dev	4.748

**Gaus mean : 30.3410**

**Gaus Width: 3.6022**



# B1L102S, U24-ch24, adc1

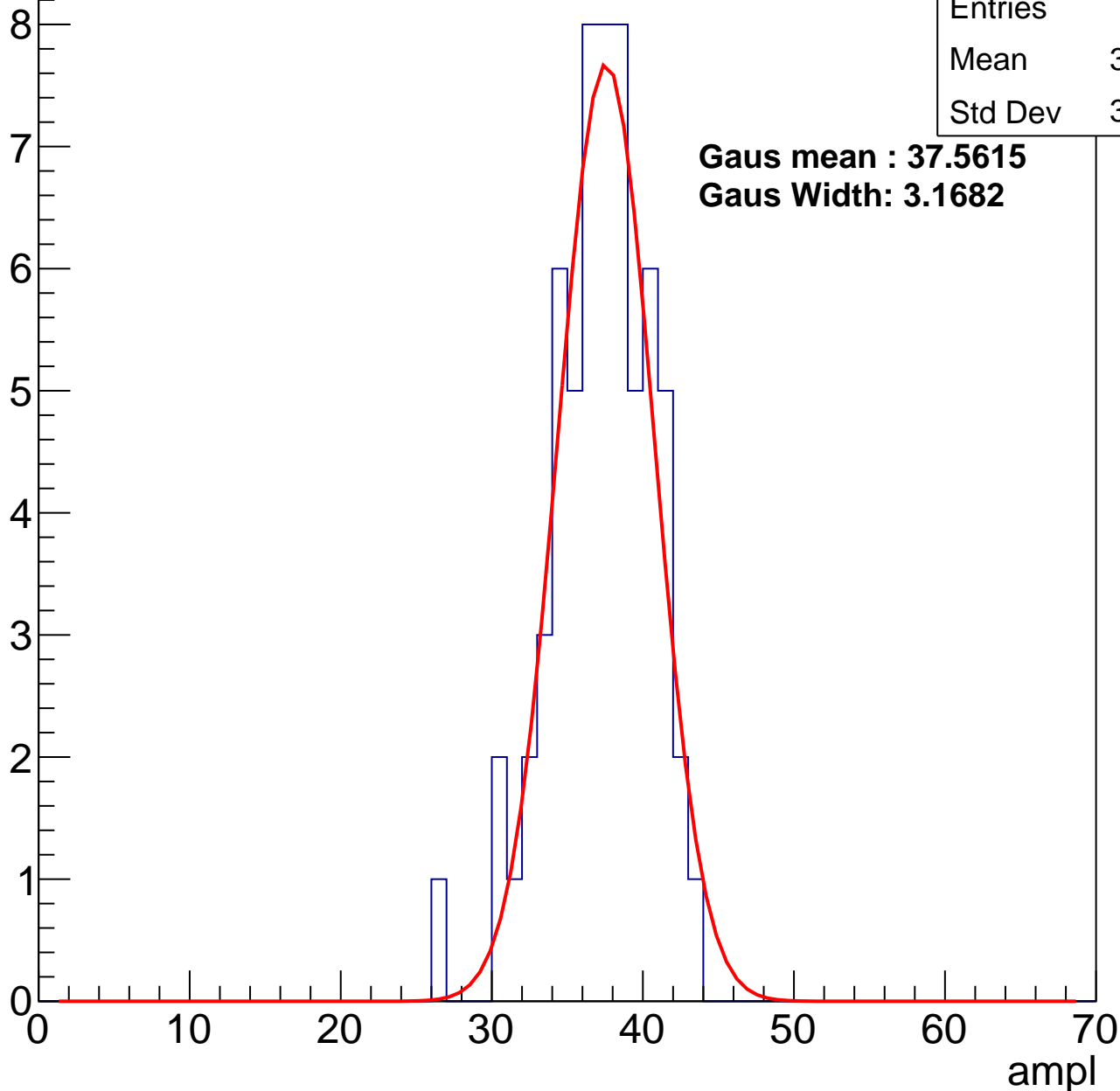
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.73
Std Dev	3.282

**Gaus mean : 37.5615**

**Gaus Width: 3.1682**



# B1L102S, U24-ch24, adc2

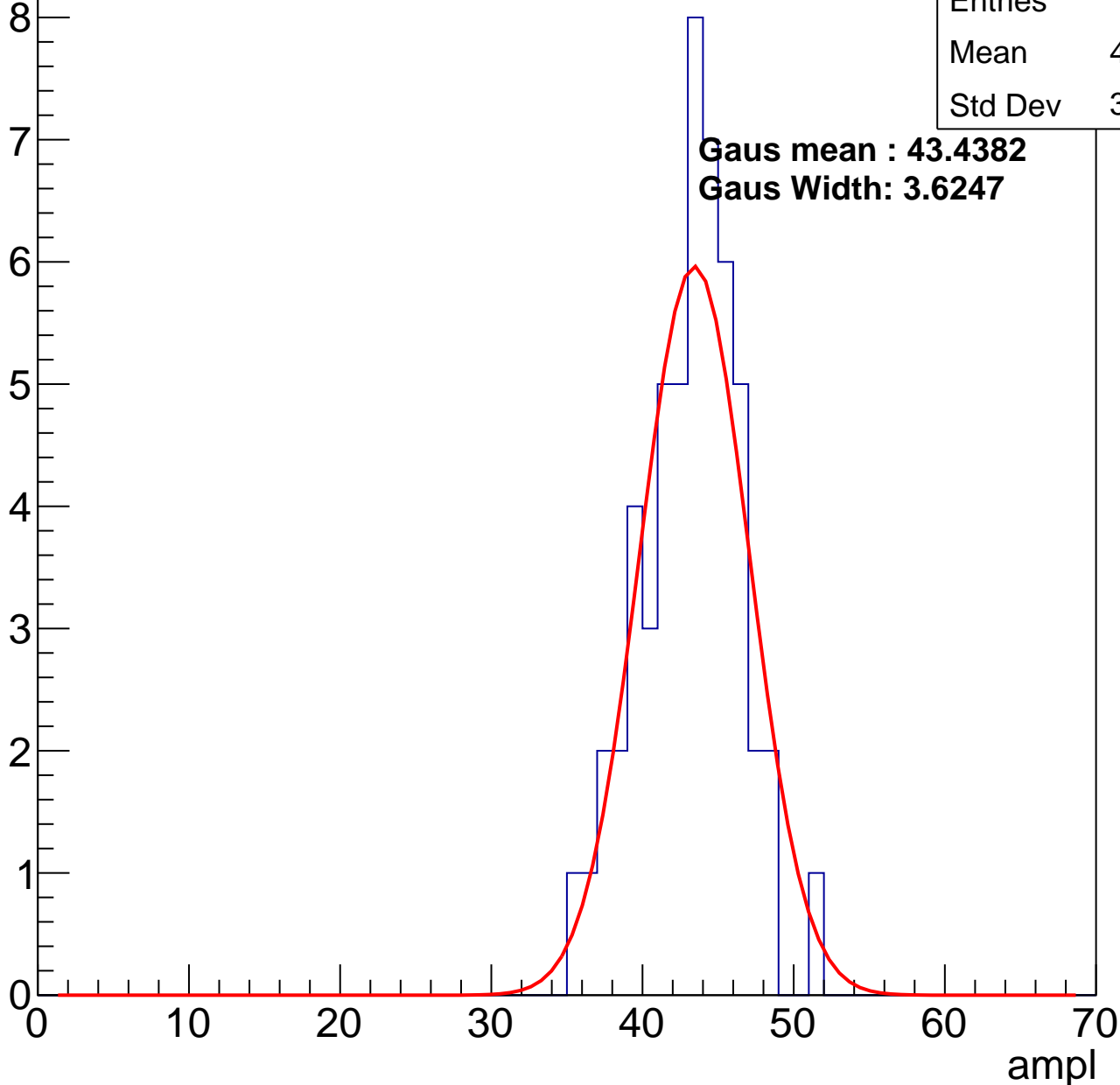
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.69
Std Dev	3.242

**Gaus mean : 43.4382**

**Gaus Width: 3.6247**

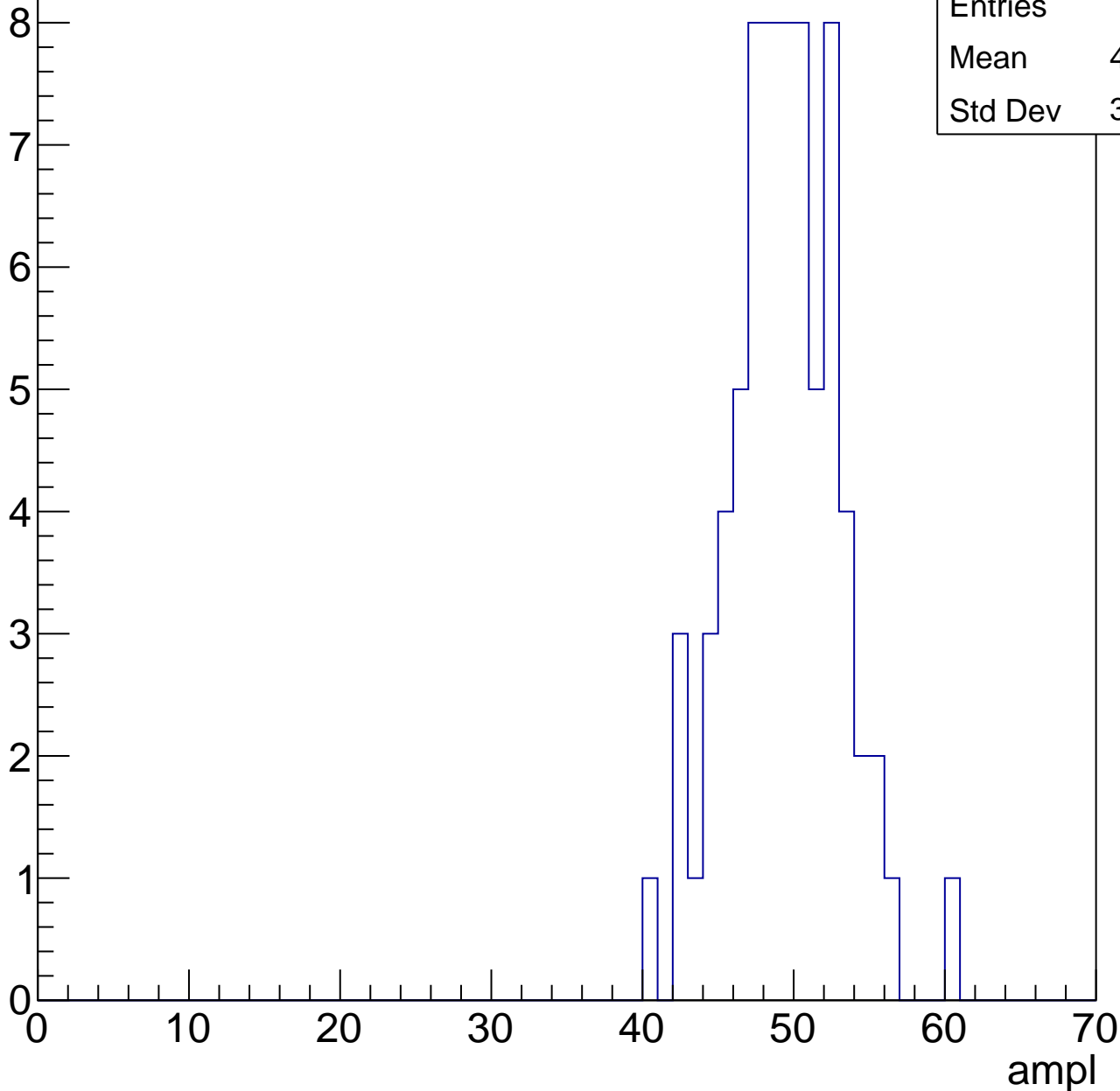


# B1L102S, U24-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.89
Std Dev	3.619

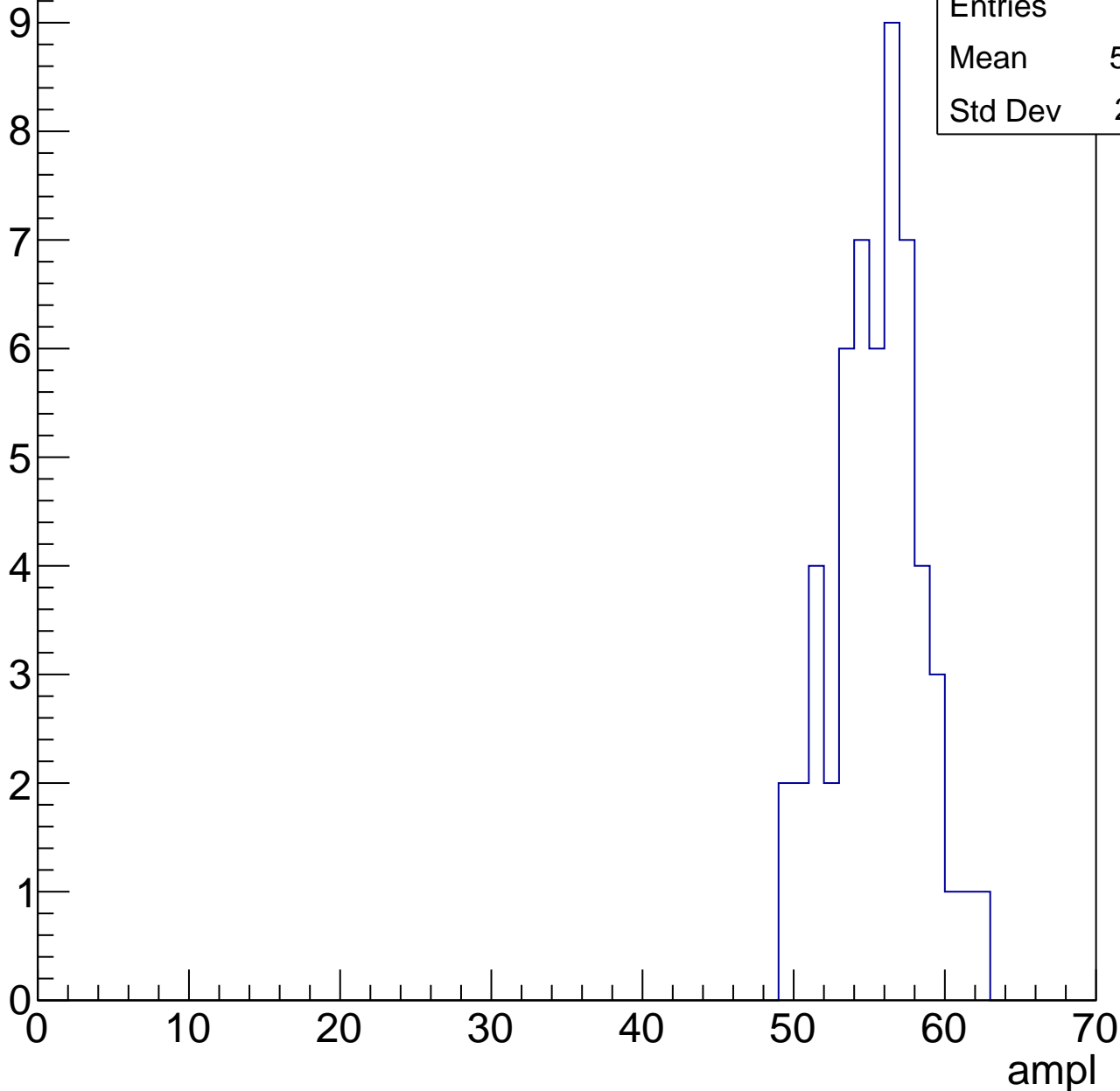


# B1L102S, U24-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.04
Std Dev	2.911

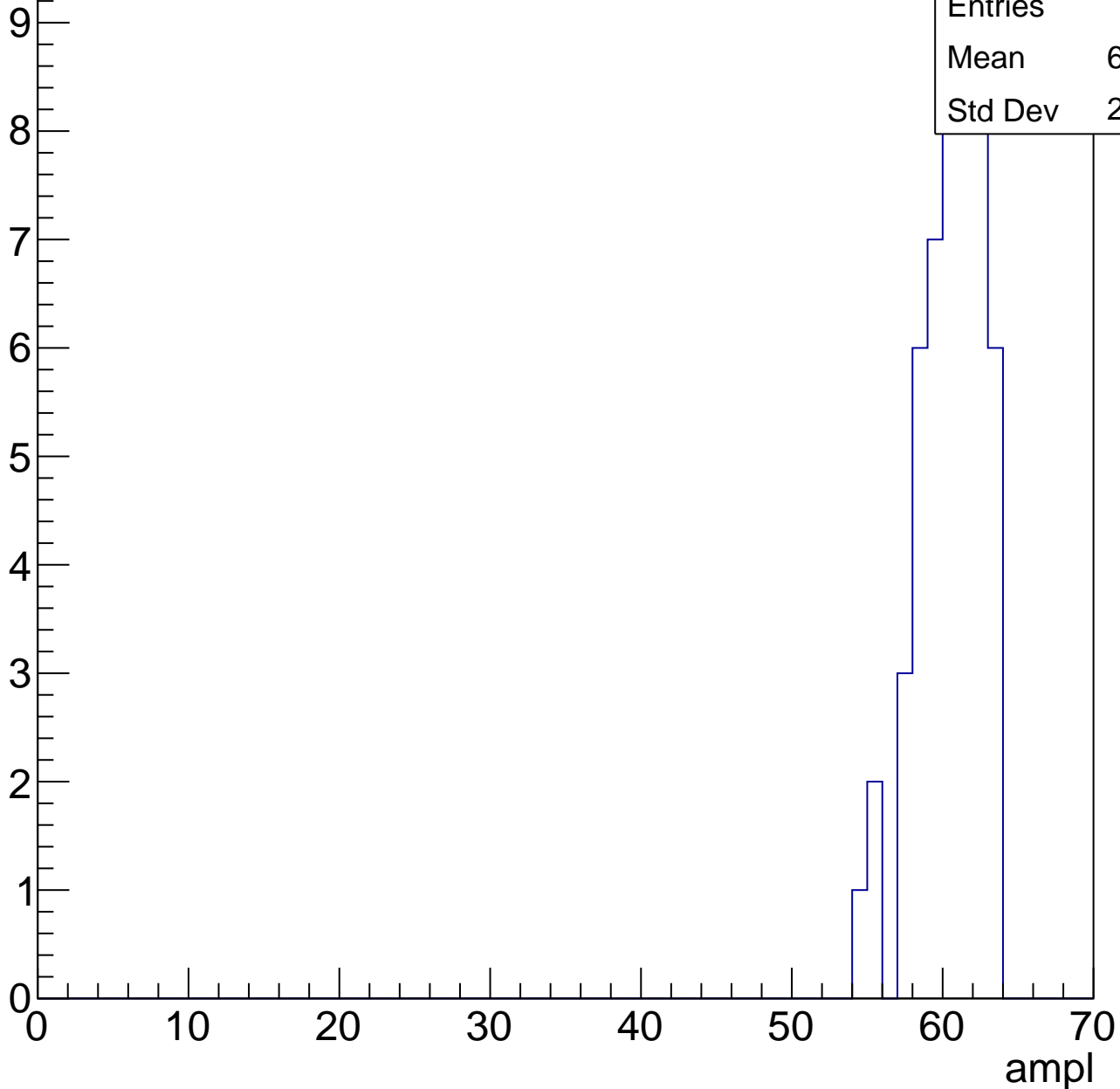


# B1L102S, U24-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

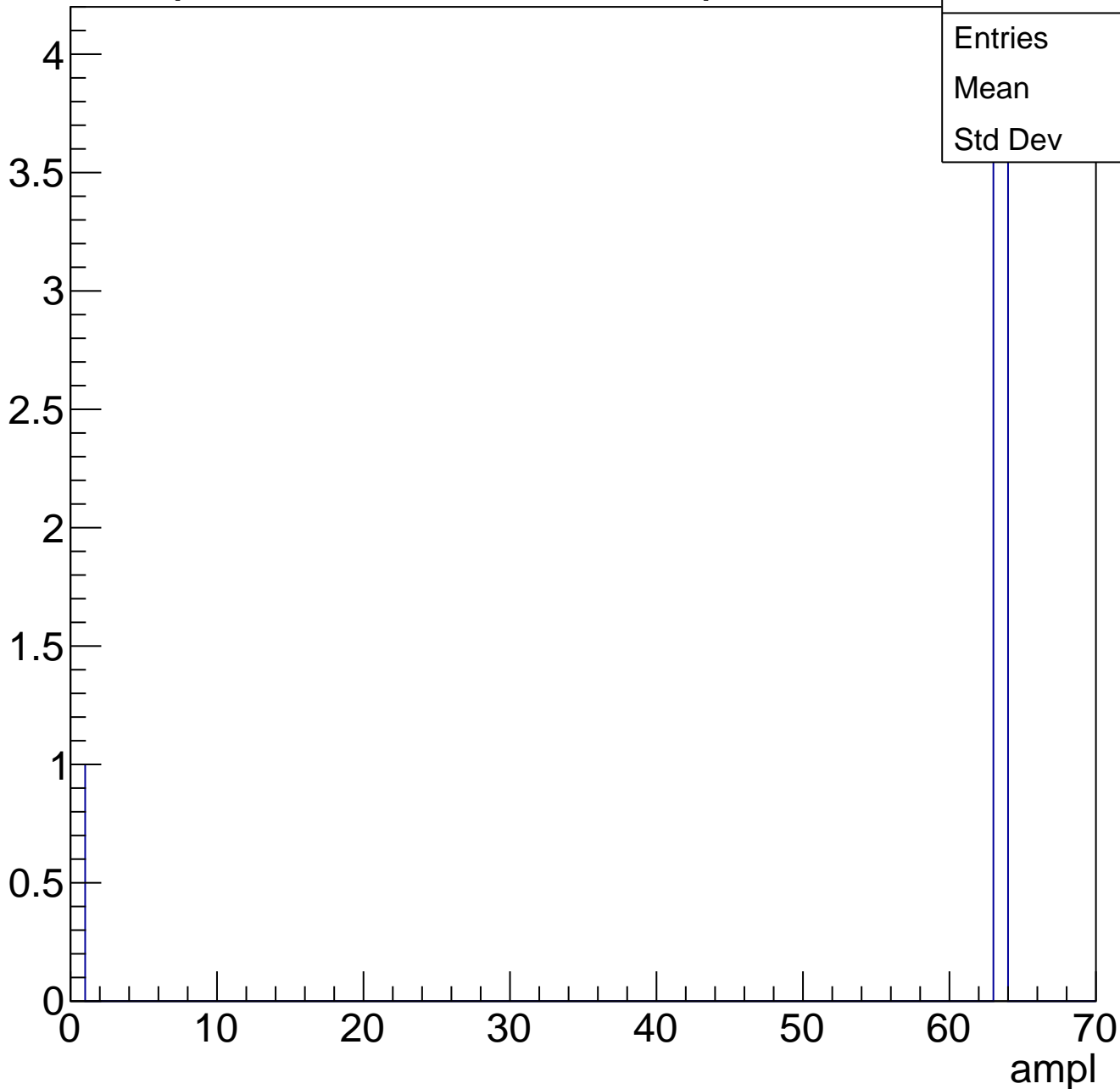
Entries	51
Mean	60.02
Std Dev	2.183



# B1L102S, U24-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B1L102S, U24-ch25, adc0

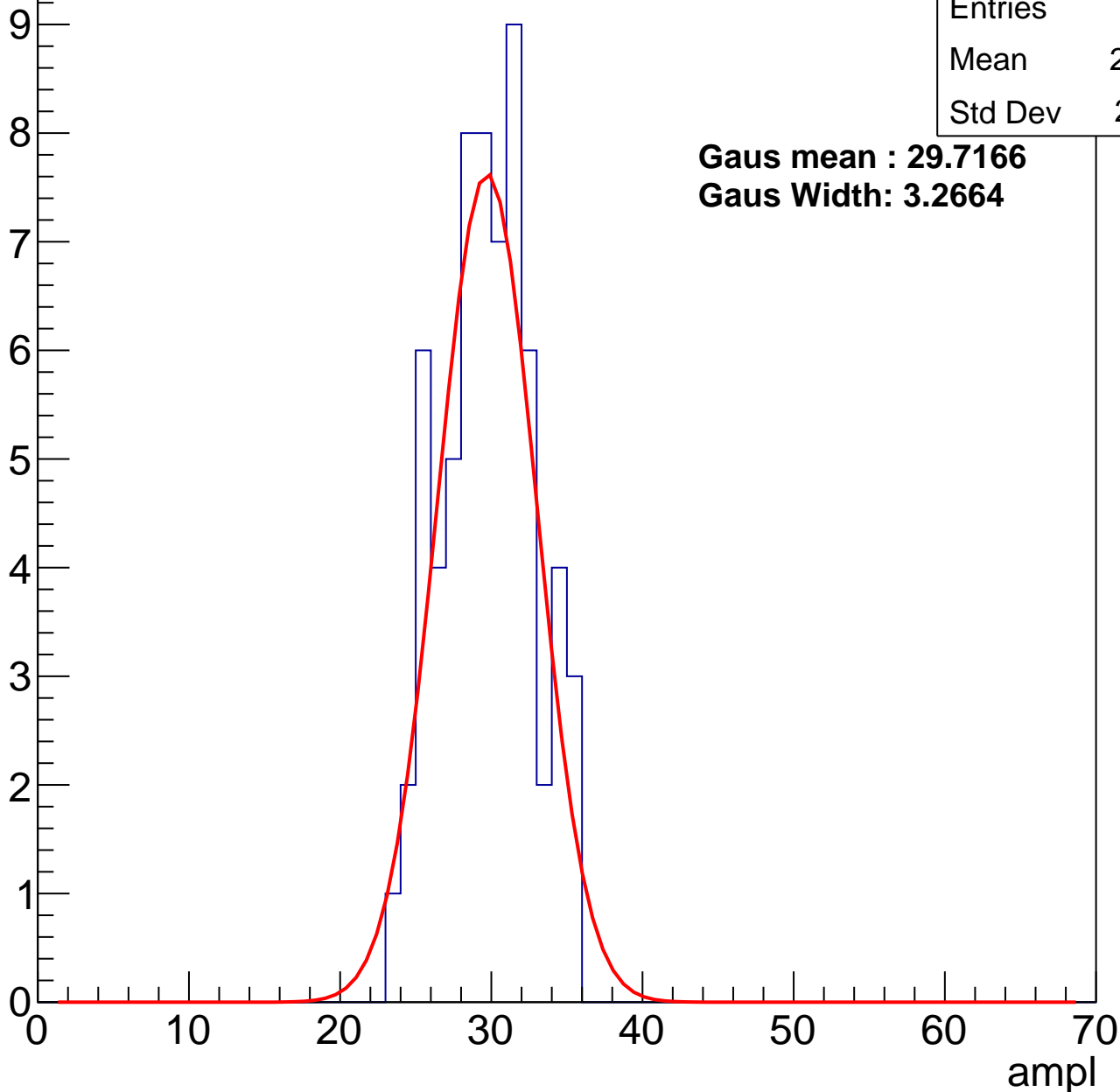
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.29
Std Dev	2.981

**Gaus mean : 29.7166**

**Gaus Width: 3.2664**



# B1L102S, U24-ch25, adc1

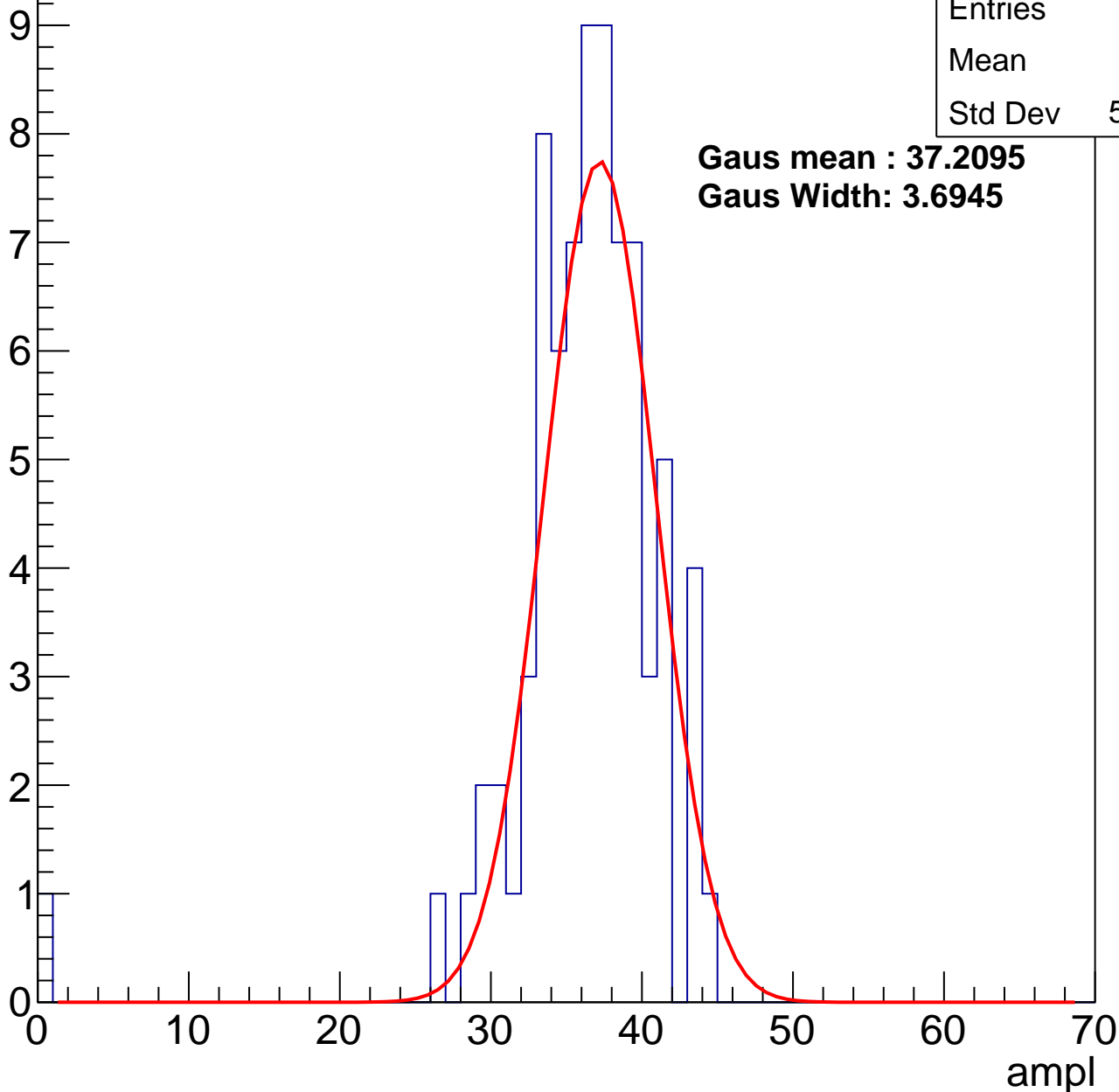
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.7
Std Dev	5.503

**Gaus mean : 37.2095**

**Gaus Width: 3.6945**



# B1L102S, U24-ch25, adc2

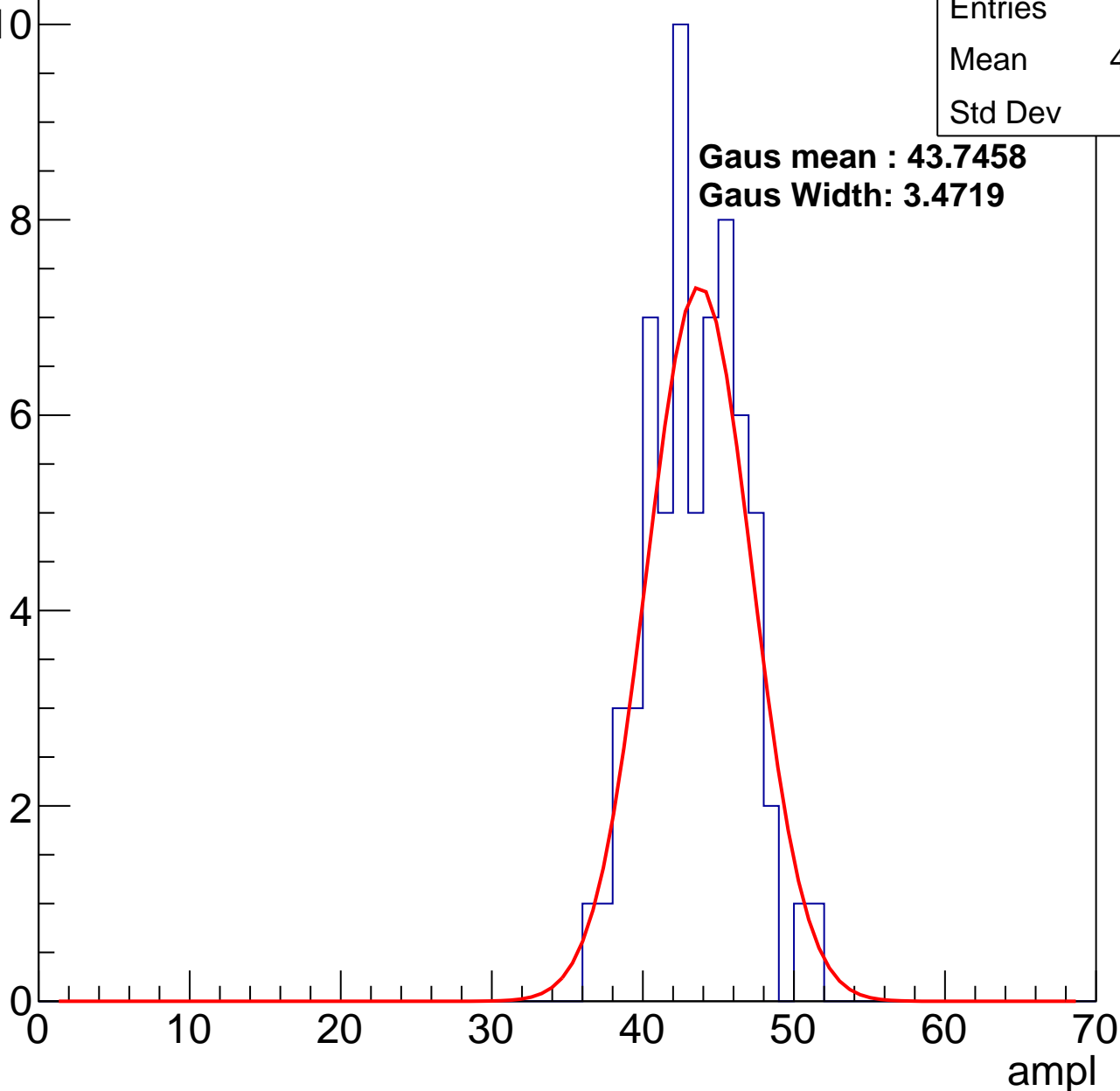
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.08
Std Dev	3.13

**Gaus mean : 43.7458**

**Gaus Width: 3.4719**



# B1L102S, U24-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

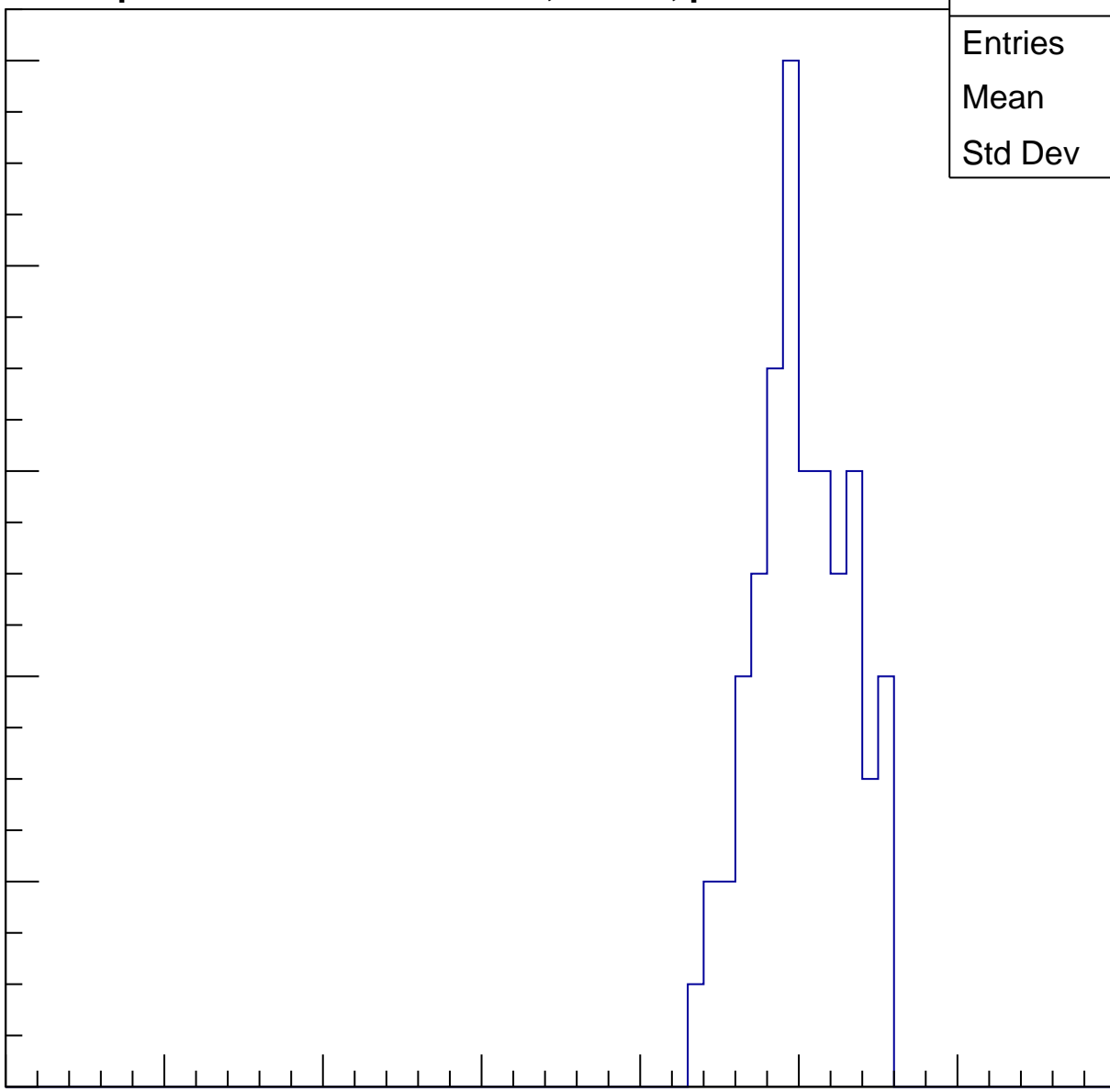
Entries	61
Mean	49.7
Std Dev	2.977

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

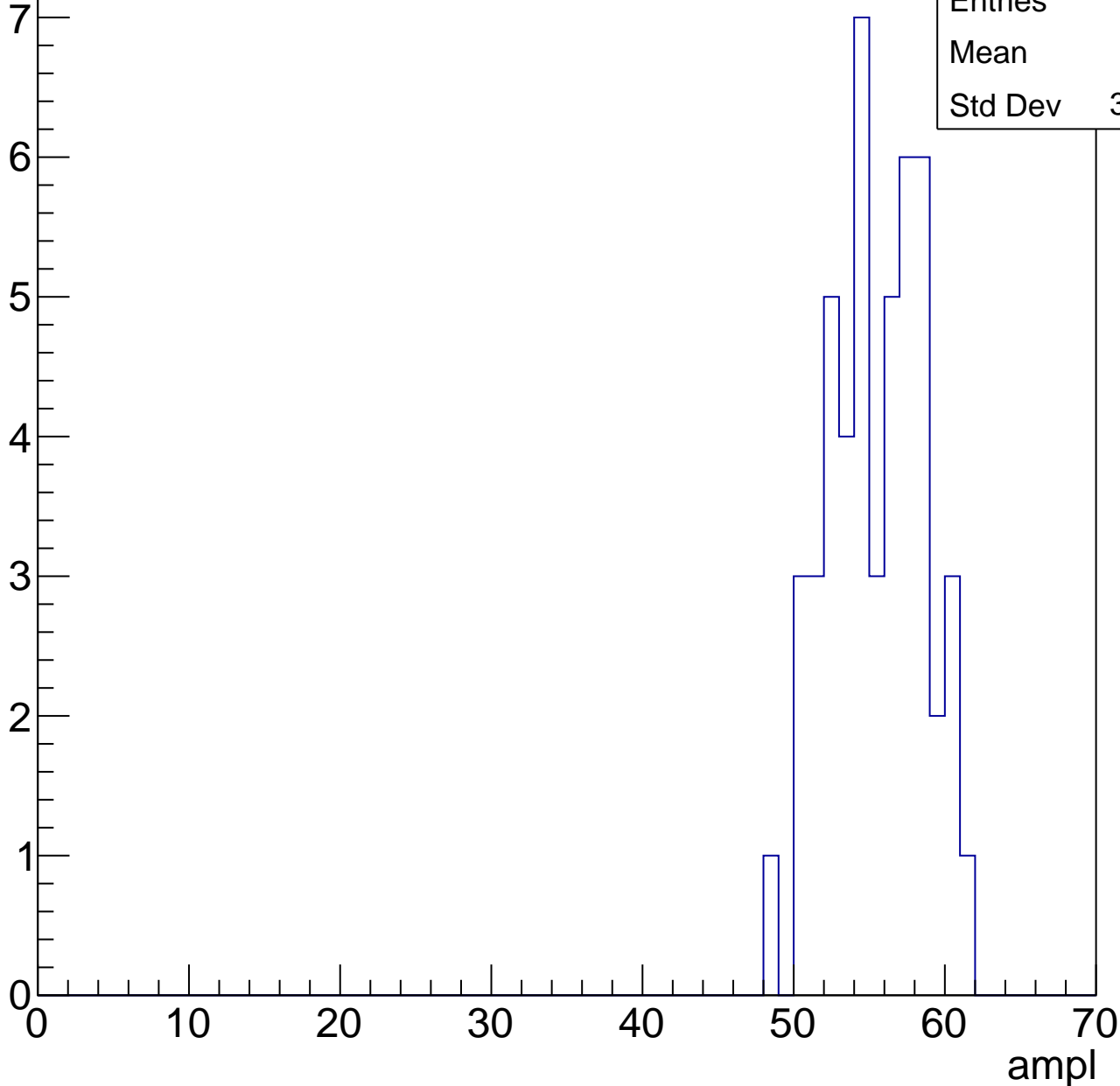


# B1L102S, U24-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55
Std Dev	3.084



# B1L102S, U24-ch25, adc5

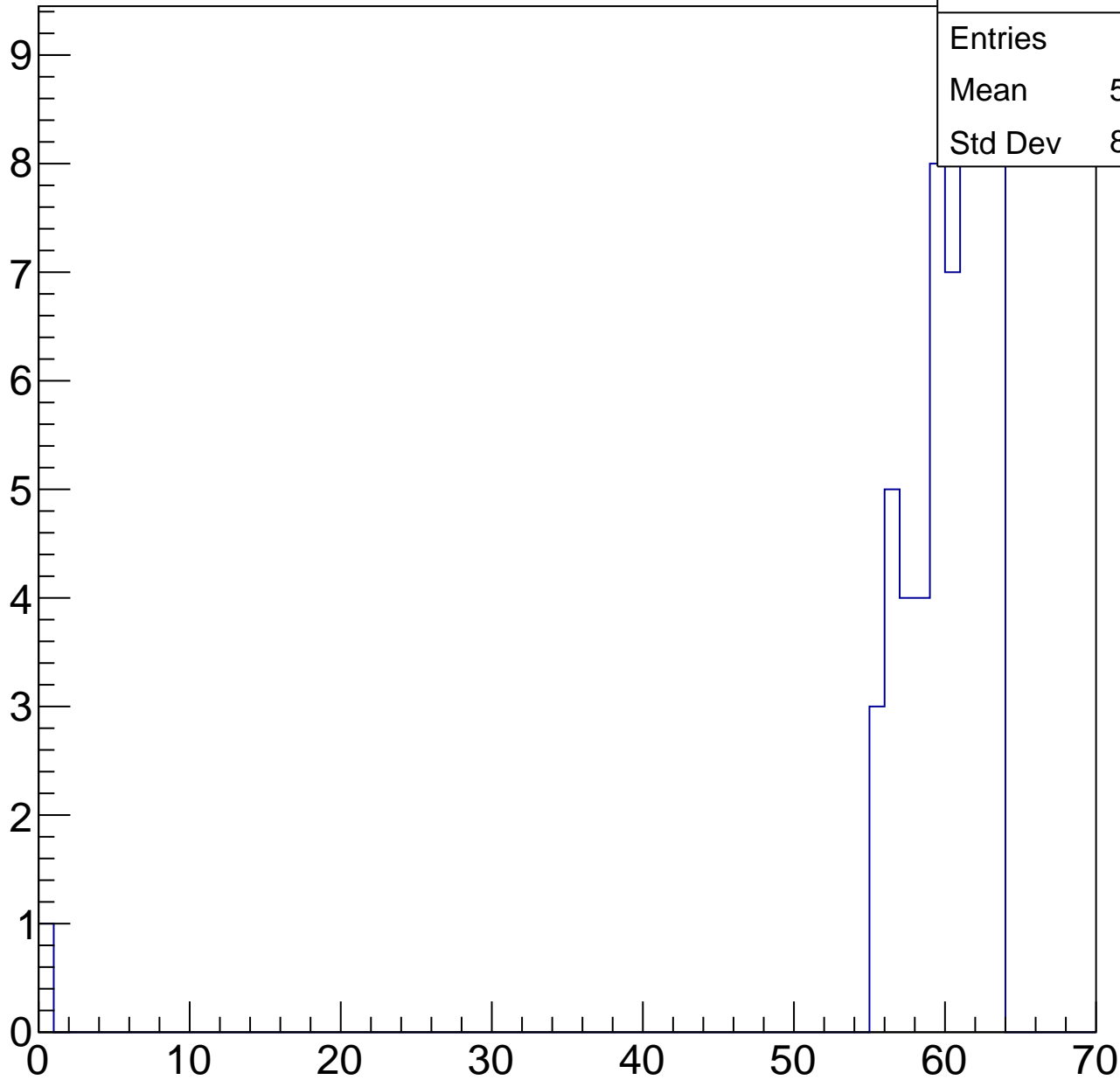
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	58.83
Std Dev	8.089

ampl



# B1L102S, U24-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch26, adc0

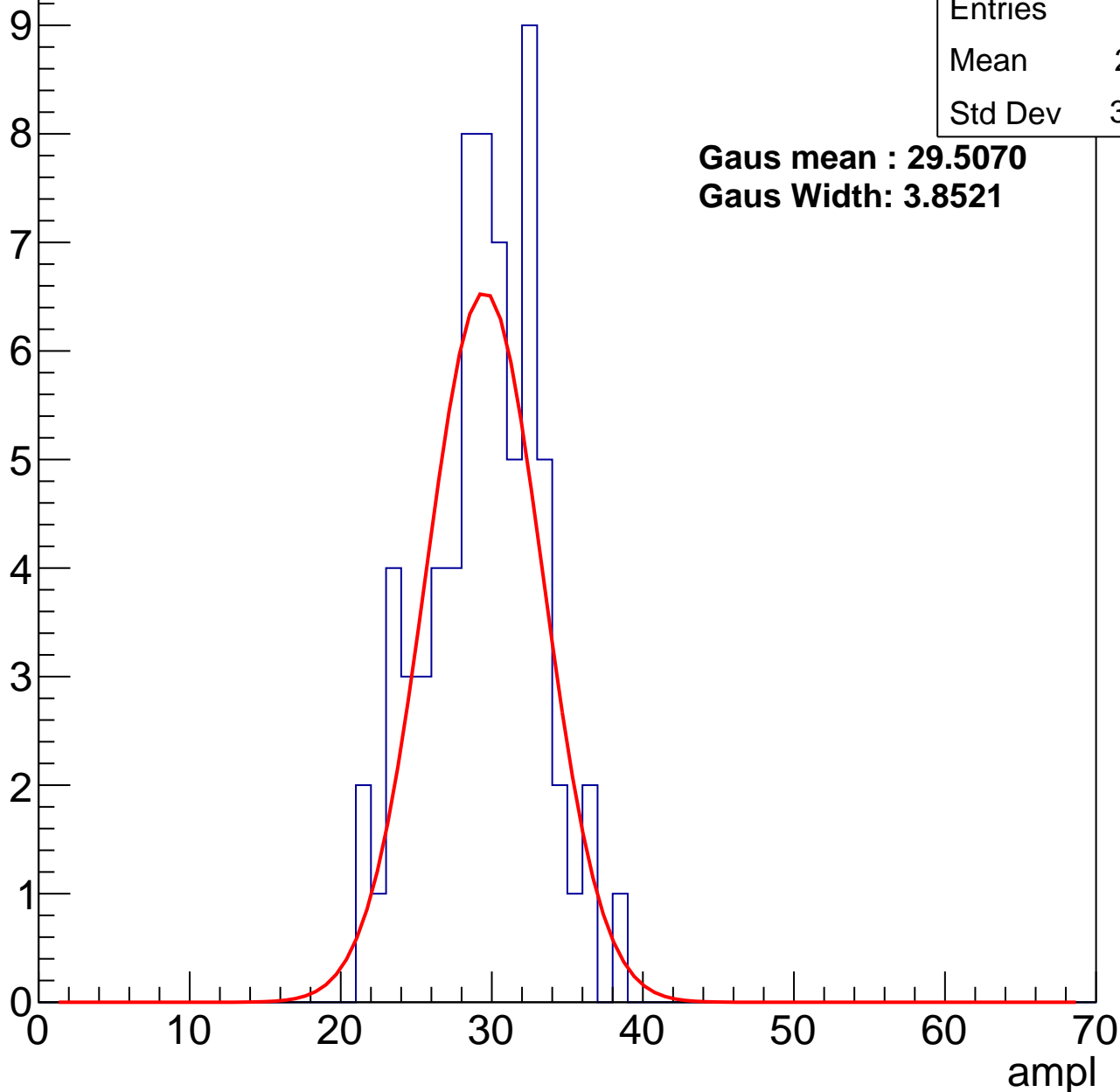
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.01
Std Dev	3.724

**Gaus mean : 29.5070**

**Gaus Width: 3.8521**



# B1L102S, U24-ch26, adc1

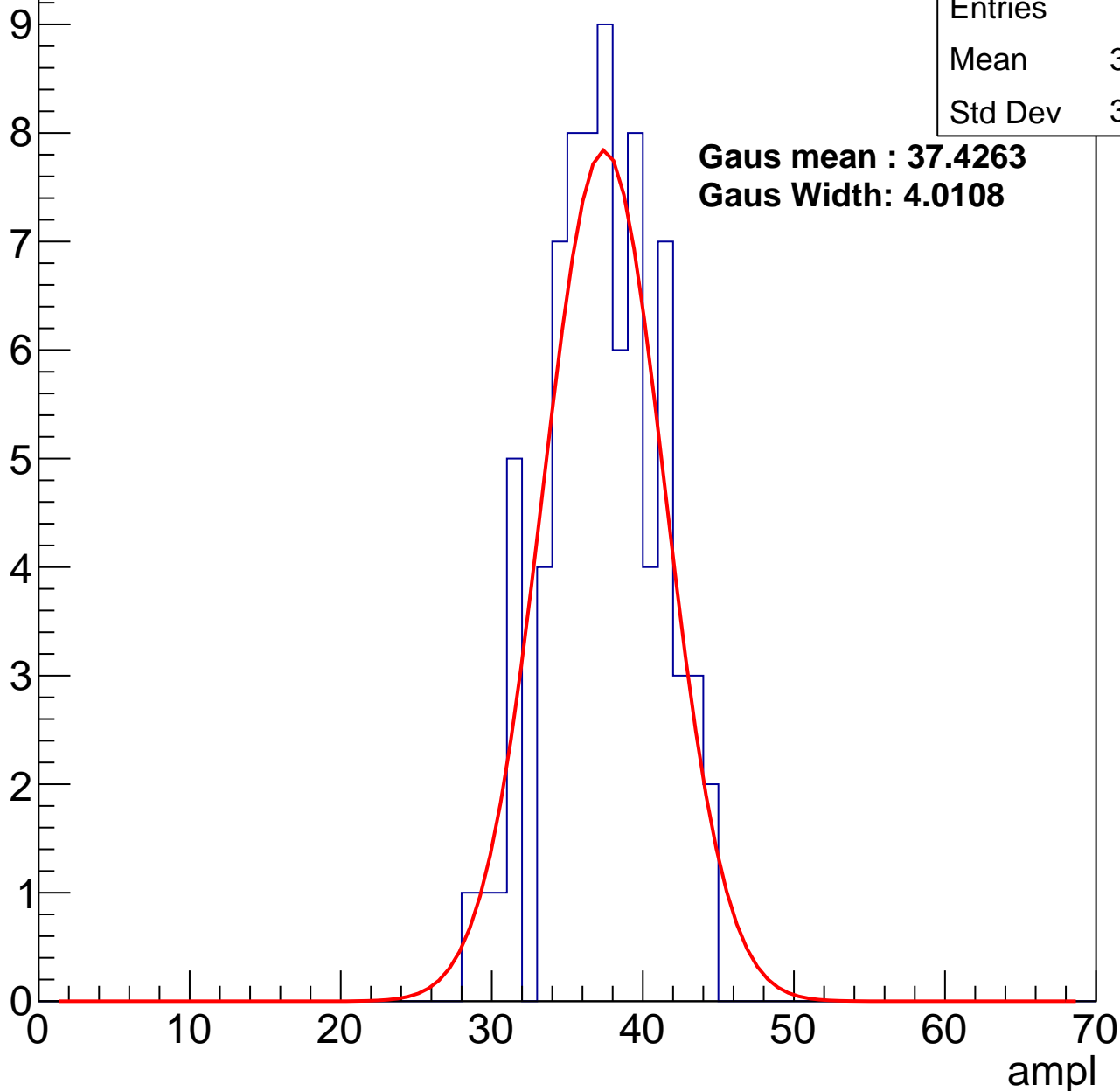
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.92
Std Dev	3.617

**Gaus mean : 37.4263**

**Gaus Width: 4.0108**



# B1L102S, U24-ch26, adc2

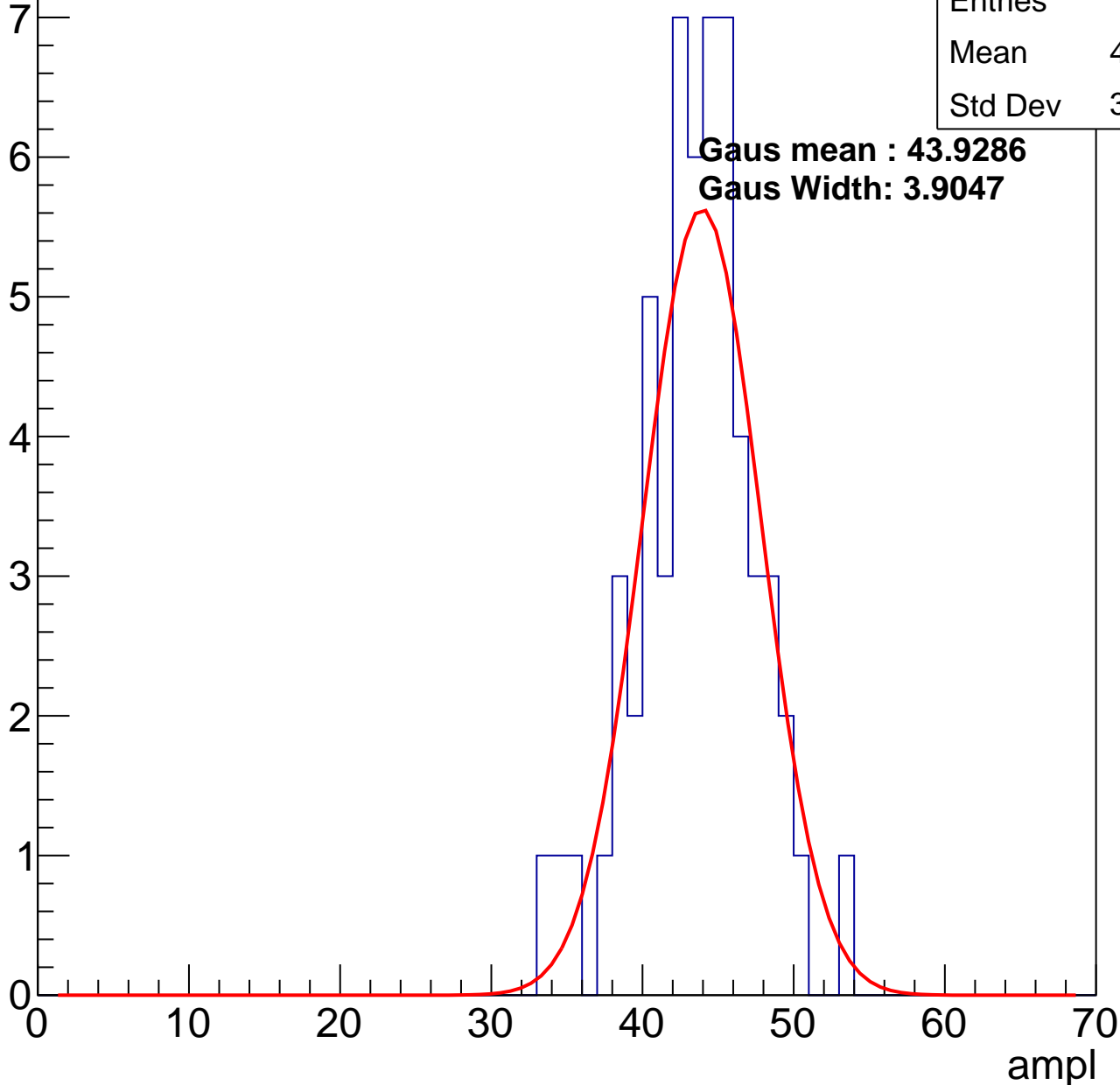
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.09
Std Dev	3.865

**Gaus mean : 43.9286**

**Gaus Width: 3.9047**

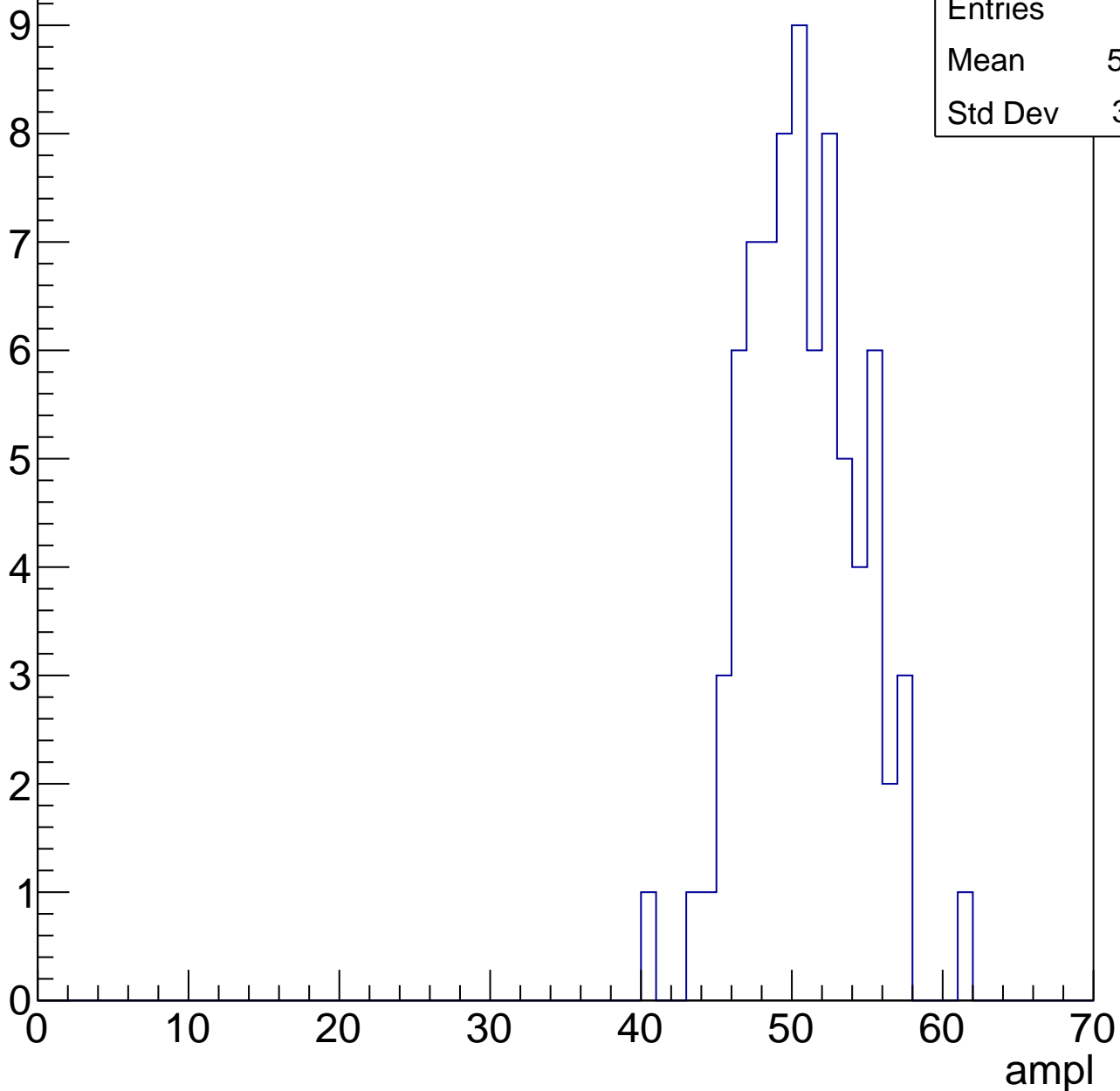


# B1L102S, U24-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

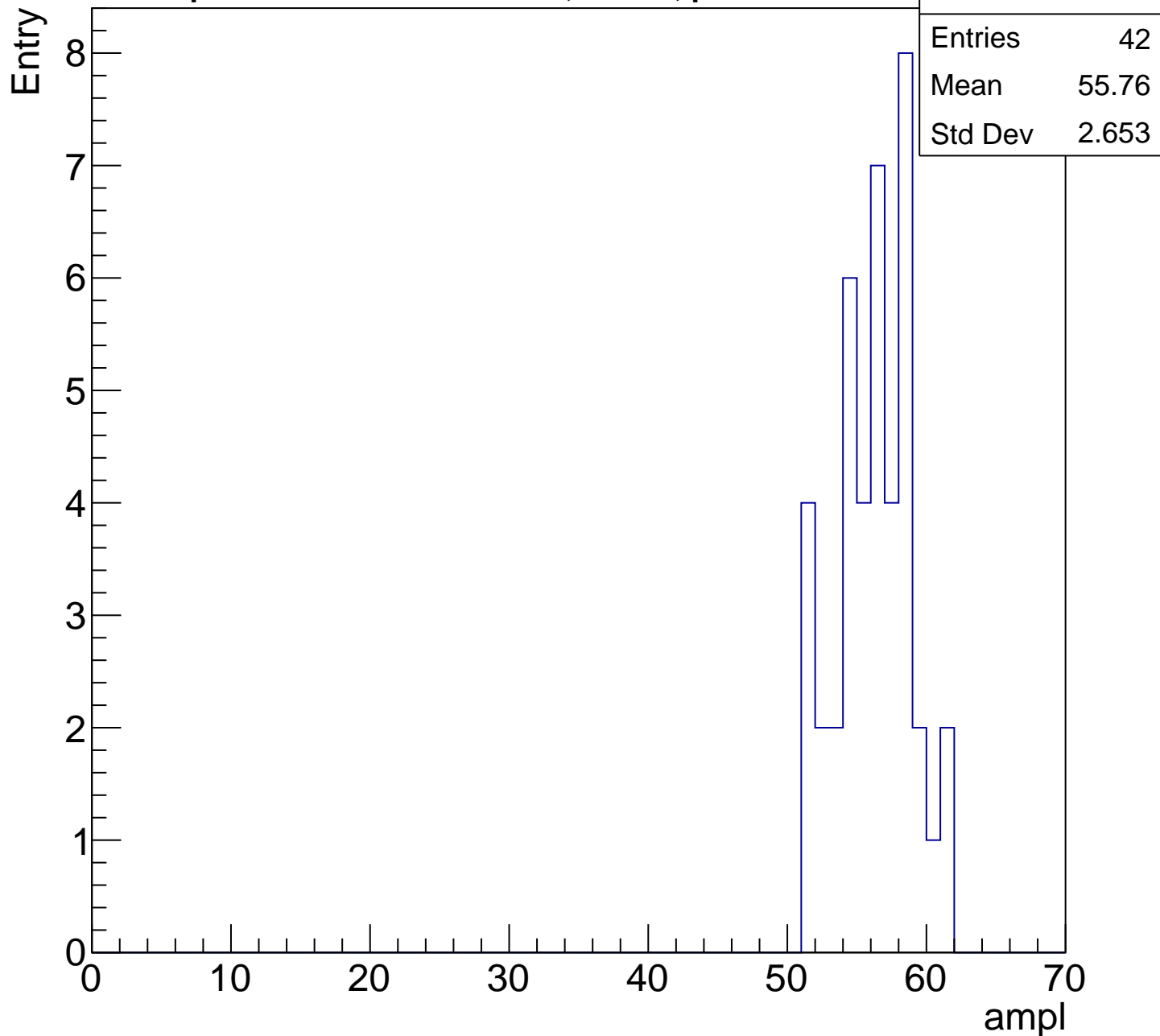
Entry

Entries	78
Mean	50.28
Std Dev	3.731



# B1L102S, U24-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2



# B1L102S, U24-ch26, adc5

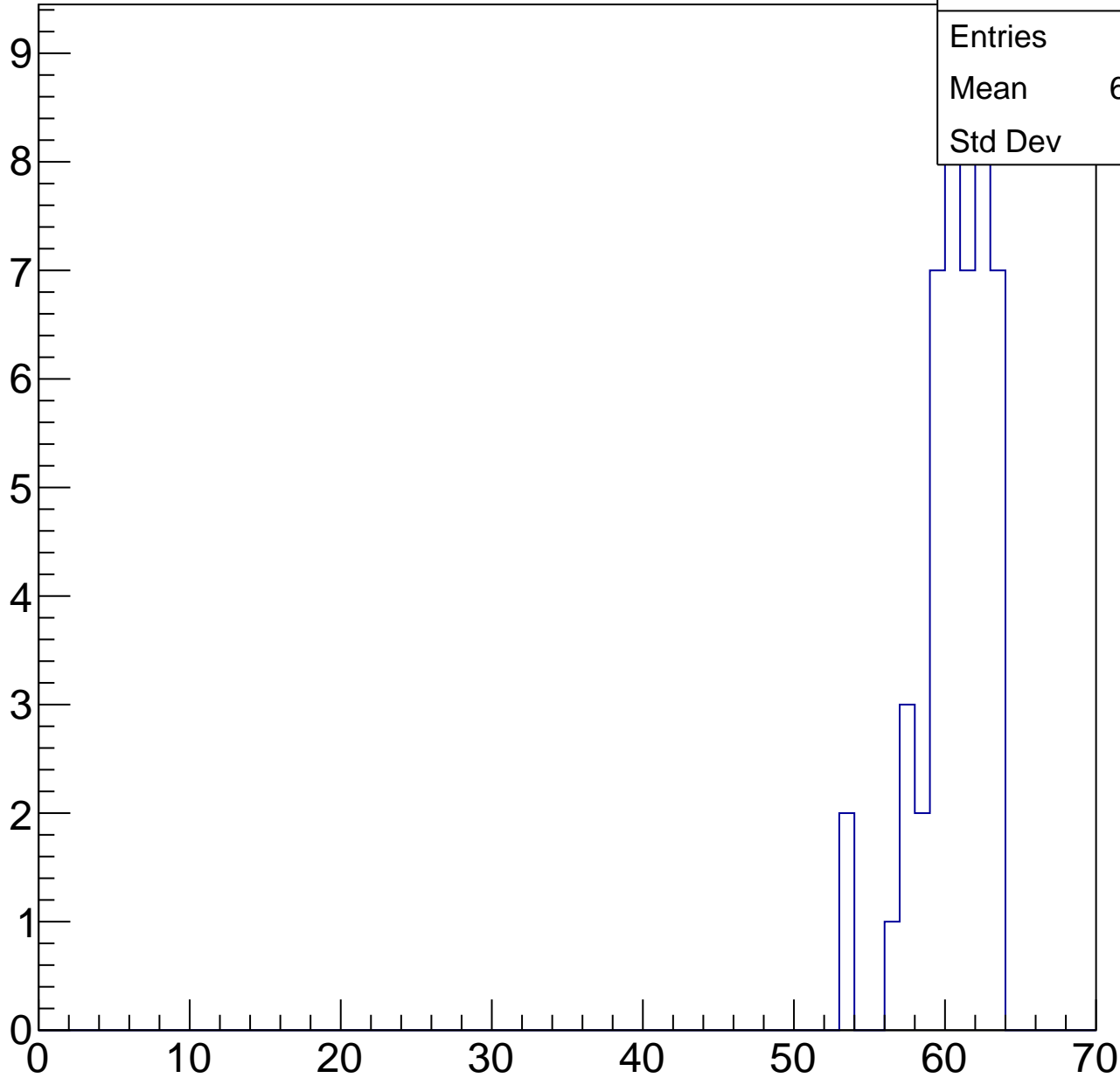
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	60.17
Std Dev	2.38

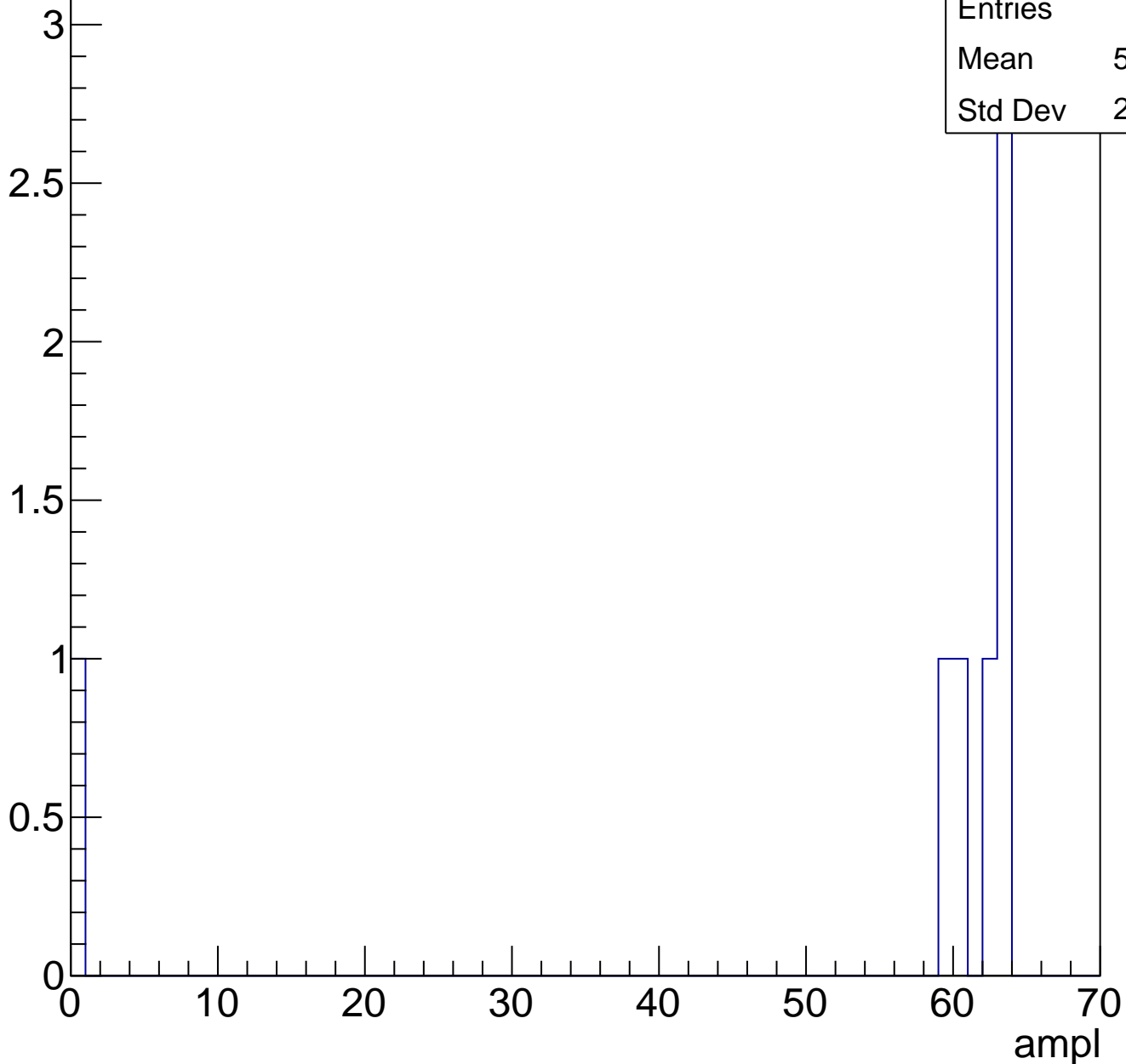
ampl



# B1L102S, U24-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch27, adc0

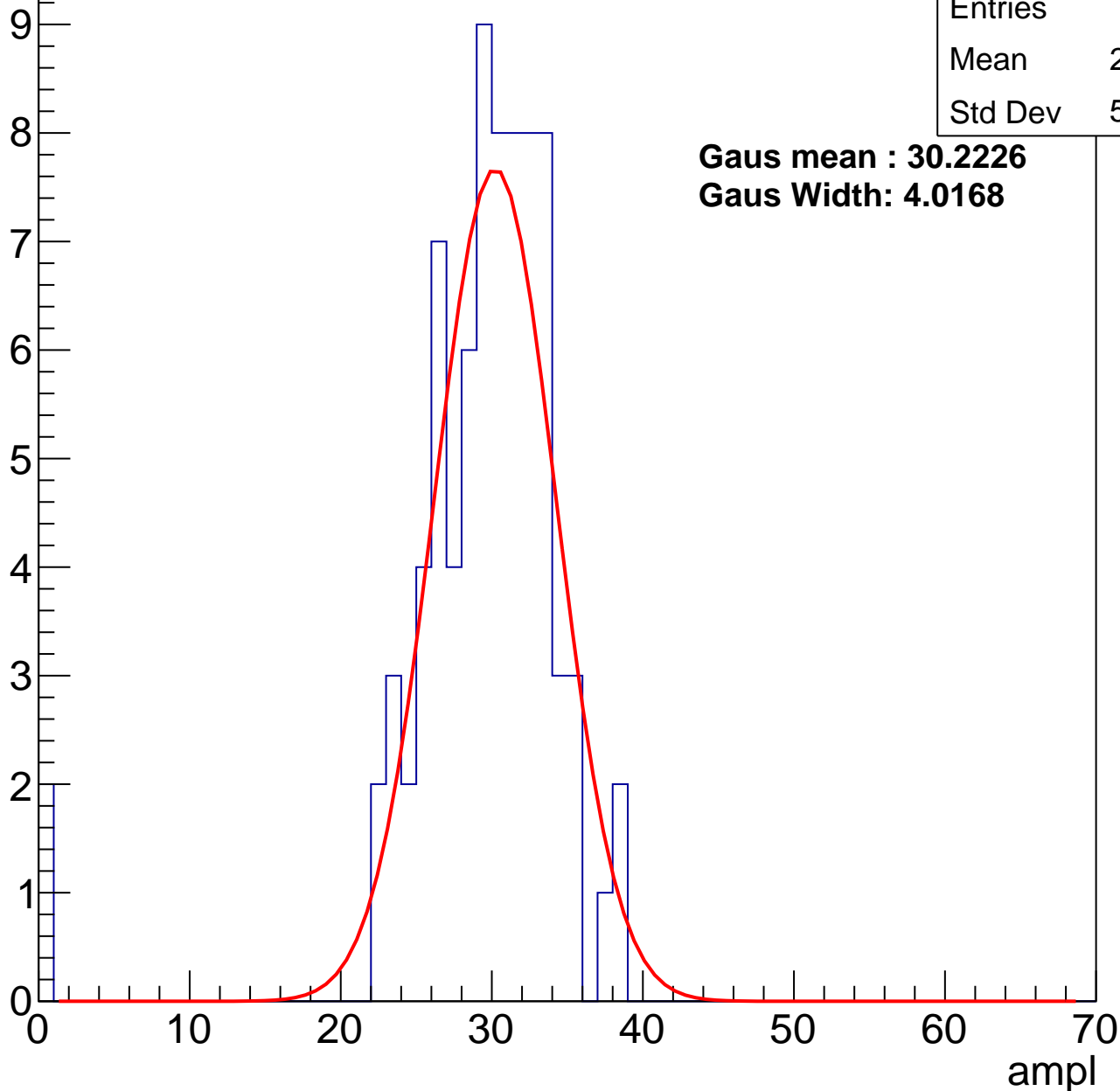
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.85
Std Dev	5.846

**Gaus mean : 30.2226**

**Gaus Width: 4.0168**



# B1L102S, U24-ch27, adc1

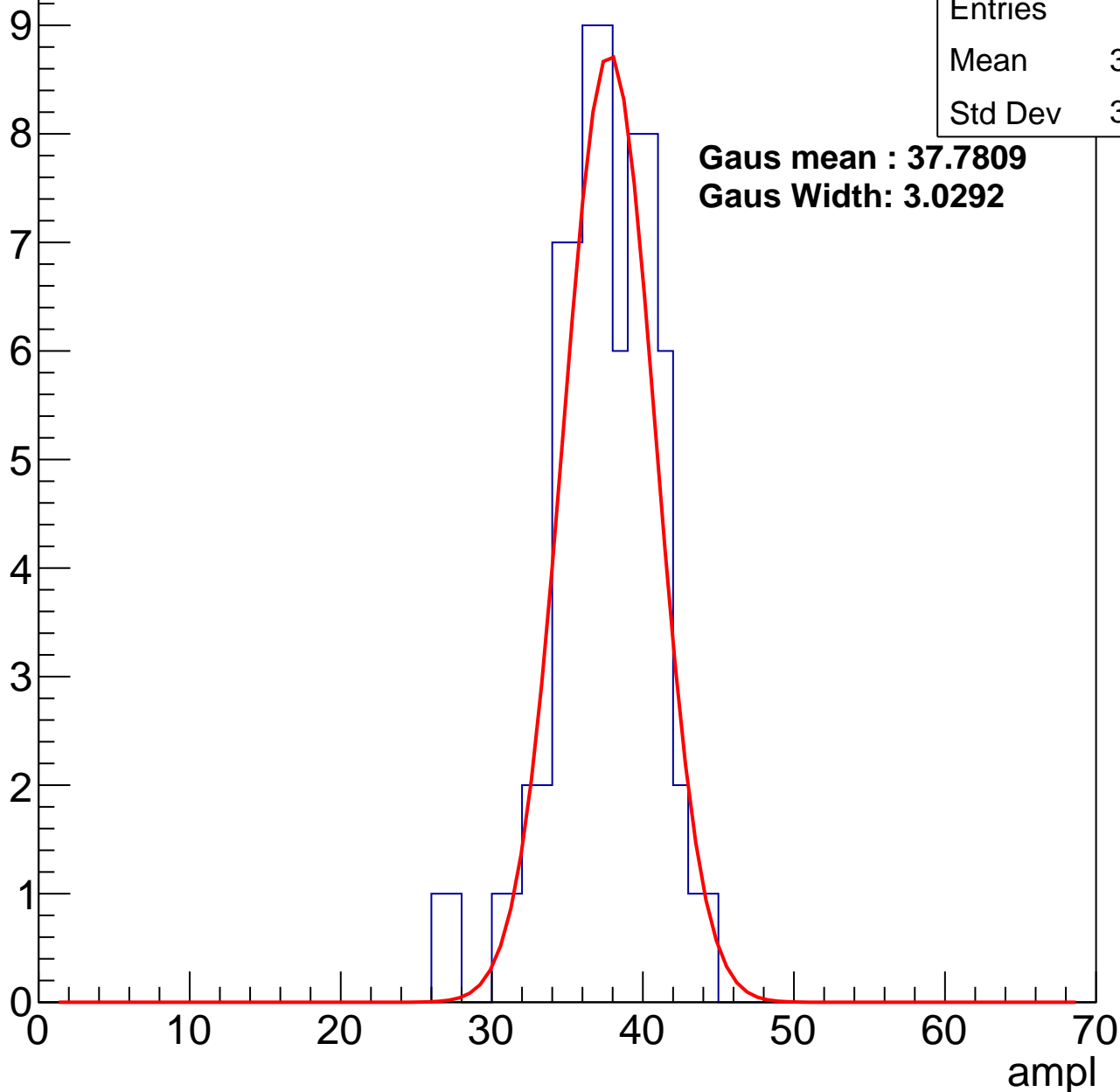
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.96
Std Dev	3.393

**Gaus mean : 37.7809**

**Gaus Width: 3.0292**



# B1L102S, U24-ch27, adc2

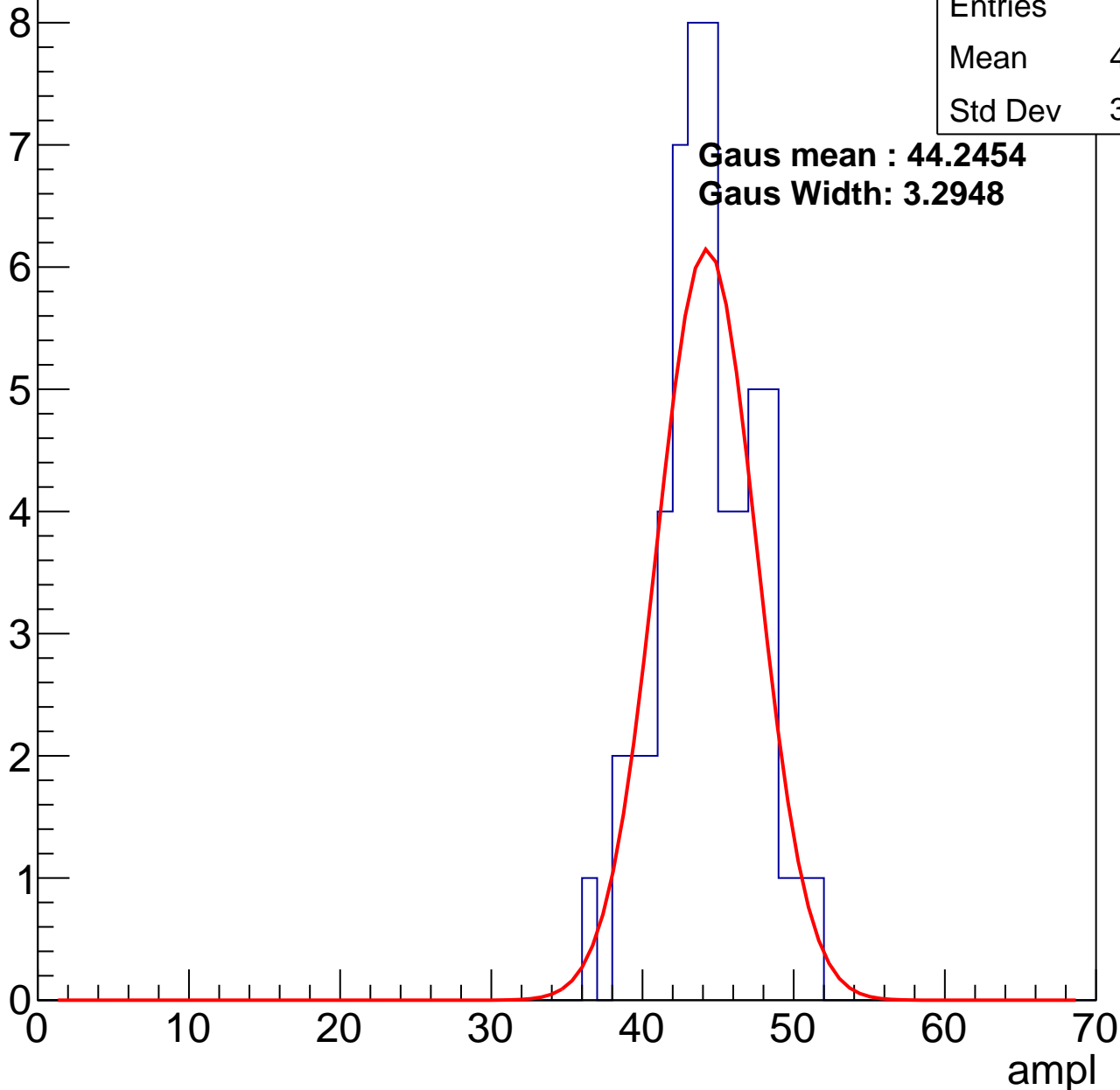
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.87
Std Dev	3.145

**Gaus mean : 44.2454**

**Gaus Width: 3.2948**

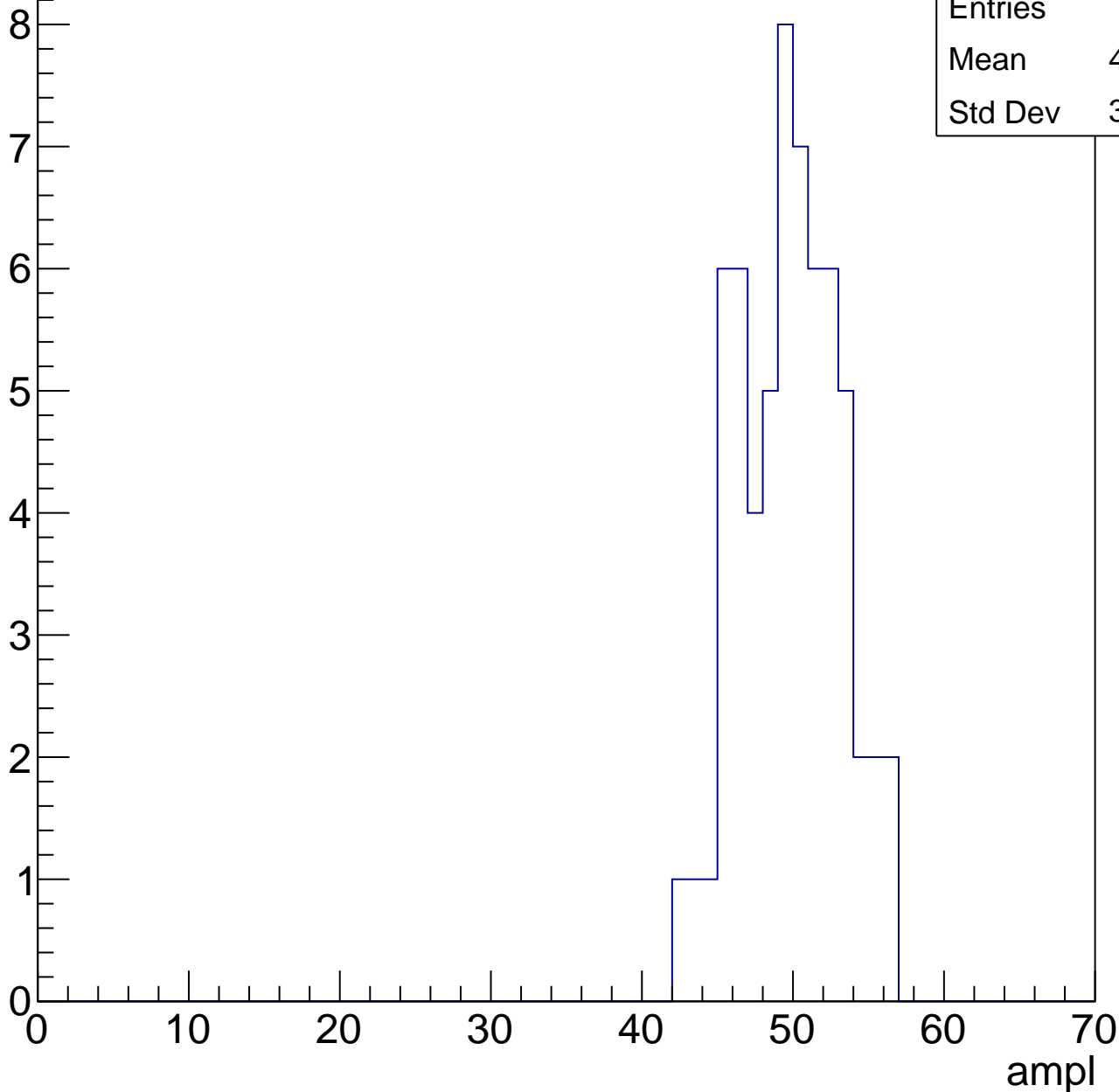


# B1L102S, U24-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.32
Std Dev	3.262

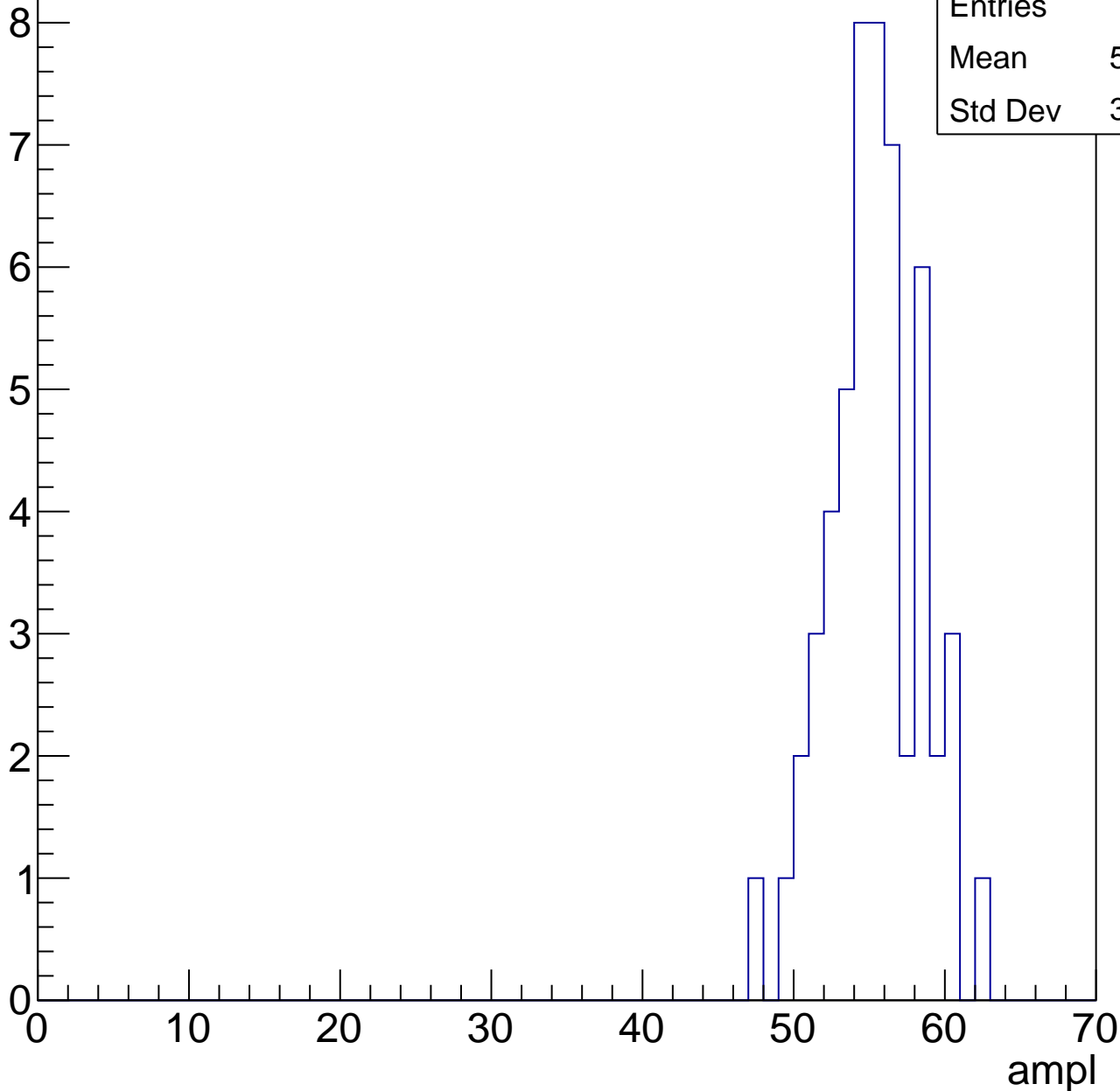


# B1L102S, U24-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

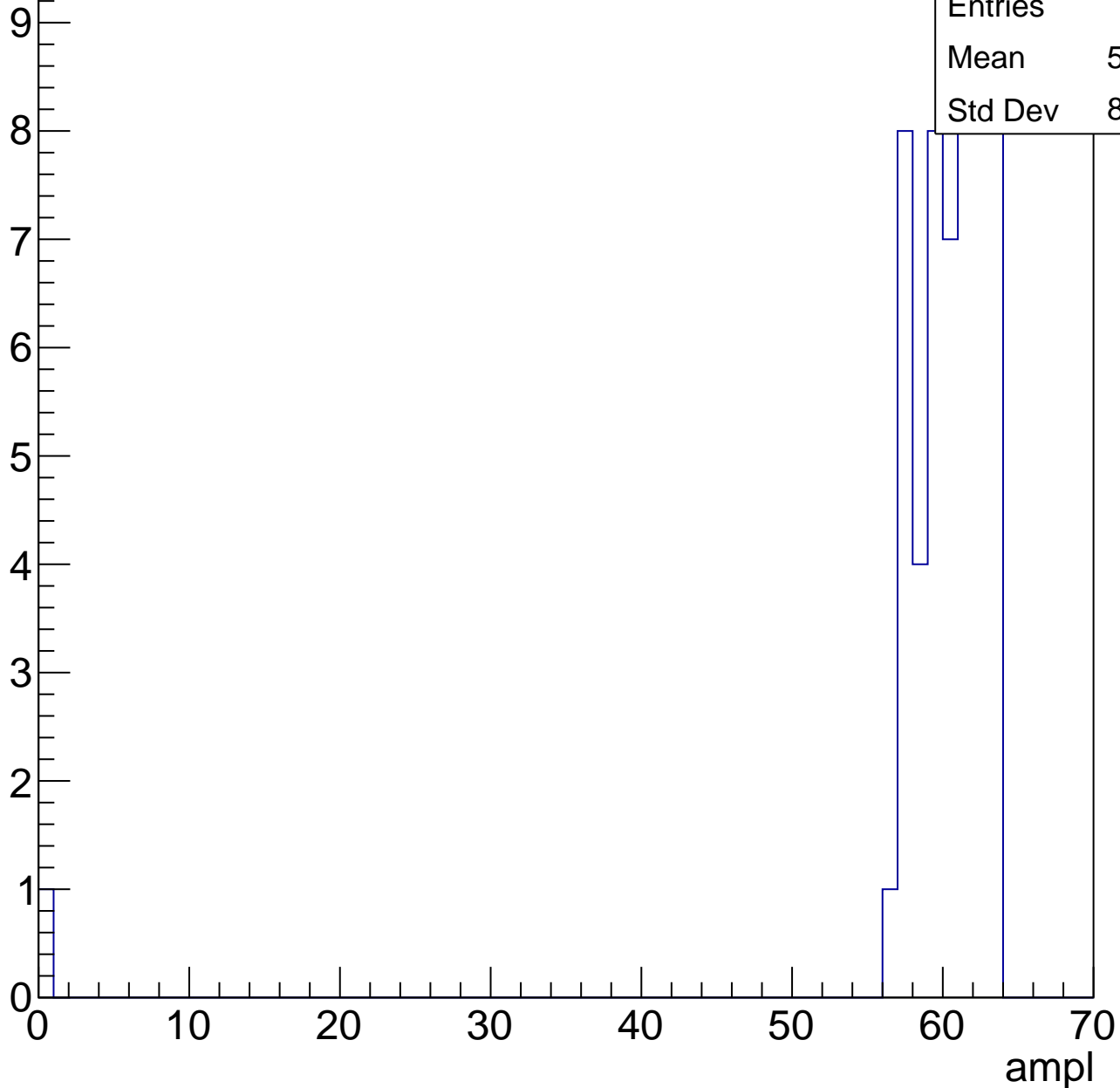
Entries	53
Mean	54.87
Std Dev	3.028



# B1L102S, U24-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L102S, U24-ch28, adc0

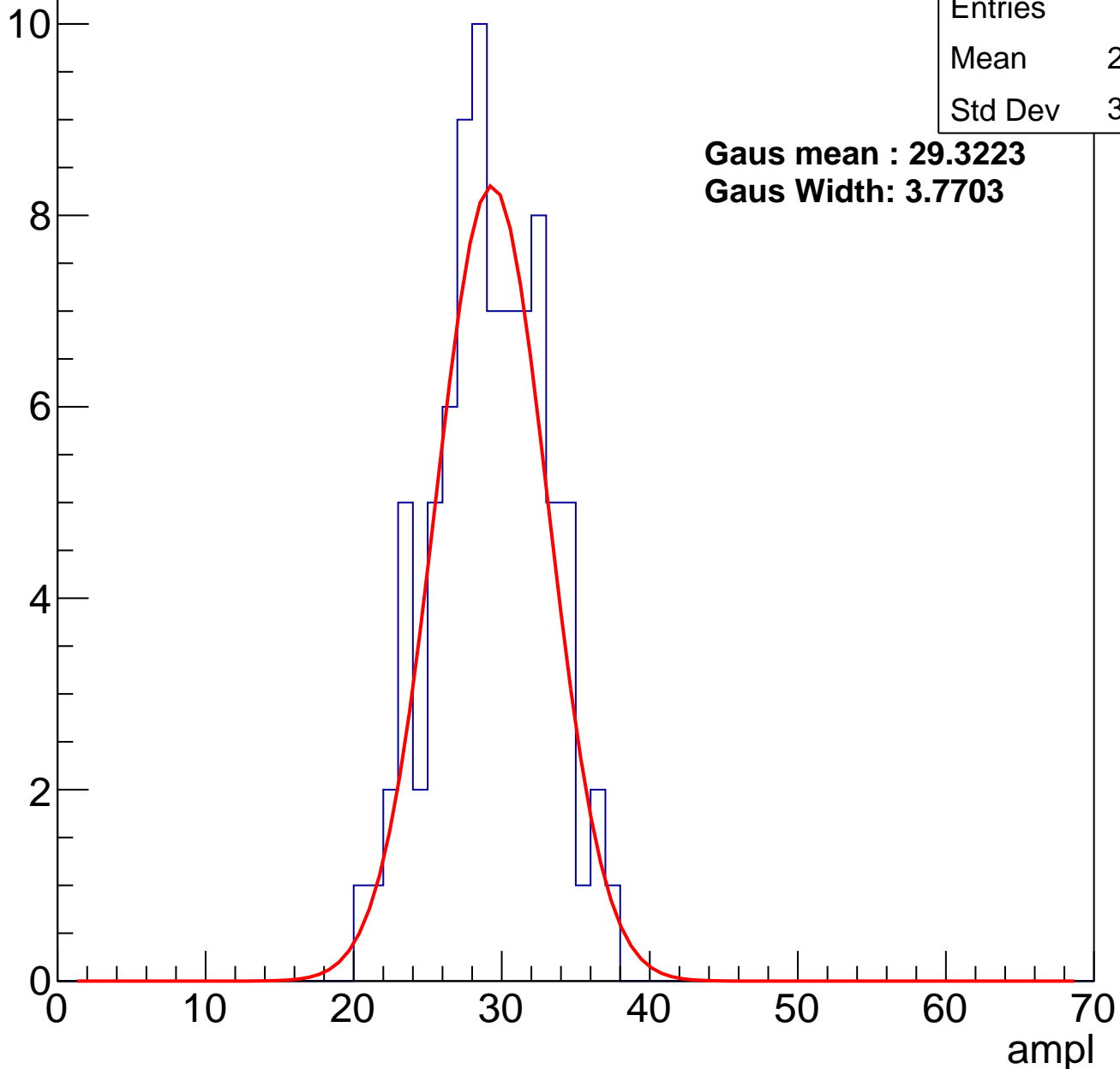
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	84
Mean	28.77
Std Dev	3.714

**Gaus mean : 29.3223**

**Gaus Width: 3.7703**

Entry



# B1L102S, U24-ch28, adc1

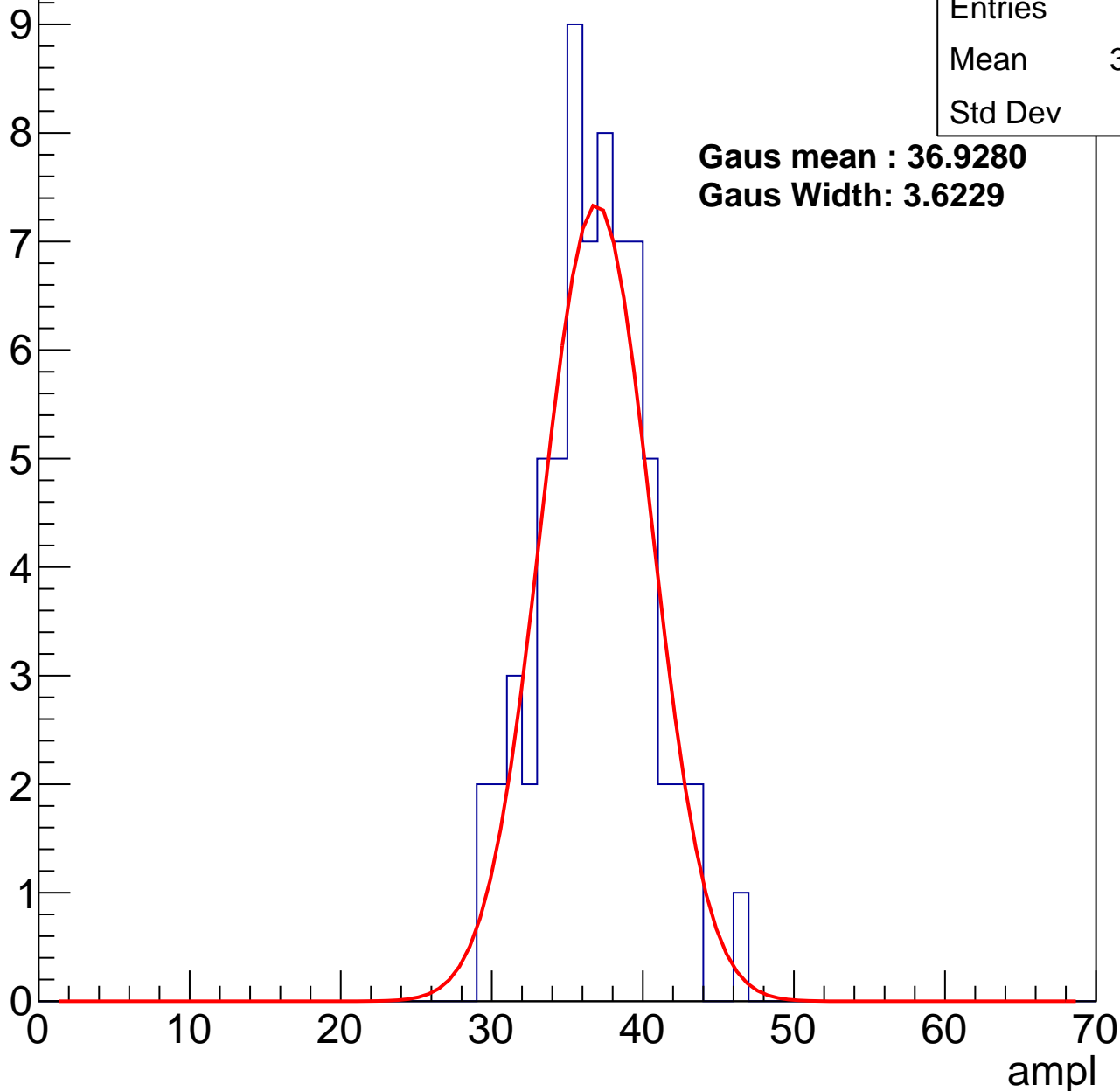
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.38
Std Dev	3.49

**Gaus mean : 36.9280**

**Gaus Width: 3.6229**



# B1L102S, U24-ch28, adc2

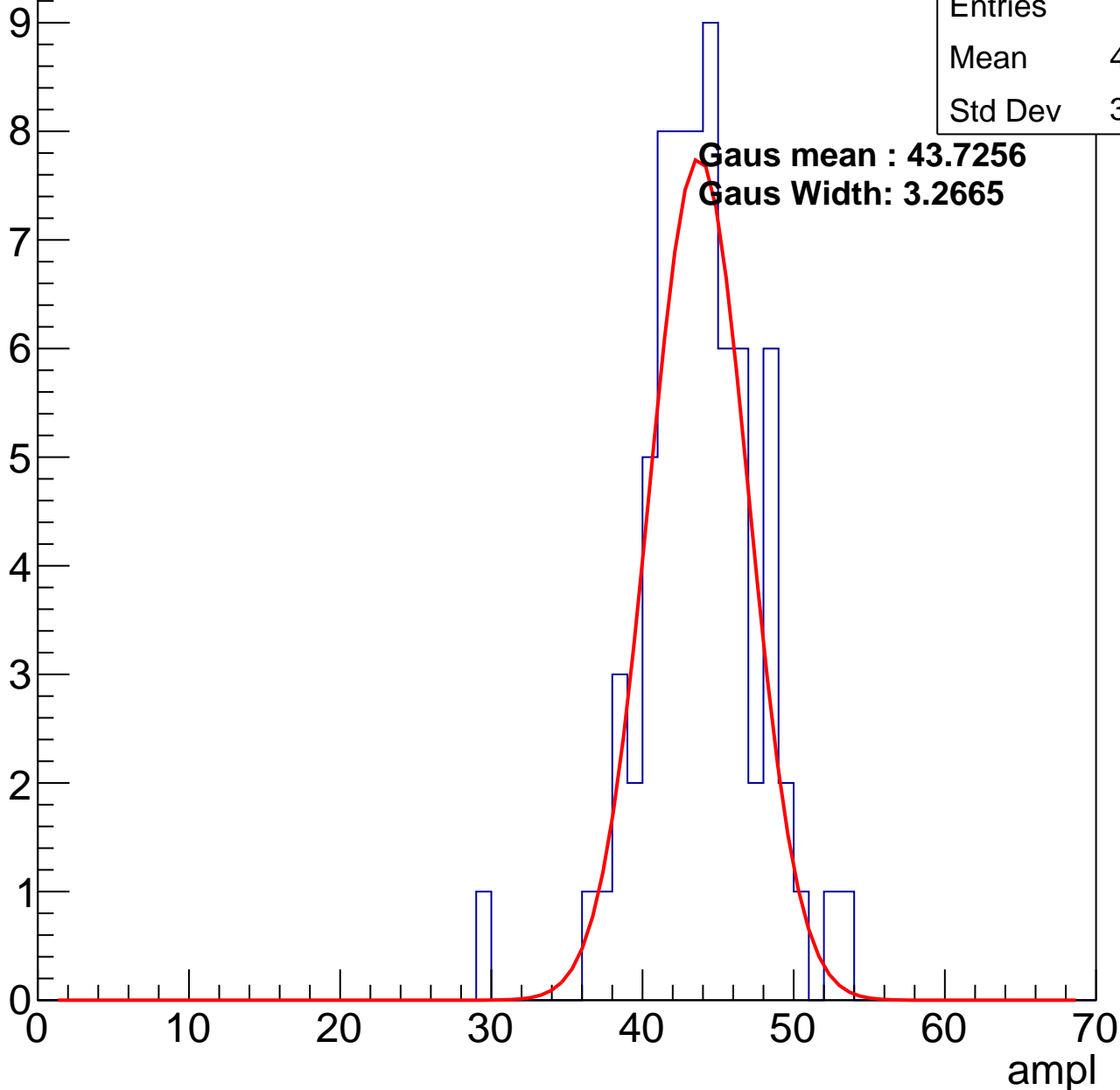
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.37
Std Dev	3.817

**Gaus mean : 43.7256**

**Gaus Width: 3.2665**

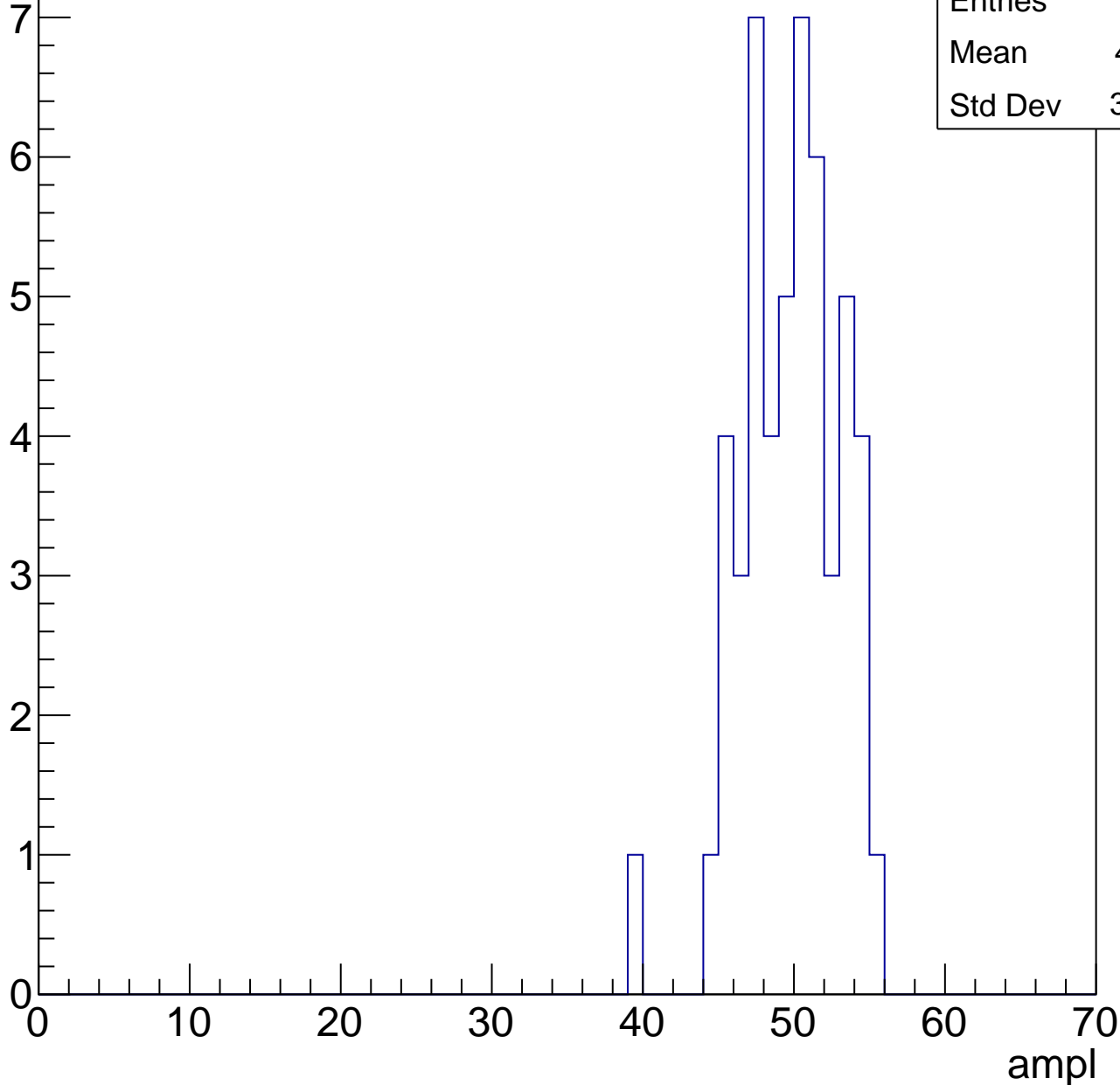


# B1L102S, U24-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.31
Std Dev	3.184

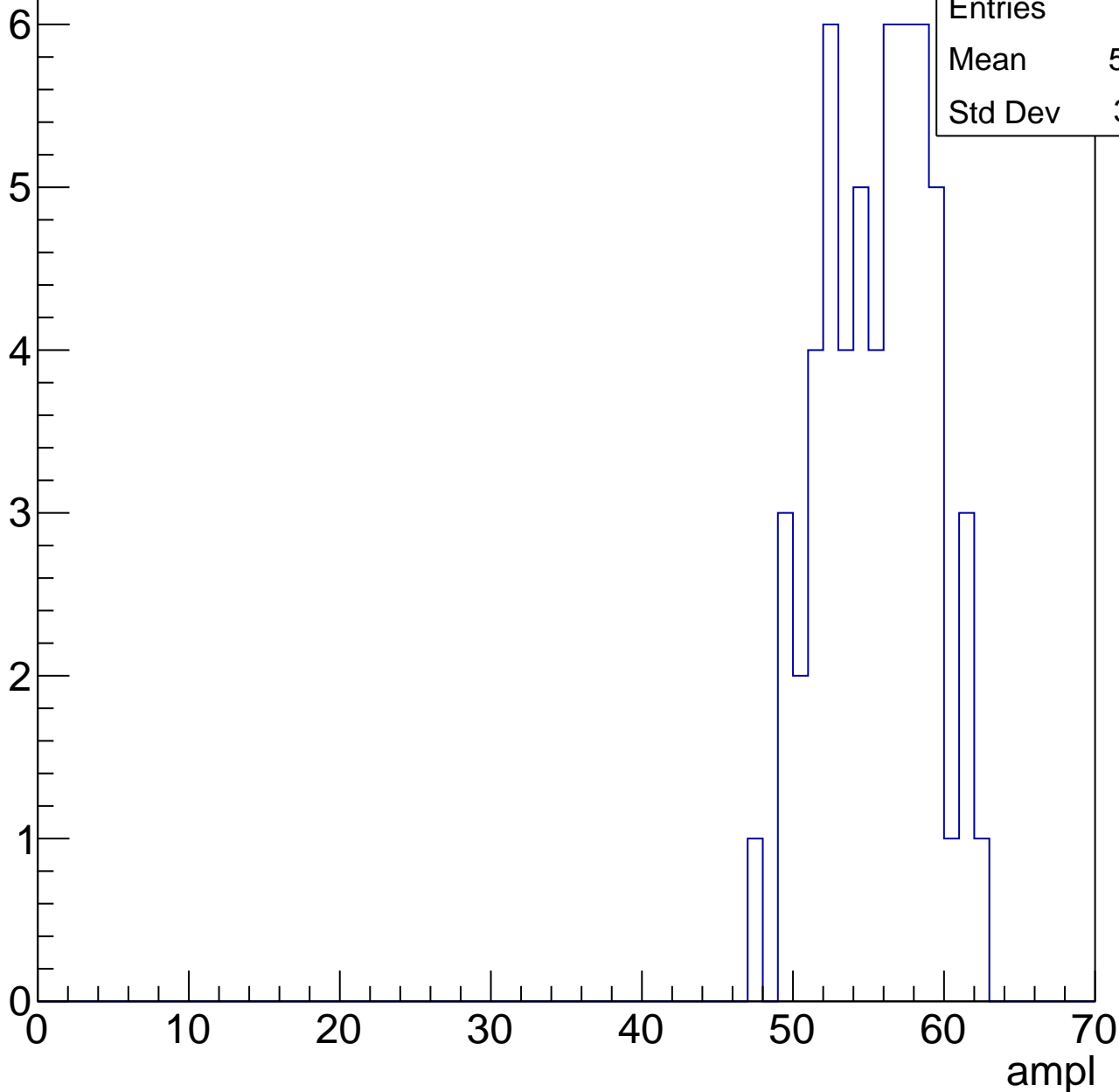


# B1L102S, U24-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.05
Std Dev	3.521

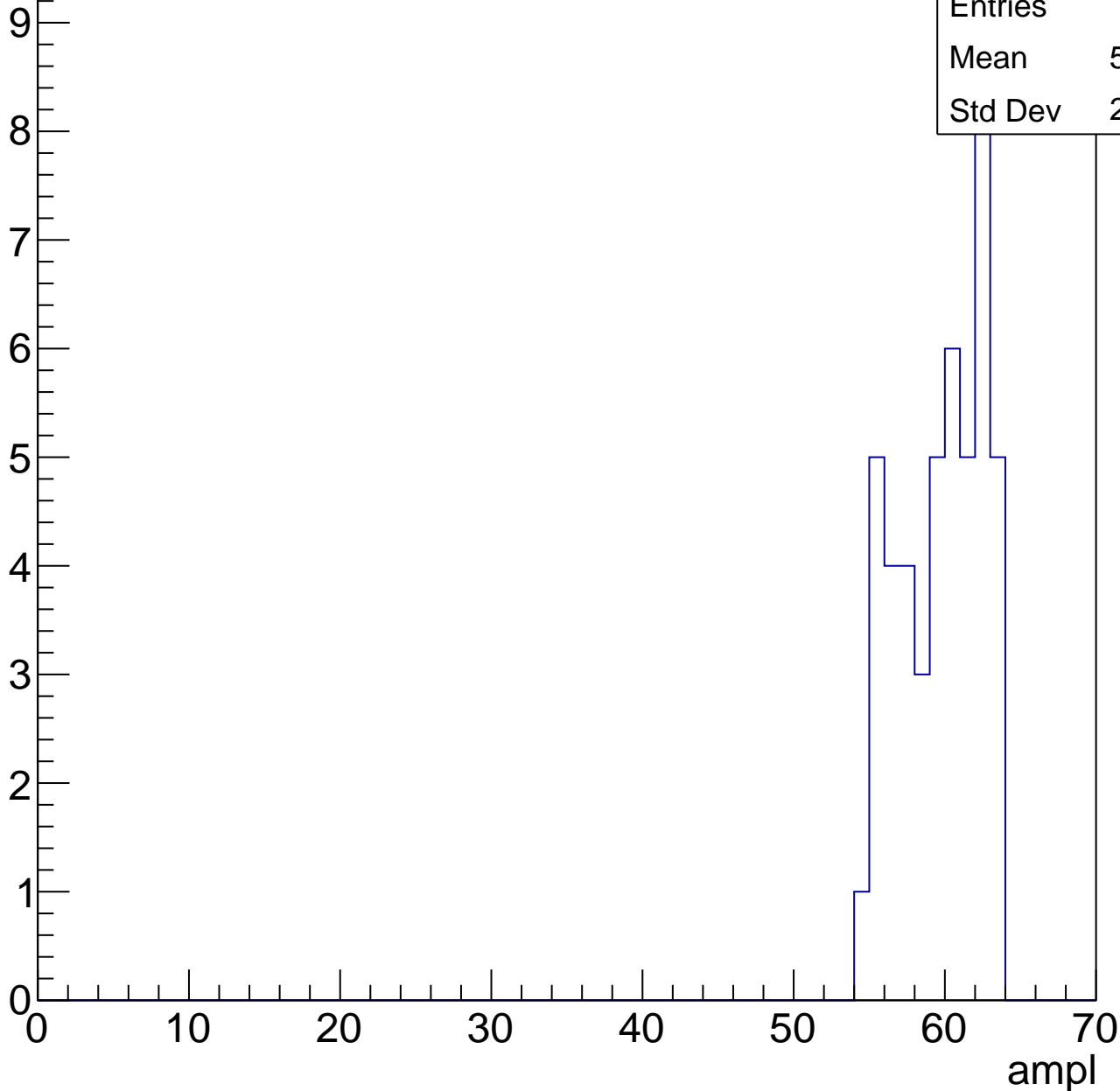


# B1L102S, U24-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

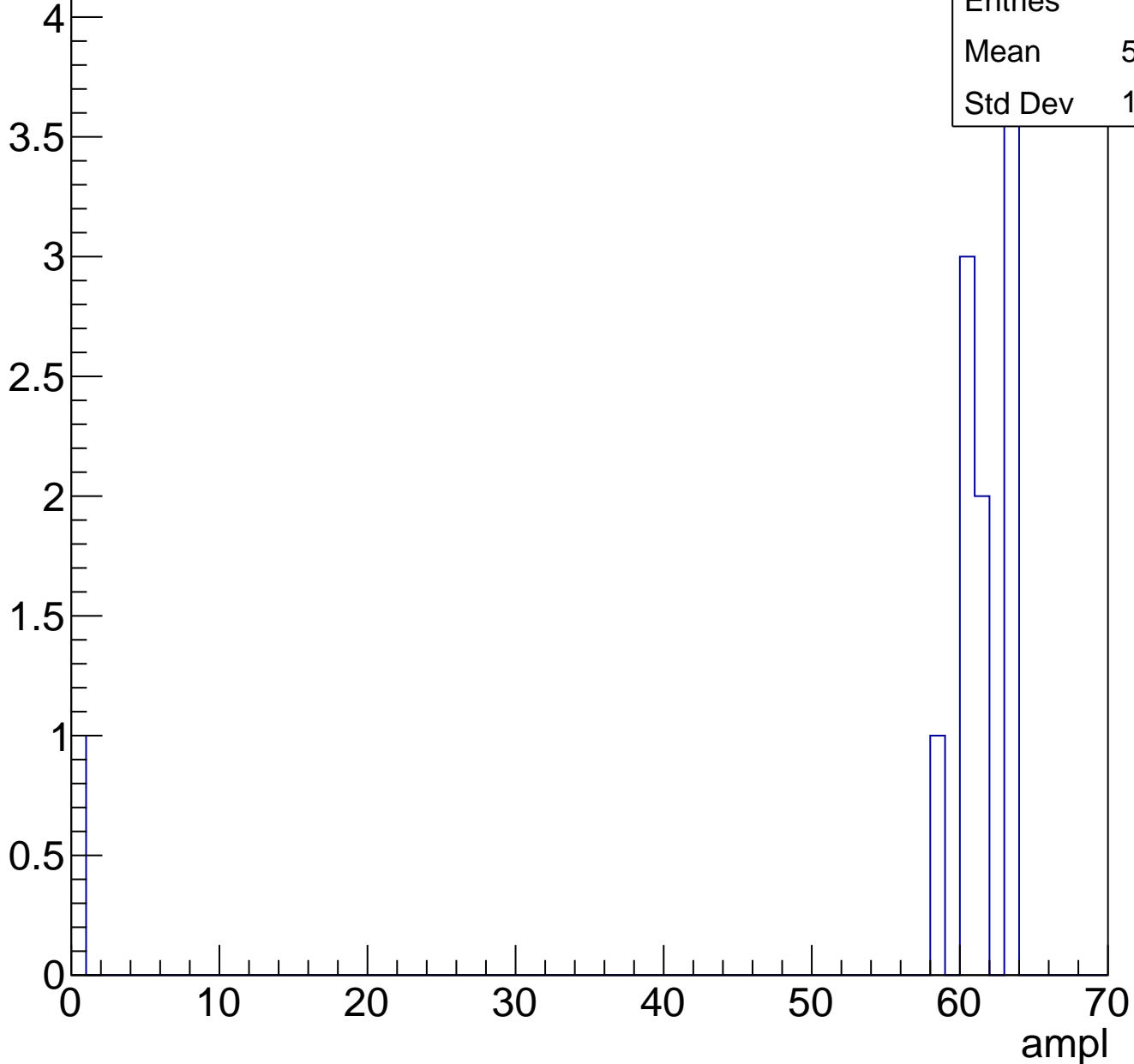
Entries	47
Mean	59.32
Std Dev	2.698



# B1L102S, U24-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

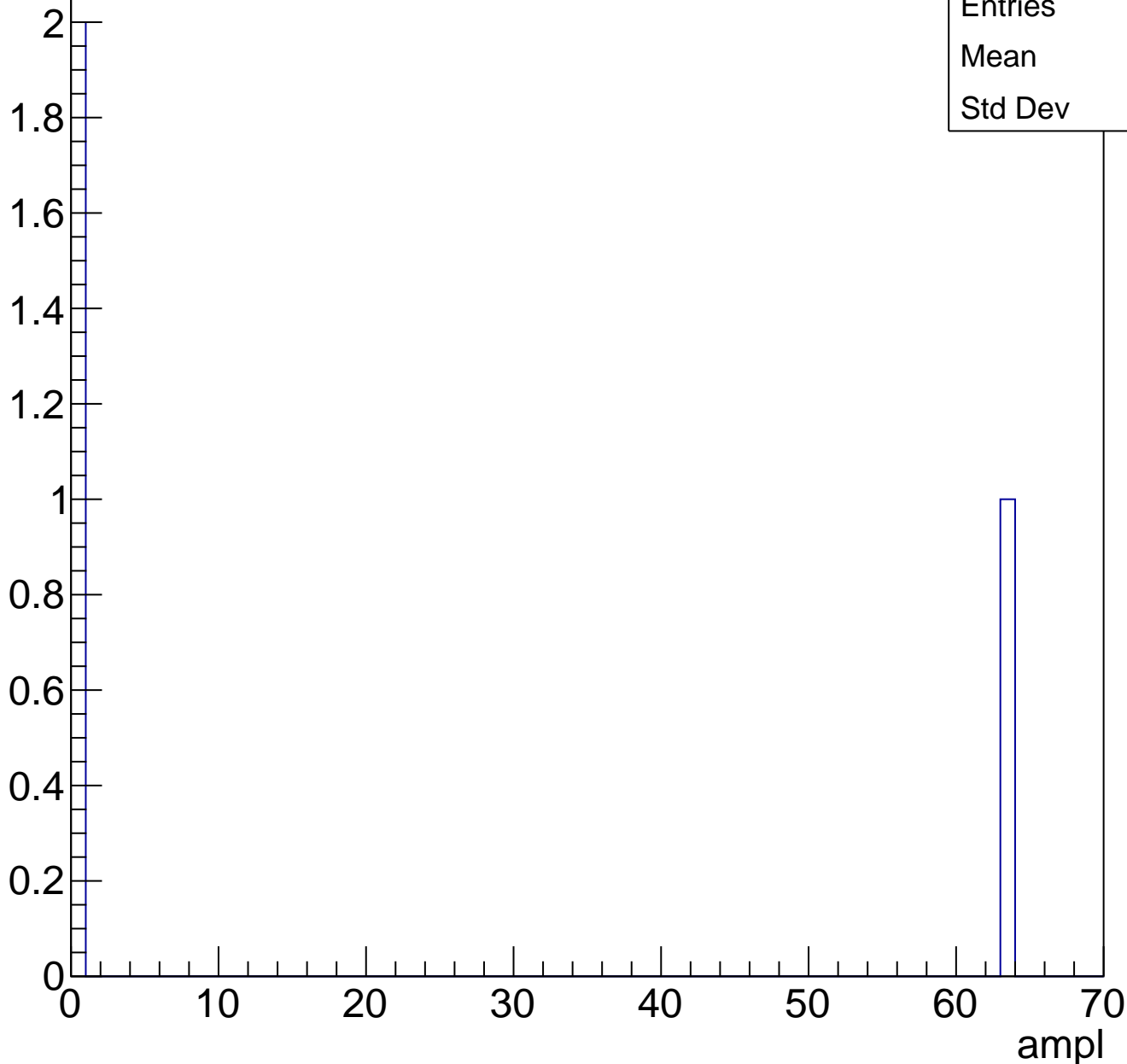




# B1L102S, U24-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch29, adc0

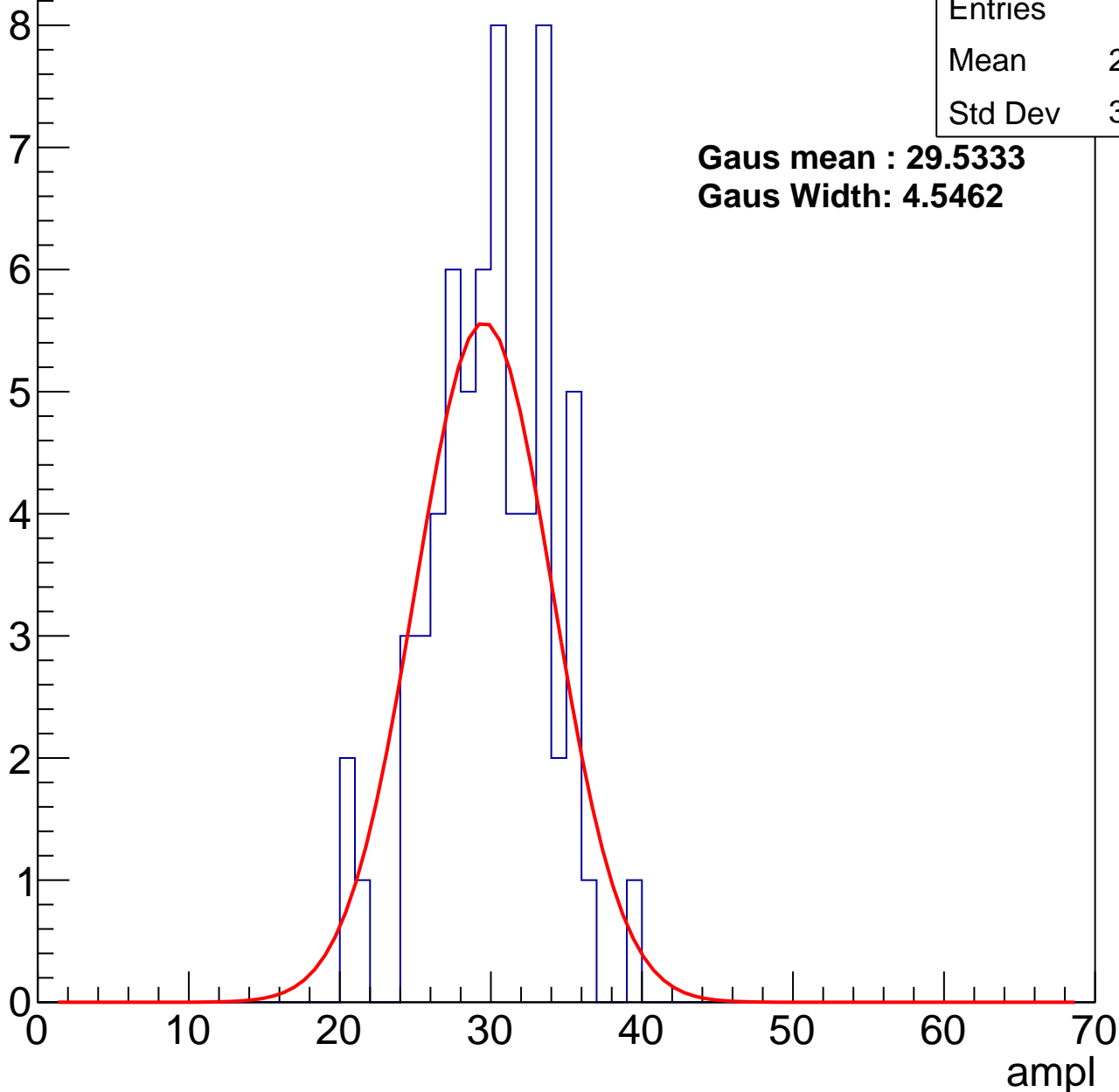
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.56
Std Dev	3.907

**Gaus mean : 29.5333**

**Gaus Width: 4.5462**



# B1L102S, U24-ch29, adc1

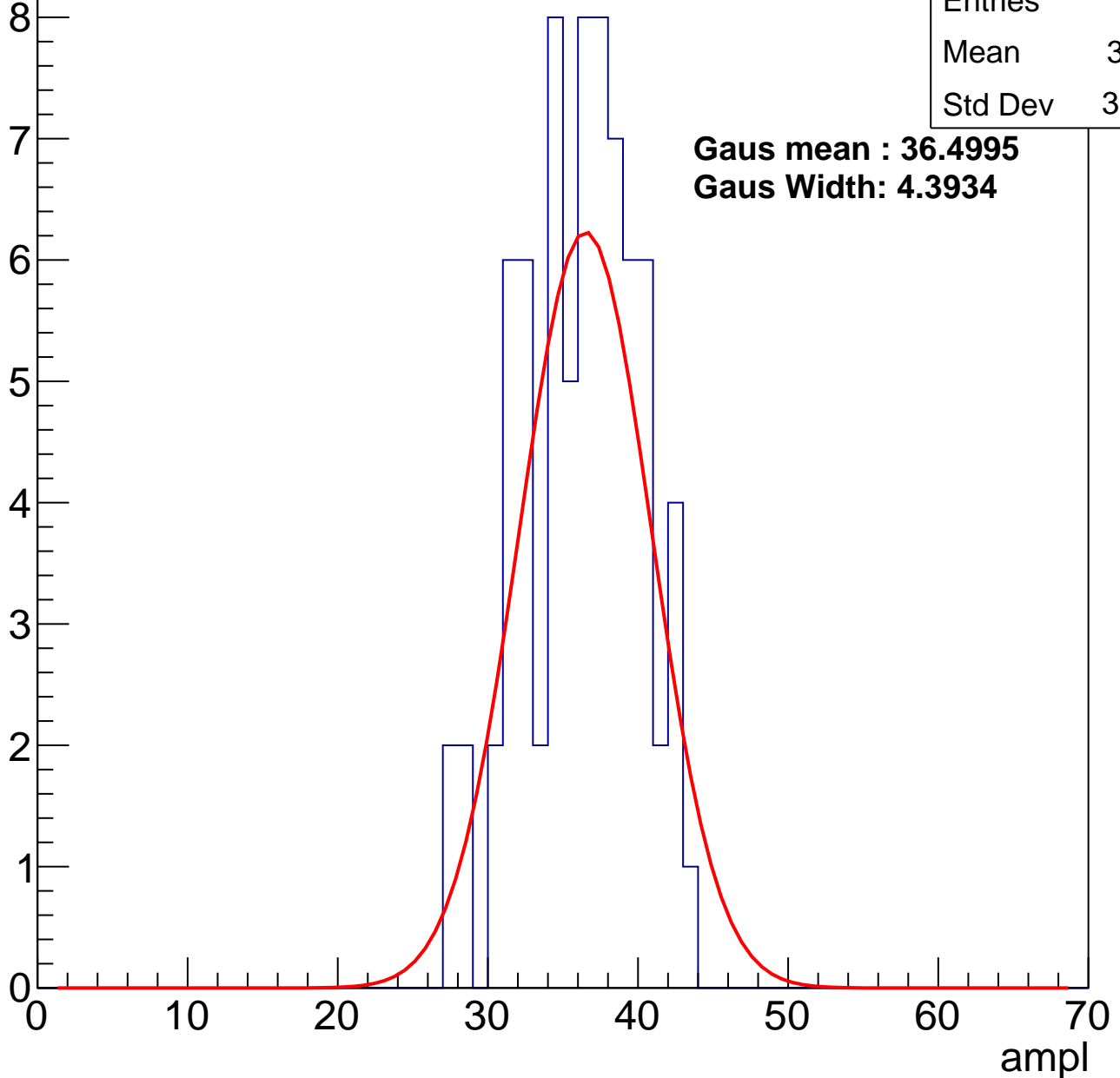
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.71
Std Dev	3.804

**Gaus mean : 36.4995**

**Gaus Width: 4.3934**



# B1L102S, U24-ch29, adc2

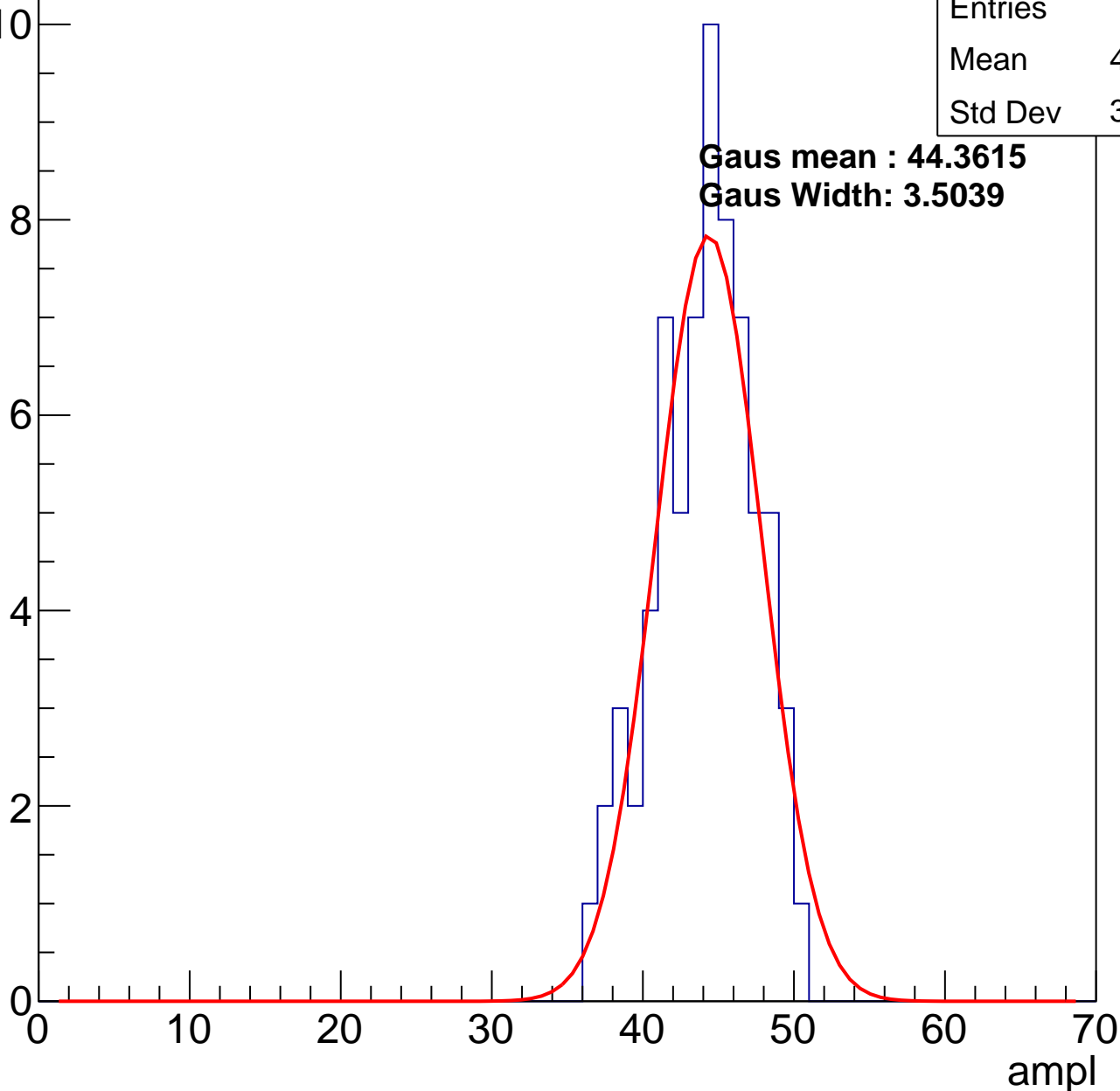
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.63
Std Dev	3.243

**Gaus mean : 44.3615**

**Gaus Width: 3.5039**

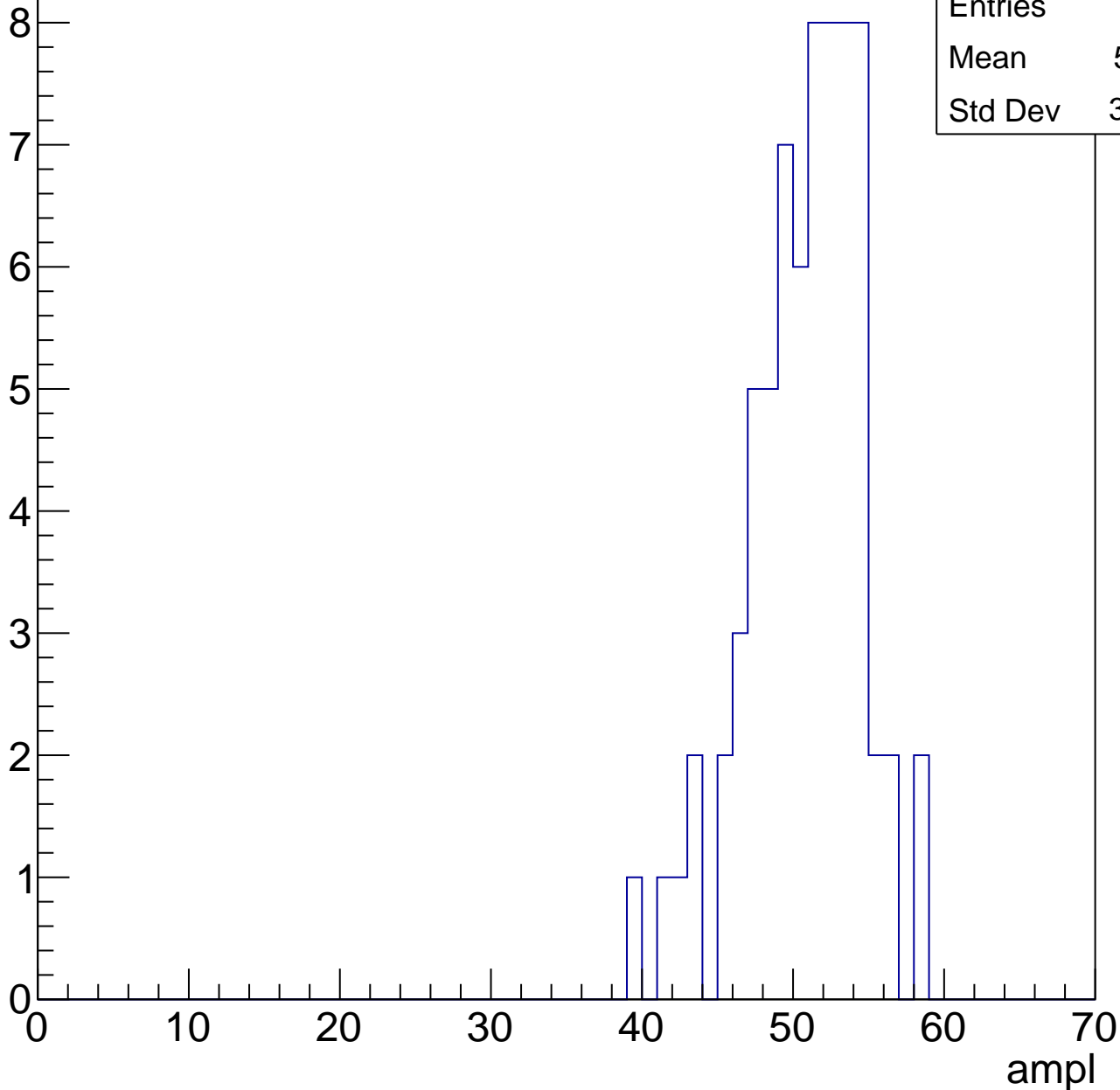


# B1L102S, U24-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

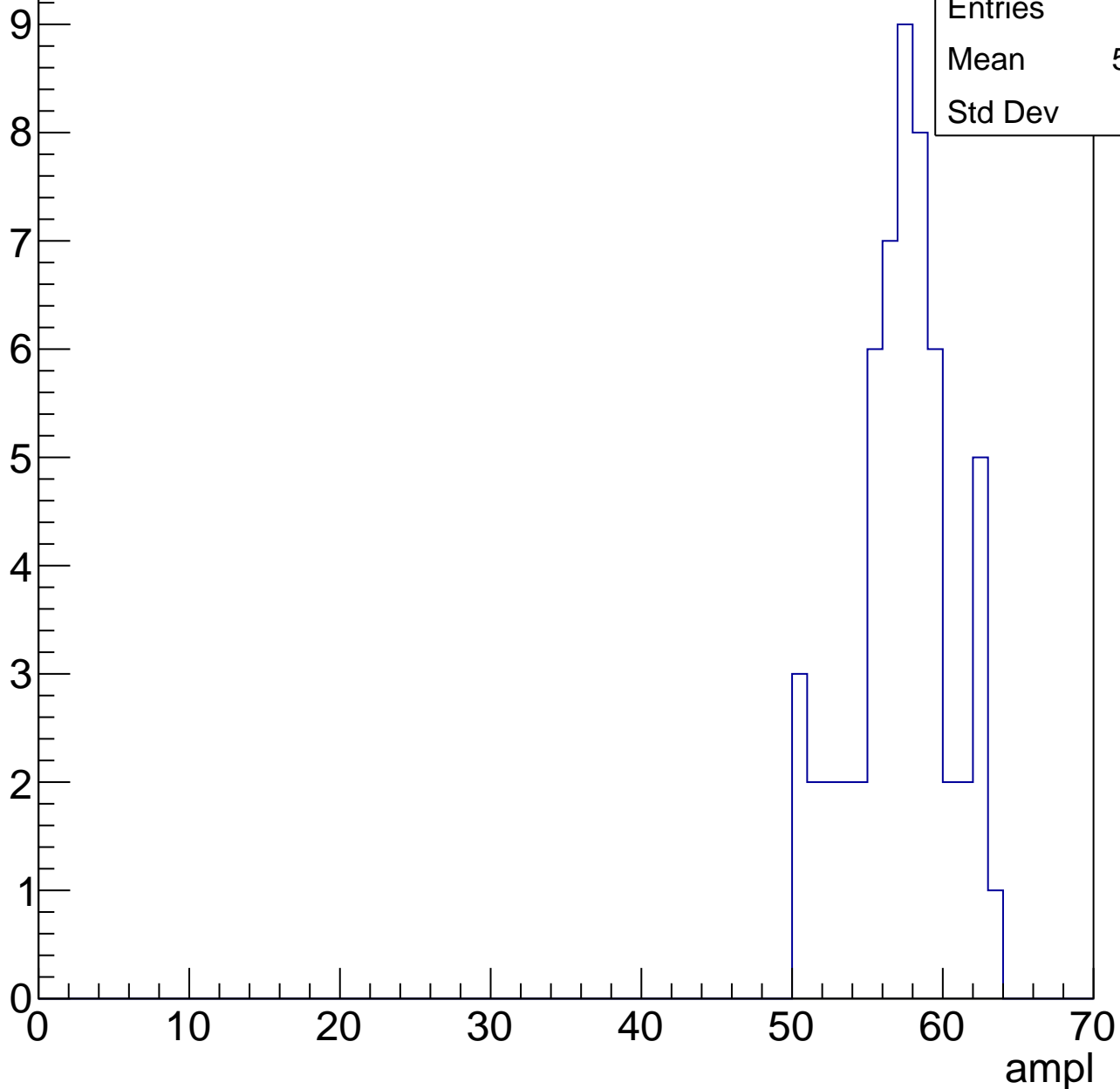
Entries	71
Mean	50.31
Std Dev	3.785



# B1L102S, U24-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

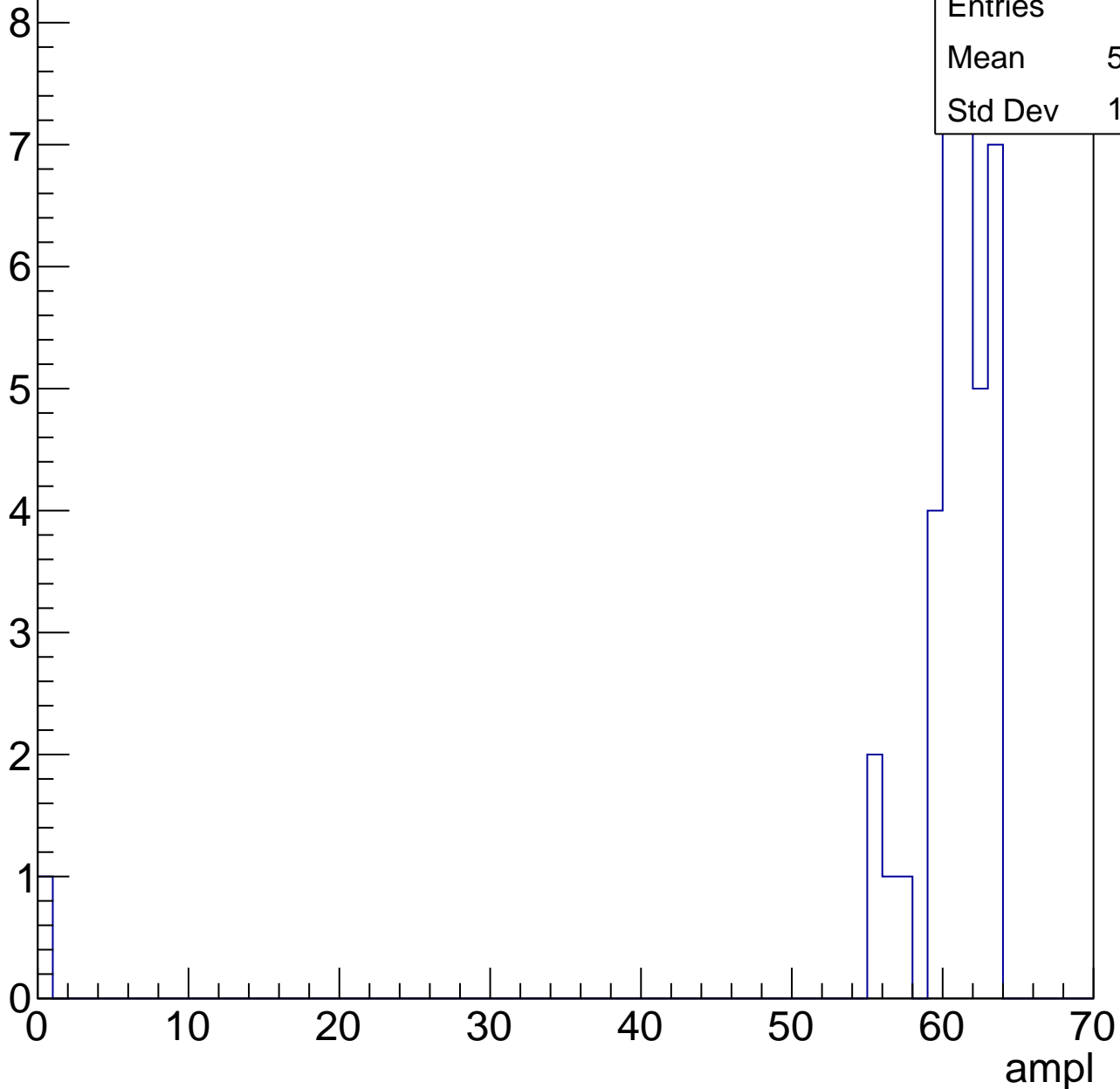


# B1L102S, U24-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.86
Std Dev	10.03



# B1L102S, U24-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch30, adc0

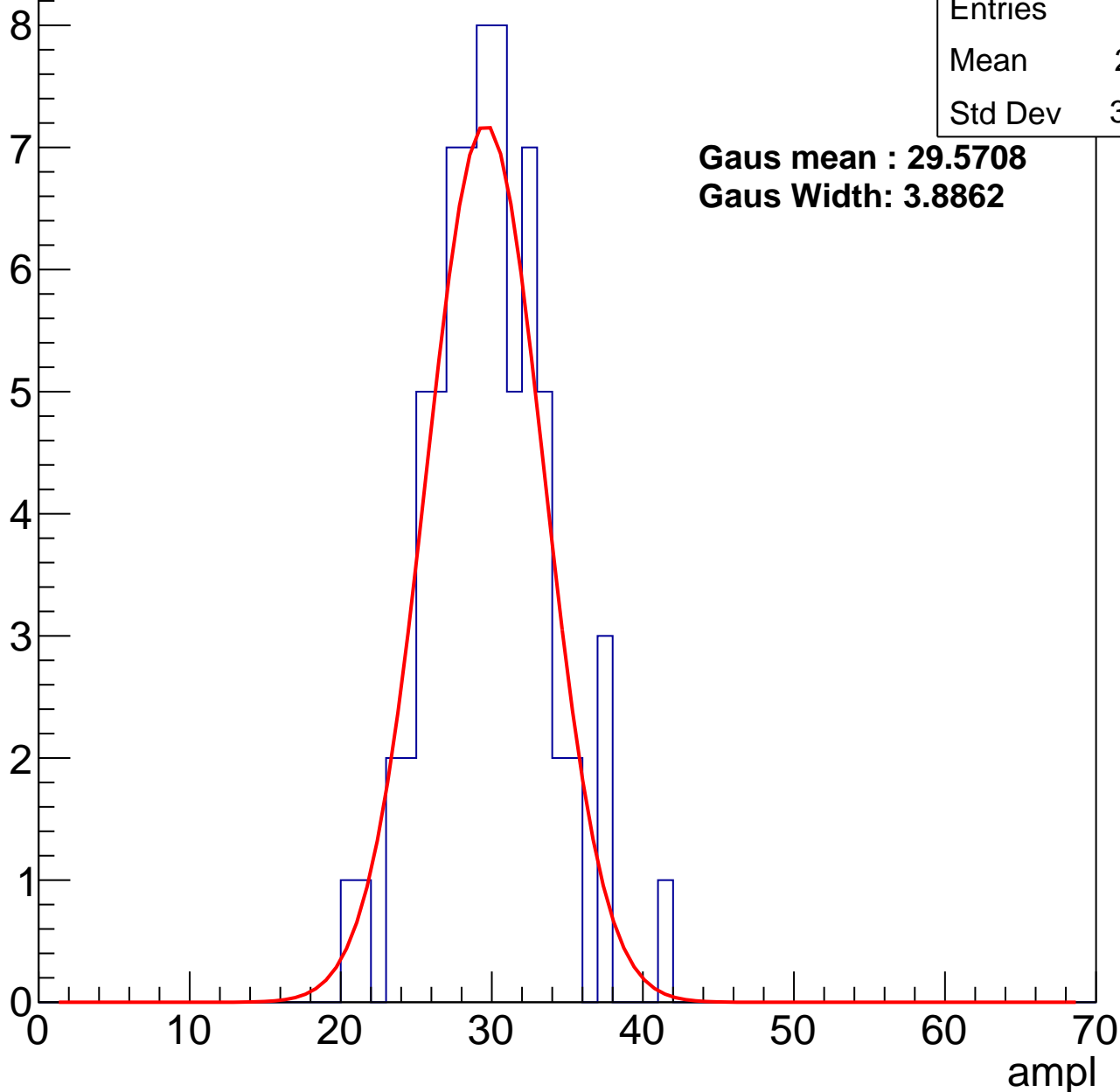
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.31
Std Dev	3.844

**Gaus mean : 29.5708**

**Gaus Width: 3.8862**



# B1L102S, U24-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	95
Mean	37.39
Std Dev	3.492

**Gaus mean : 37.9342**

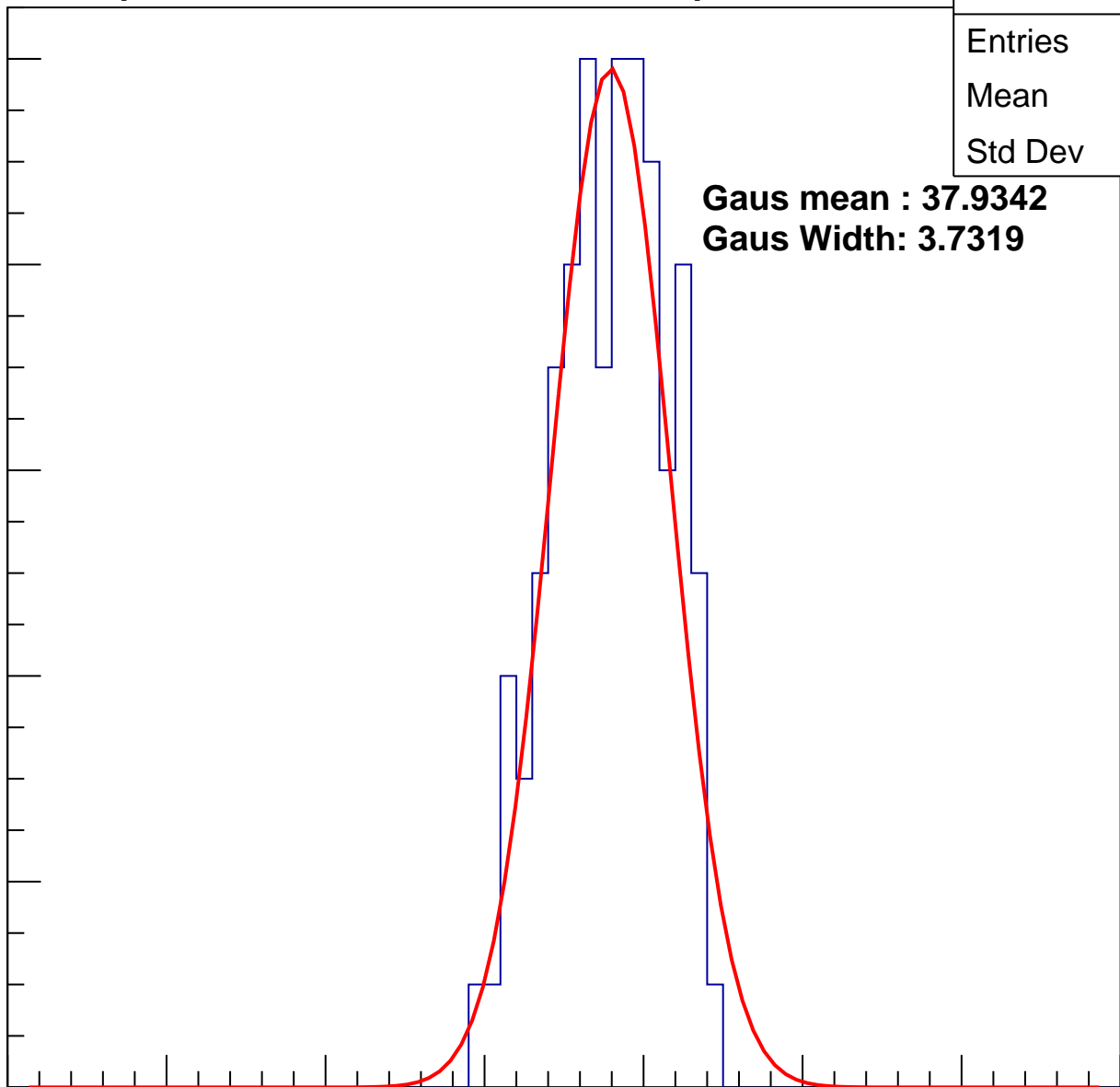
**Gaus Width: 3.7319**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch30, adc2

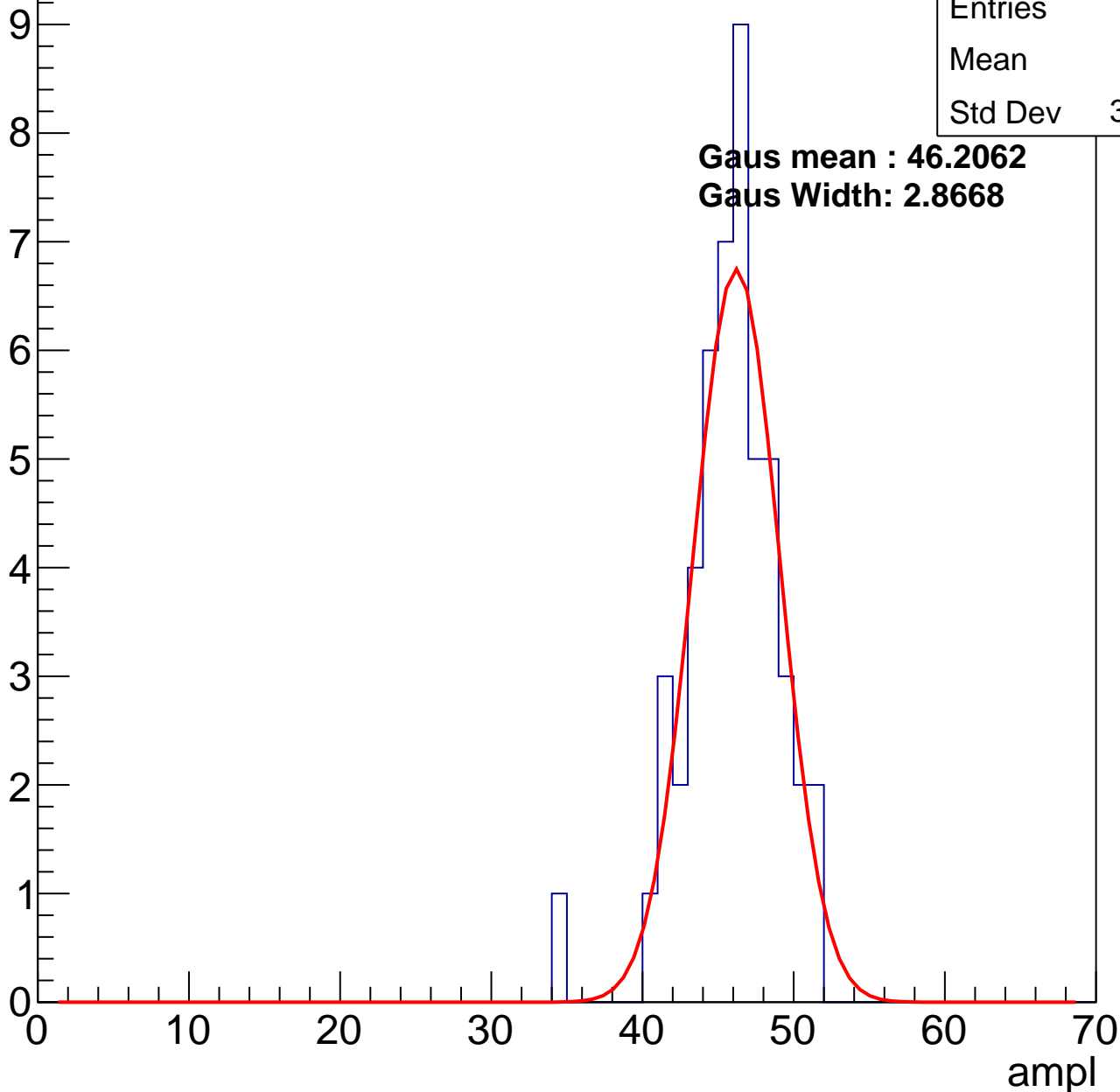
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	45.4
Std Dev	3.066

**Gaus mean : 46.2062**

**Gaus Width: 2.8668**

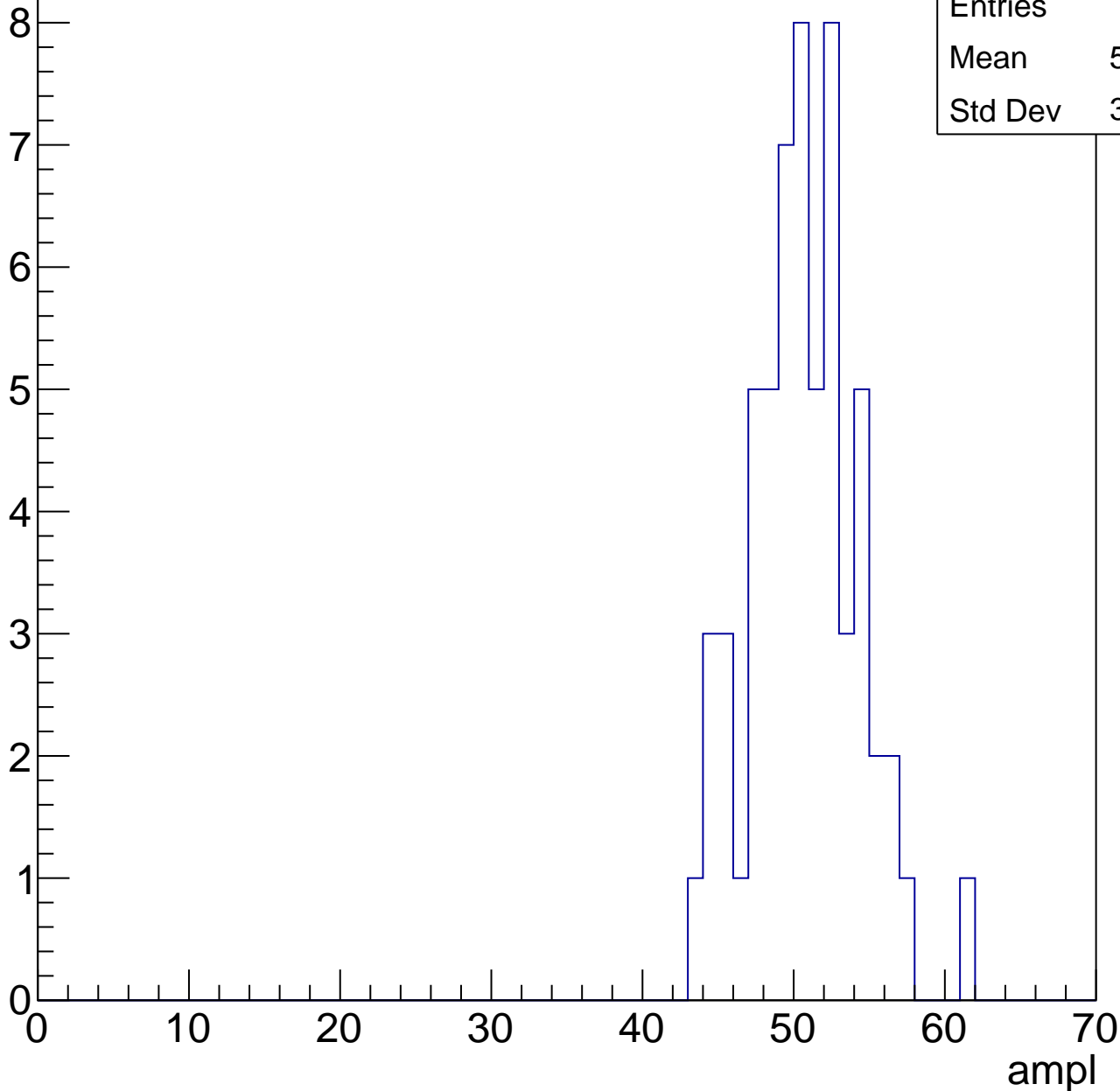


# B1L102S, U24-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.23
Std Dev	3.542

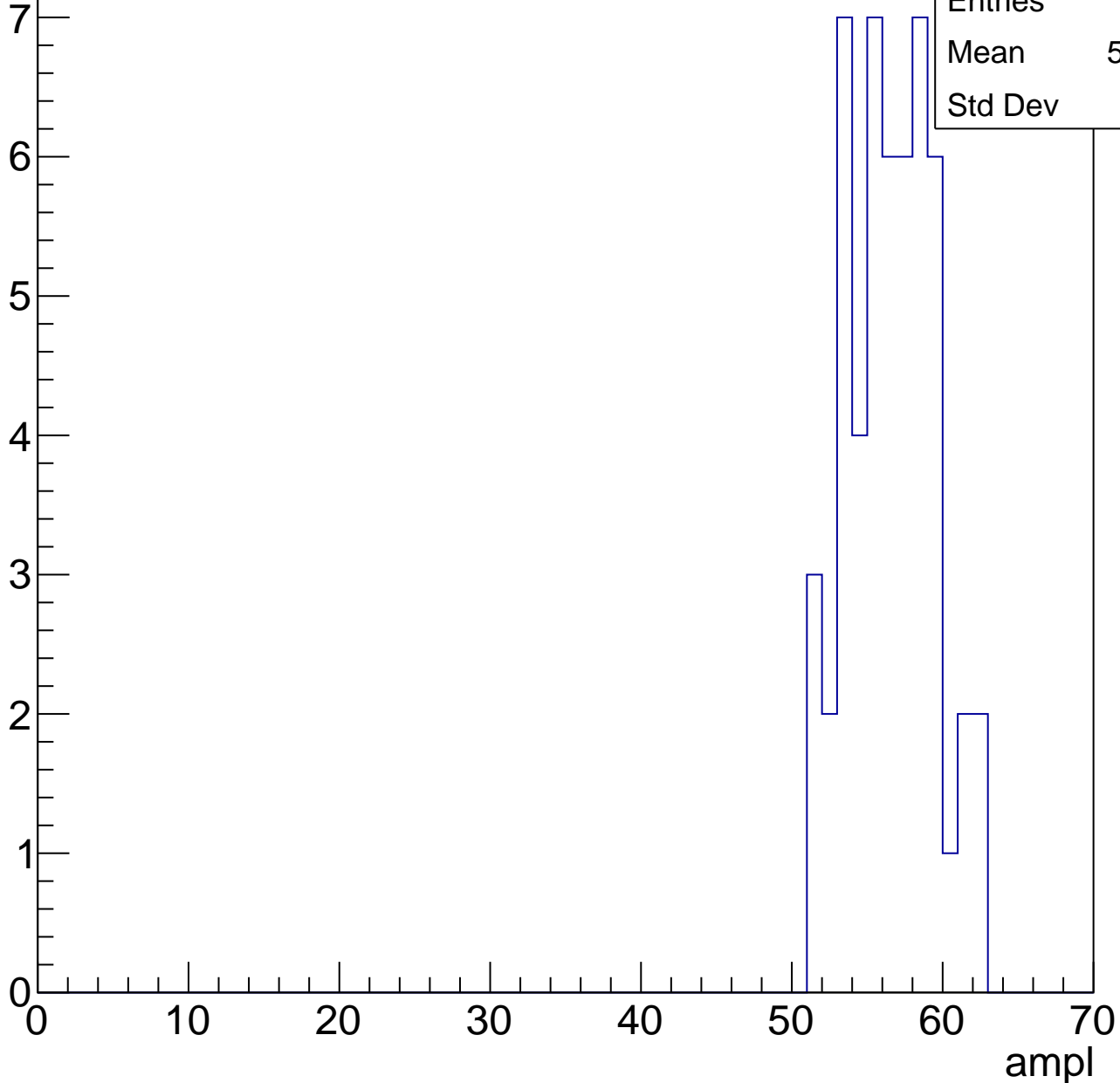


# B1L102S, U24-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.09
Std Dev	2.81



# B1L102S, U24-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.8
Std Dev	9.157

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

# B1L102S, U24-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch31, adc0

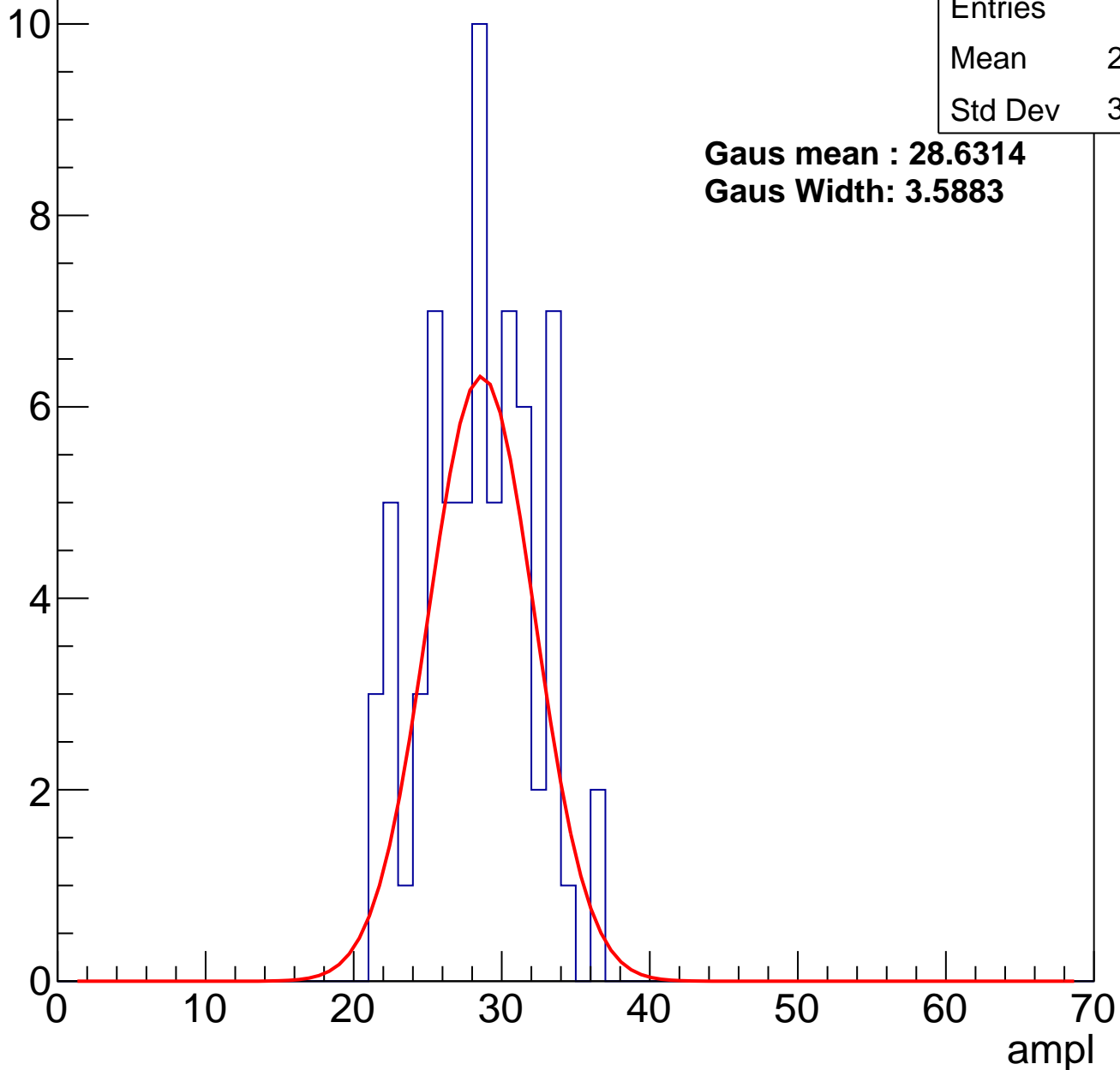
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	69
Mean	27.97
Std Dev	3.703

**Gaus mean : 28.6314**

**Gaus Width: 3.5883**

Entry



# B1L102S, U24-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	35.05
Std Dev	3.056

**Gaus mean : 35.6516**

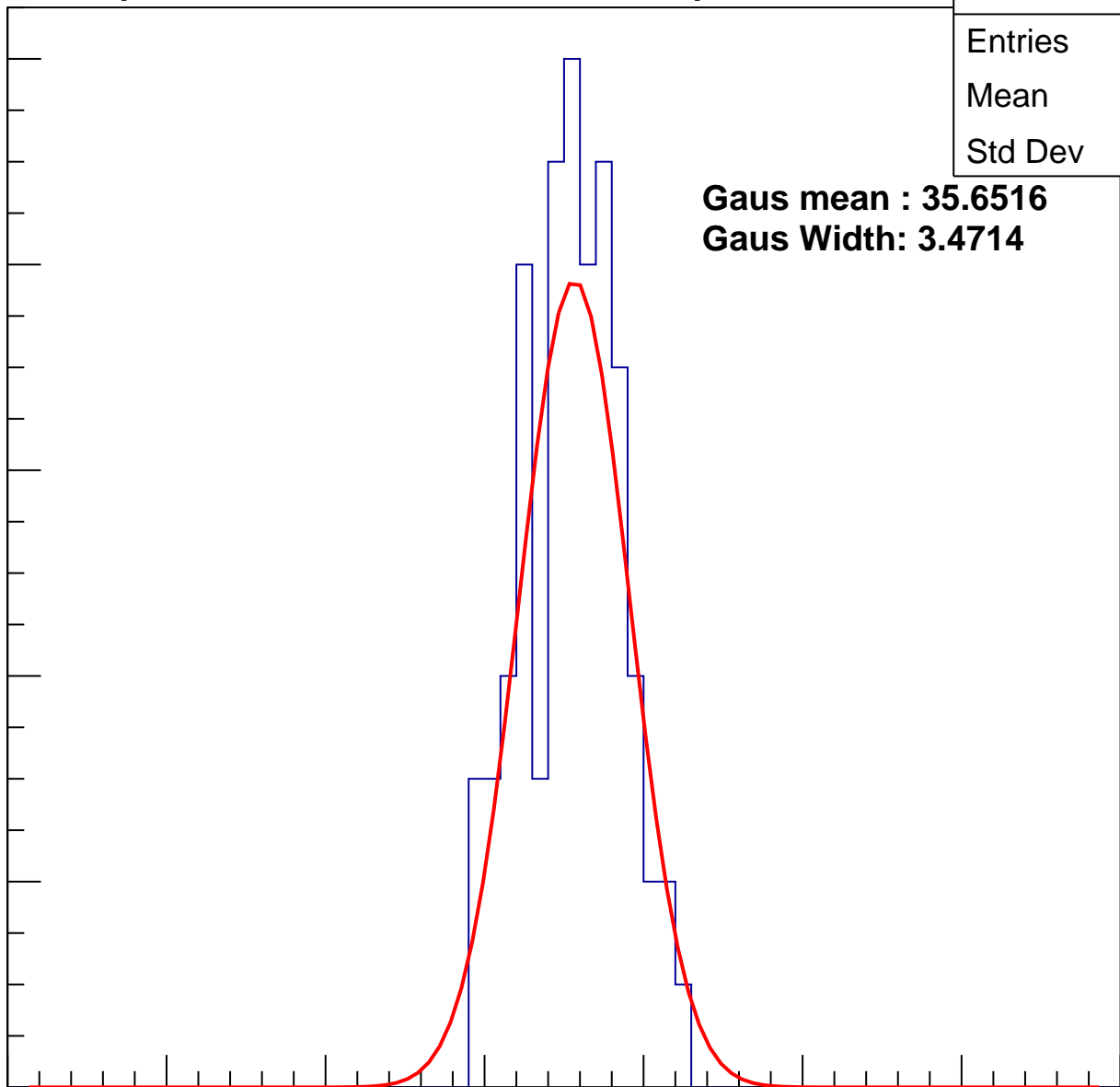
**Gaus Width: 3.4714**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch31, adc2

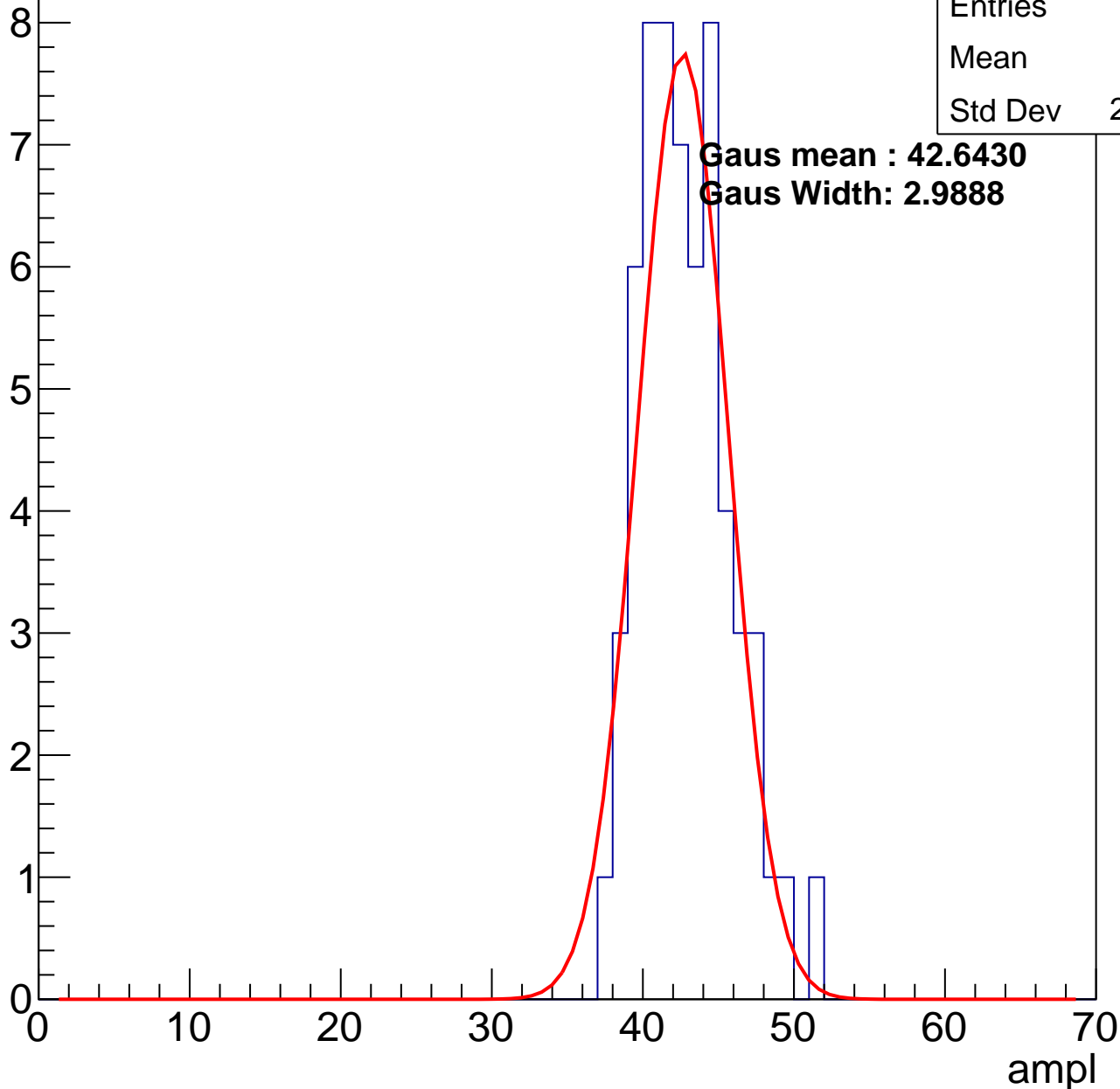
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.4
Std Dev	2.945

**Gaus mean : 42.6430**

**Gaus Width: 2.9888**

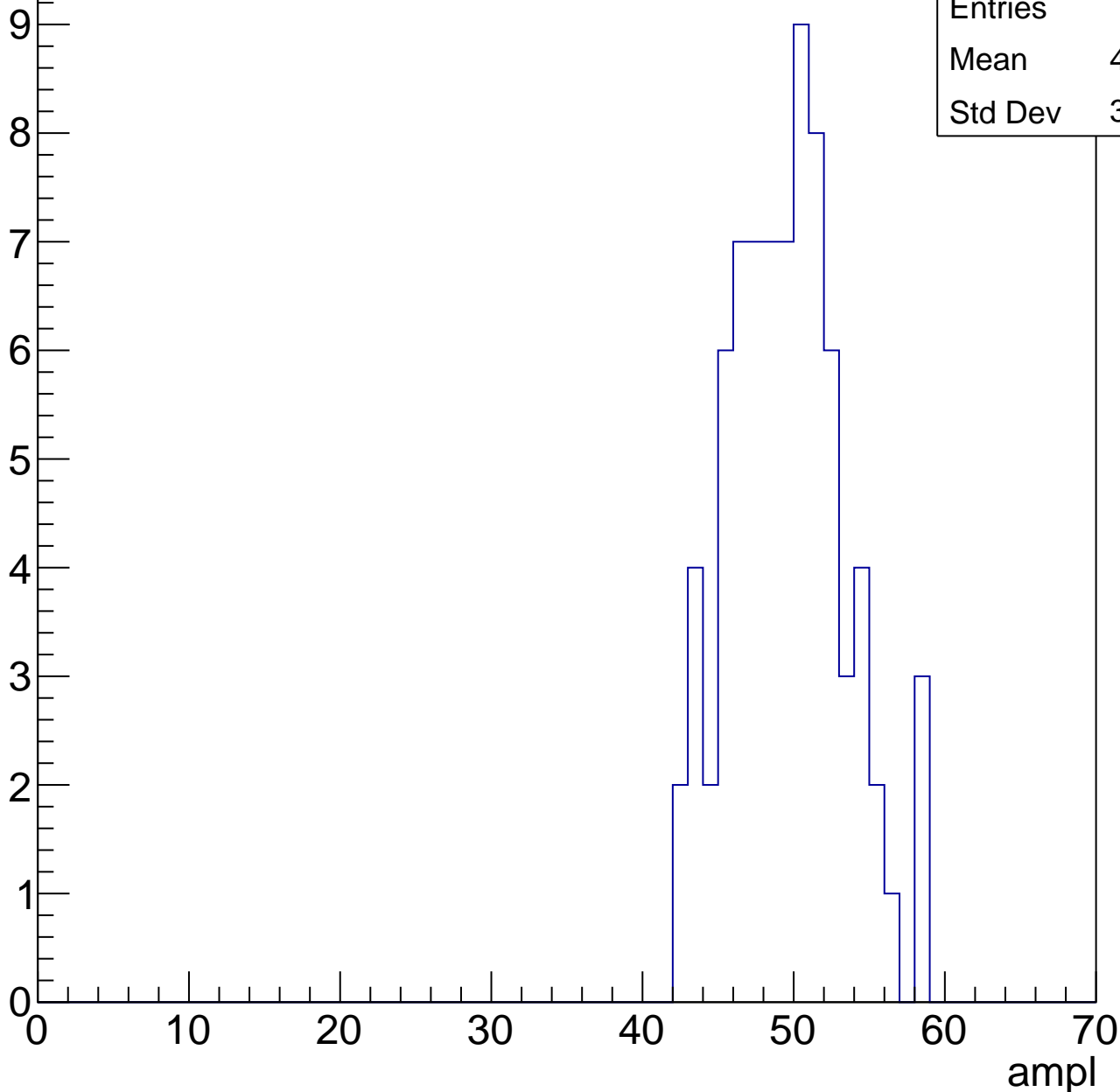


# B1L102S, U24-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	49.09
Std Dev	3.742



# B1L102S, U24-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

53

Mean

55.34

Std Dev

3.336

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

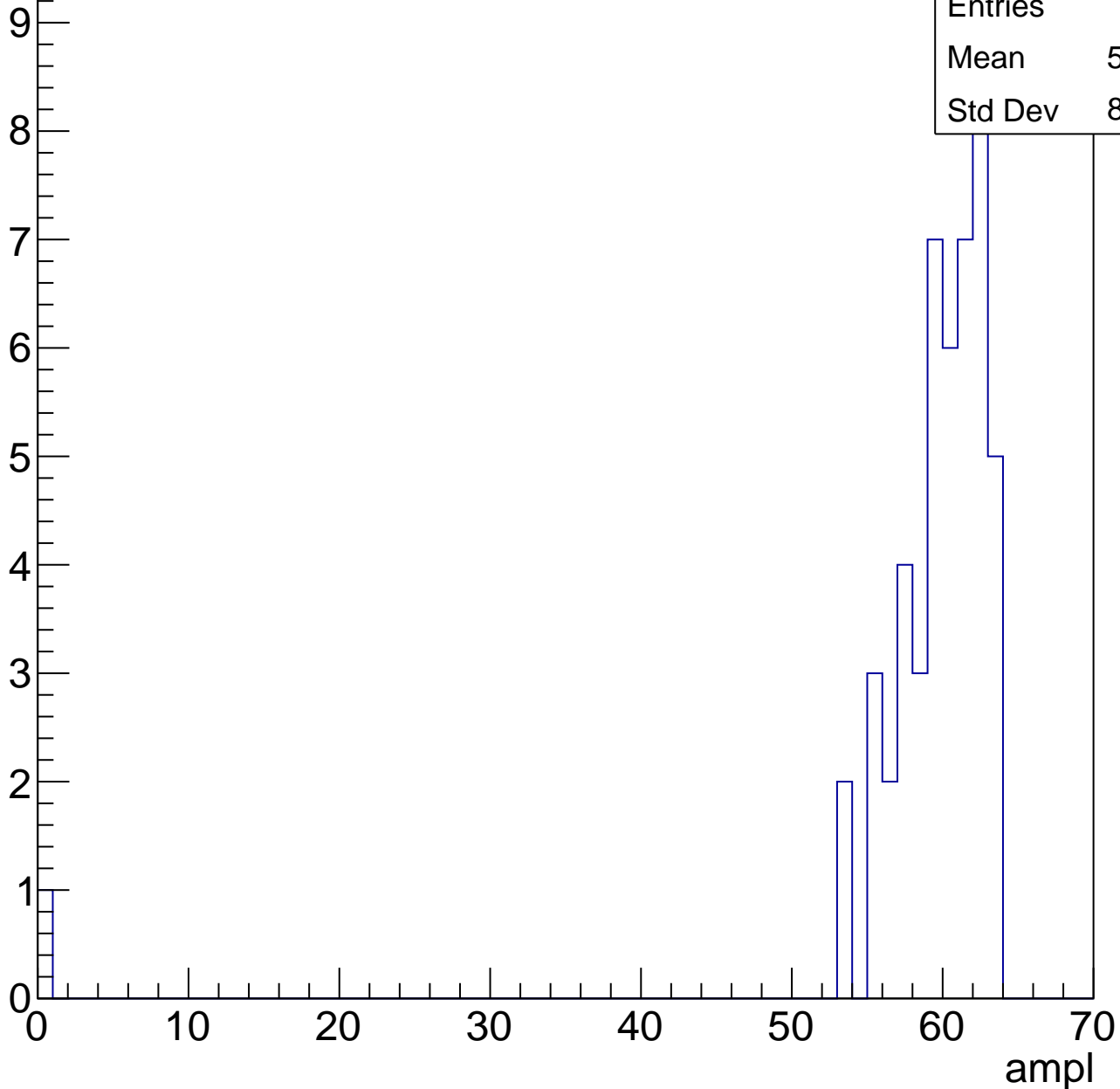
7

# B1L102S, U24-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.33
Std Dev	8.819

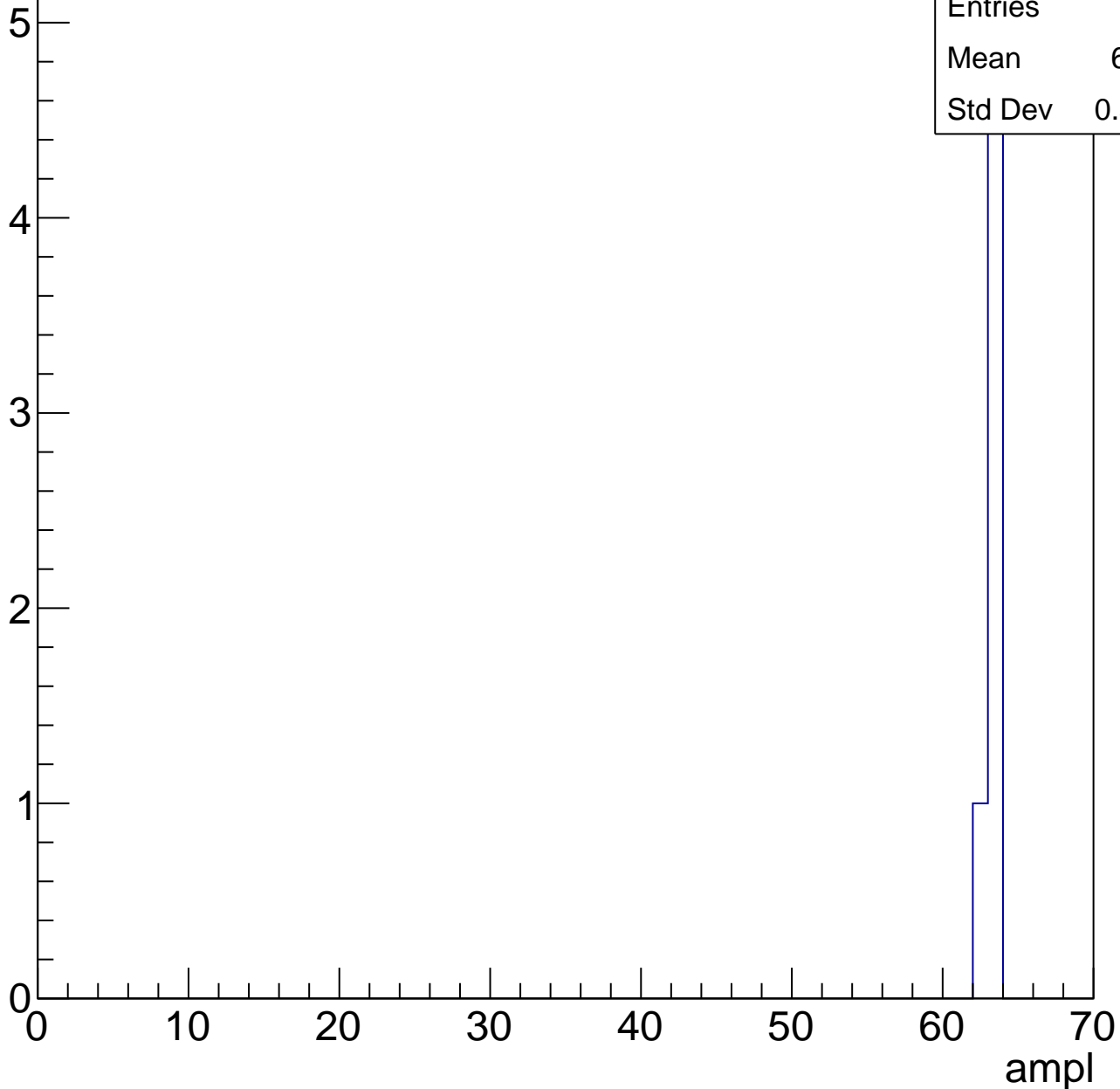


# B1L102S, U24-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727





# B1L102S, U24-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

# B1L102S, U24-ch32, adc0

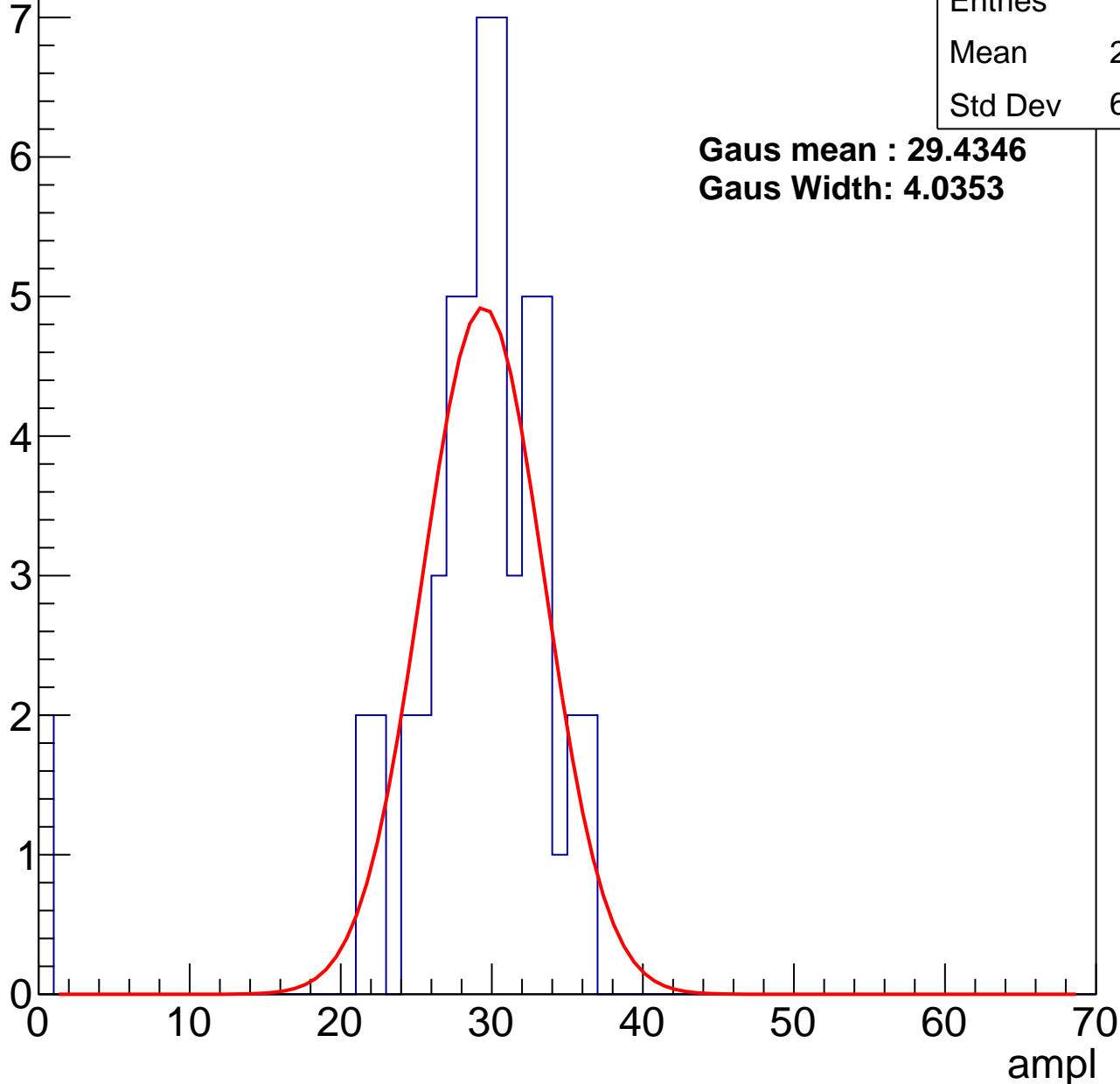
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.07
Std Dev	6.514

**Gaus mean : 29.4346**

**Gaus Width: 4.0353**



# B1L102S, U24-ch32, adc1

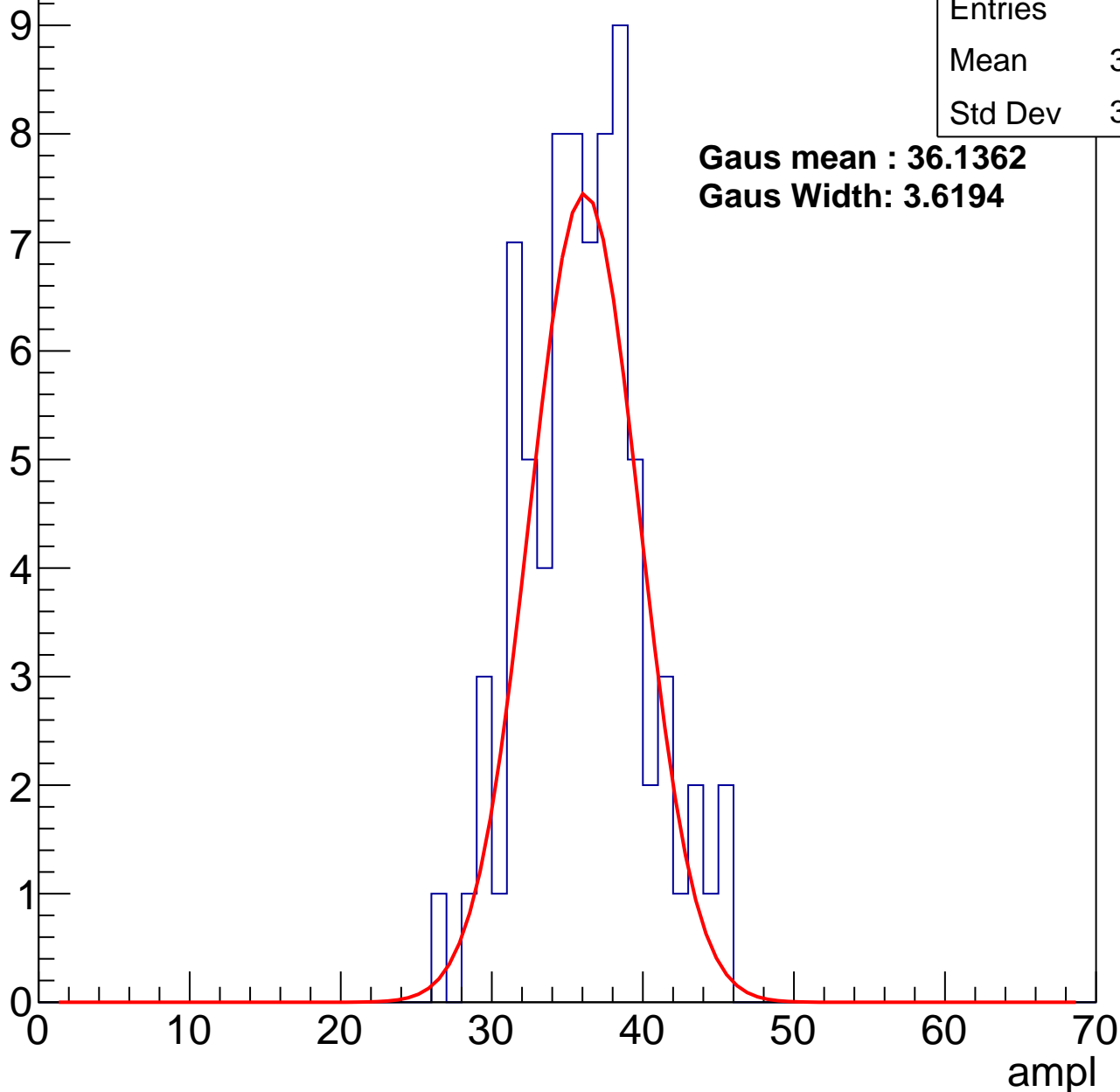
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.67
Std Dev	3.964

**Gaus mean : 36.1362**

**Gaus Width: 3.6194**



# B1L102S, U24-ch32, adc2

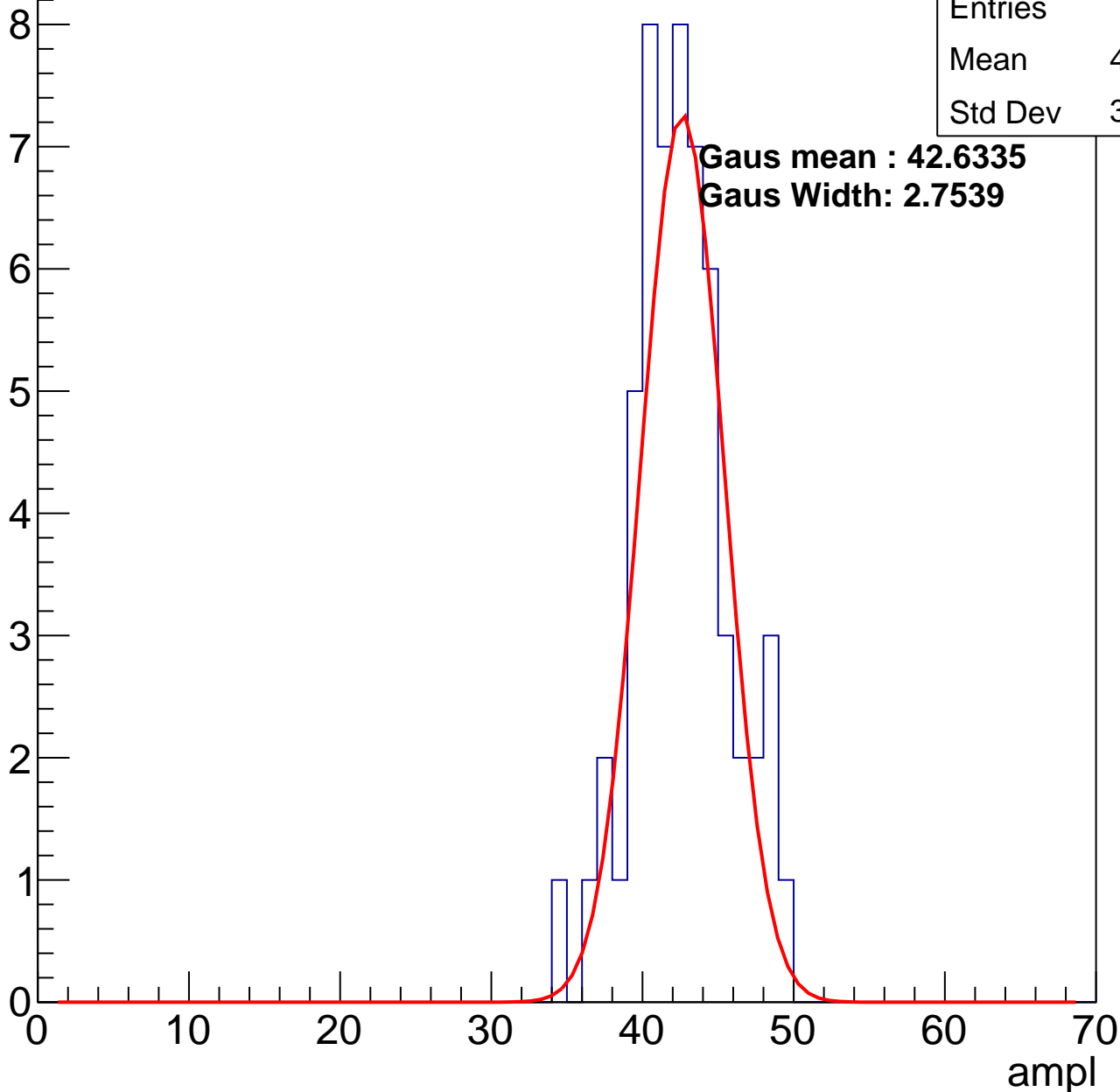
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.09
Std Dev	3.097

**Gaus mean : 42.6335**

**Gaus Width: 2.7539**

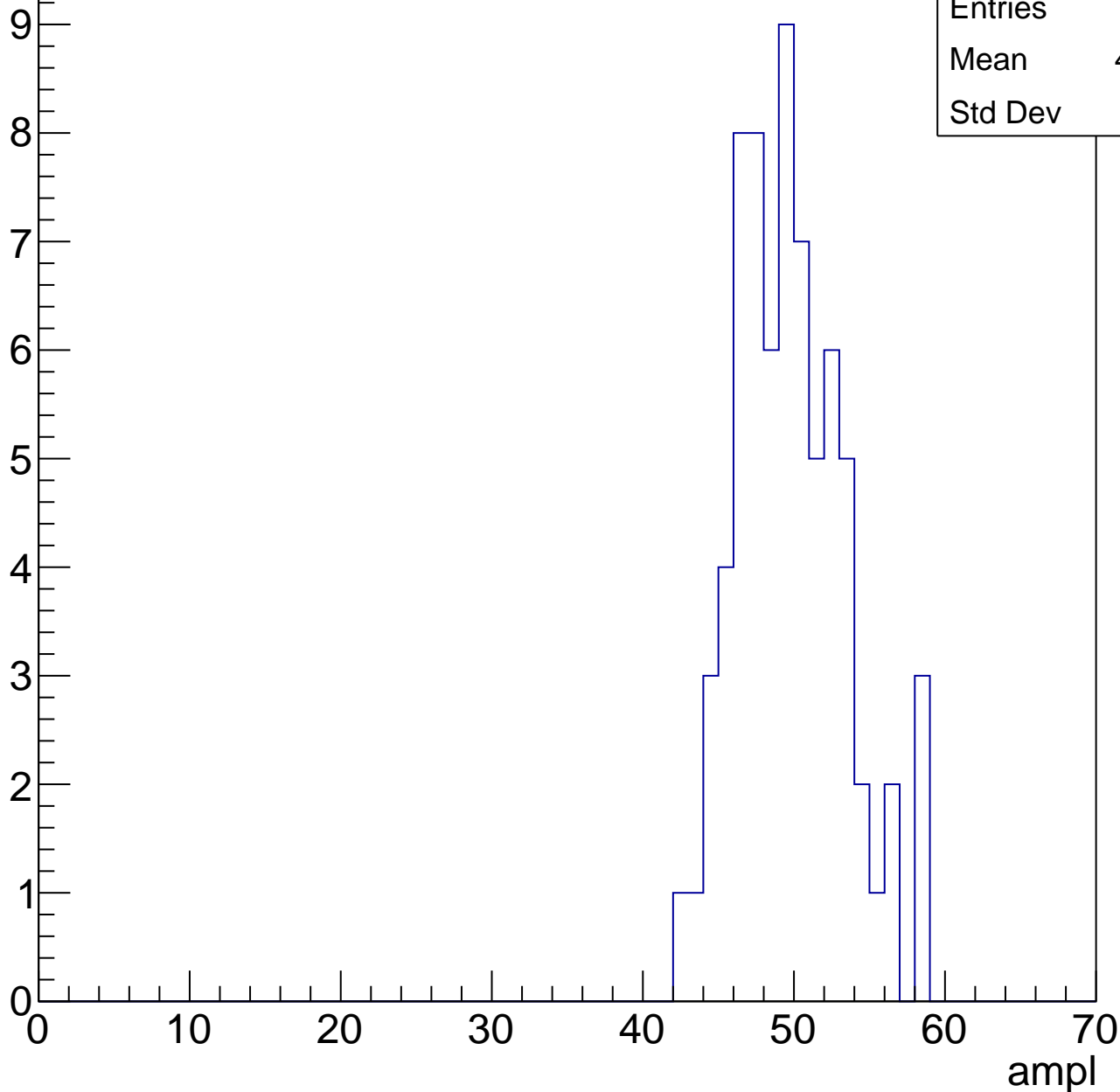


# B1L102S, U24-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.31
Std Dev	3.59

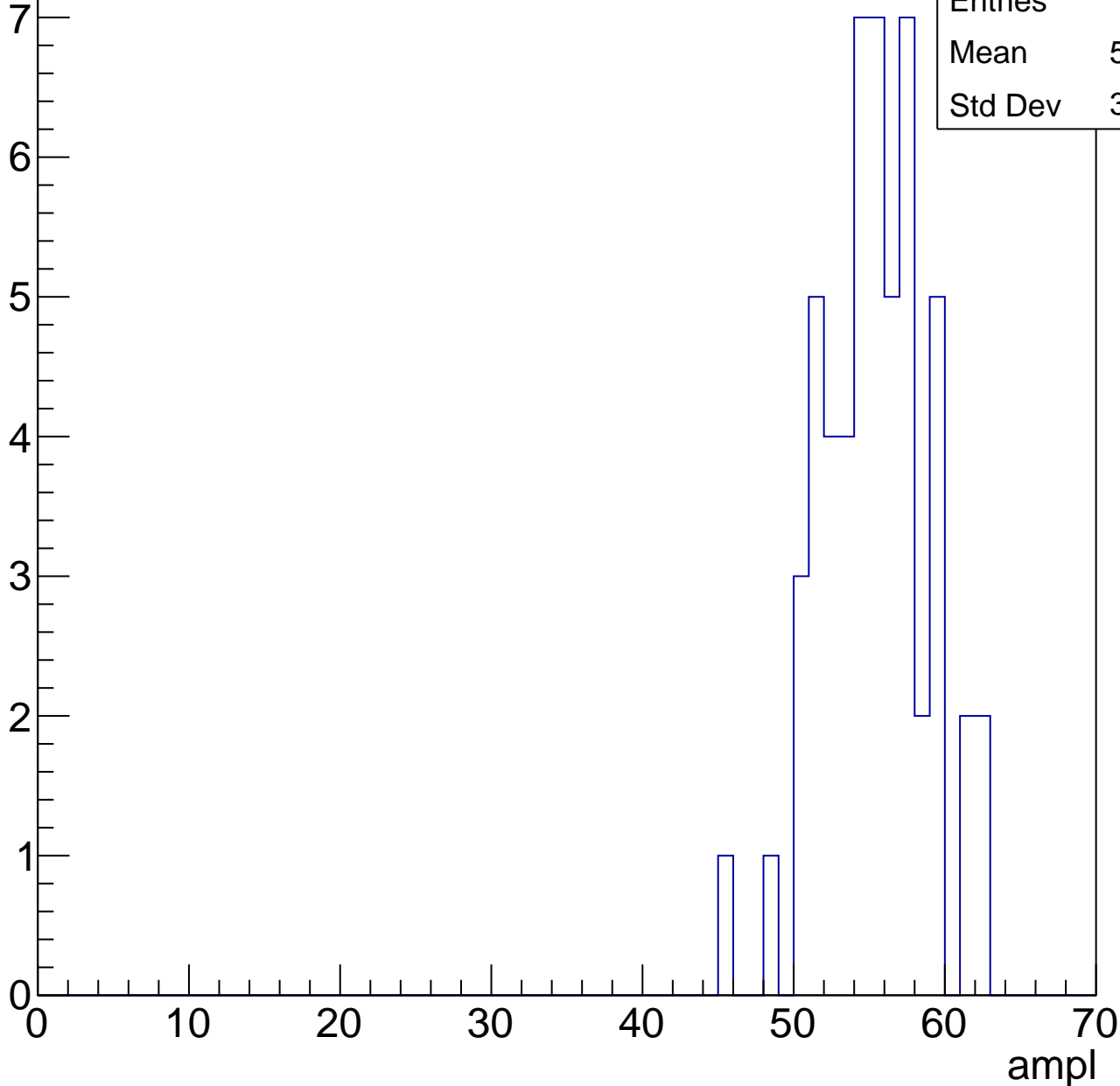


# B1L102S, U24-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.85
Std Dev	3.472



# B1L102S, U24-ch32, adc5

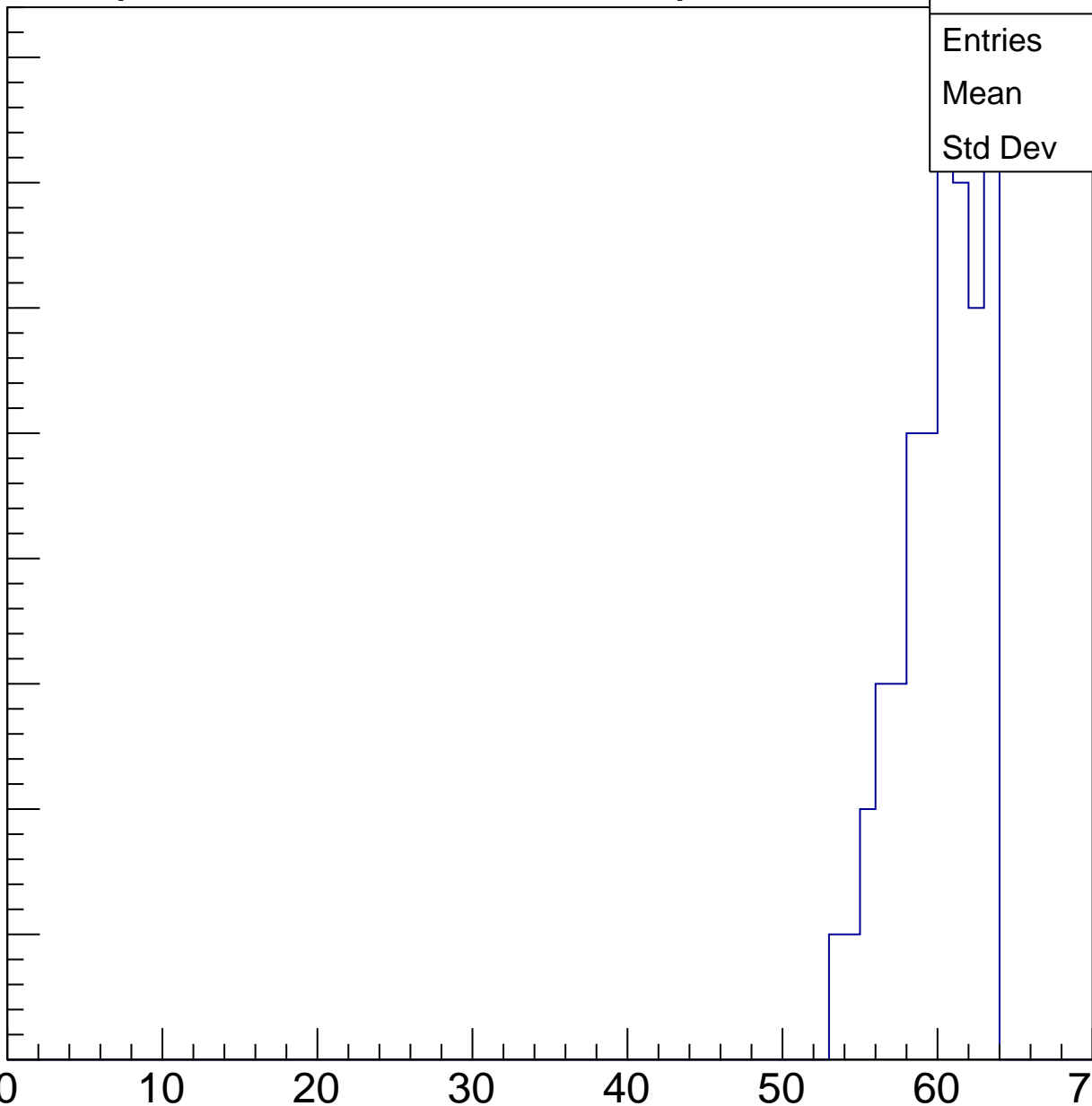
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	59.67
Std Dev	2.606

ampl



# B1L102S, U24-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

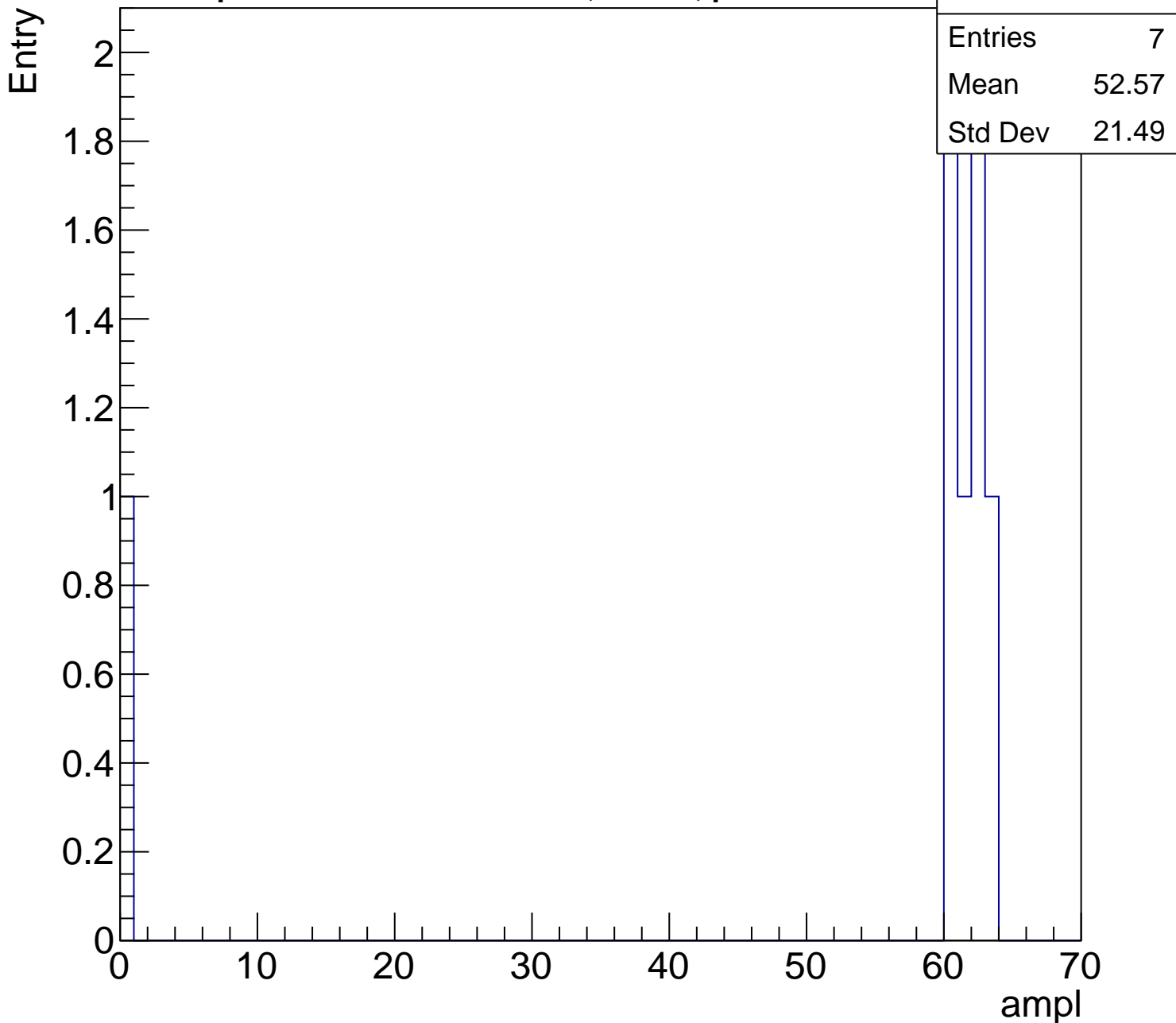
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.57
Std Dev	21.49

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L102S, U24-ch33, adc0

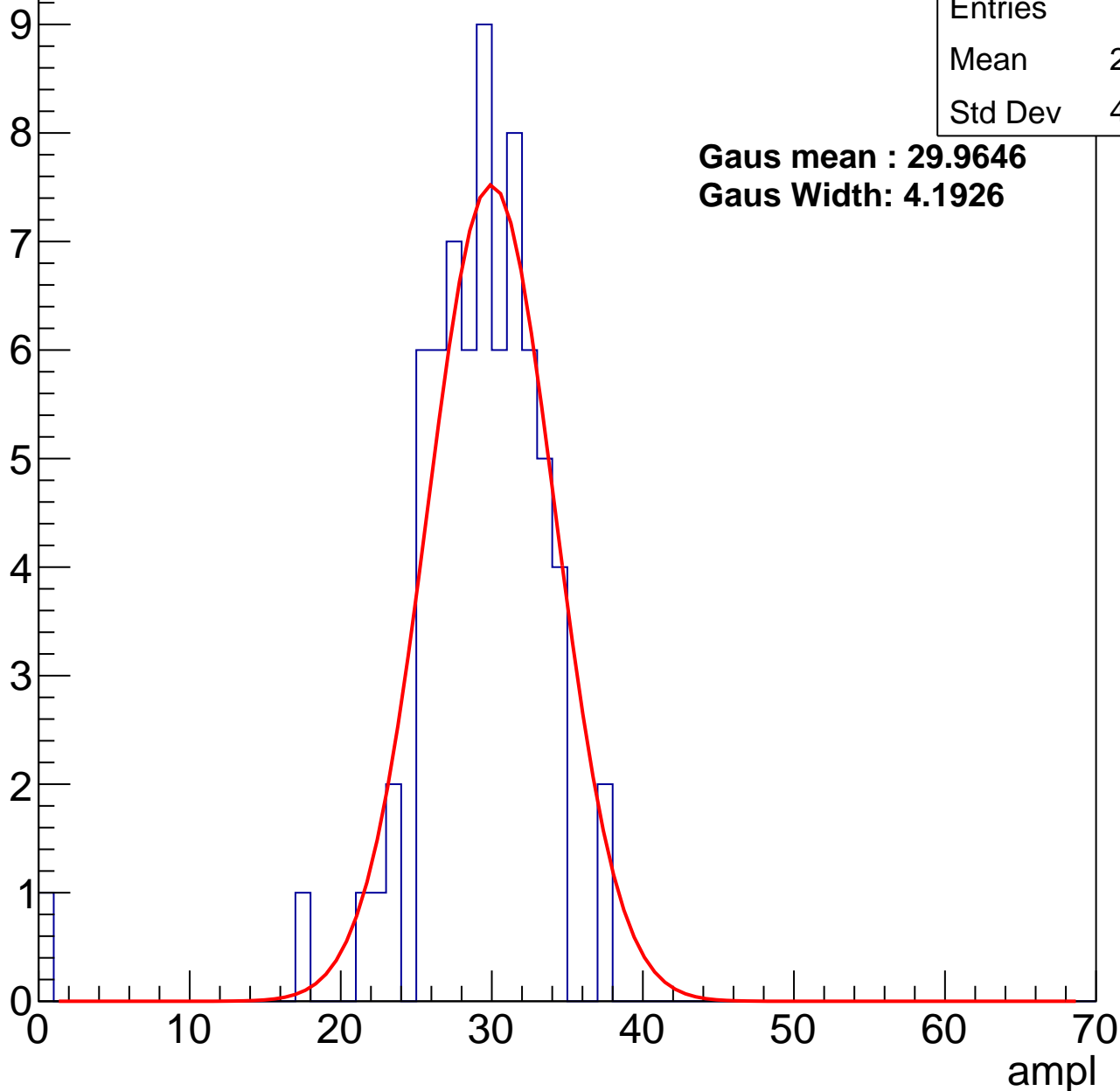
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.52
Std Dev	4.947

**Gaus mean : 29.9646**

**Gaus Width: 4.1926**



# B1L102S, U24-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	88
Mean	37.39
Std Dev	3.619

**Gaus mean : 37.6507**

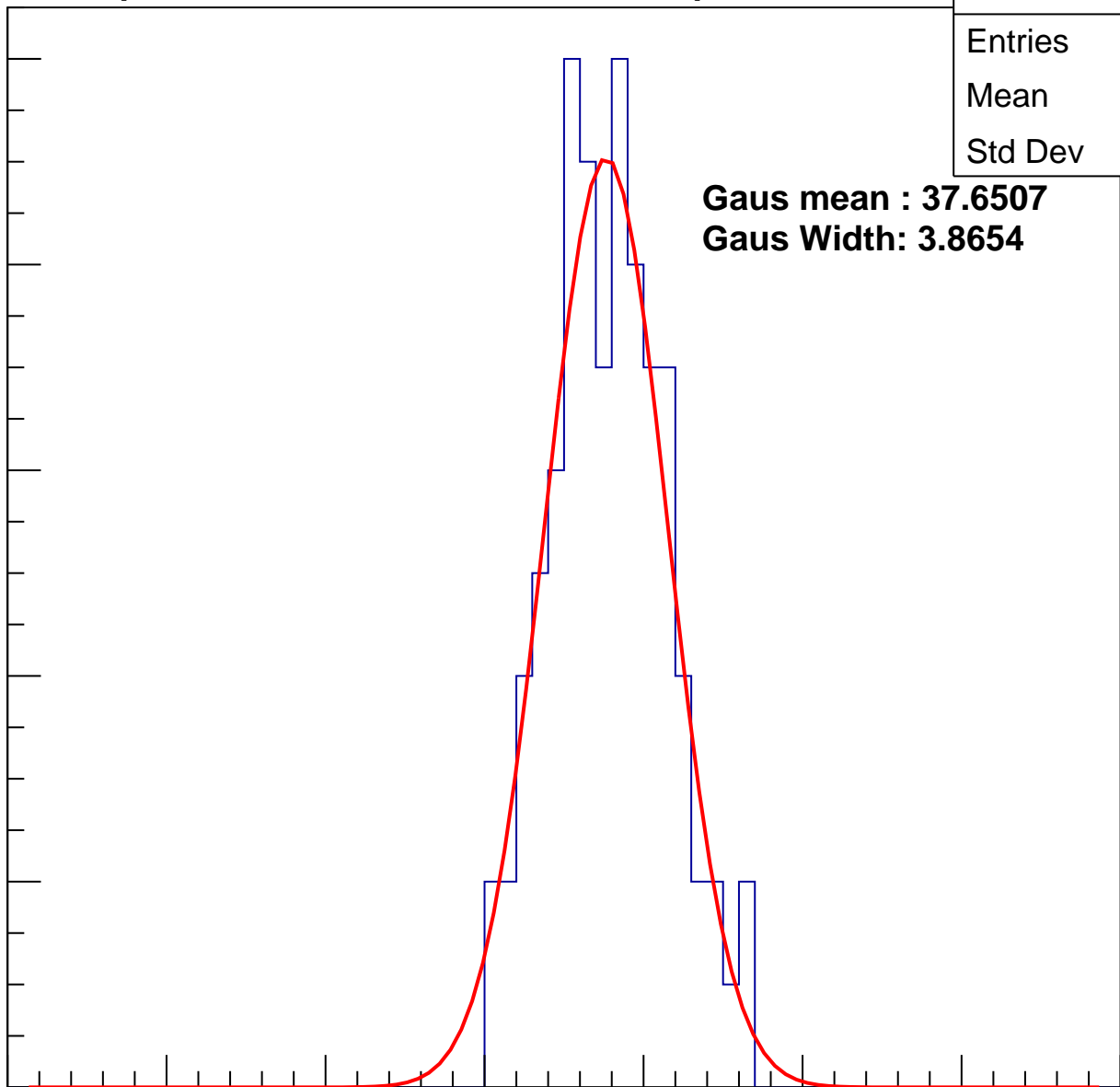
**Gaus Width: 3.8654**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch33, adc2

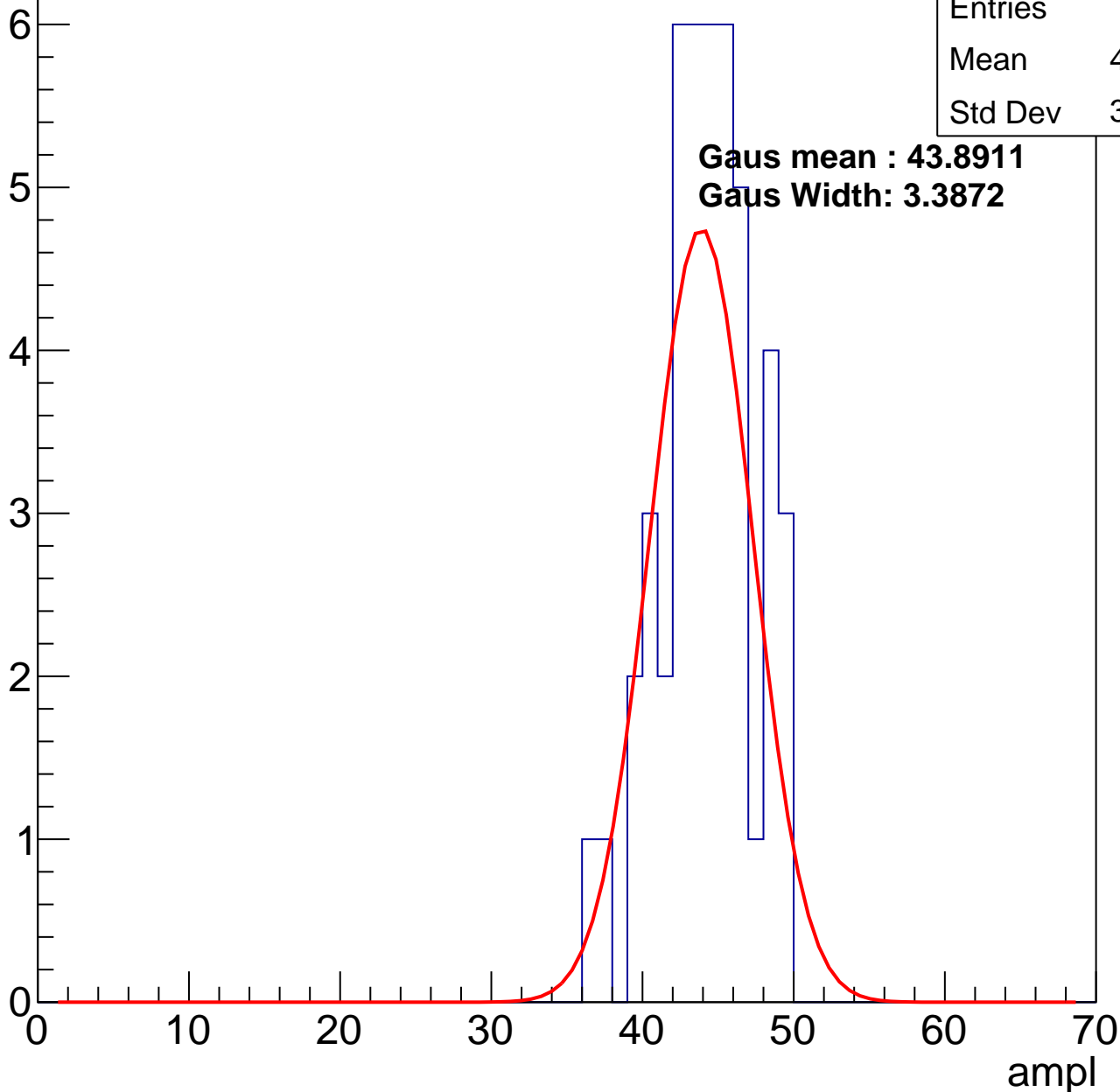
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	43.76
Std Dev	3.059

**Gaus mean : 43.8911**

**Gaus Width: 3.3872**

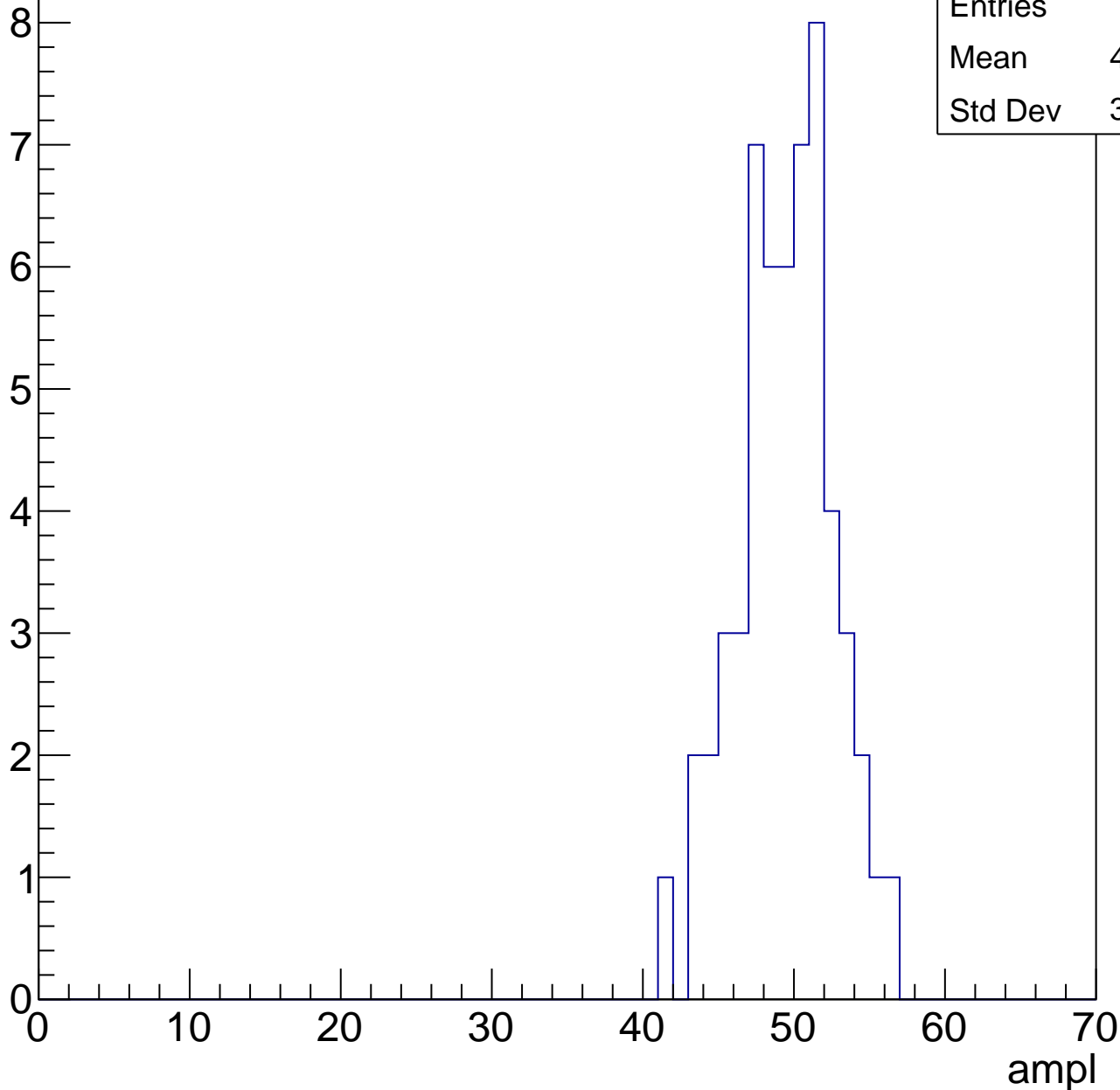


# B1L102S, U24-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.98
Std Dev	3.142



# B1L102S, U24-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

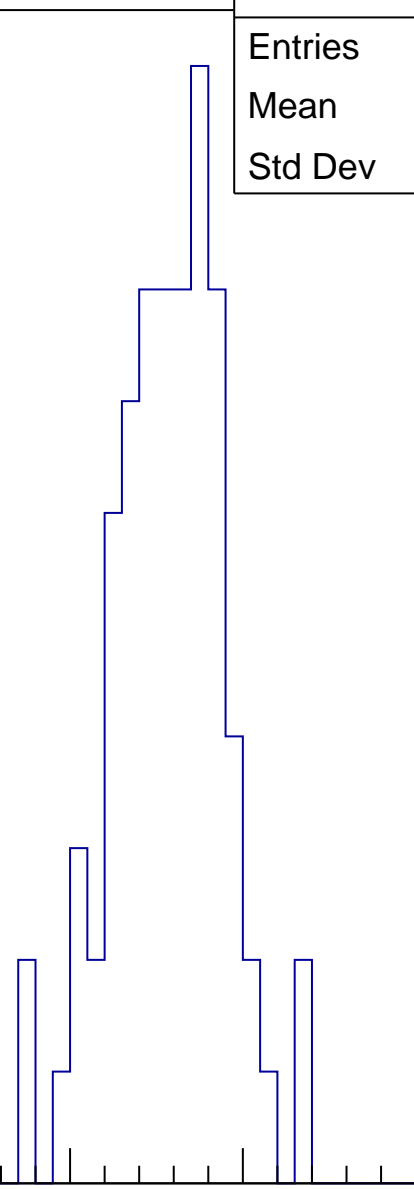
Entries	72
Mean	55.21
Std Dev	3.24

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

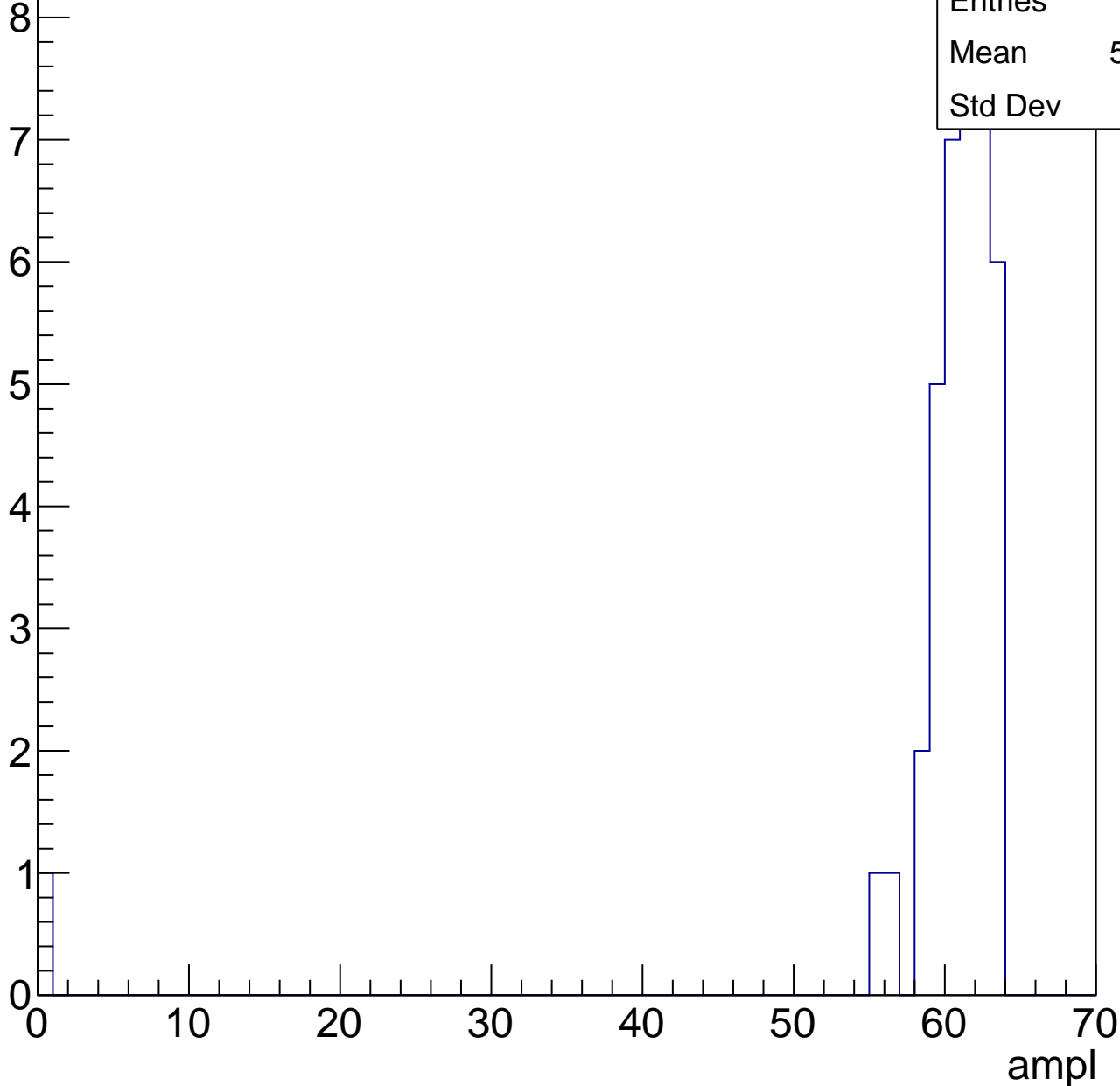


# B1L102S, U24-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

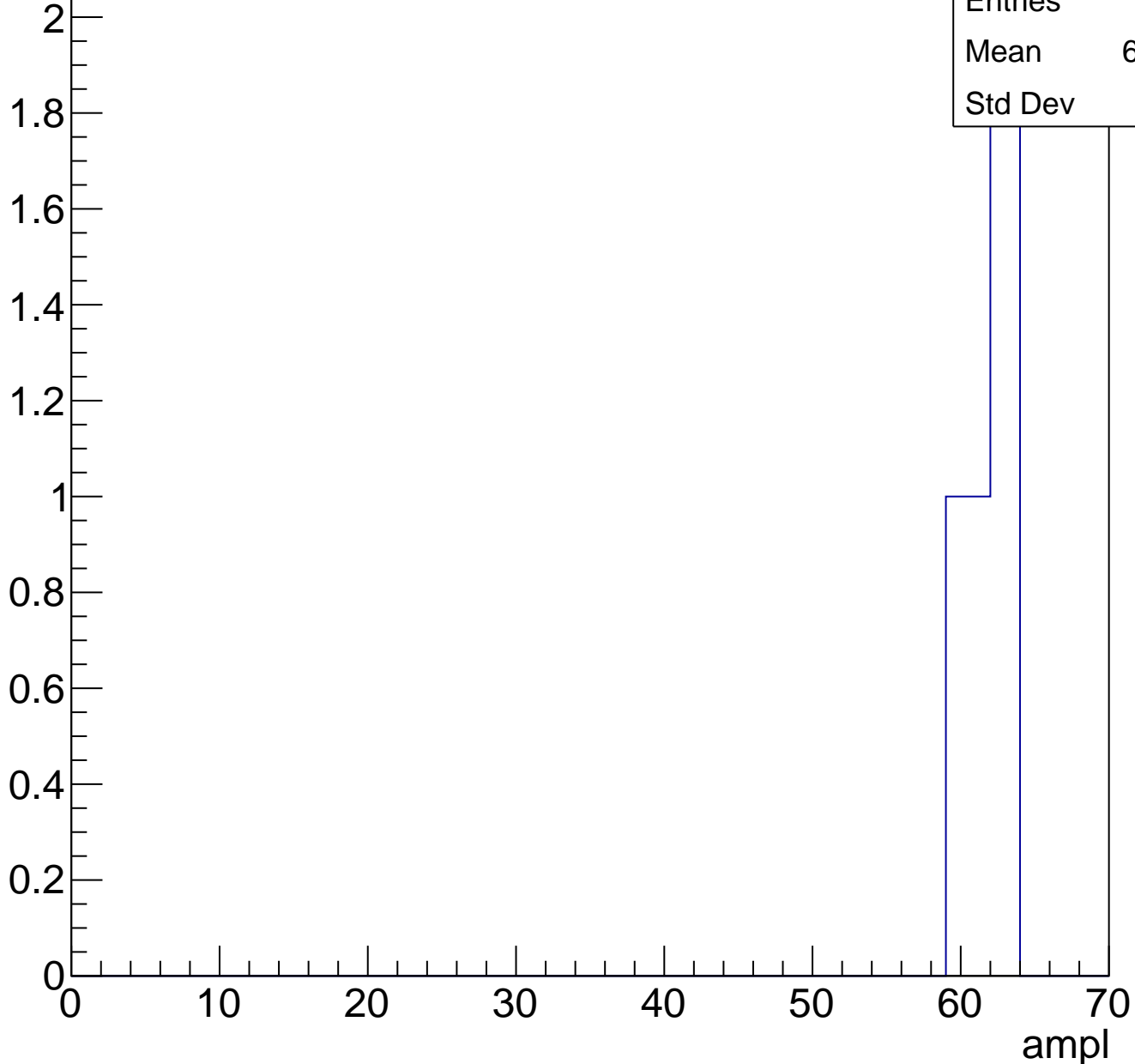
Entries	39
Mean	59.08
Std Dev	9.76



# B1L102S, U24-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch34, adc0

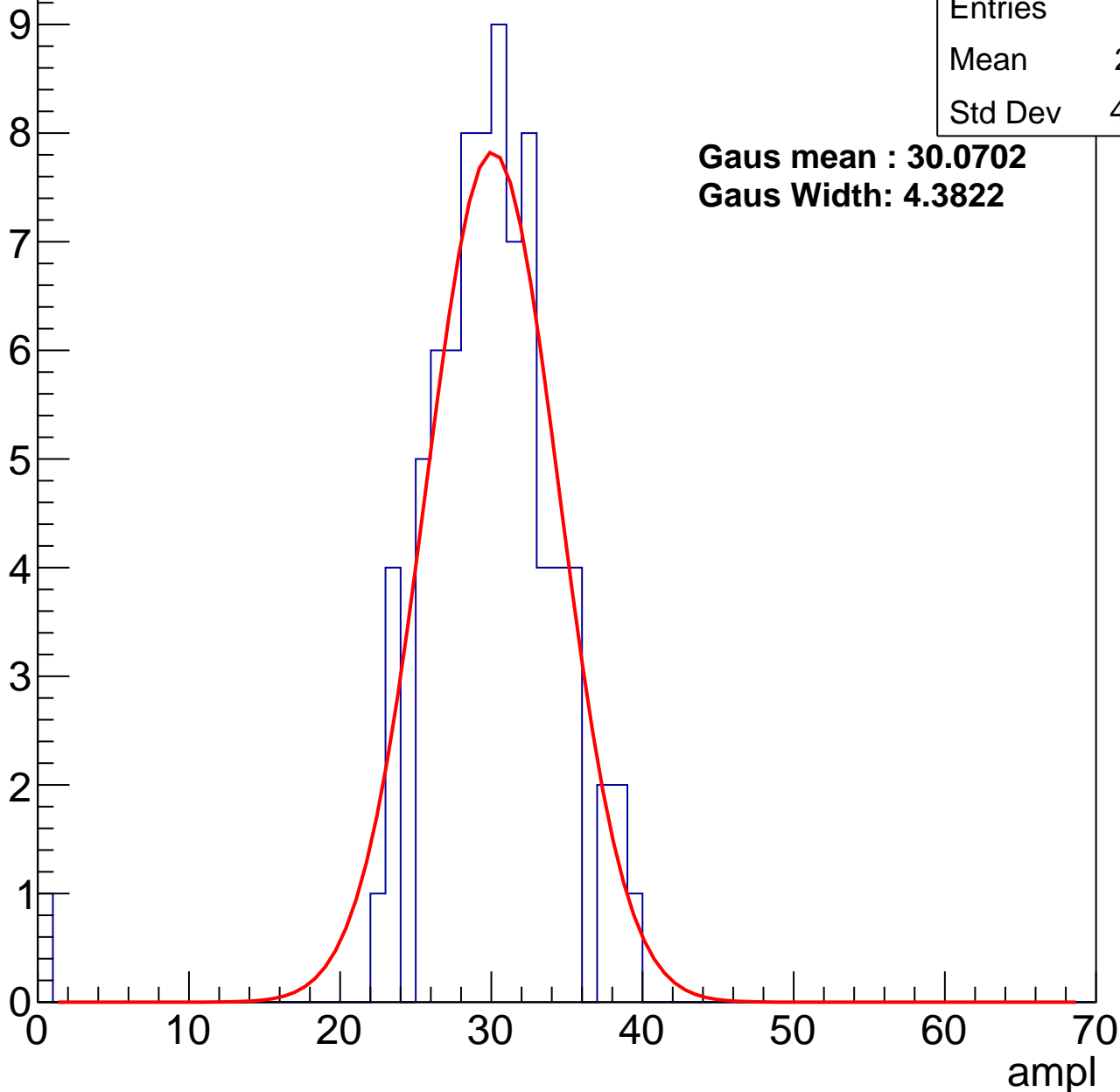
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	29.41
Std Dev	4.992

**Gaus mean : 30.0702**

**Gaus Width: 4.3822**



# B1L102S, U24-ch34, adc1

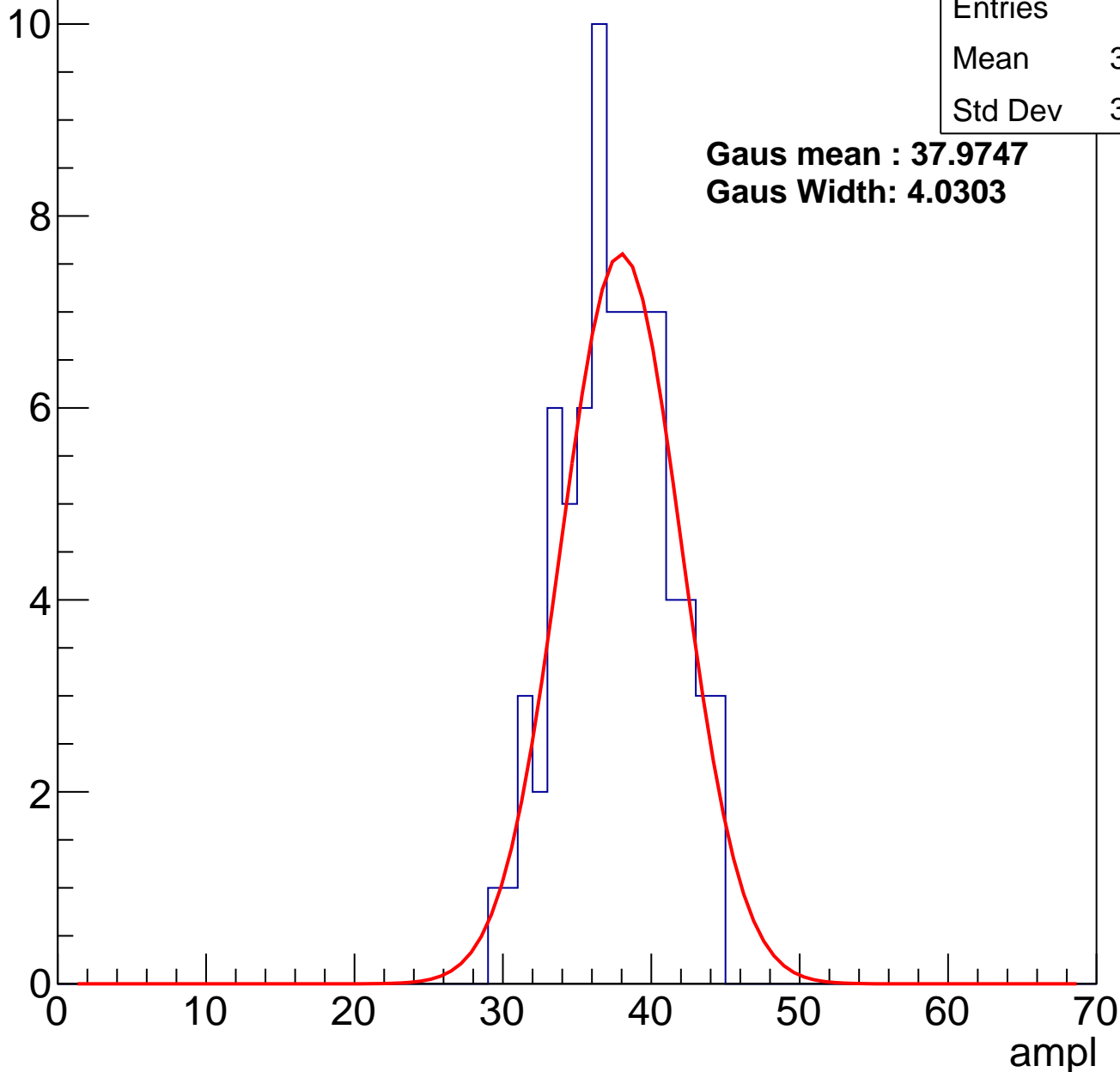
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	76
Mean	37.17
Std Dev	3.556

**Gaus mean : 37.9747**

**Gaus Width: 4.0303**

Entry



# B1L102S, U24-ch34, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	57
Mean	44.26
Std Dev	3.63

**Gaus mean : 44.7574**

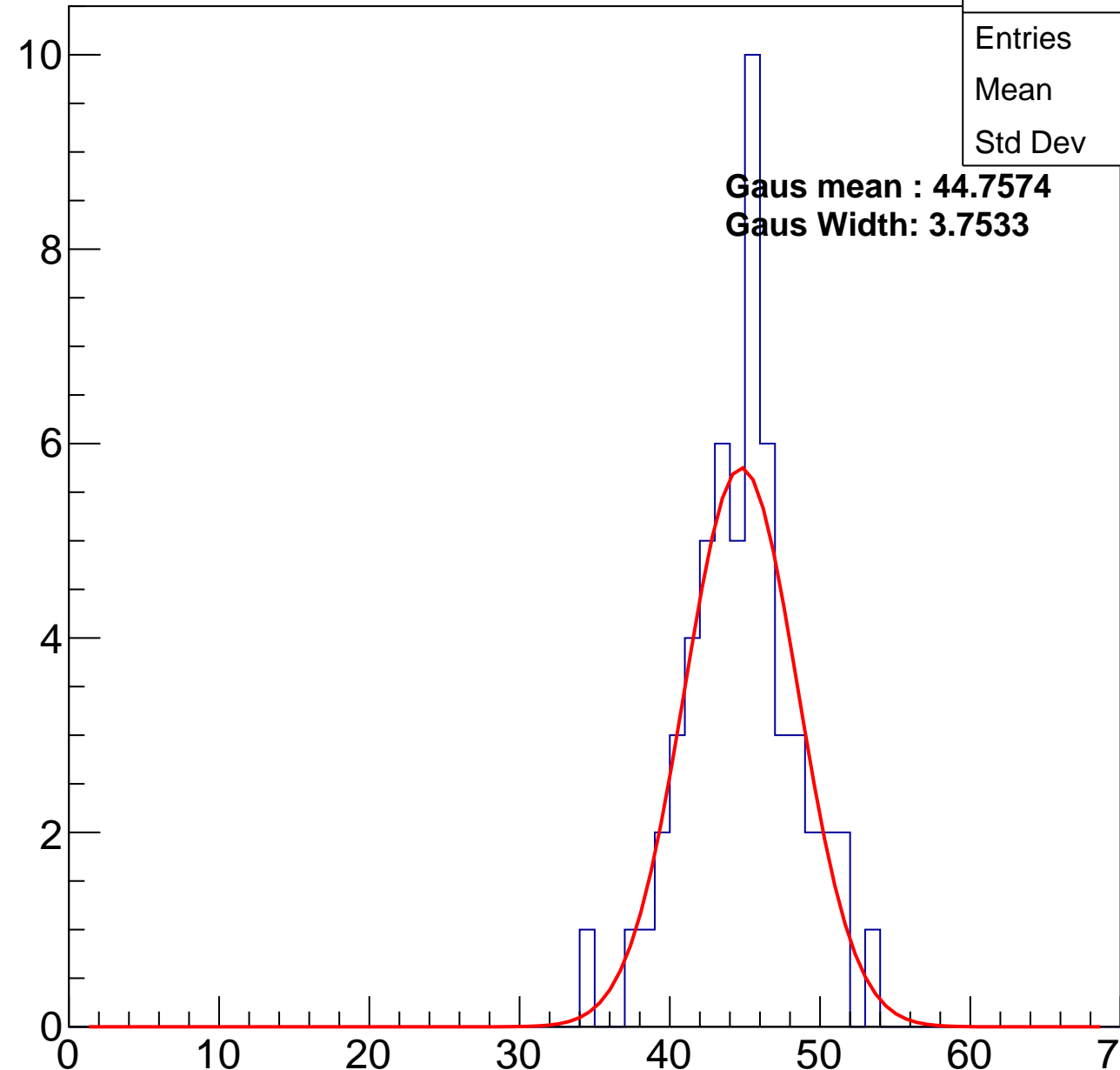
**Gaus Width: 3.7533**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

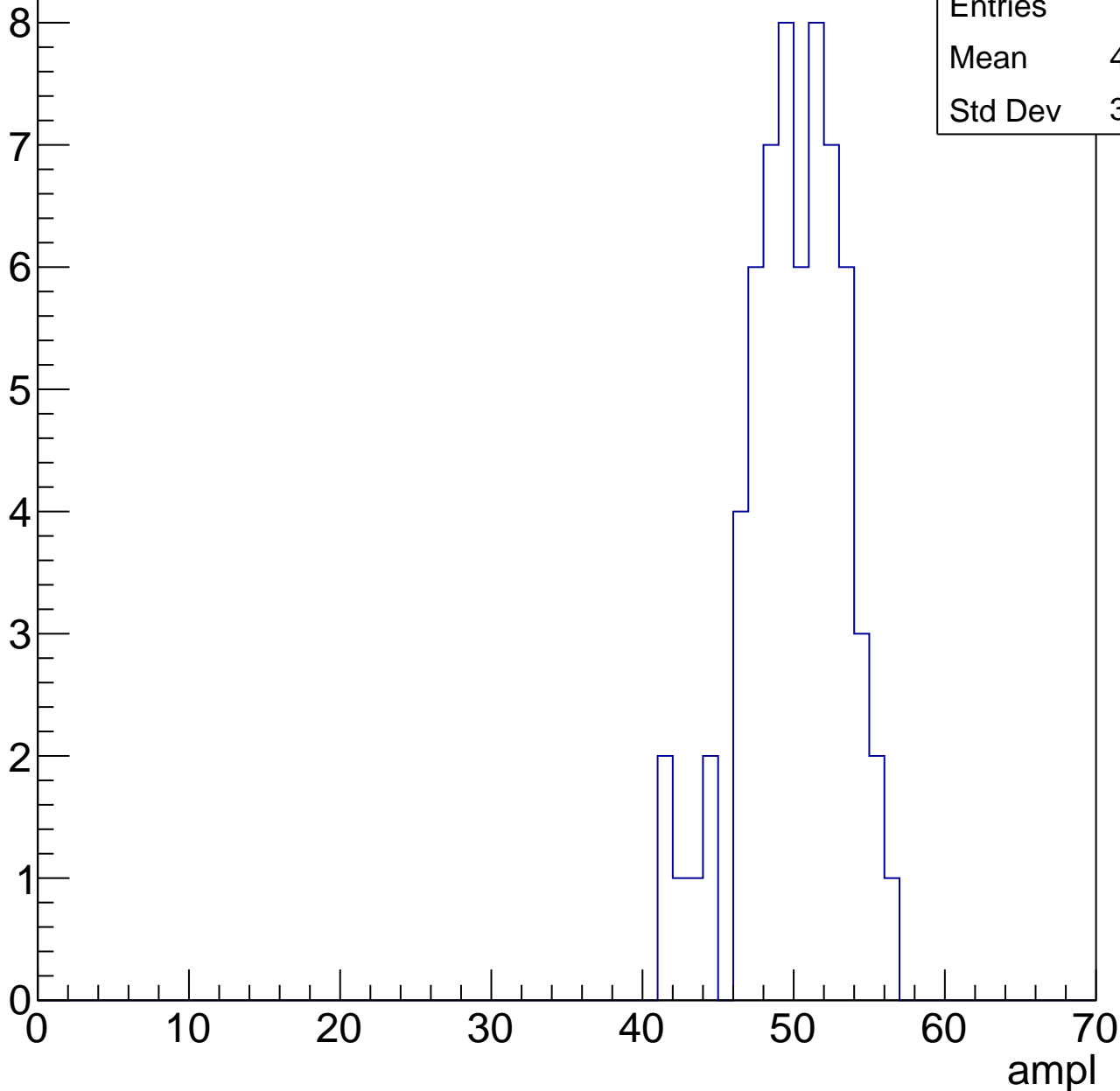


# B1L102S, U24-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.48
Std Dev	3.326

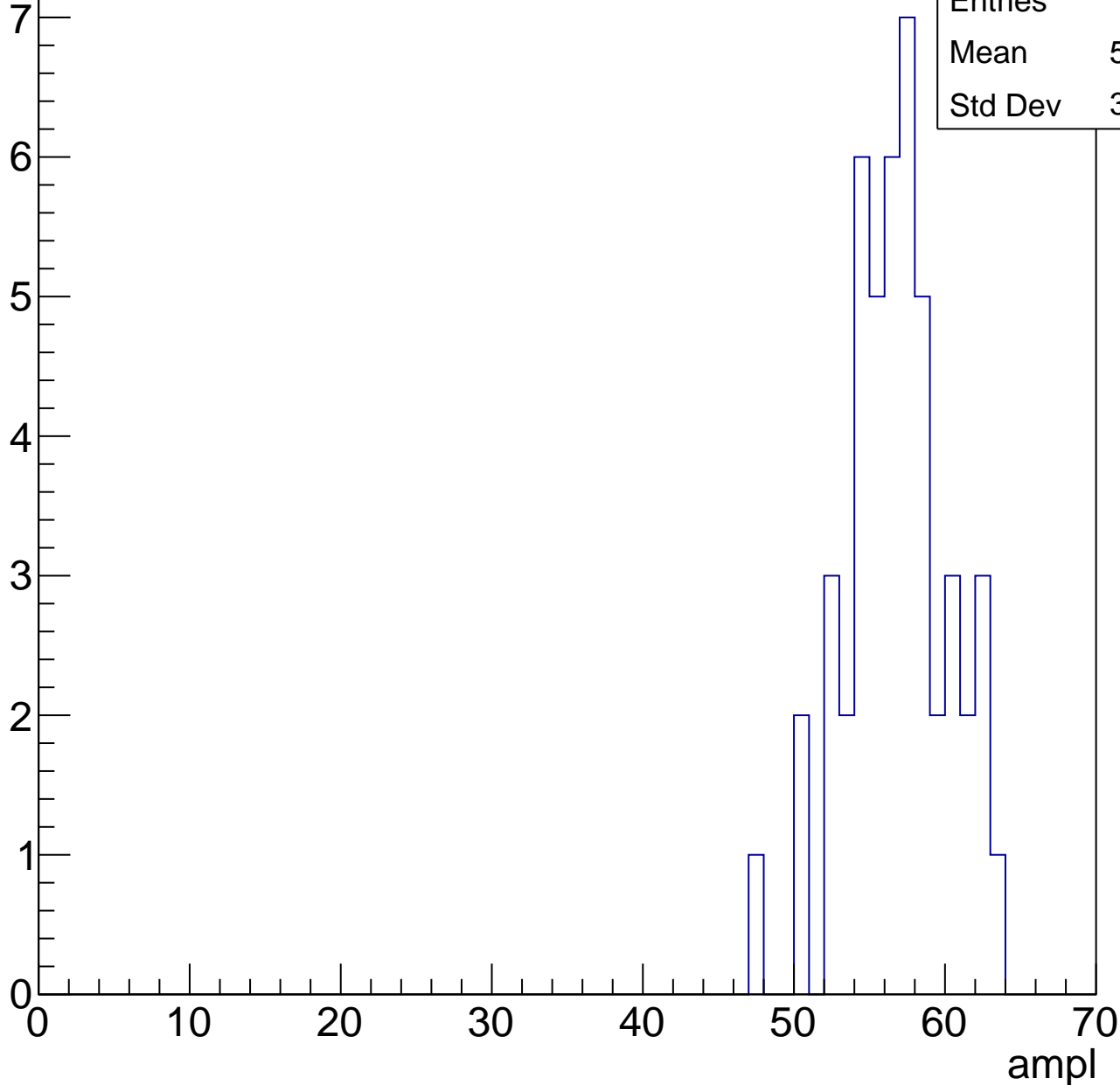


# B1L102S, U24-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.29
Std Dev	3.366



# B1L102S, U24-ch34, adc5

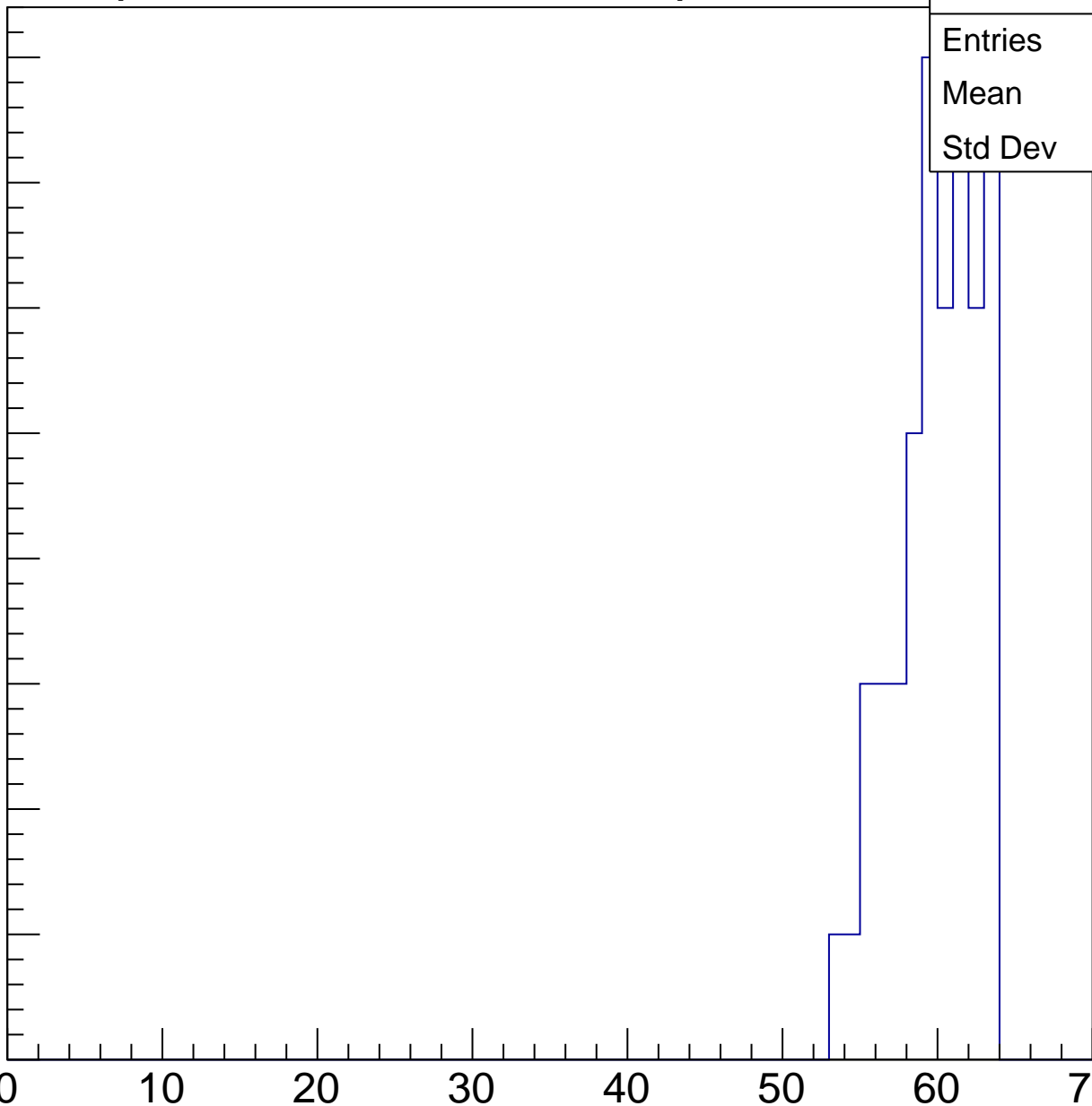
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	59.56
Std Dev	2.62

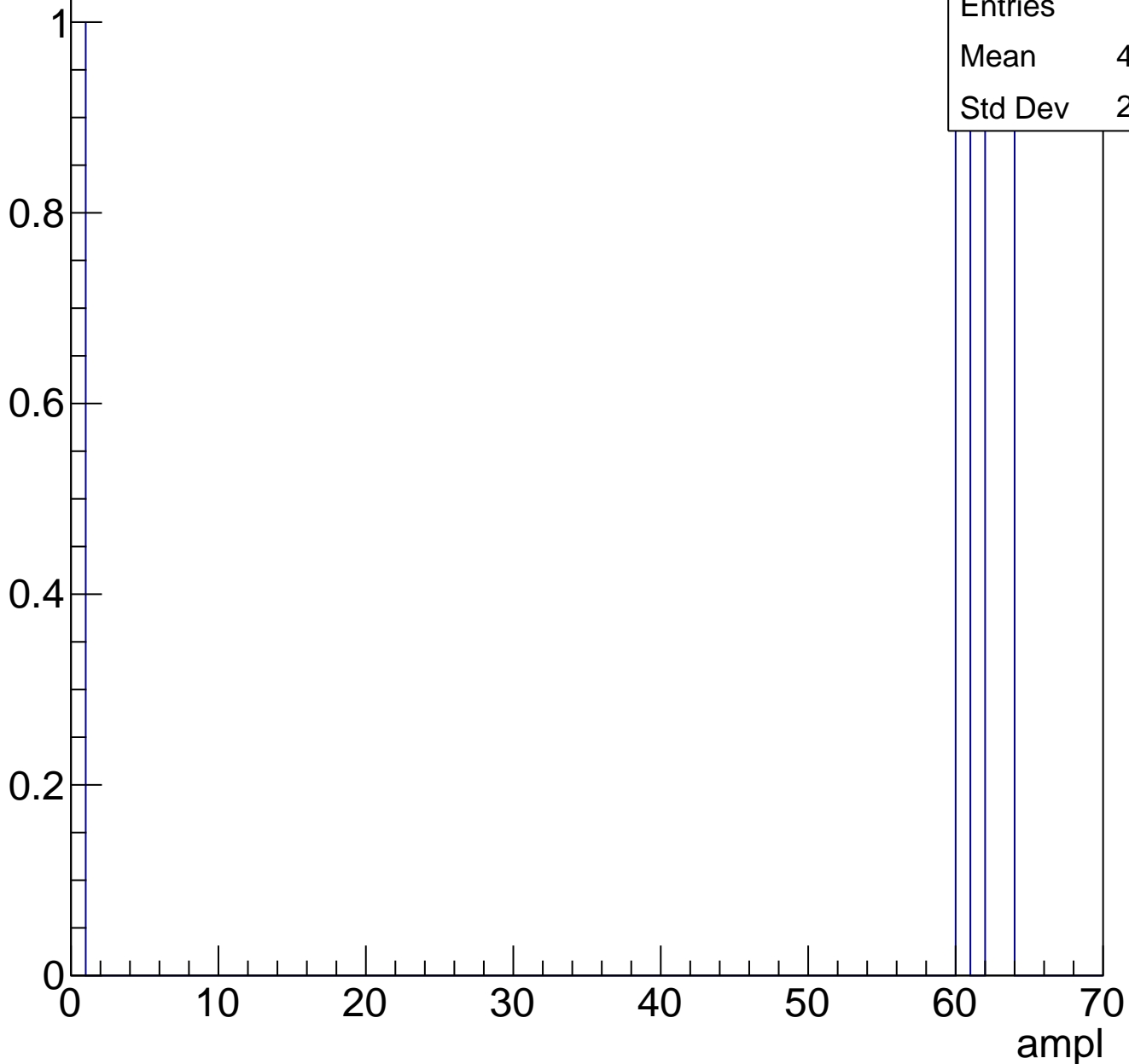
ampl



# B1L102S, U24-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L102S, U24-ch35, adc0

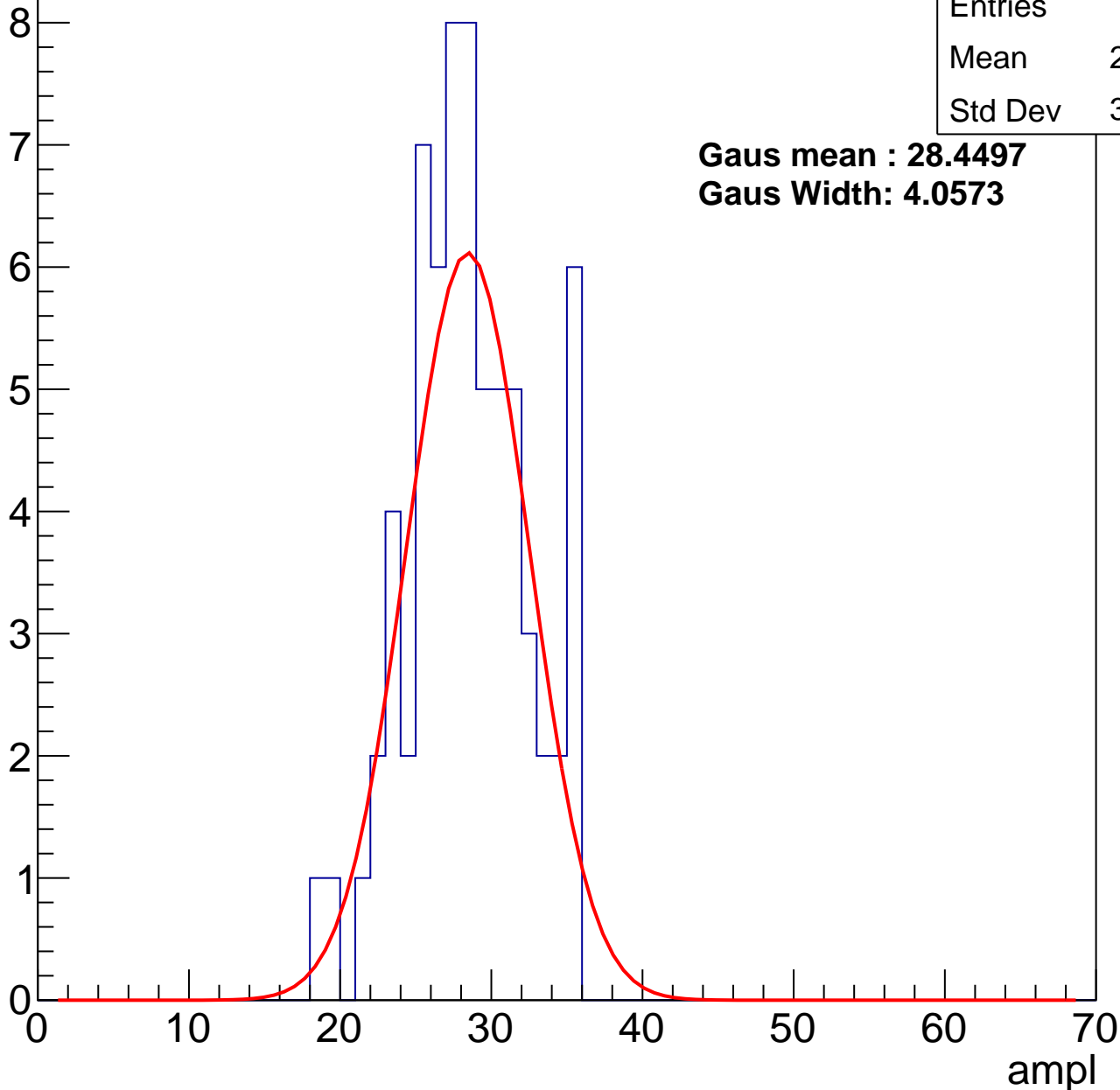
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.99
Std Dev	3.954

**Gaus mean : 28.4497**

**Gaus Width: 4.0573**



# B1L102S, U24-ch35, adc1

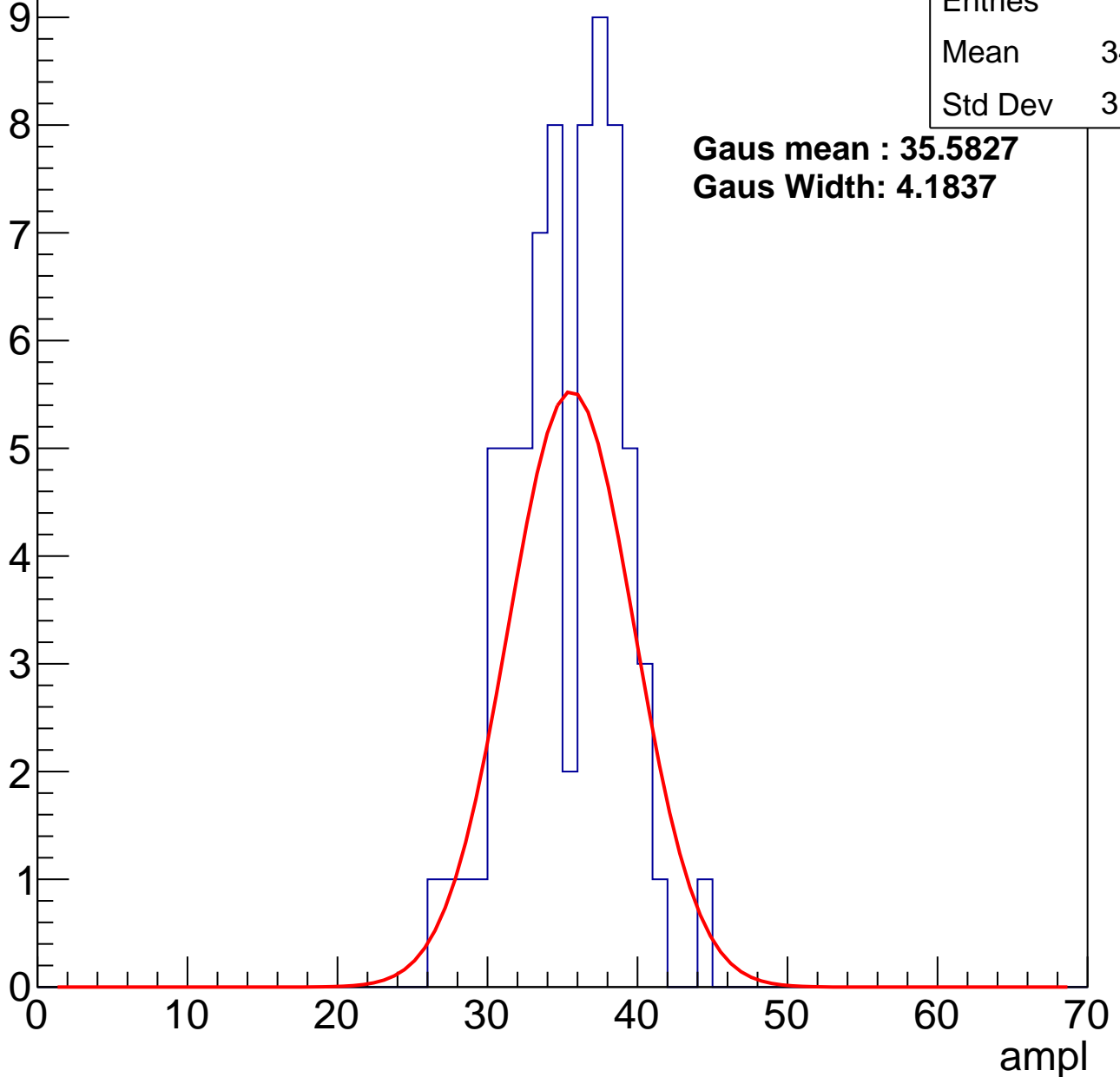
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.83
Std Dev	3.564

**Gaus mean : 35.5827**

**Gaus Width: 4.1837**



# B1L102S, U24-ch35, adc2

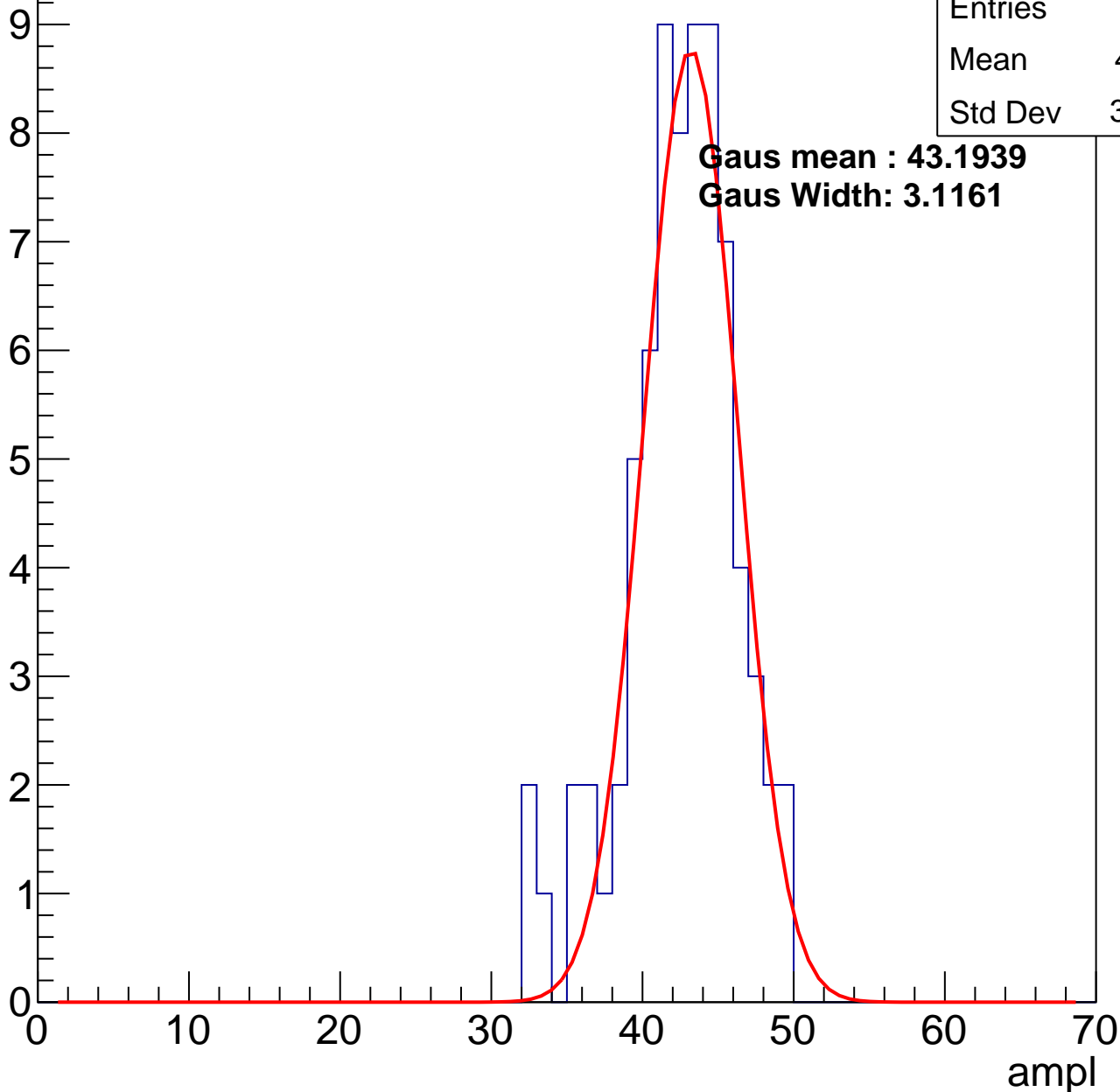
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.01
Std Dev	3.682

**Gaus mean : 43.1939**

**Gaus Width: 3.1161**

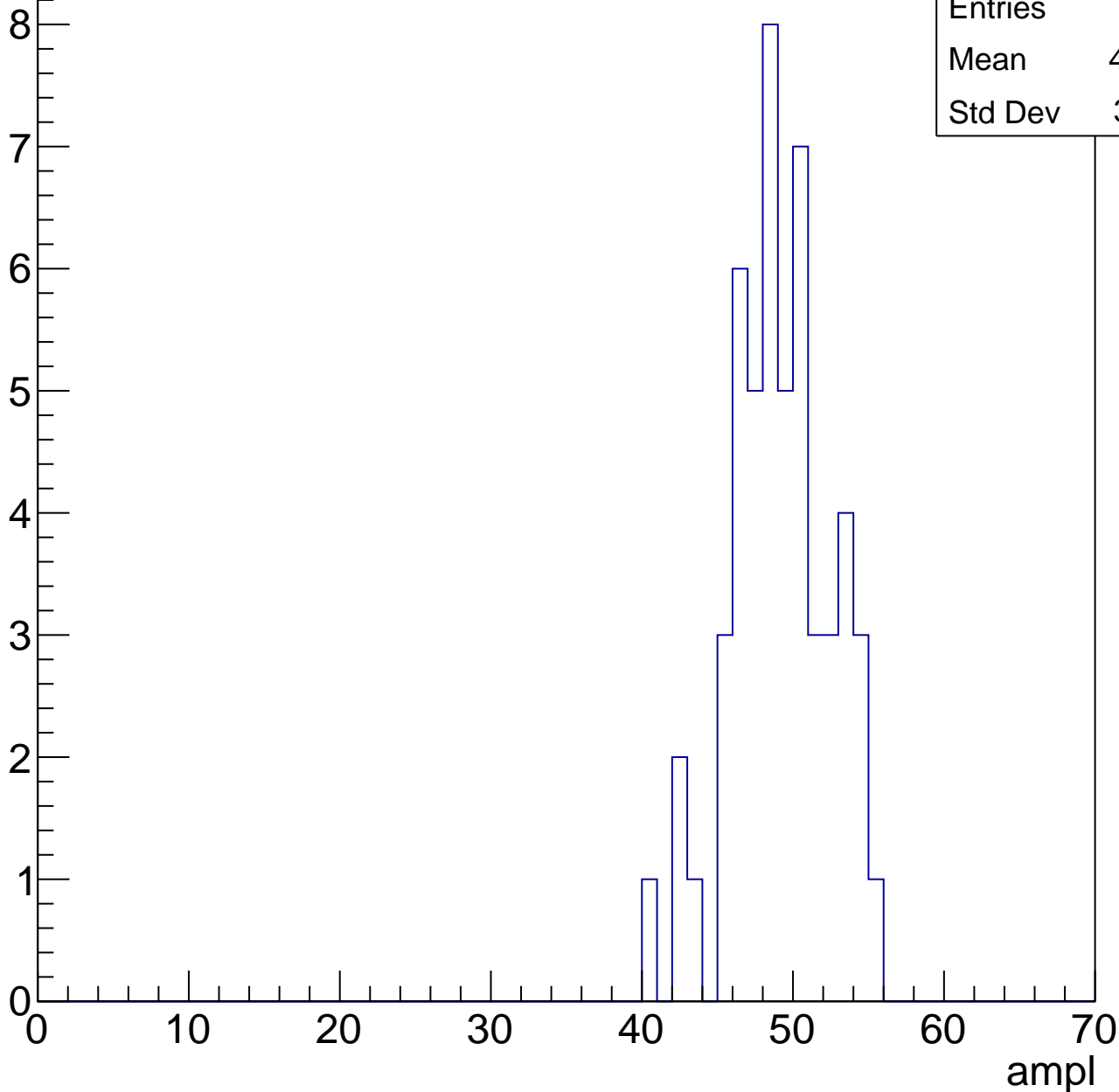


# B1L102S, U24-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	48.65
Std Dev	3.281

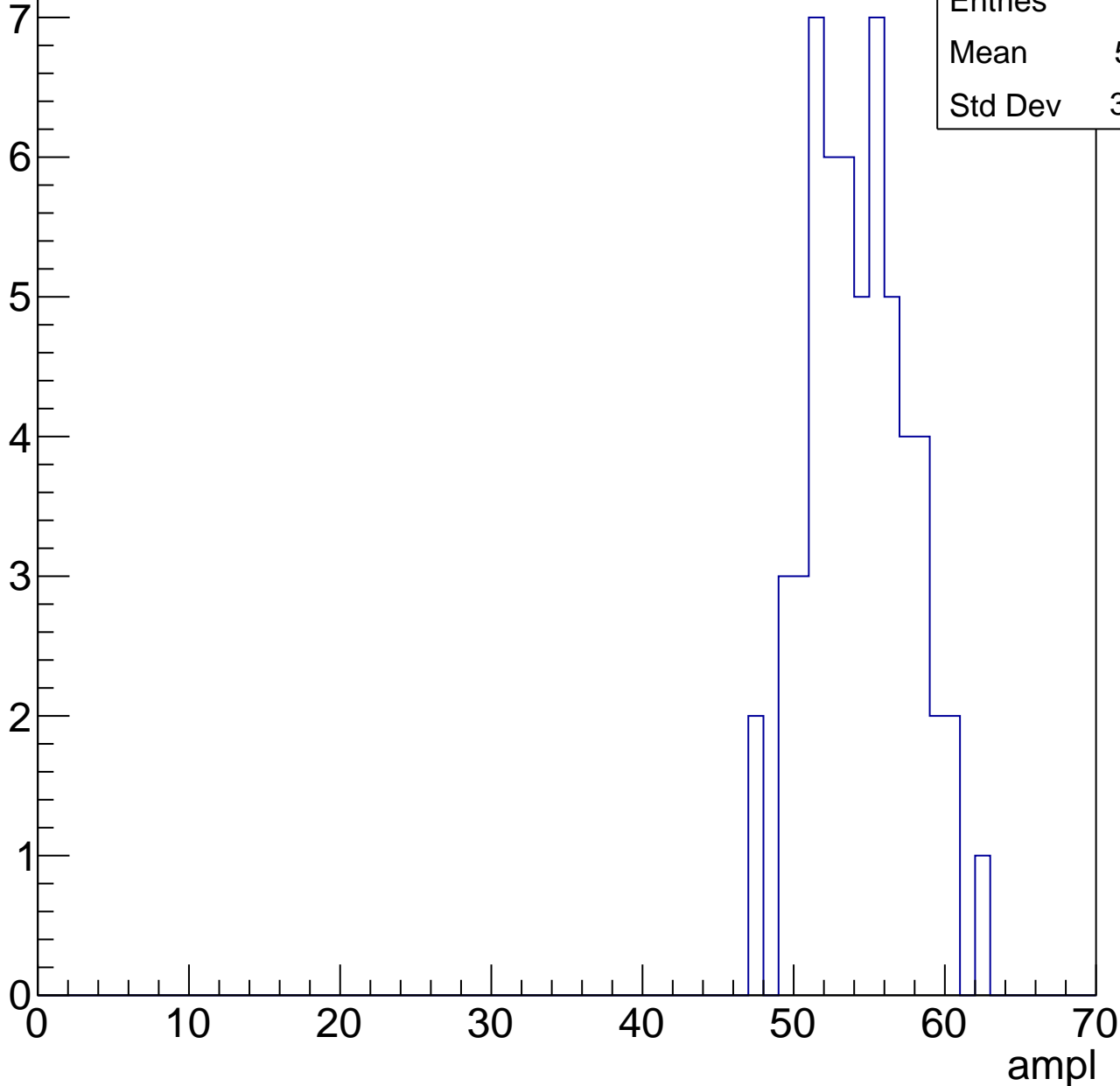


# B1L102S, U24-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

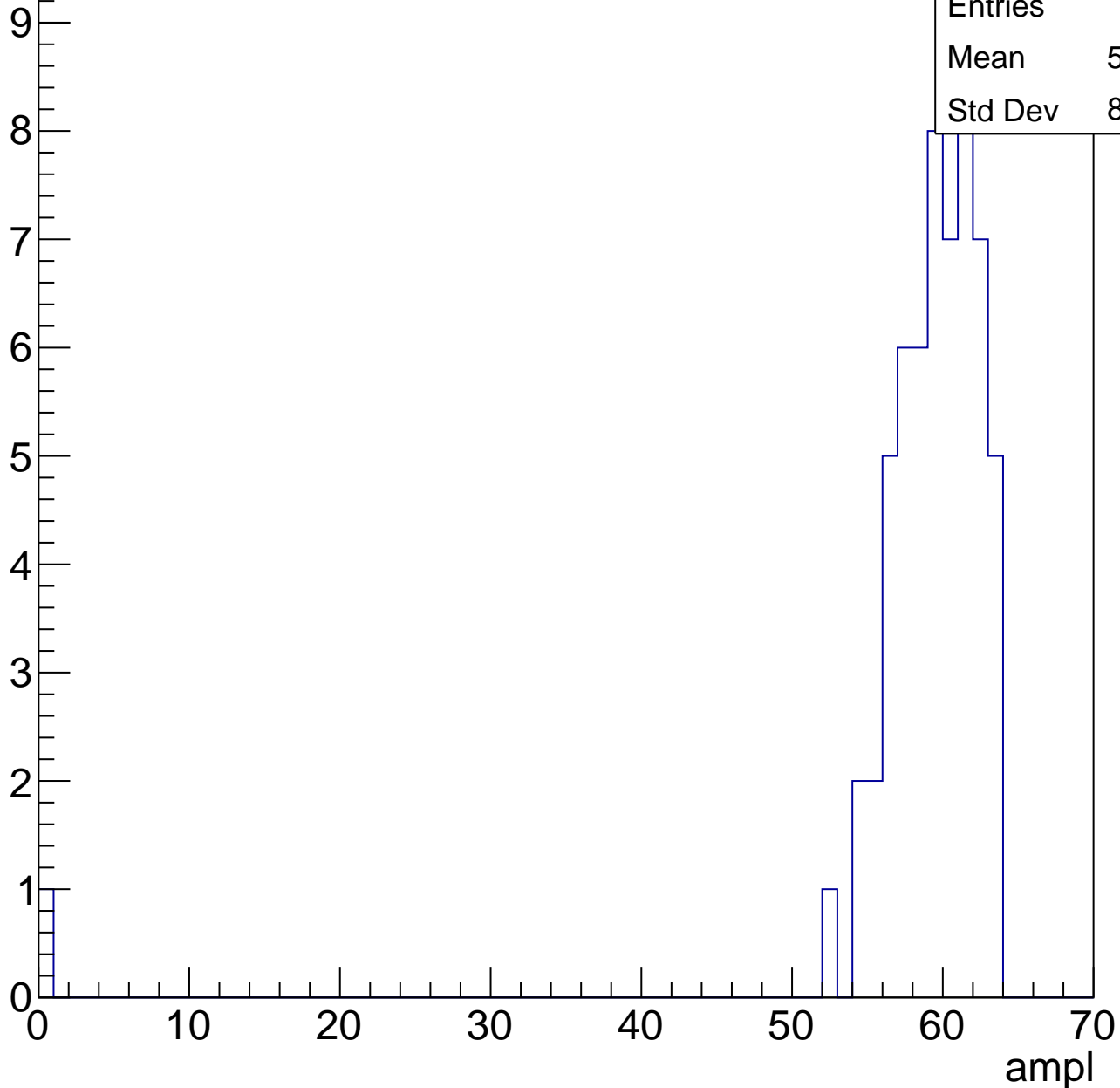
Entries	57
Mean	53.91
Std Dev	3.315



# B1L102S, U24-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

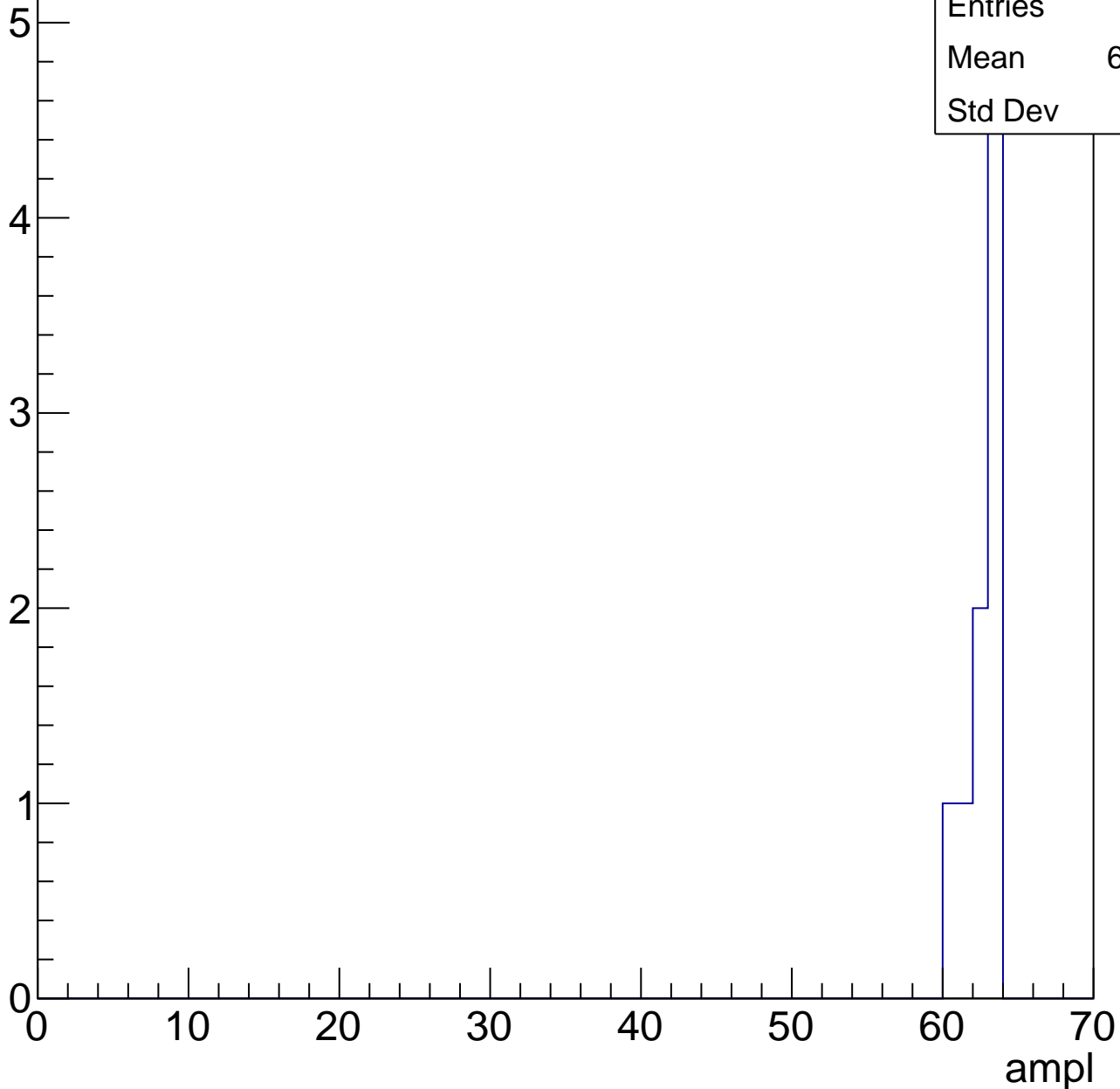


# B1L102S, U24-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.22
Std Dev	1.03





# B1L102S, U24-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U24-ch36, adc0

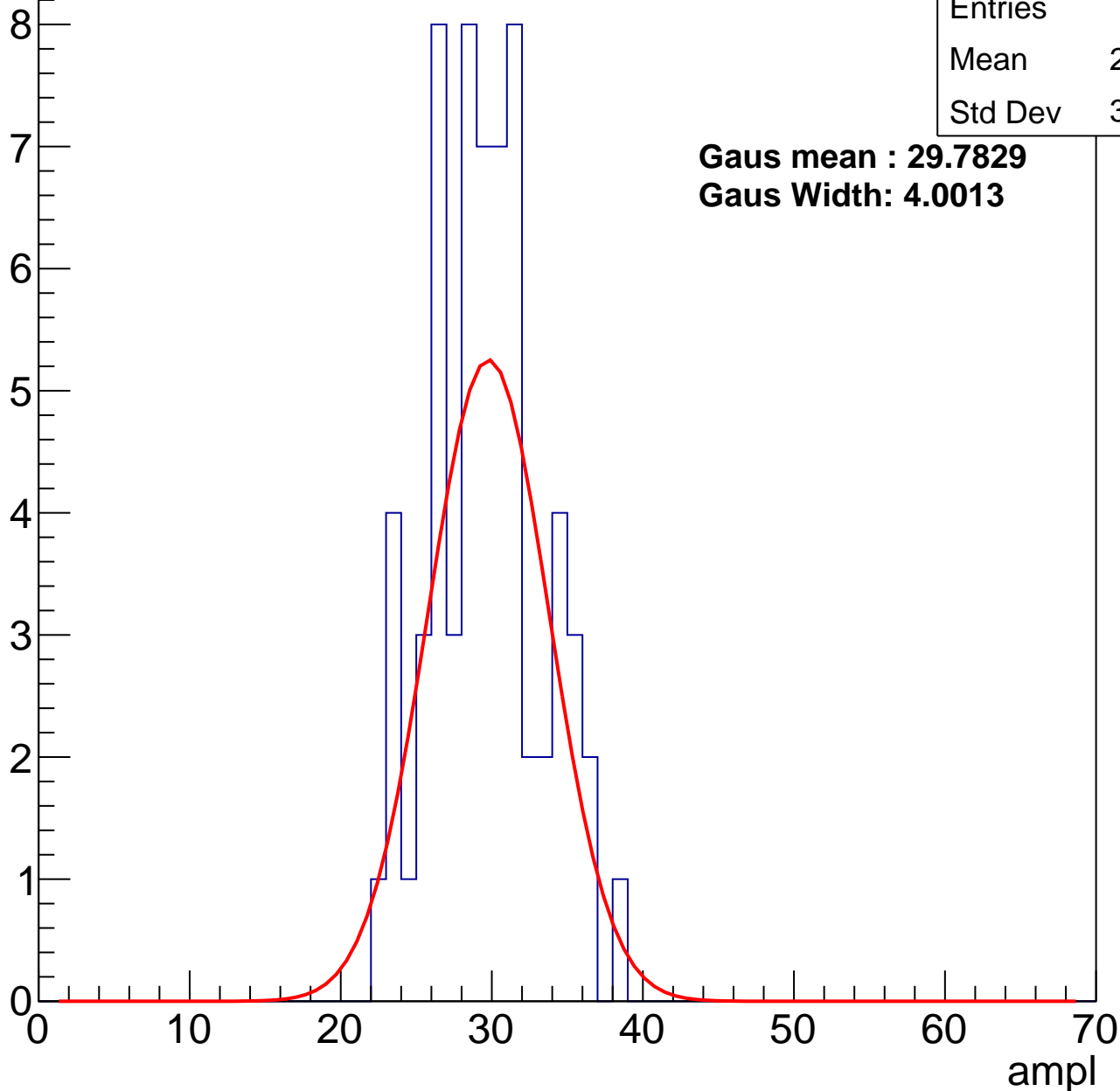
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.19
Std Dev	3.605

**Gaus mean : 29.7829**

**Gaus Width: 4.0013**



# B1L102S, U24-ch36, adc1

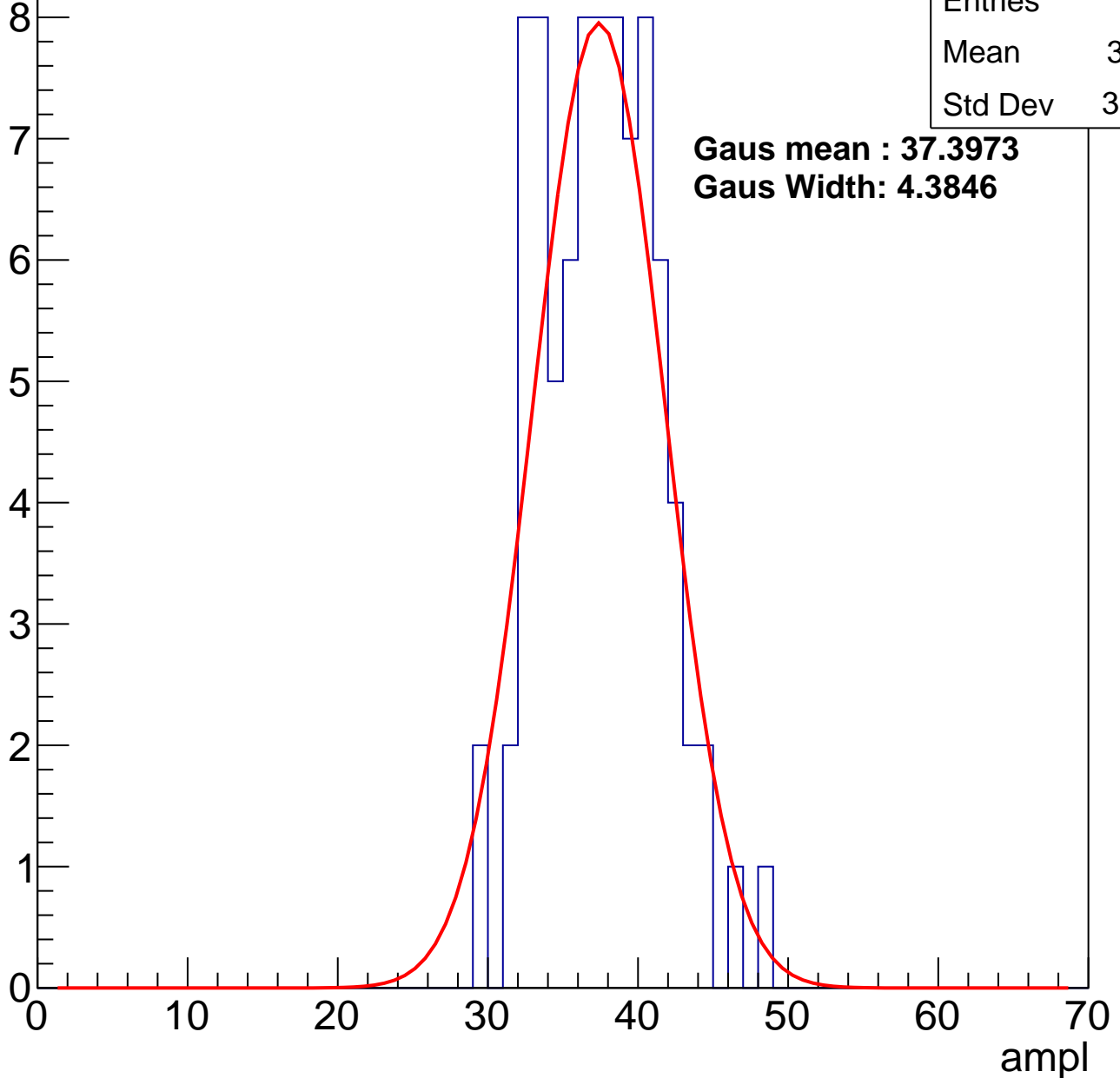
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	86
Mean	37.01
Std Dev	3.859

**Gaus mean : 37.3973**

**Gaus Width: 4.3846**

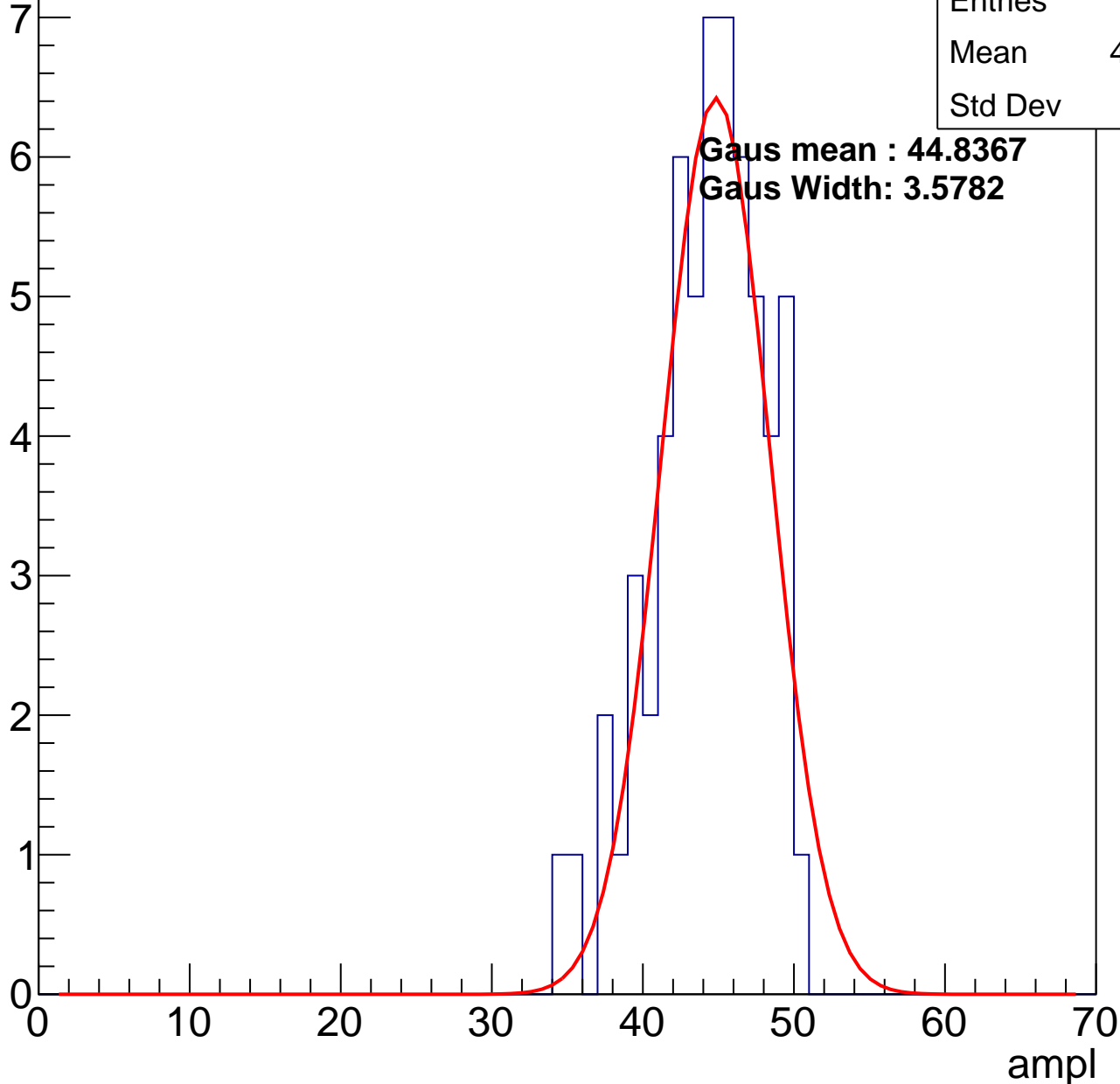


# B1L102S, U24-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.83
Std Dev	3.62

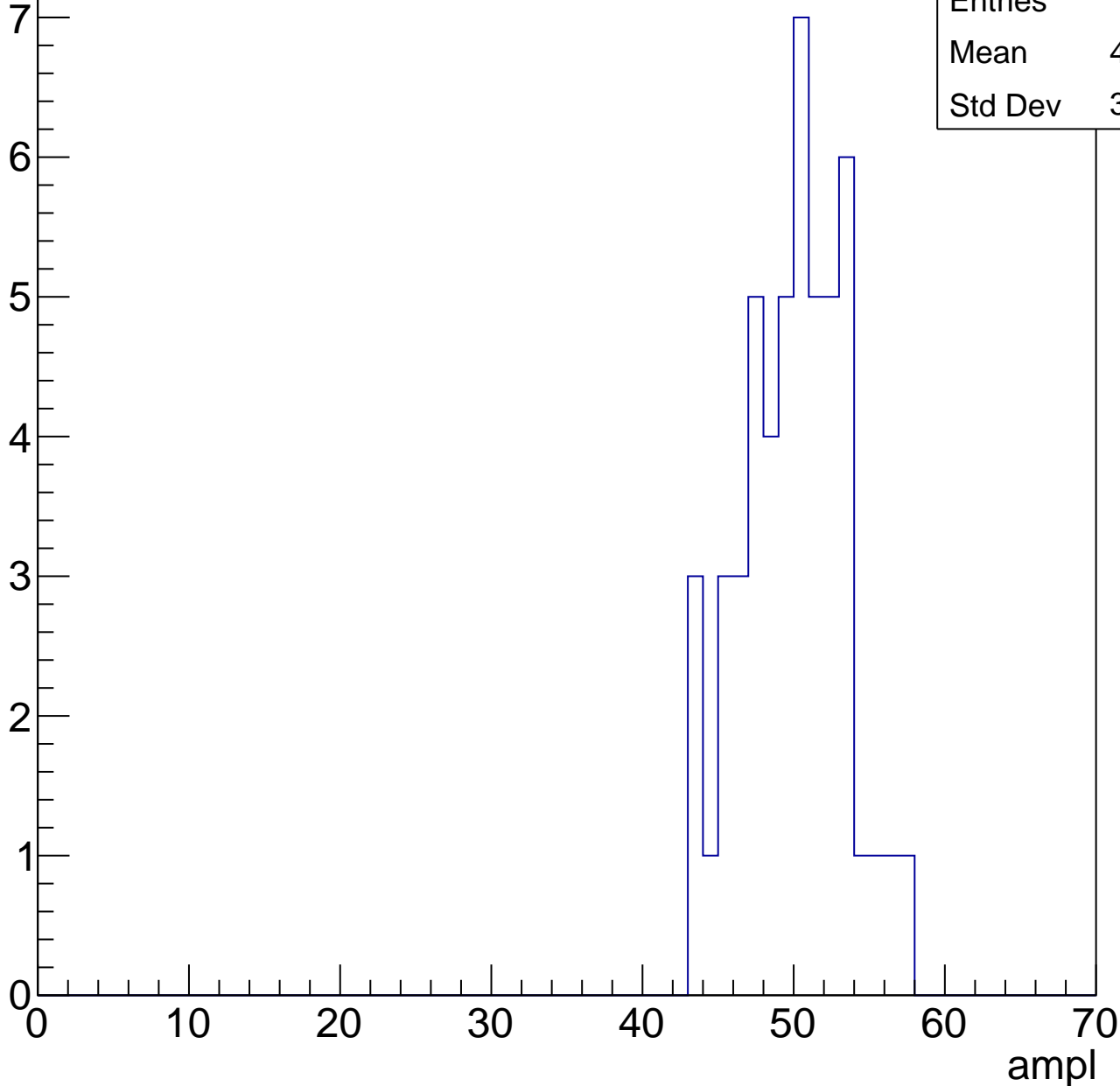


# B1L102S, U24-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

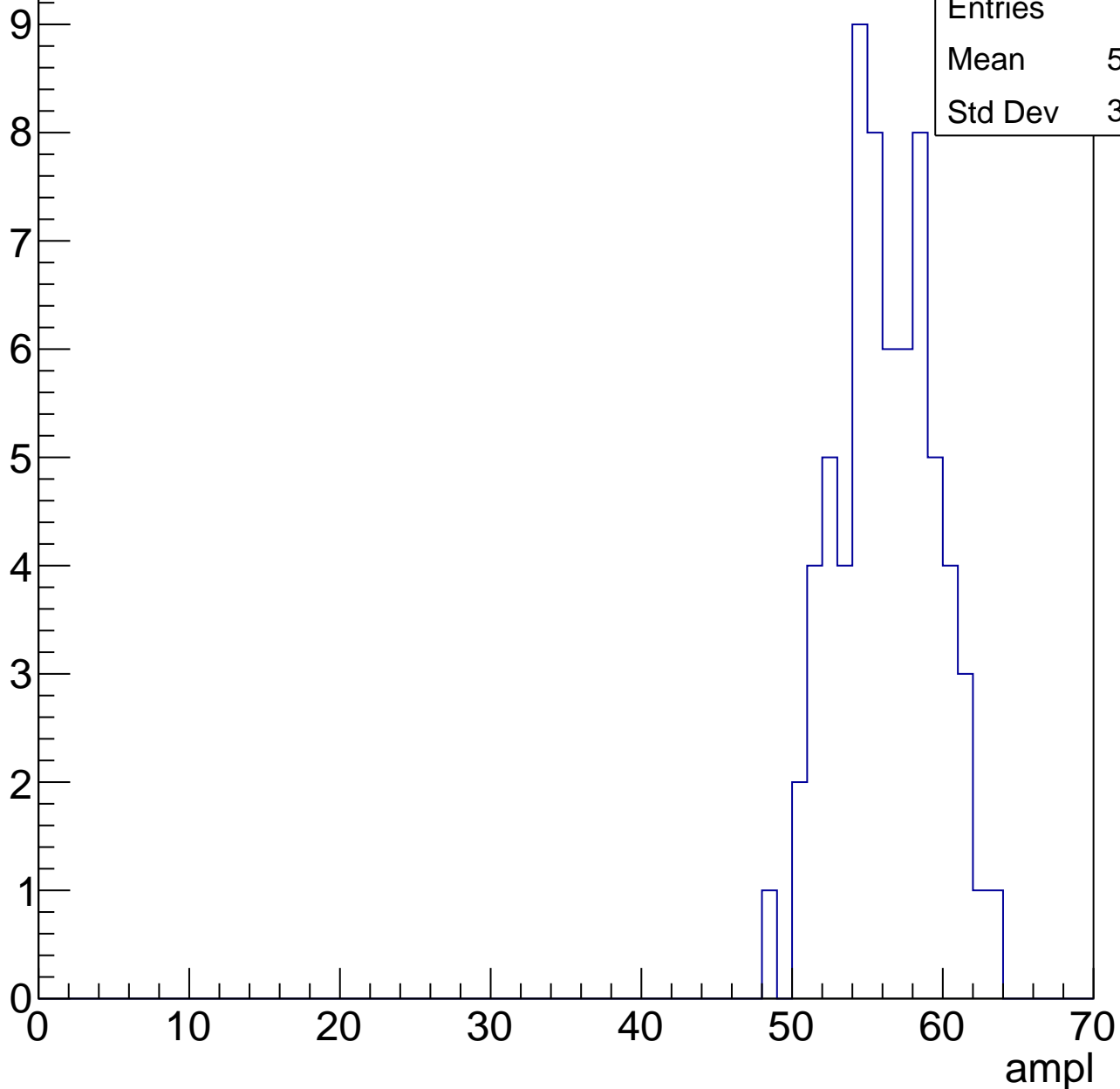
Entries	51
Mean	49.47
Std Dev	3.322



# B1L102S, U24-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



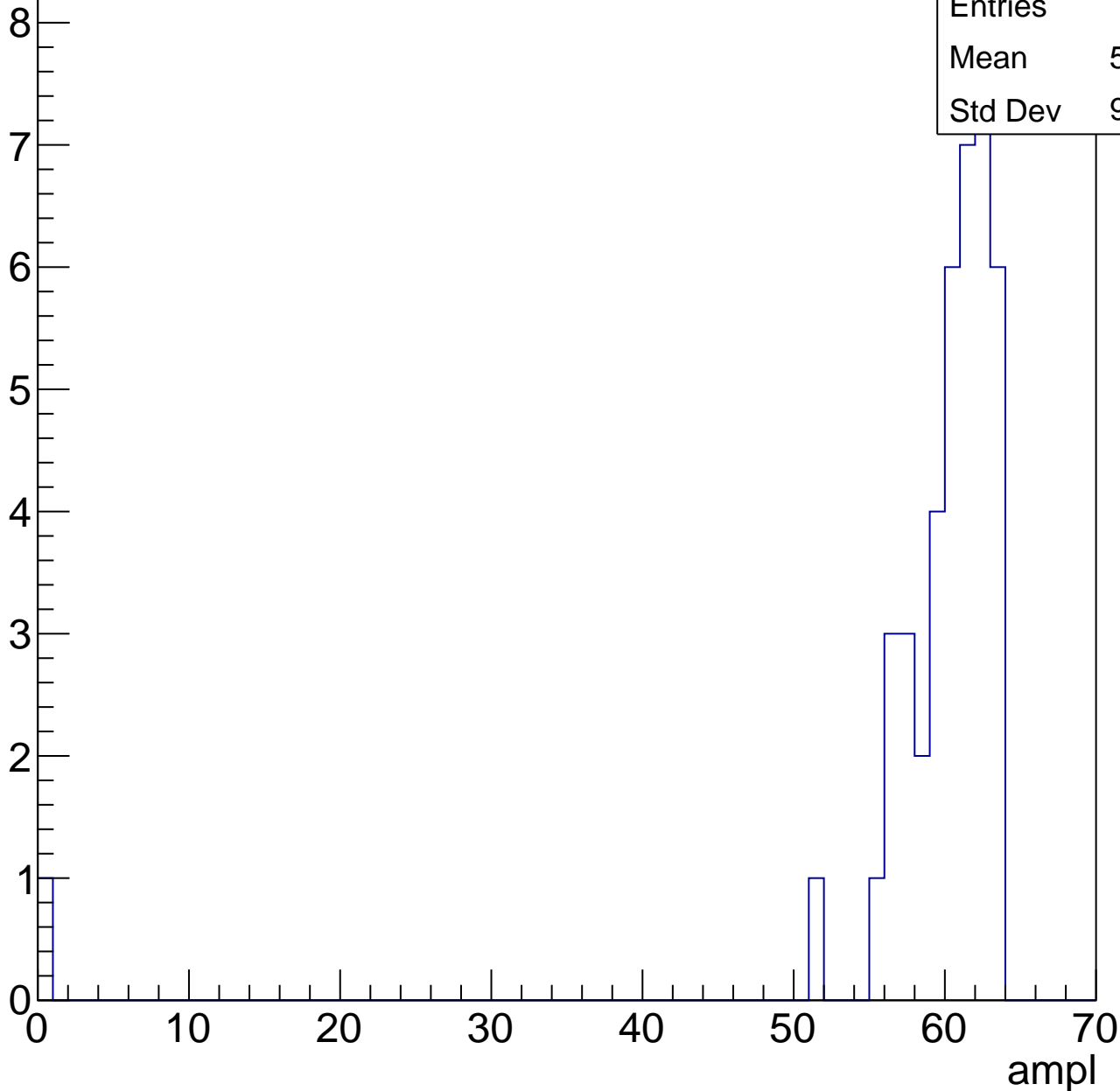
Entries	67
Mean	55.75
Std Dev	3.229

# B1L102S, U24-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.52
Std Dev	9.505



# B1L102S, U24-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch37, adc0

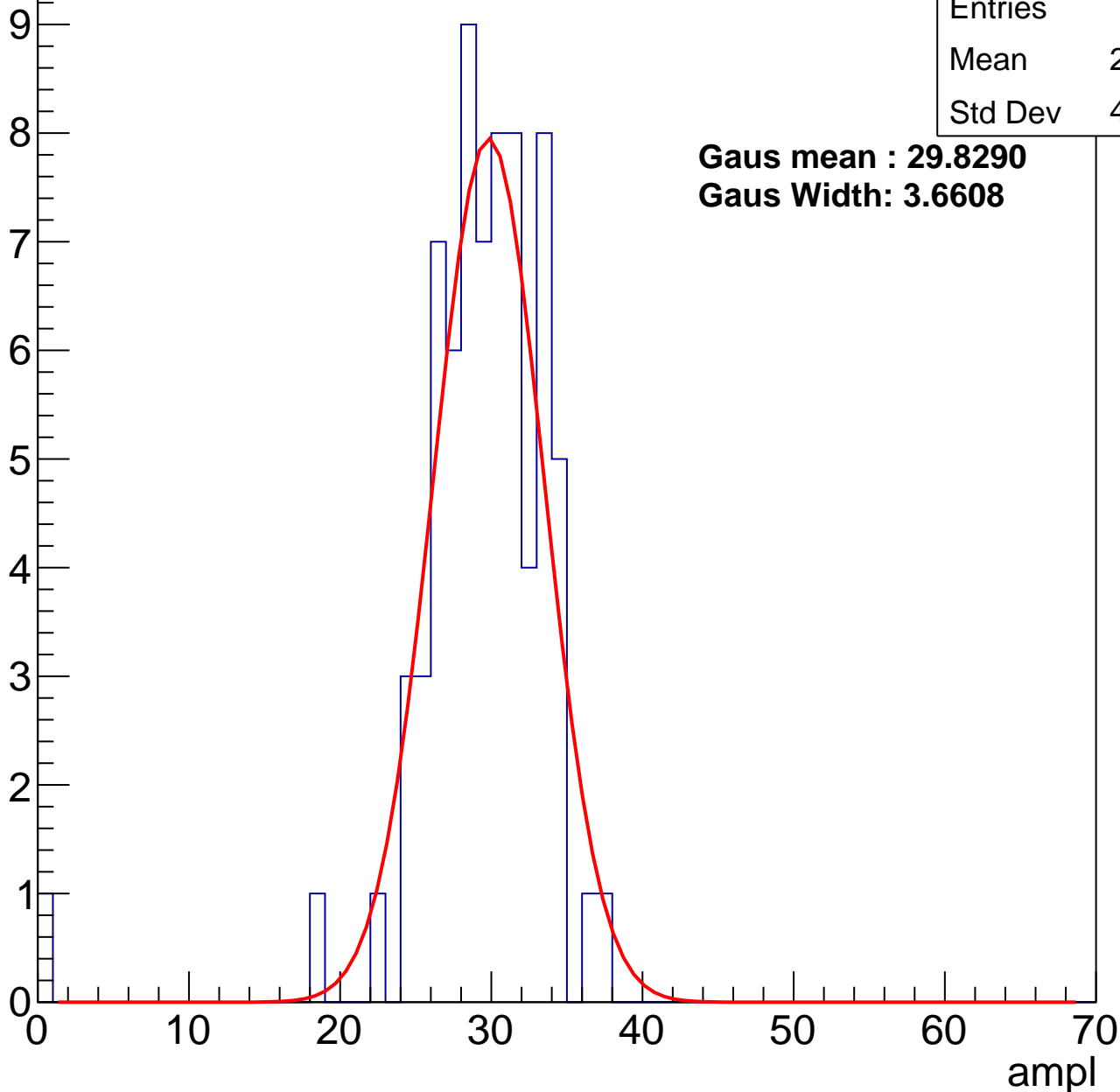
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.89
Std Dev	4.785

**Gaus mean : 29.8290**

**Gaus Width: 3.6608**



# B1L102S, U24-ch37, adc1

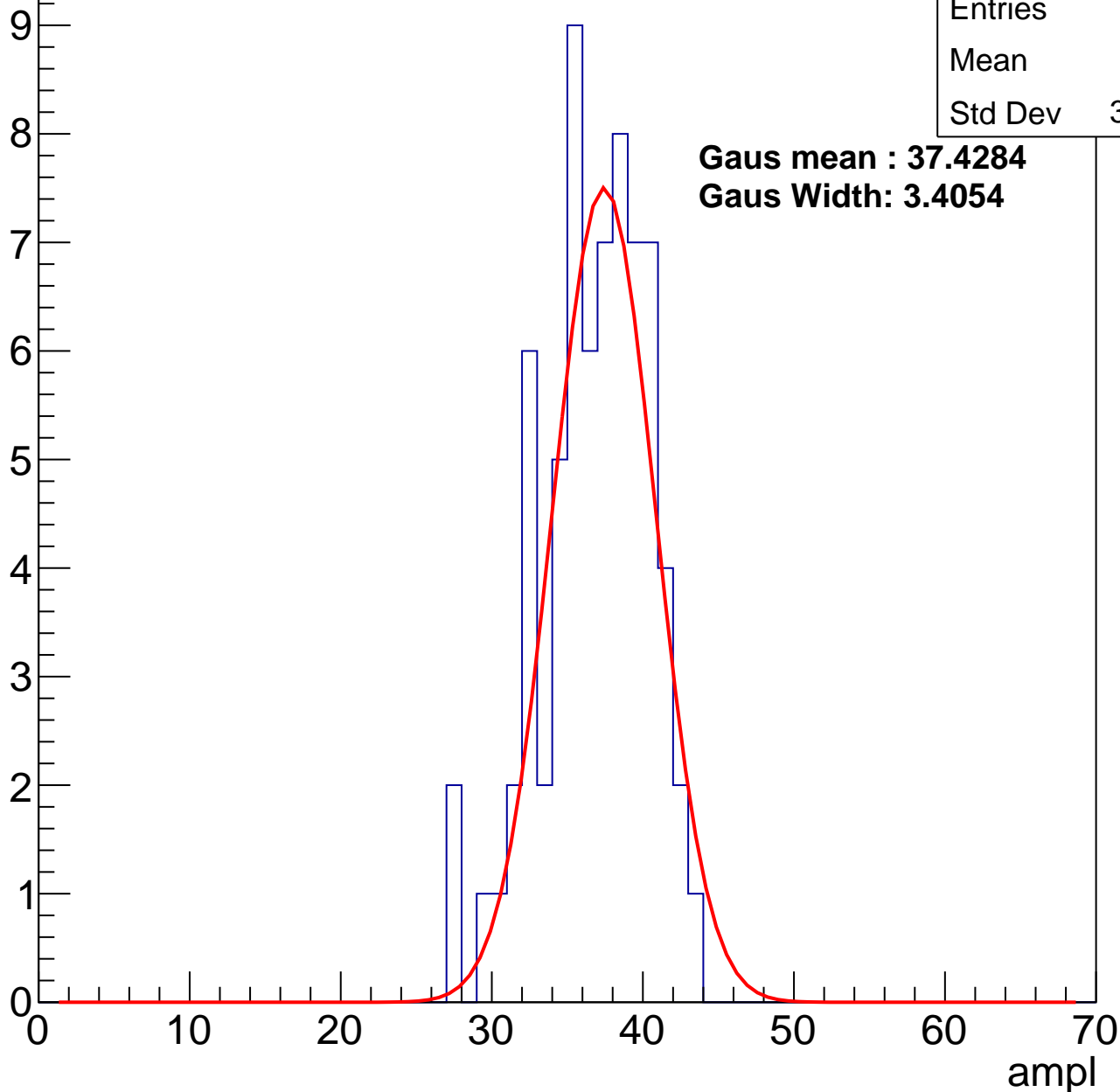
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.3
Std Dev	3.523

**Gaus mean : 37.4284**

**Gaus Width: 3.4054**



# B1L102S, U24-ch37, adc2

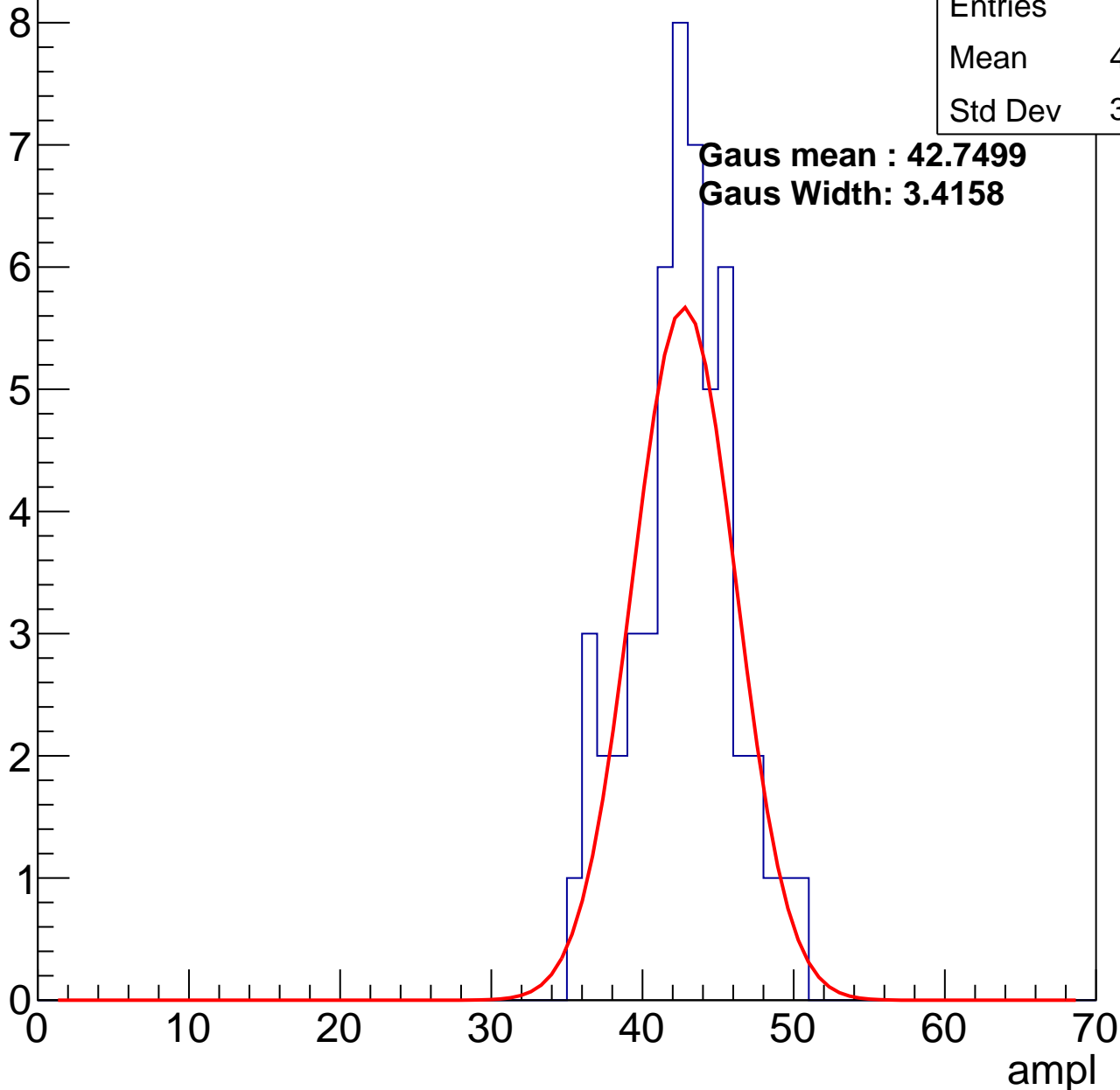
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.19
Std Dev	3.348

**Gaus mean : 42.7499**

**Gaus Width: 3.4158**

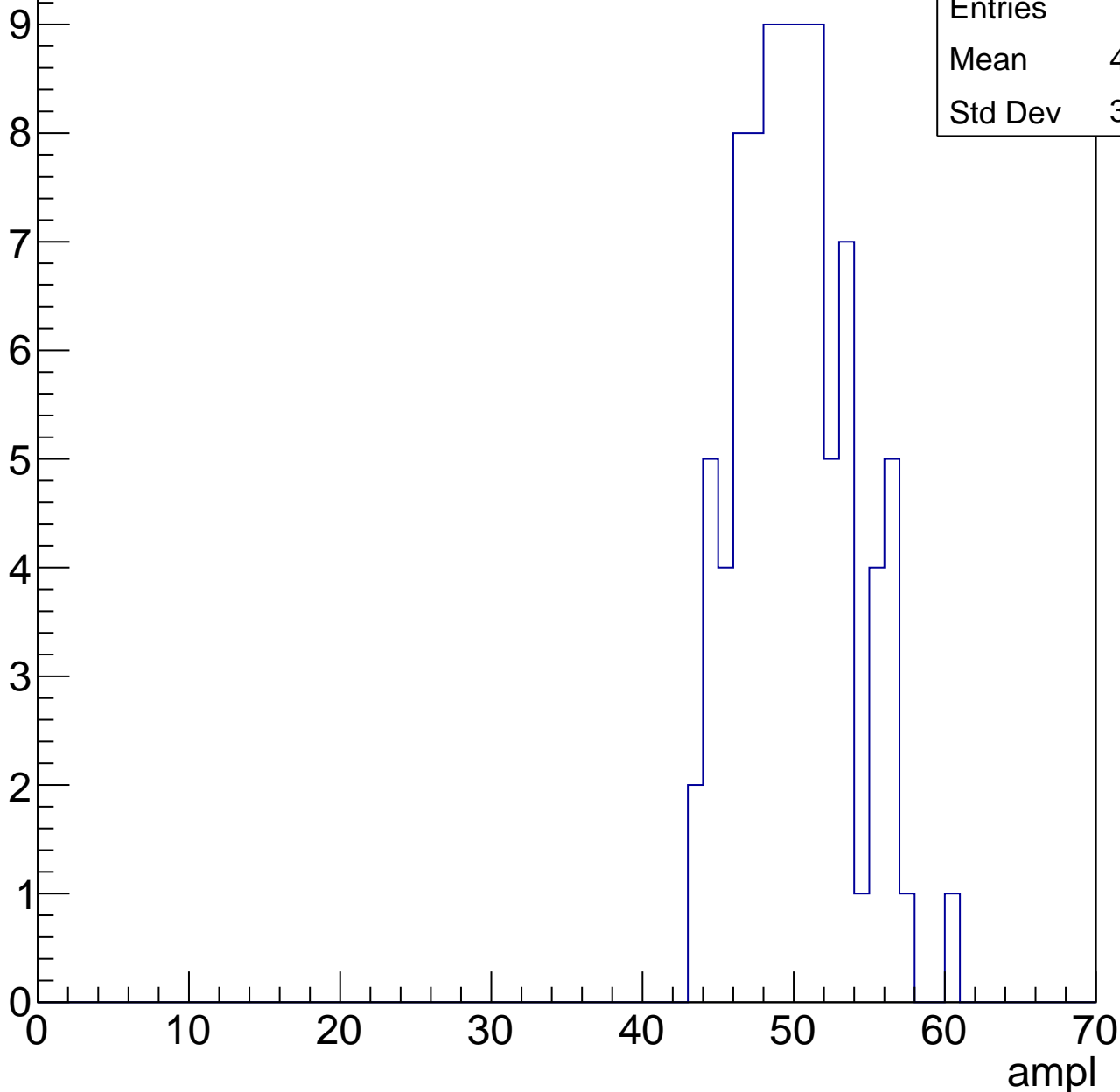


# B1L102S, U24-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

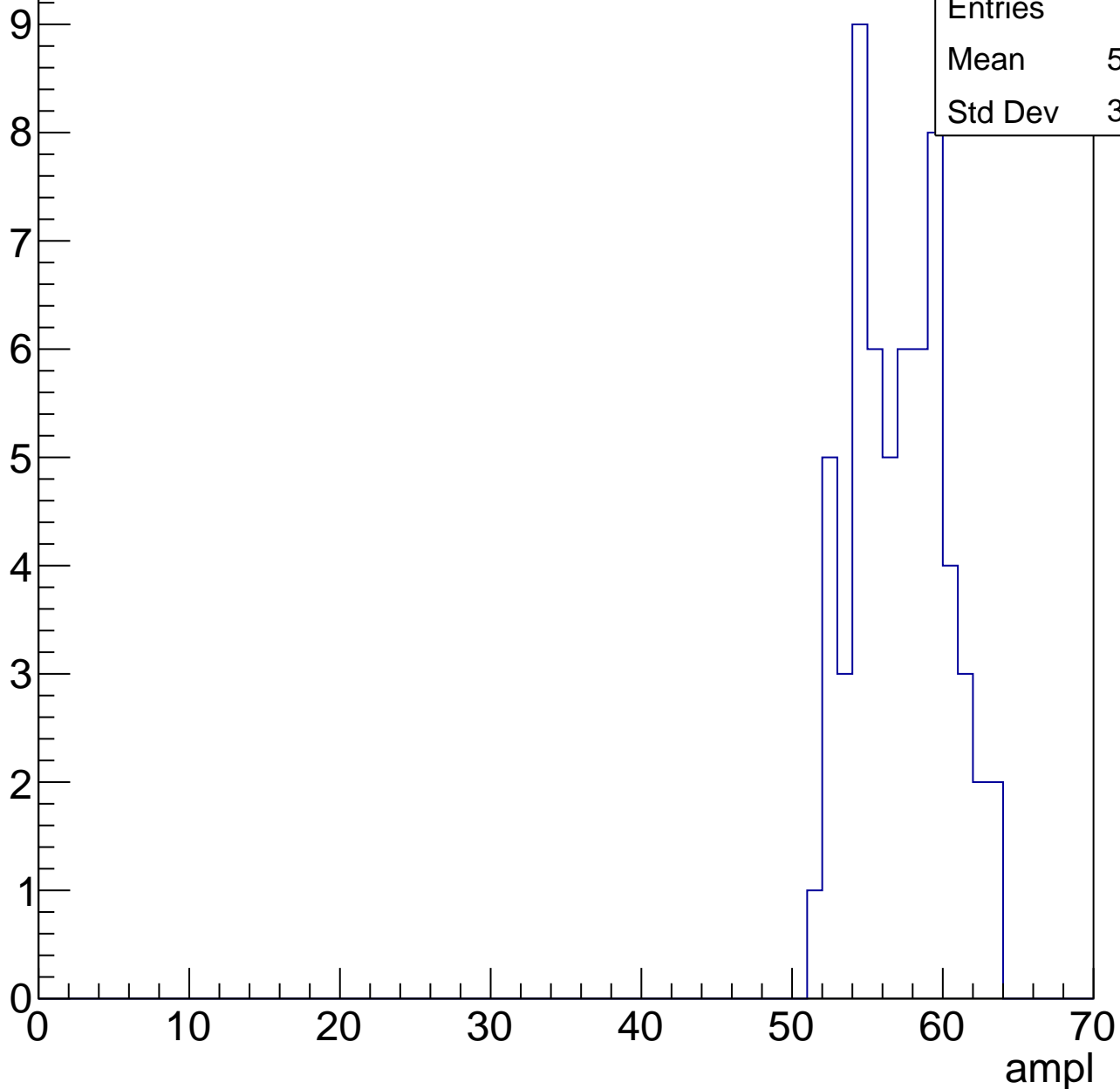
Entries	87
Mean	49.59
Std Dev	3.628



# B1L102S, U24-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

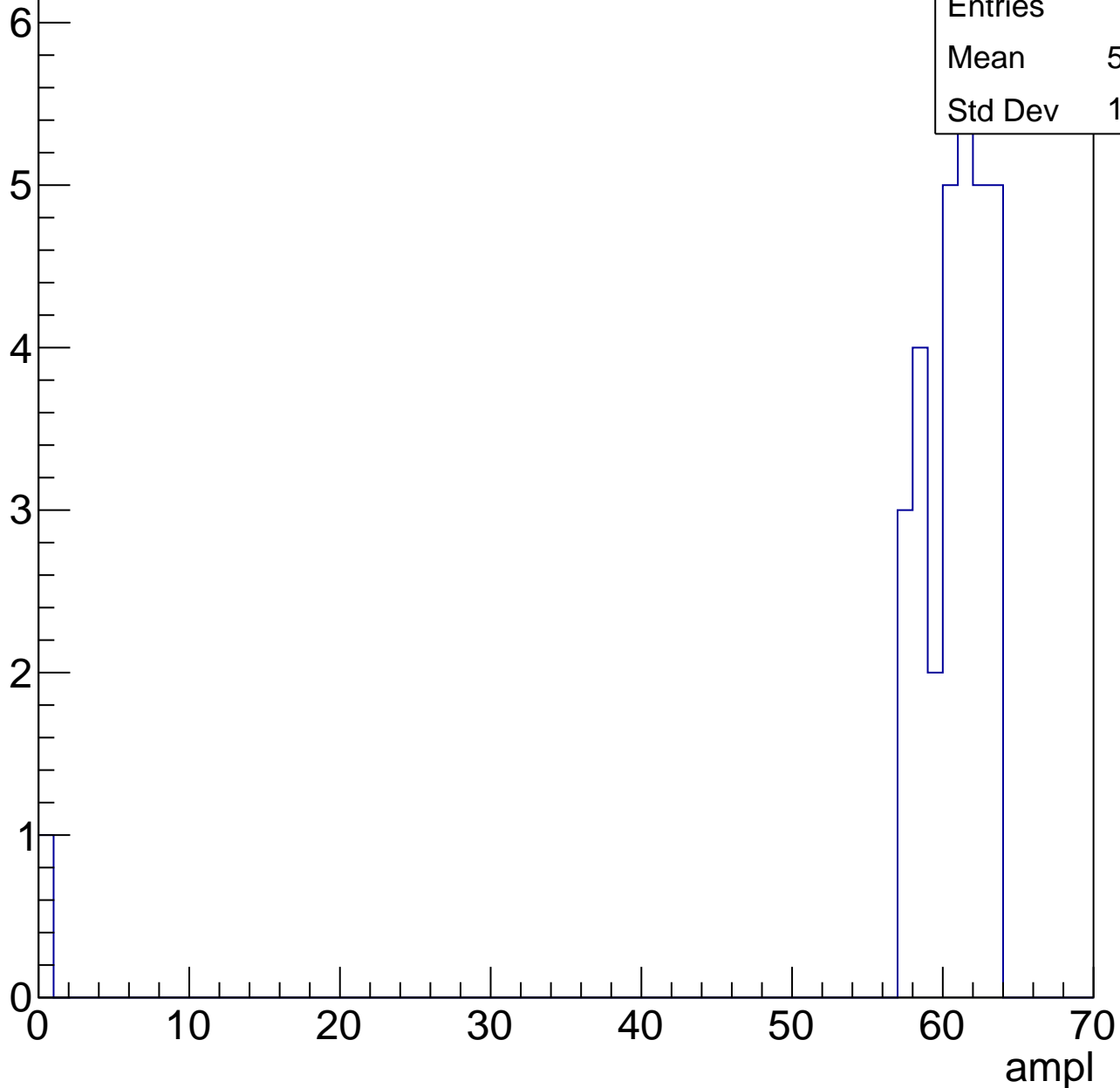
Entry



# B1L102S, U24-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch38, adc0

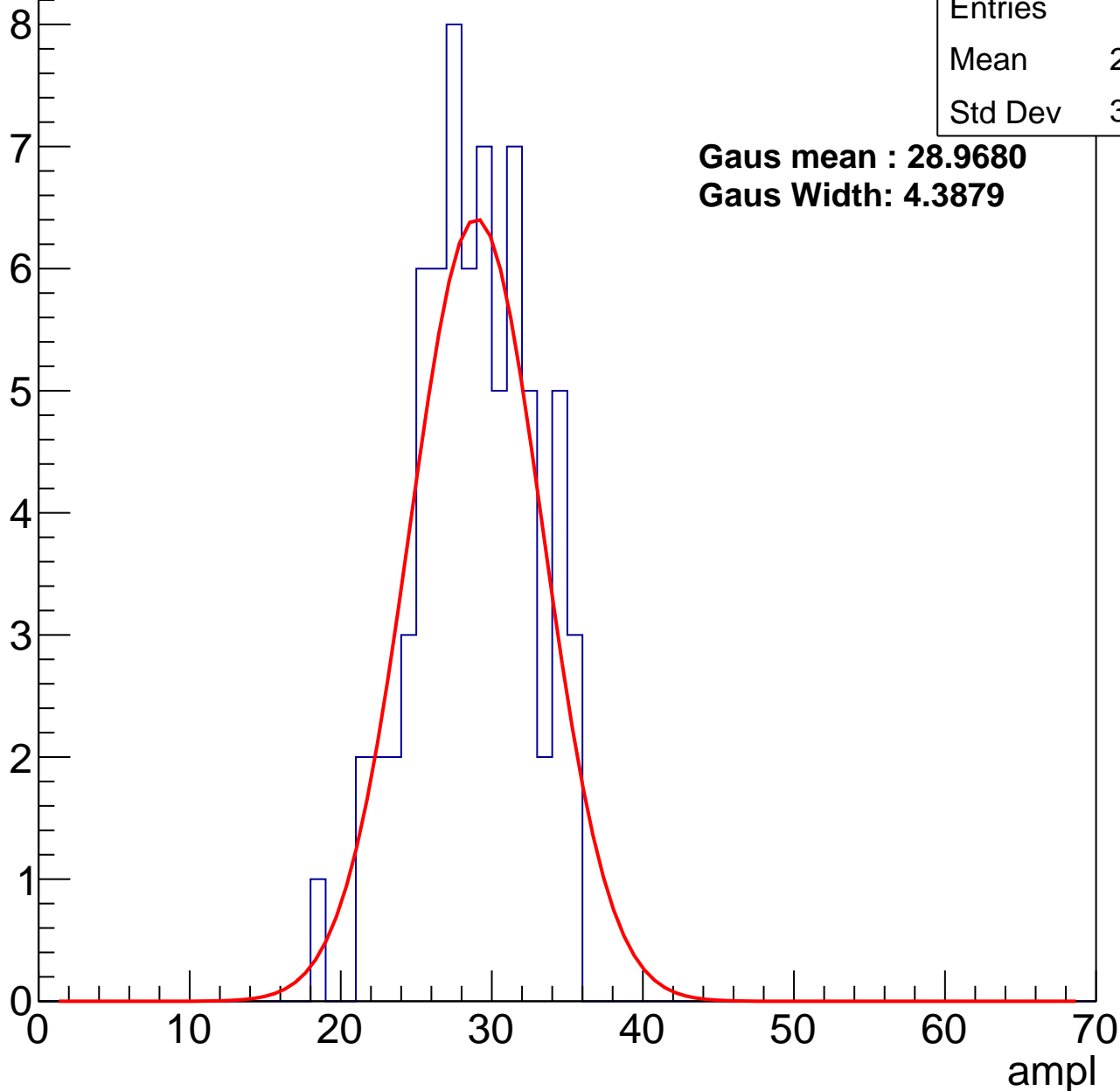
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.33
Std Dev	3.767

**Gaus mean : 28.9680**

**Gaus Width: 4.3879**



# B1L102S, U24-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	79
Mean	36.09
Std Dev	4.441

**Gaus mean : 36.8604**

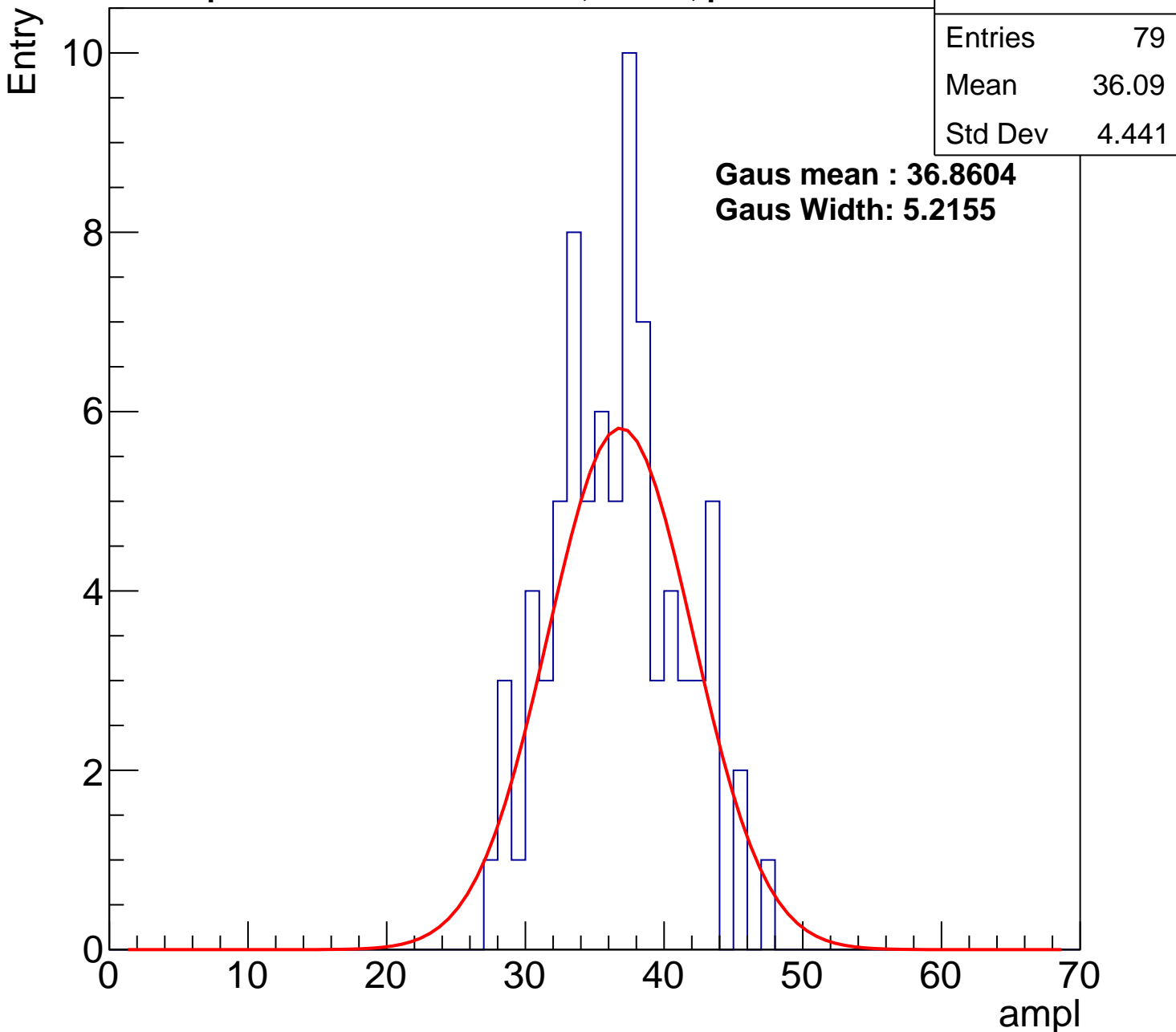
**Gaus Width: 5.2155**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch38, adc2

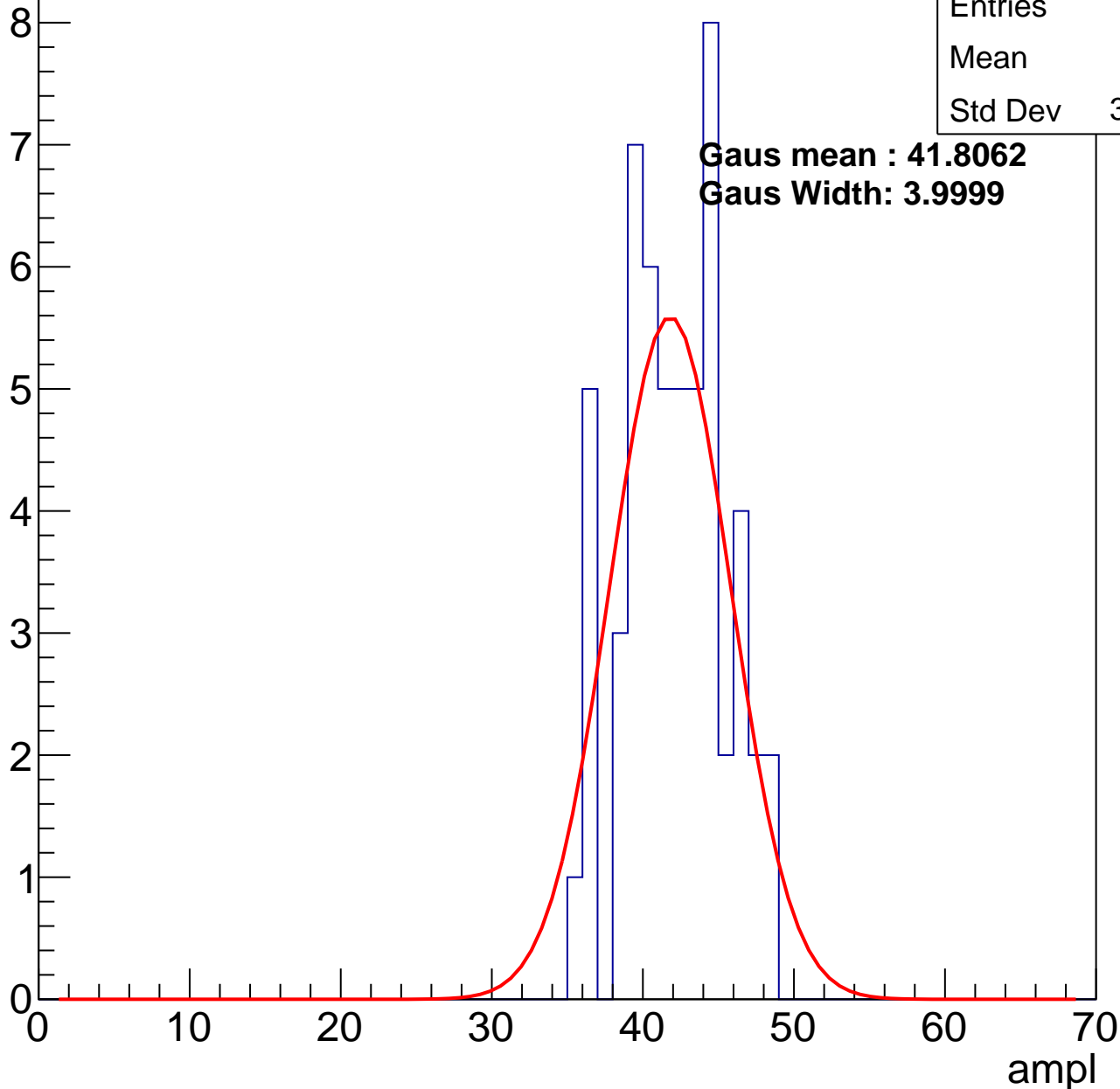
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.6
Std Dev	3.306

**Gaus mean : 41.8062**

**Gaus Width: 3.9999**

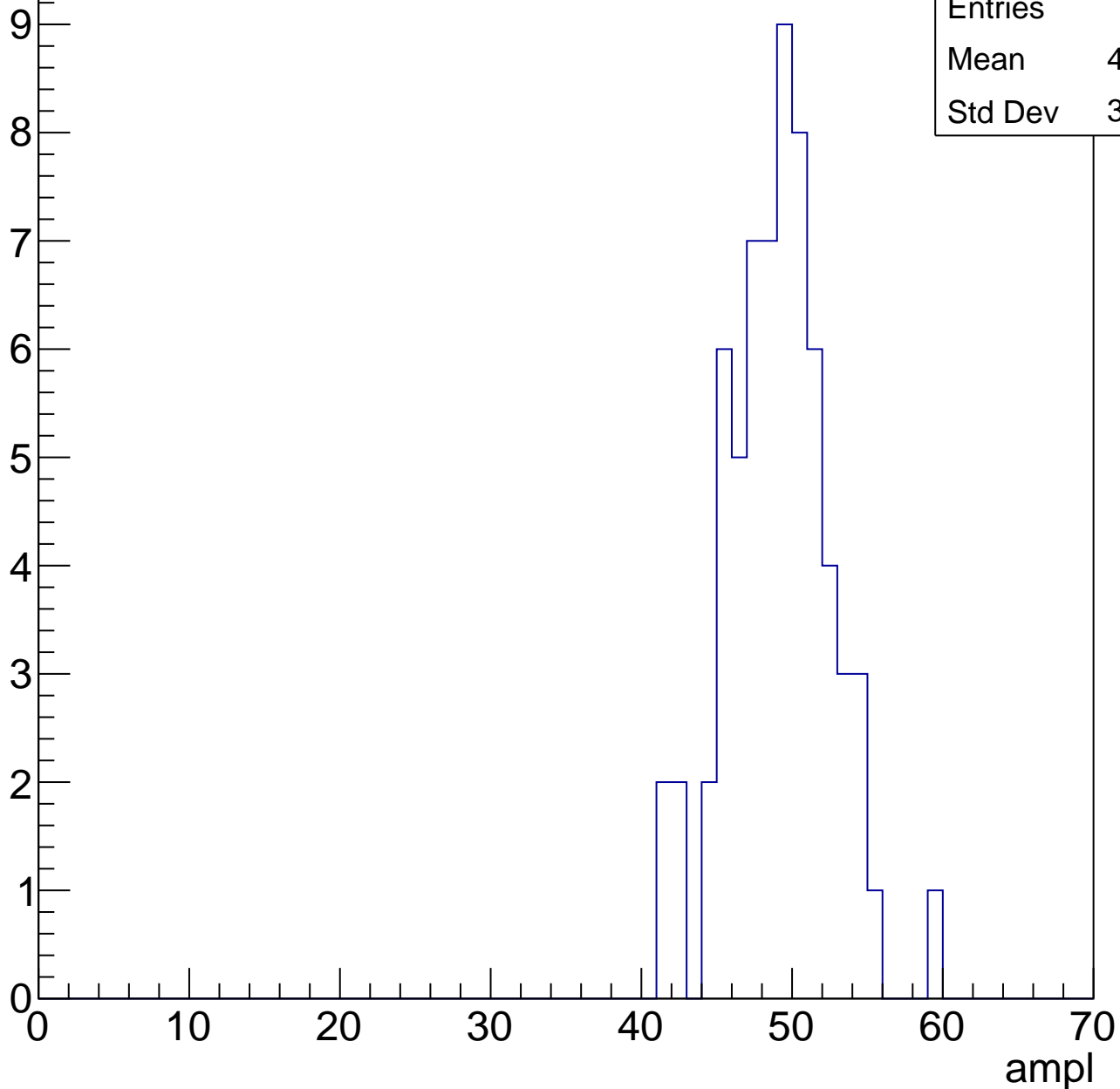


# B1L102S, U24-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.62
Std Dev	3.415

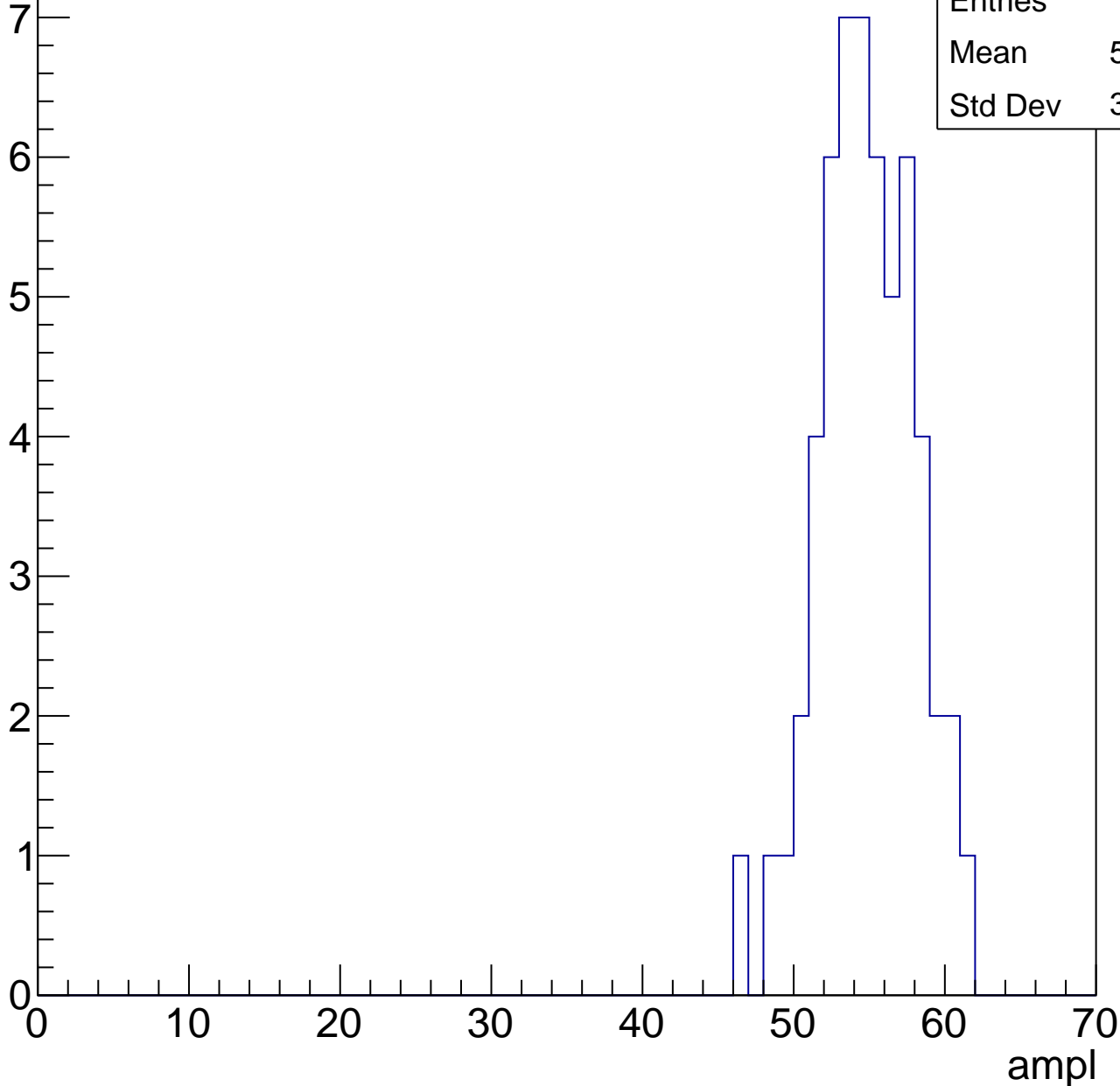


# B1L102S, U24-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

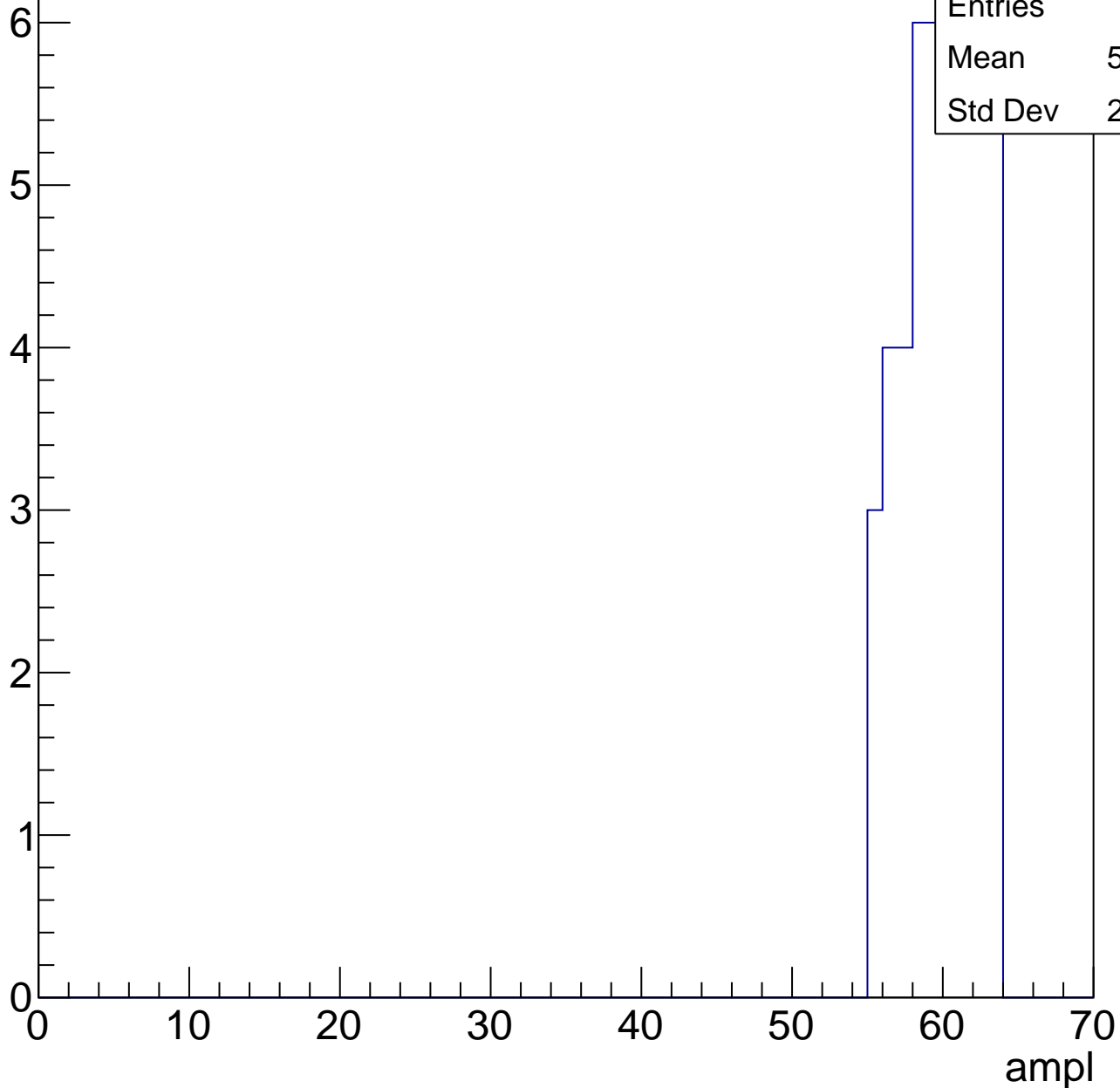
Entries	55
Mean	54.38
Std Dev	3.107



# B1L102S, U24-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

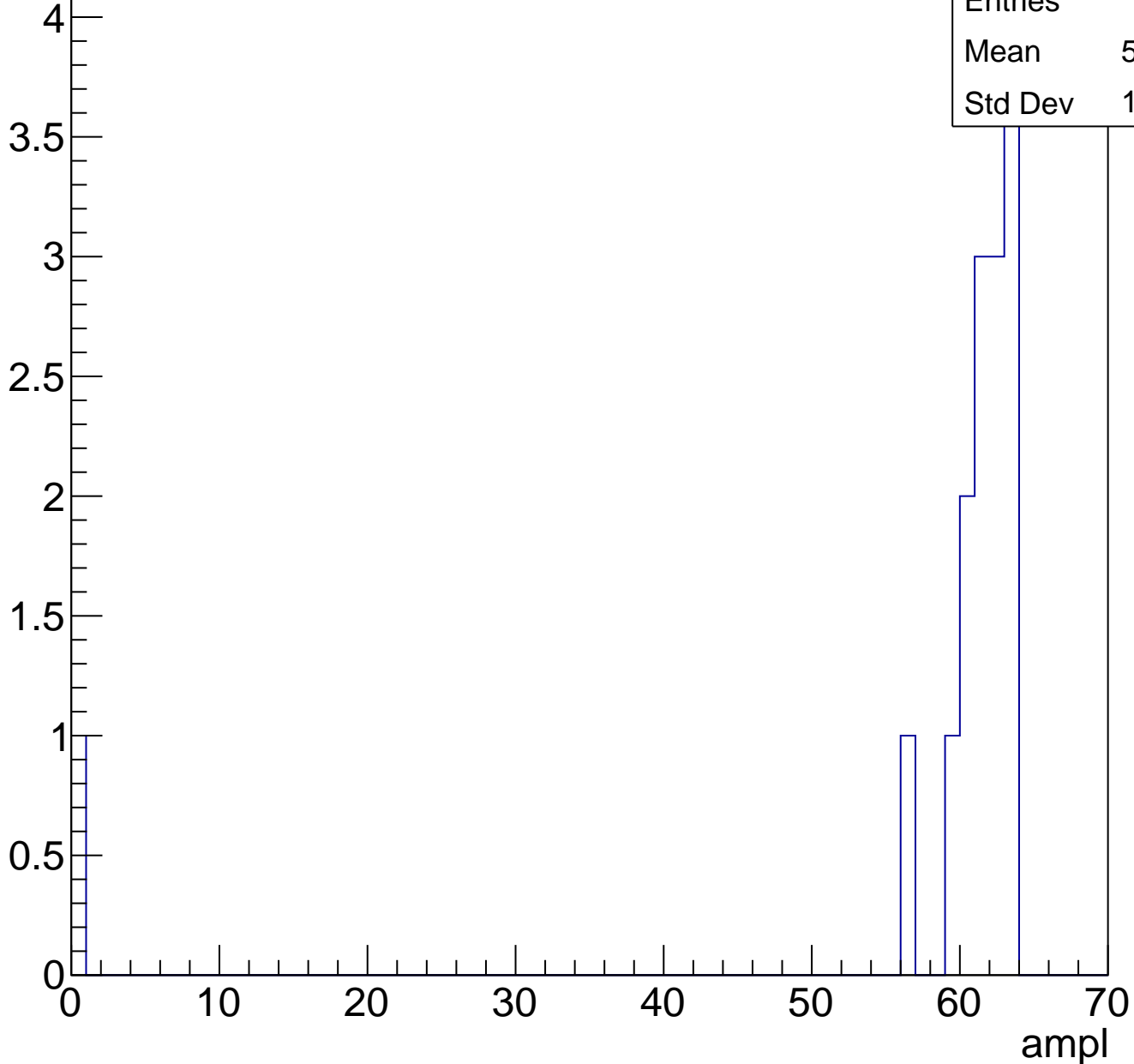


Entries	47
Mean	59.47
Std Dev	2.422

# B1L102S, U24-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	27.82
Std Dev	3.64

**Gaus mean : 28.0038**

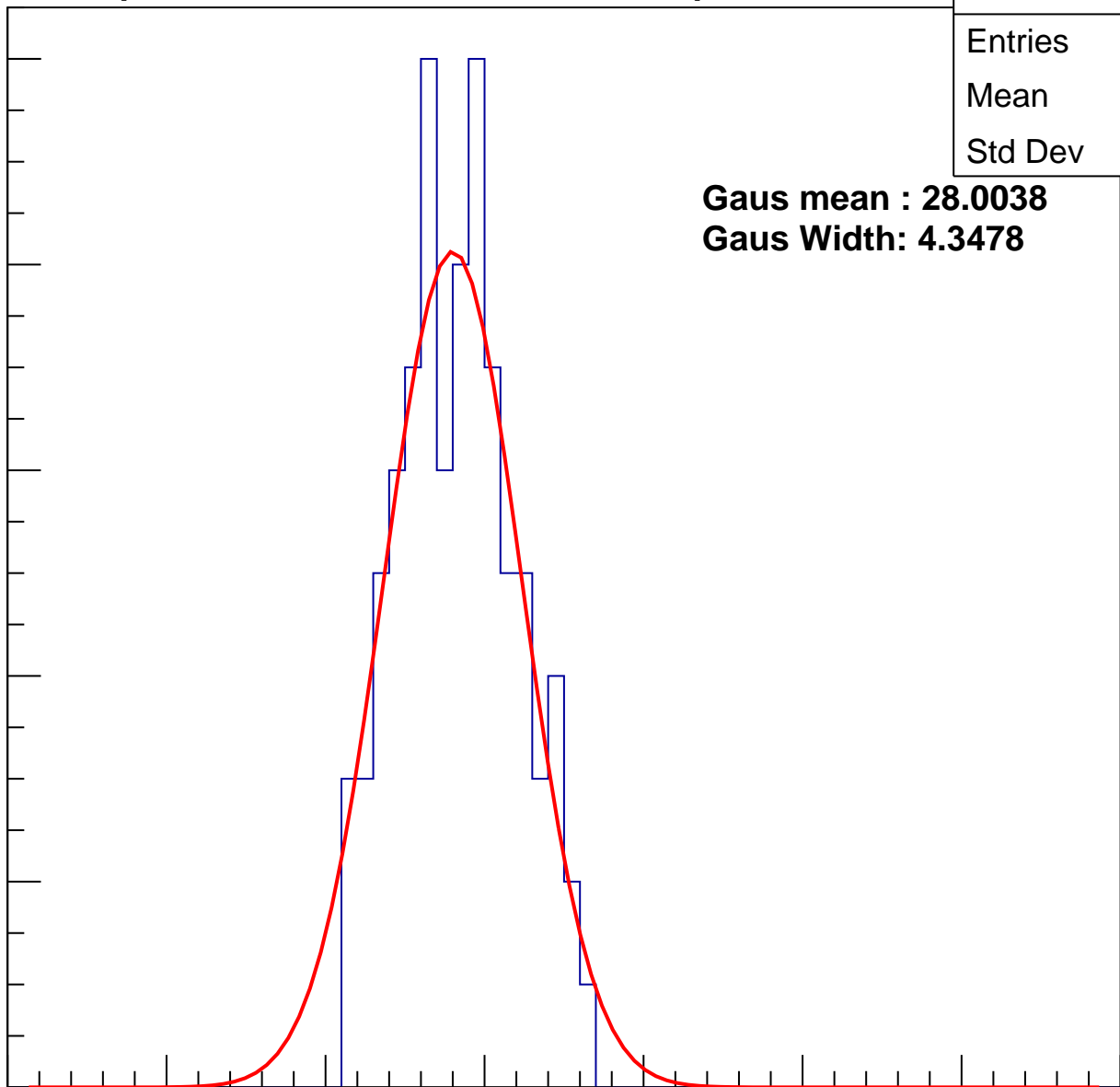
**Gaus Width: 4.3478**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch39, adc1

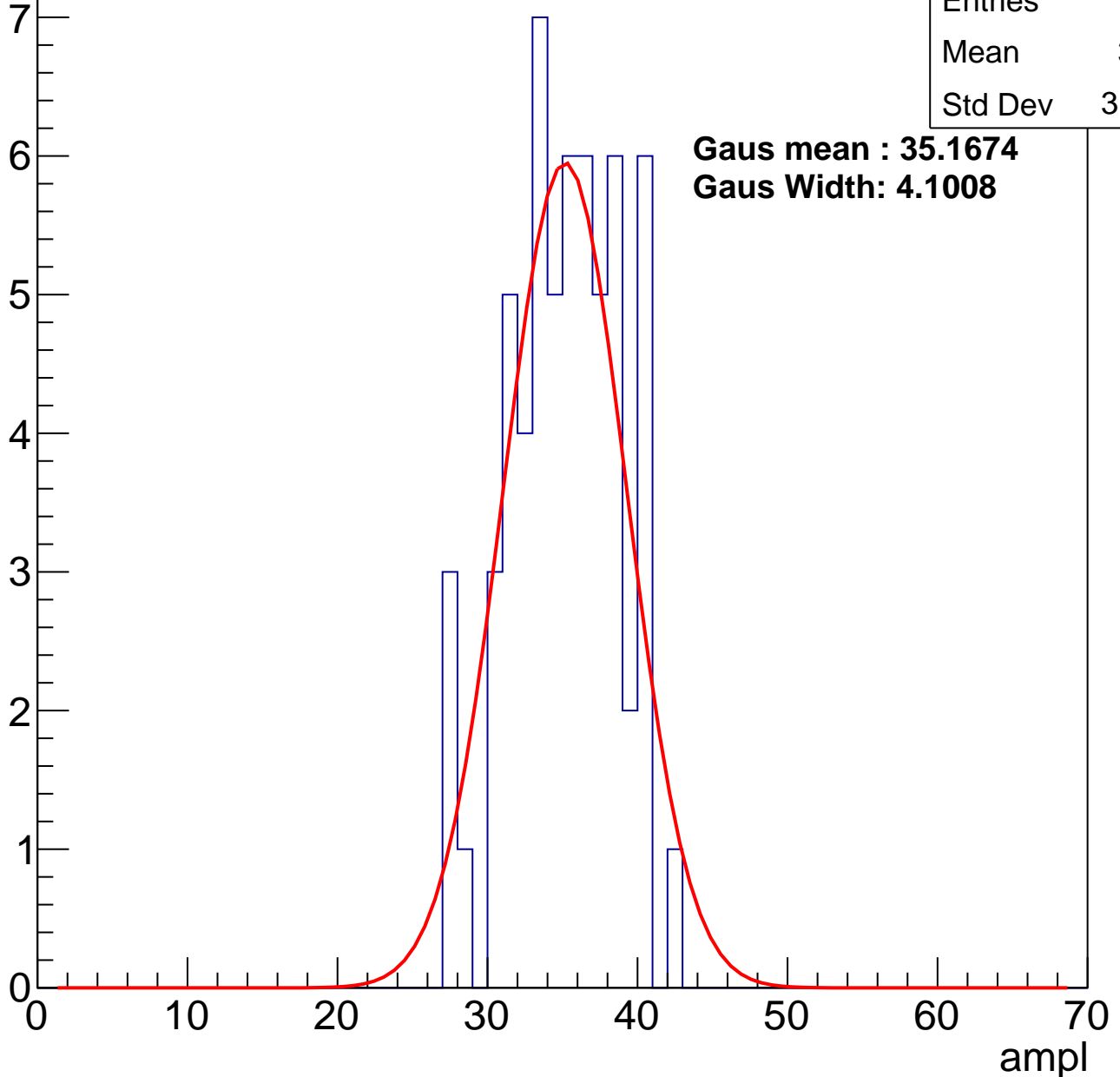
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.7
Std Dev	3.584

**Gaus mean : 35.1674**

**Gaus Width: 4.1008**



# B1L102S, U24-ch39, adc2

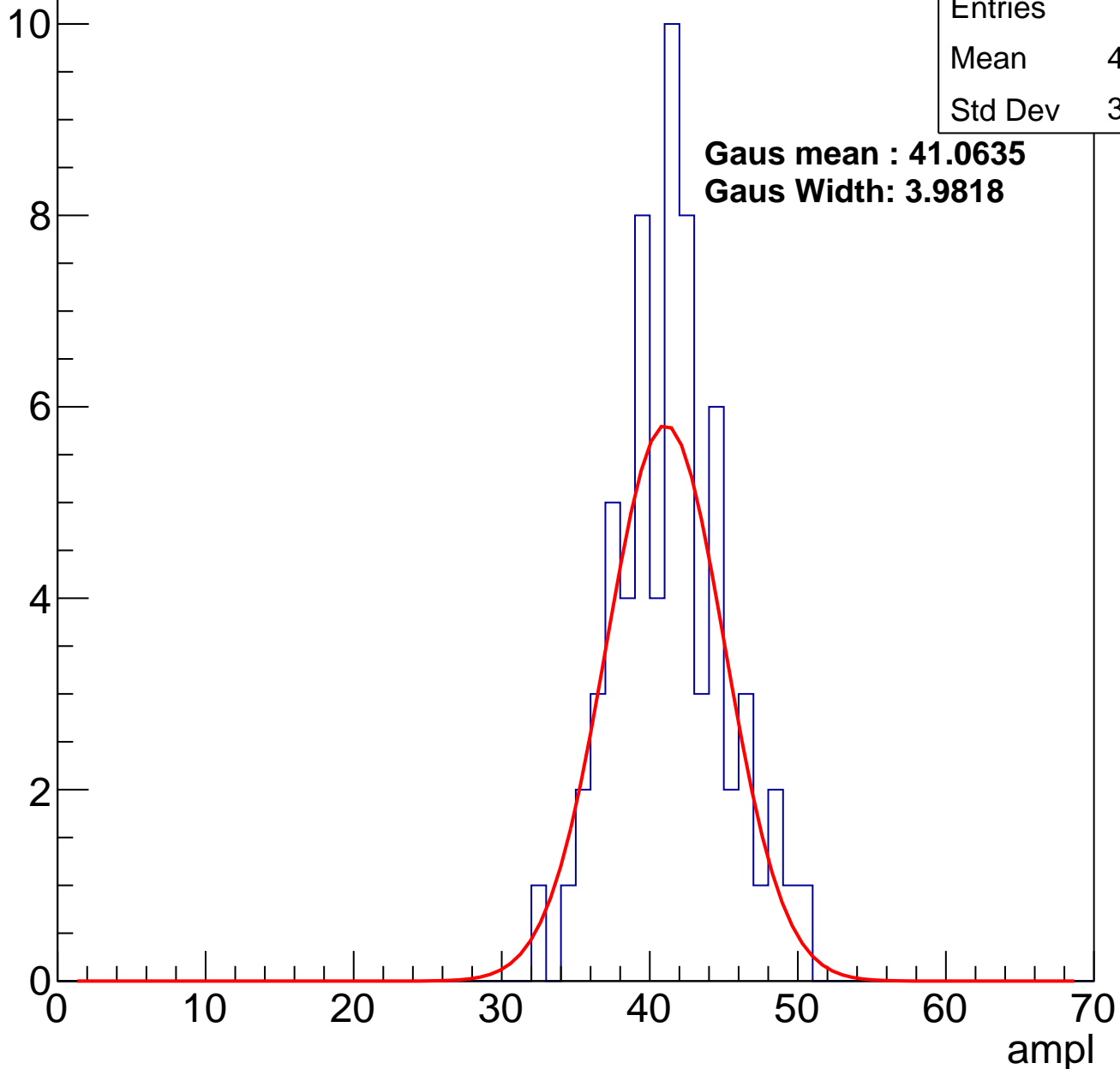
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	65
Mean	40.95
Std Dev	3.698

**Gaus mean : 41.0635**

**Gaus Width: 3.9818**

Entry

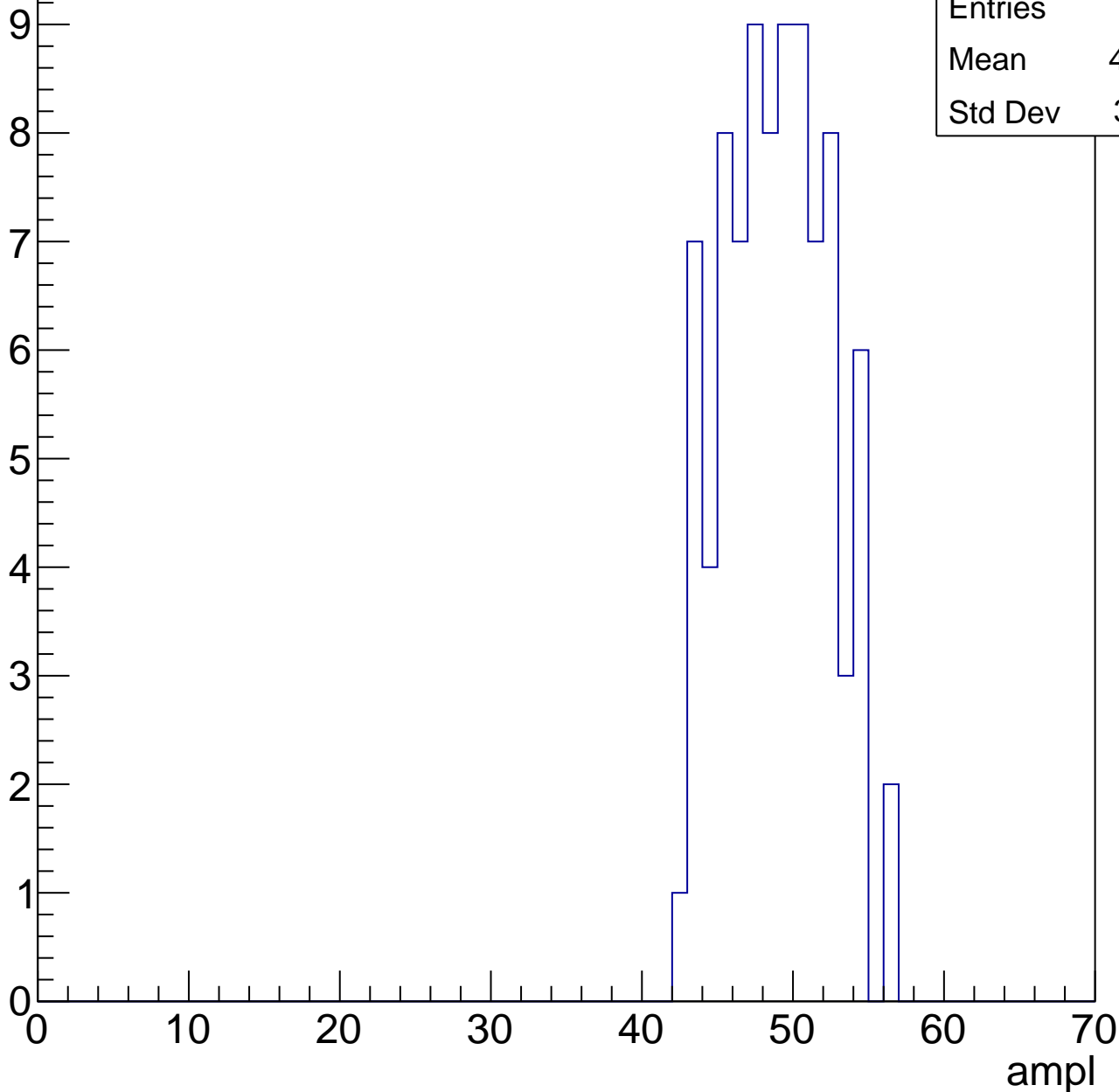


# B1L102S, U24-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	48.49
Std Dev	3.401

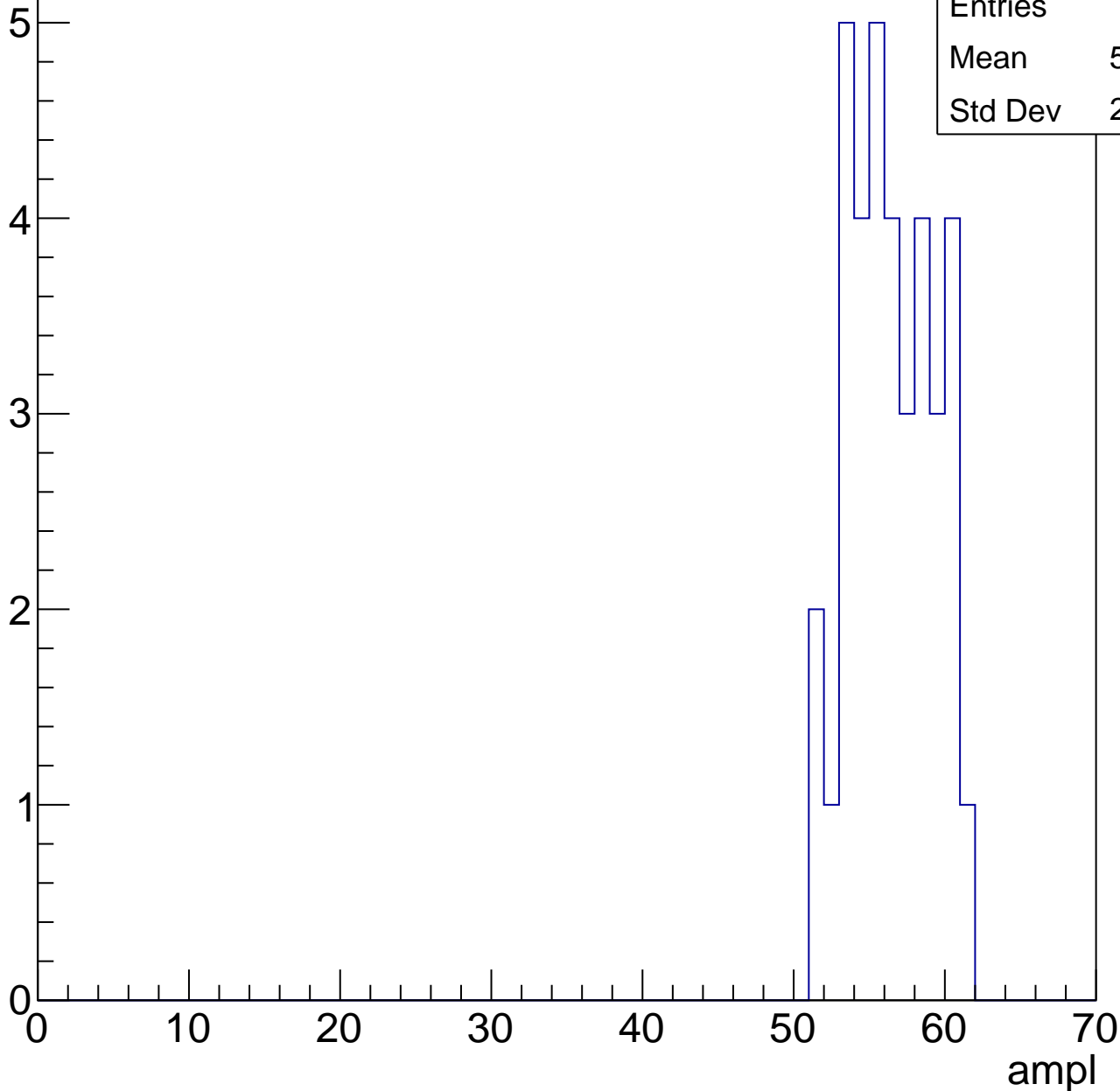


# B1L102S, U24-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	55.97
Std Dev	2.723



# B1L102S, U24-ch39, adc5

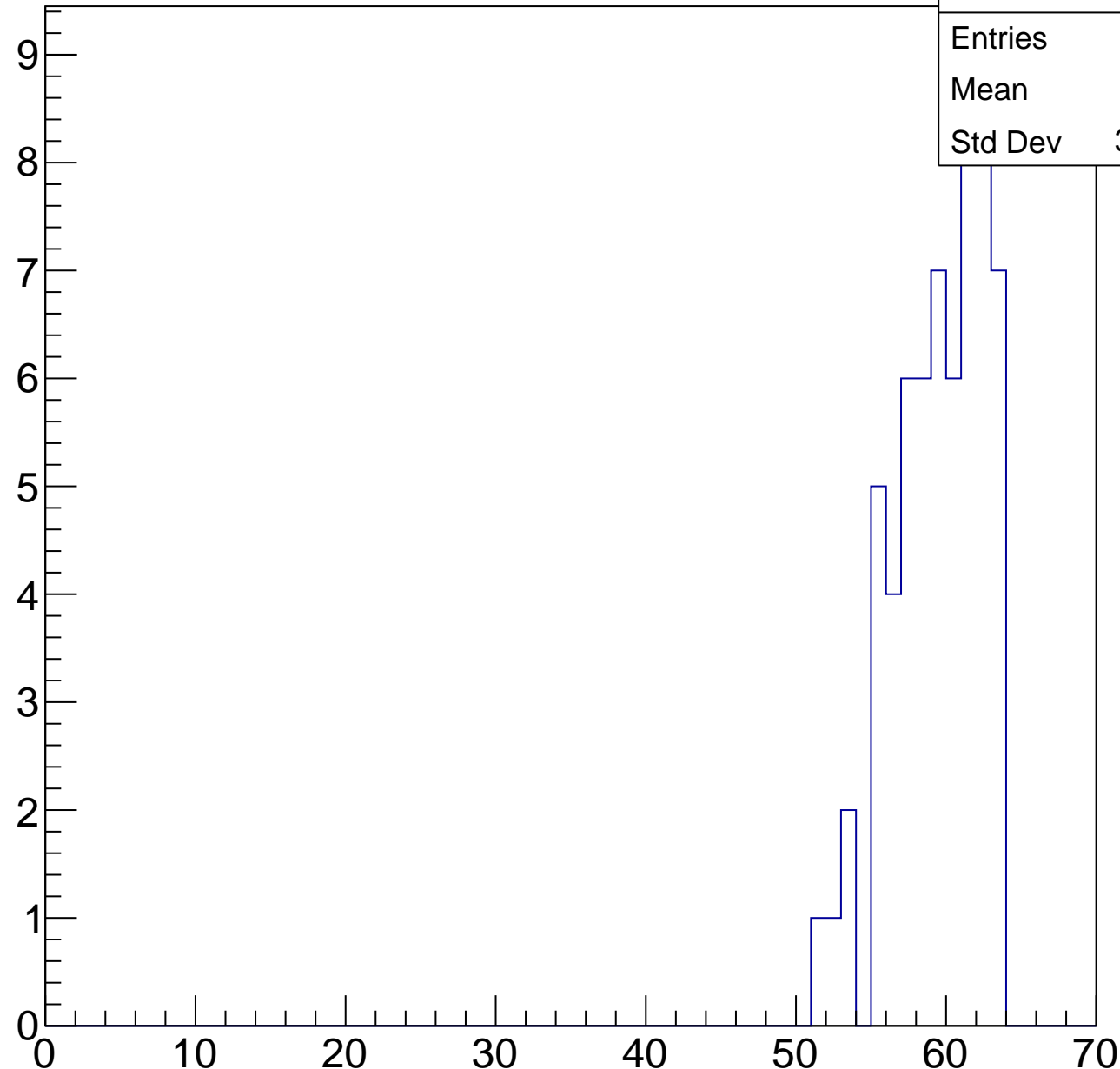
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	59
Std Dev	3.011

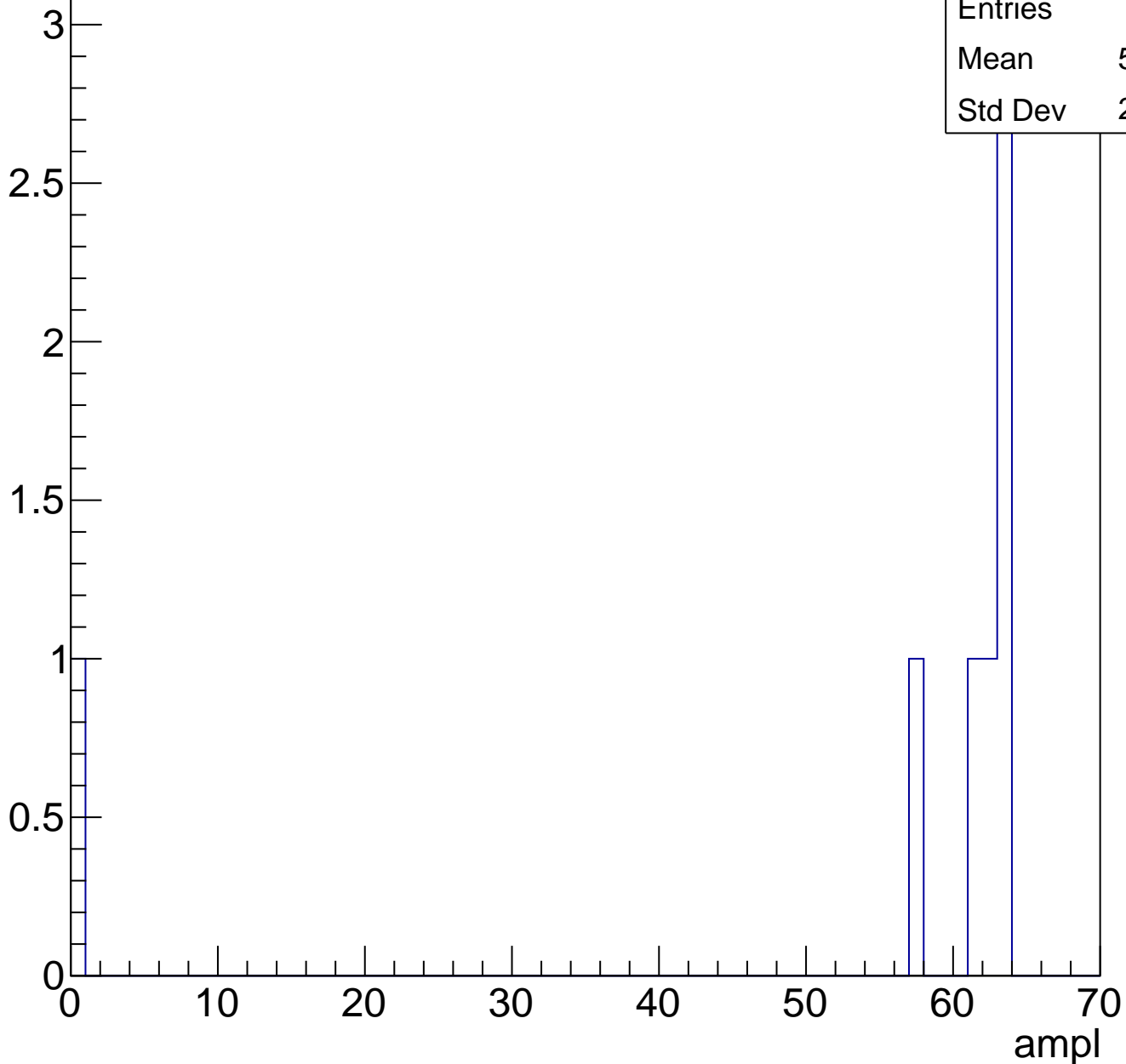
ampl



# B1L102S, U24-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L102S, U24-ch40, adc0

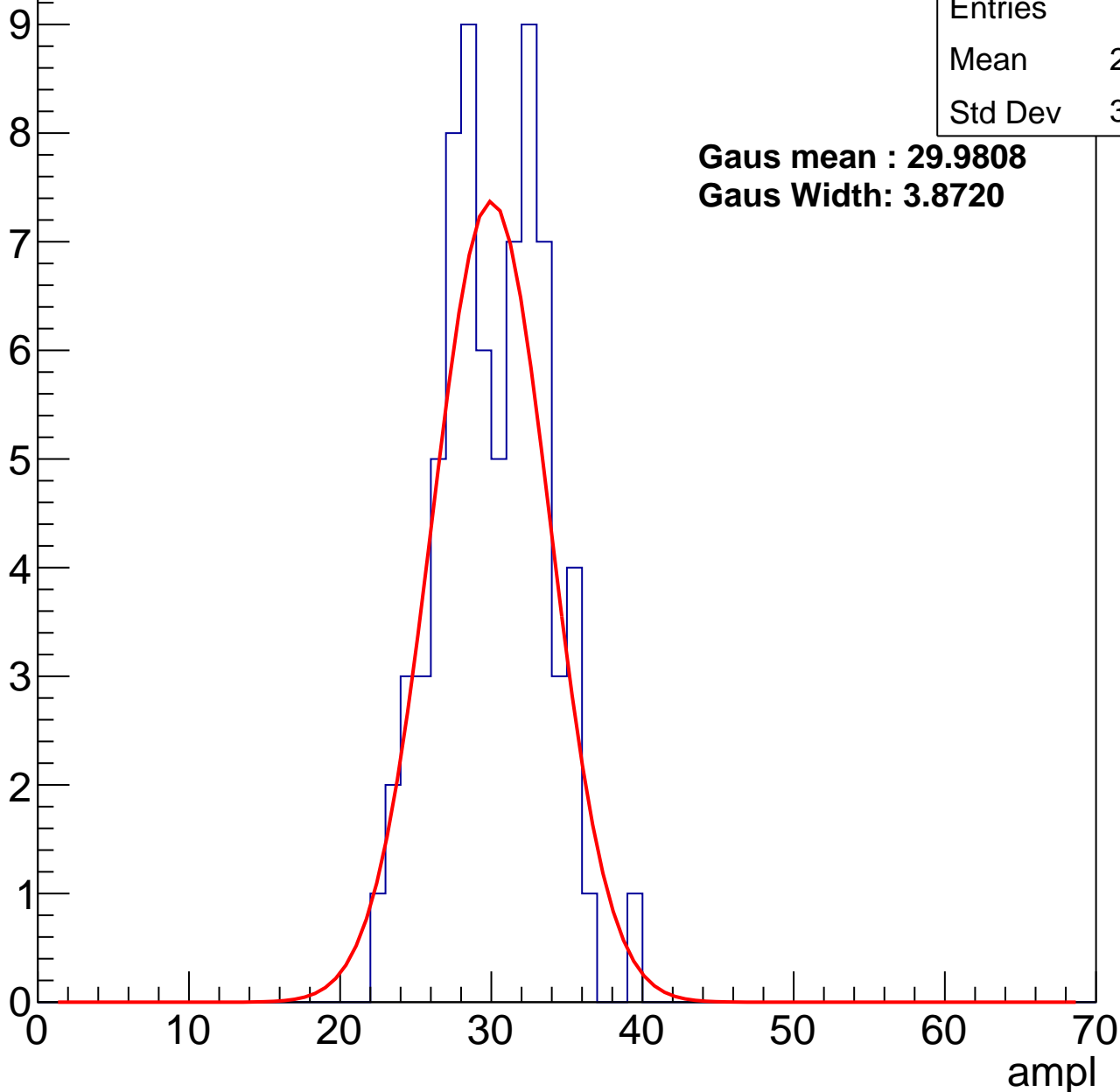
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.59
Std Dev	3.483

**Gaus mean : 29.9808**

**Gaus Width: 3.8720**

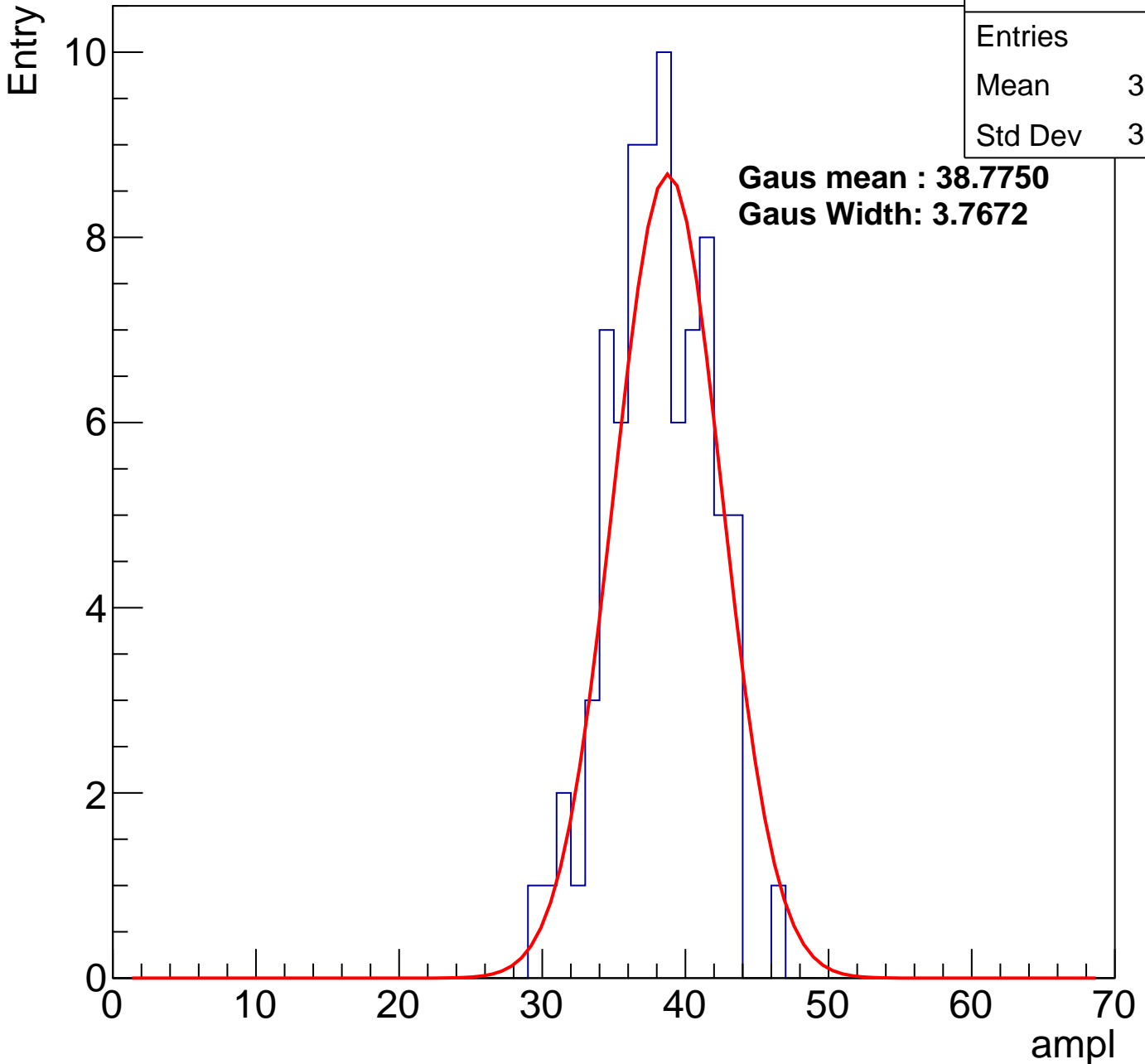


# B1L102S, U24-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	81
Mean	37.65
Std Dev	3.389

**Gaus mean : 38.7750**  
**Gaus Width: 3.7672**



# B1L102S, U24-ch40, adc2

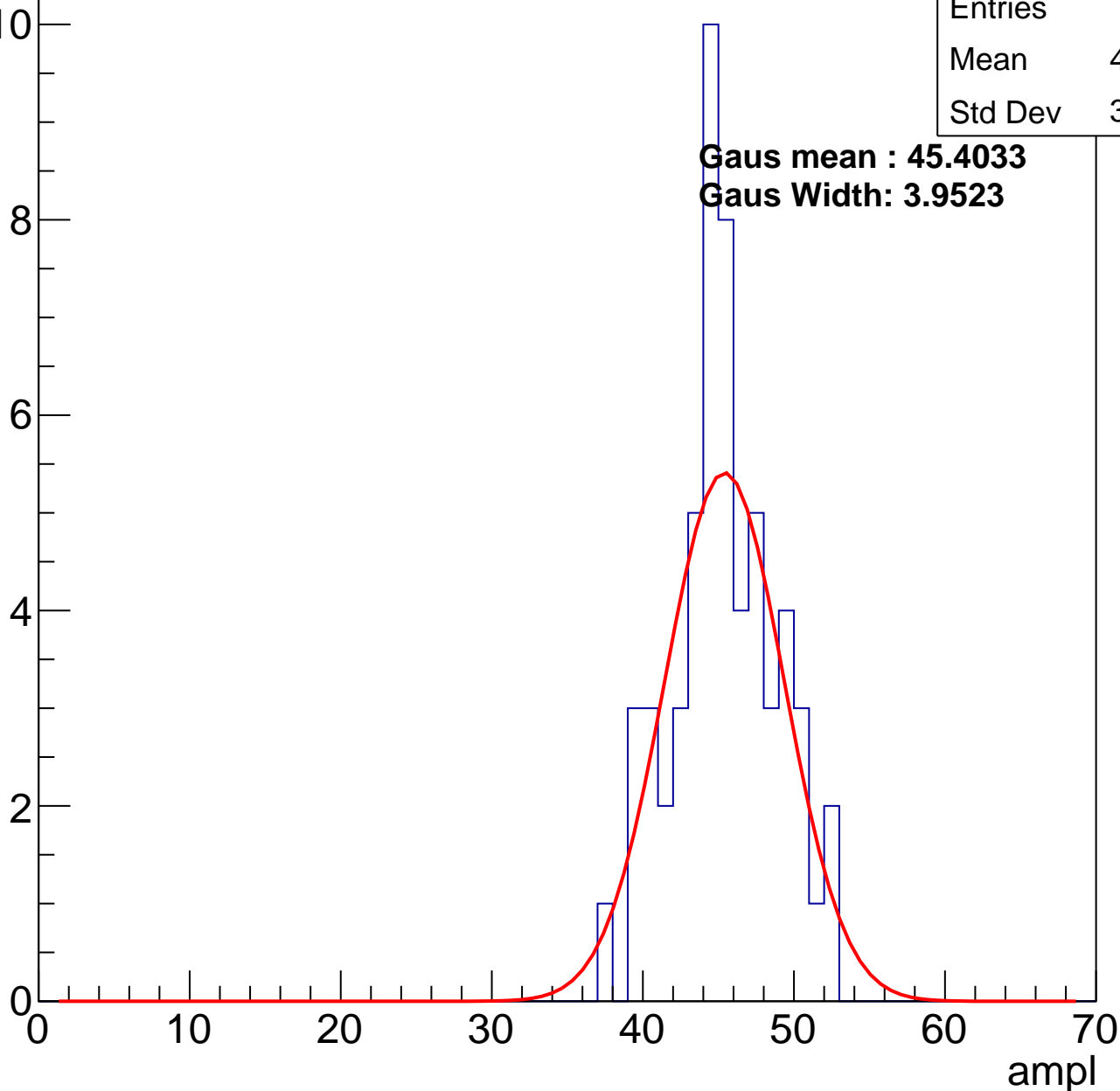
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	44.93
Std Dev	3.402

**Gaus mean : 45.4033**

**Gaus Width: 3.9523**

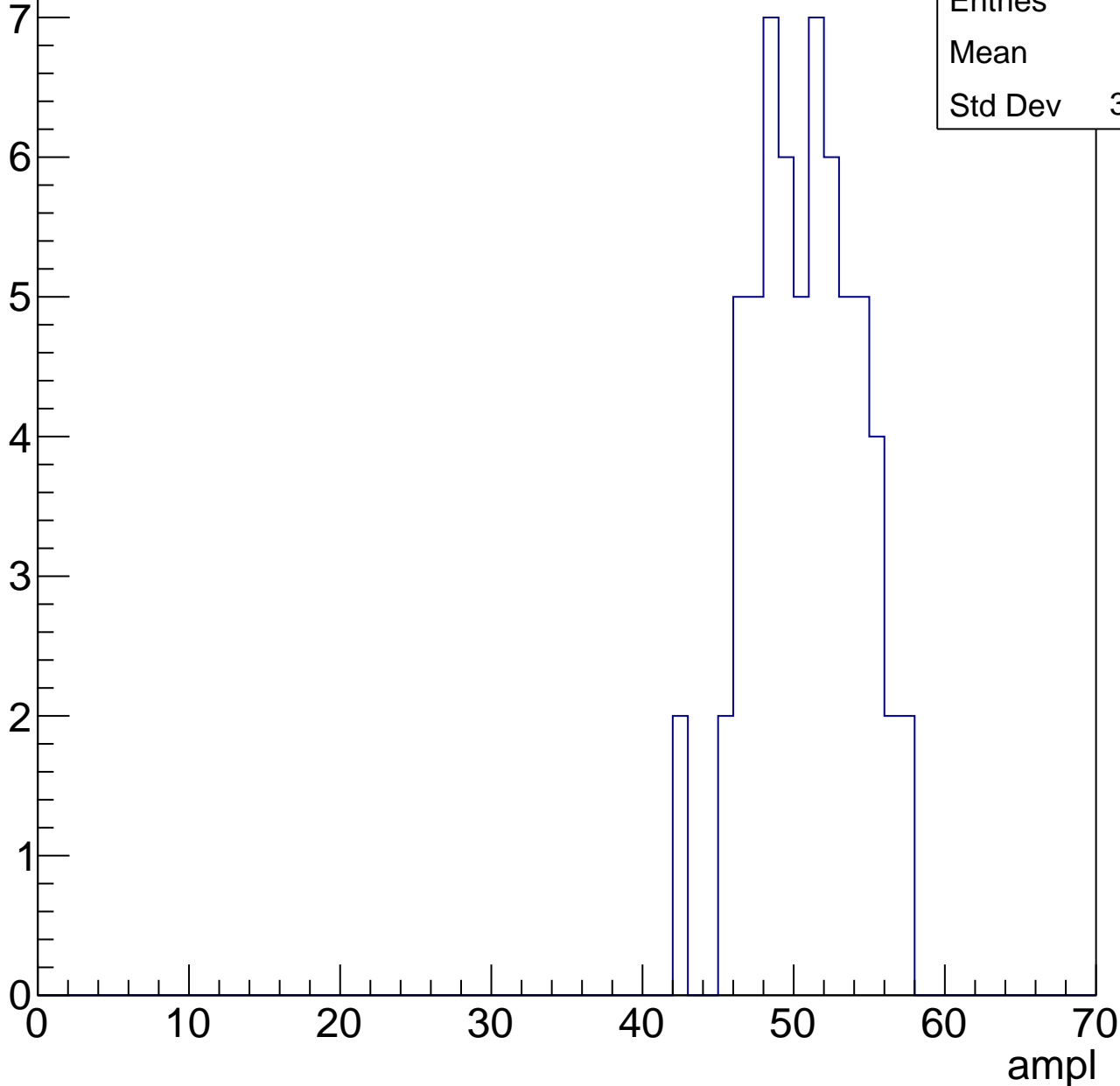


# B1L102S, U24-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.3
Std Dev	3.467

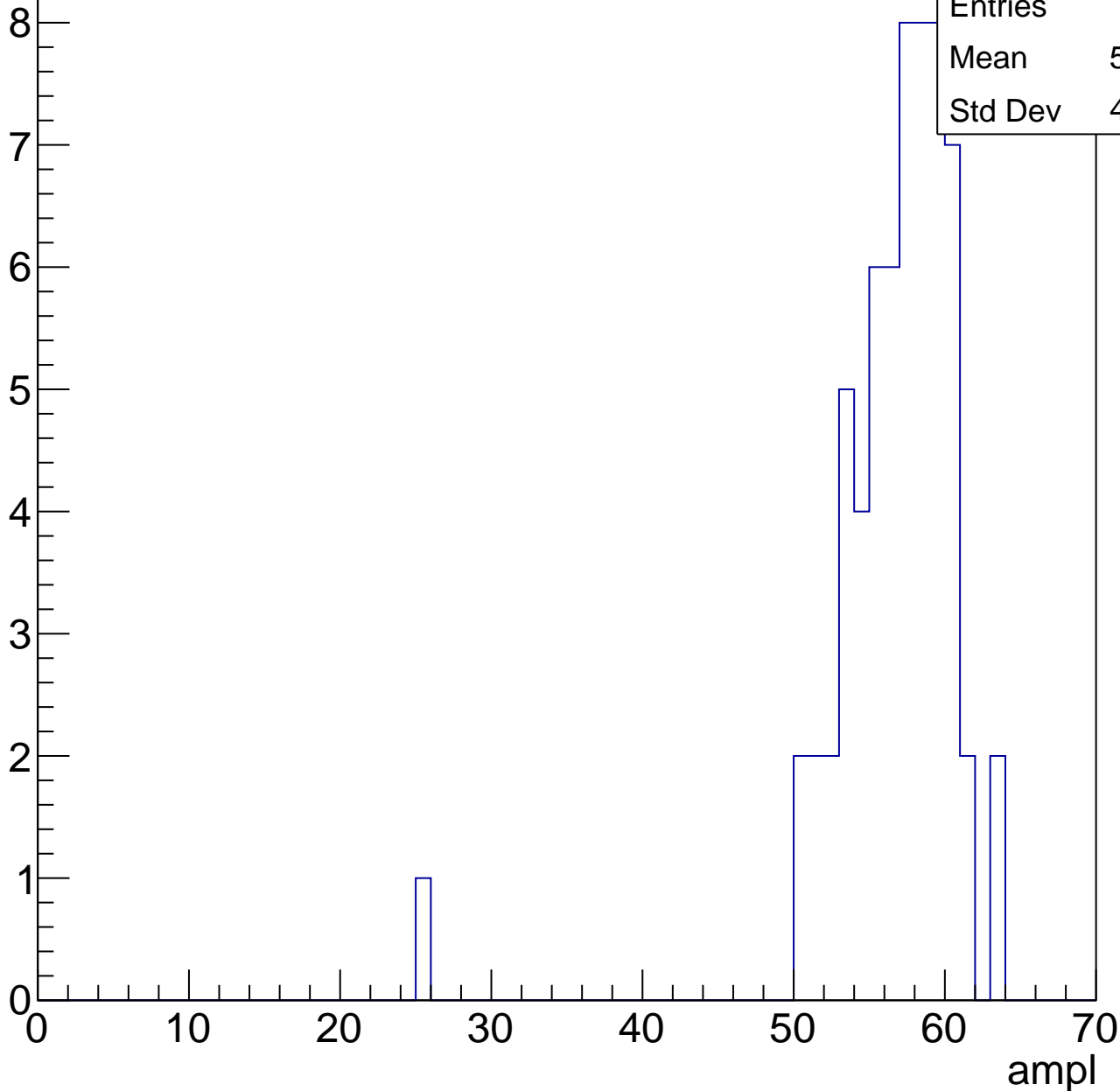


# B1L102S, U24-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	56.16
Std Dev	4.964

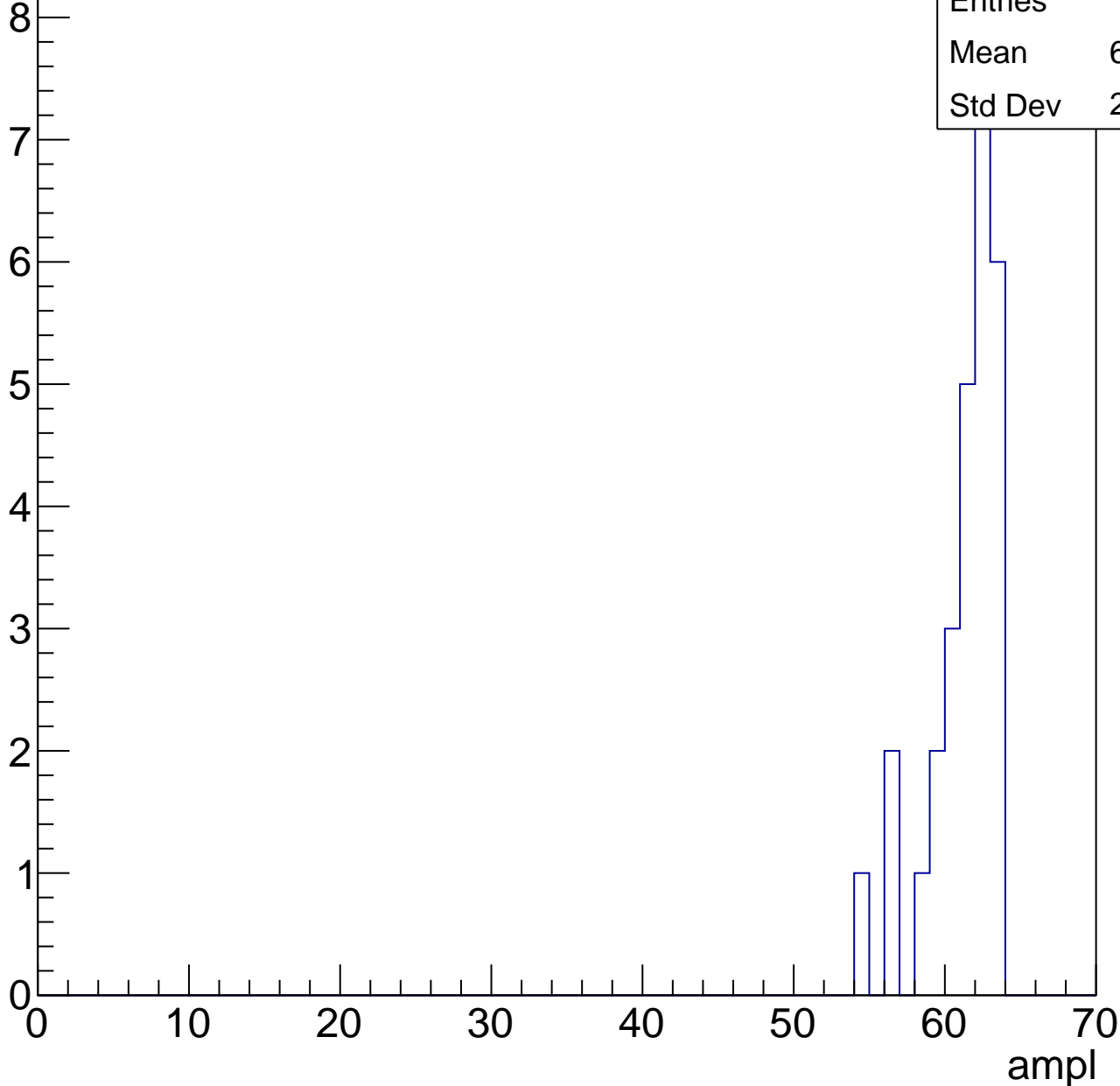


# B1L102S, U24-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	28
Mean	60.75
Std Dev	2.309



# B1L102S, U24-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

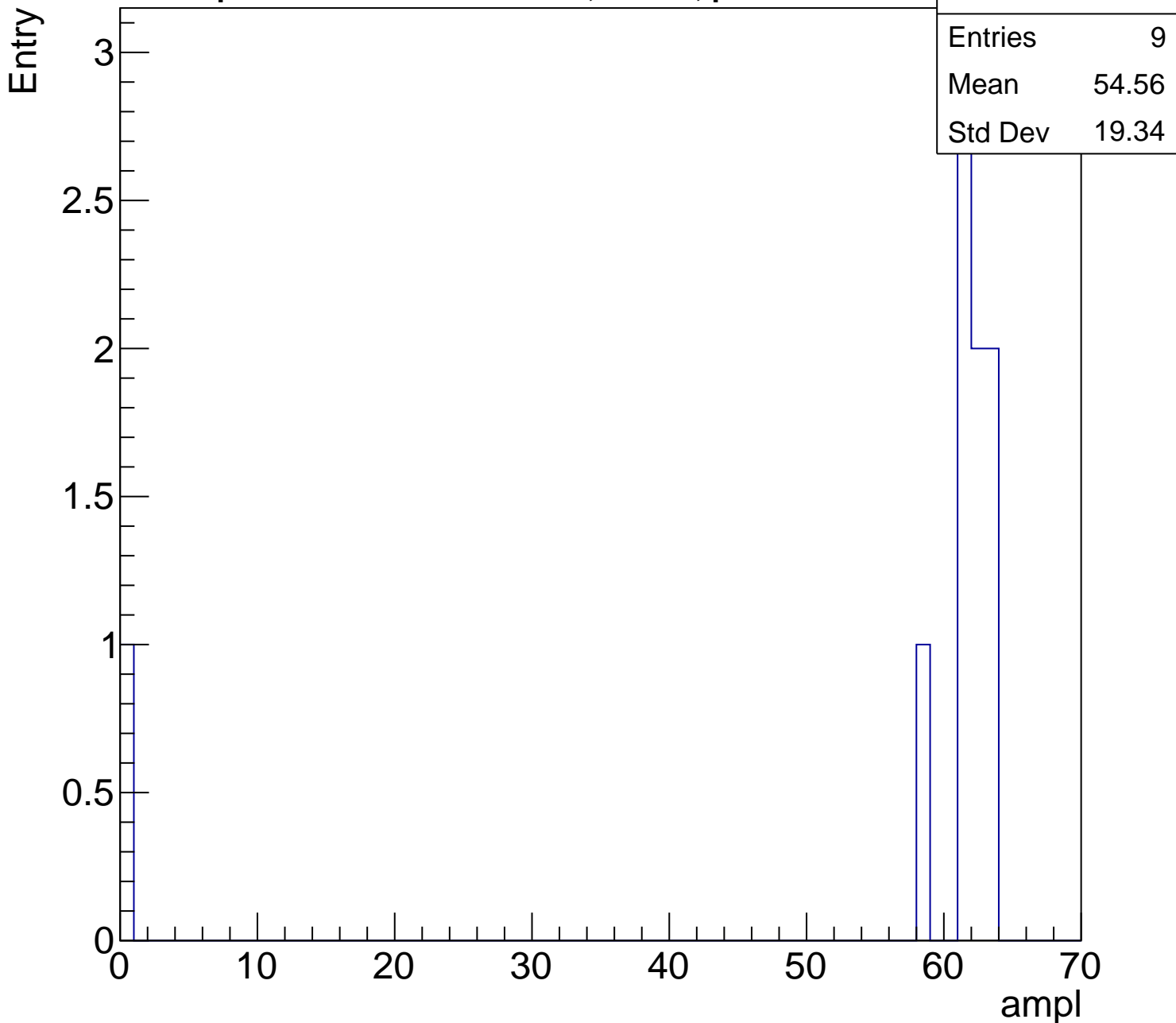
Mean

54.56

Std Dev

19.34

ampl

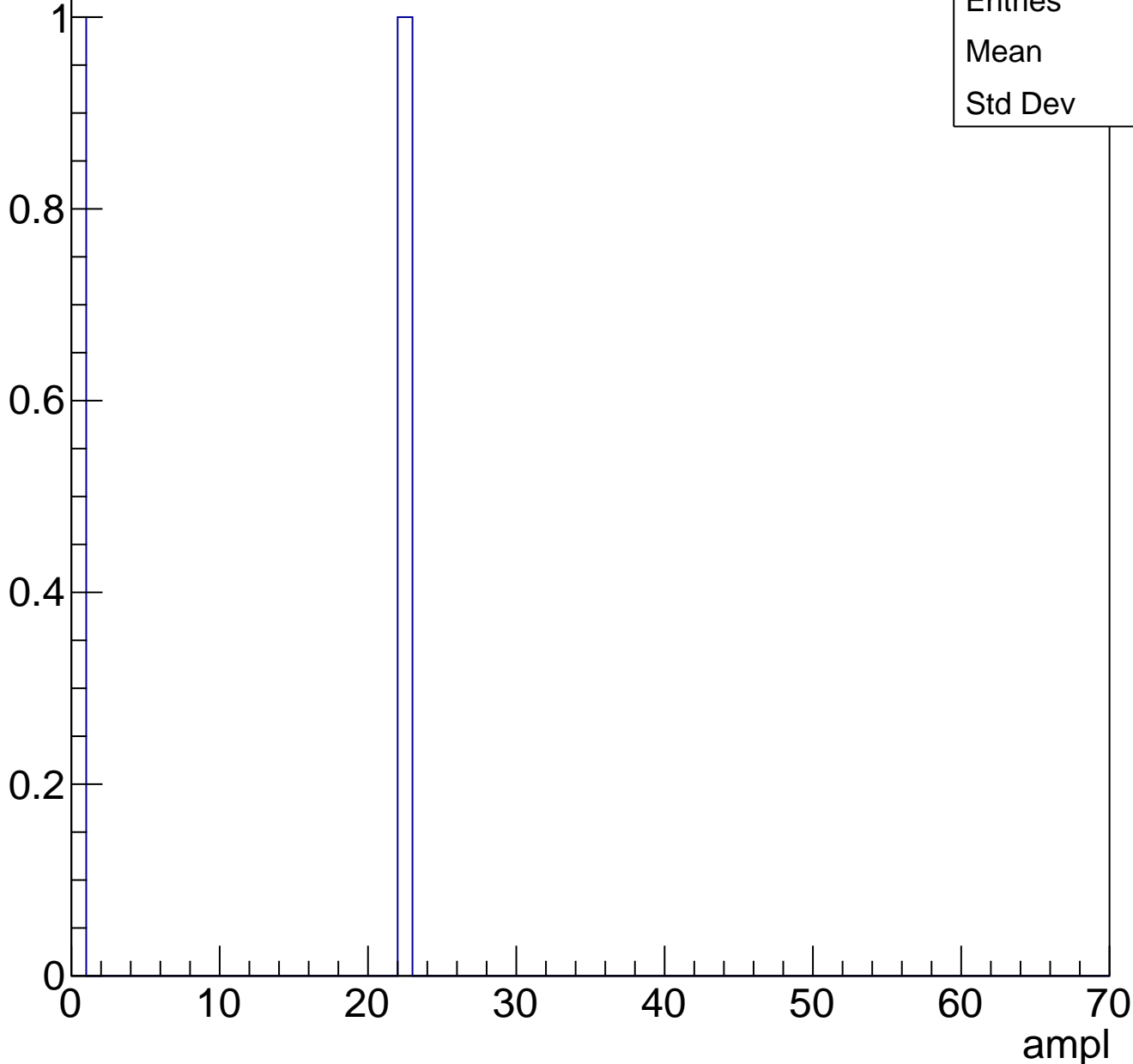




# B1L102S, U24-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U24-ch41, adc0

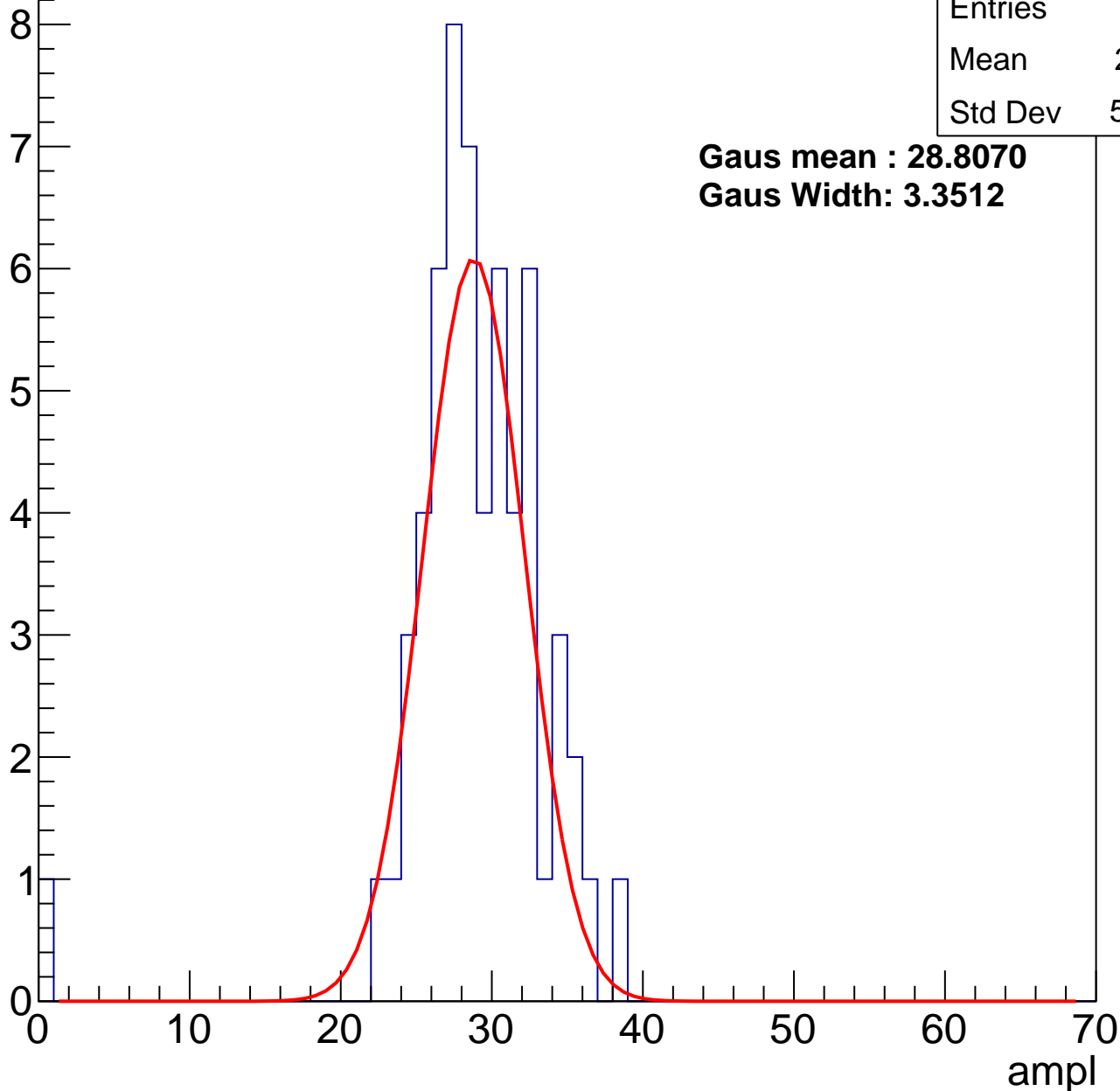
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.41
Std Dev	5.053

**Gaus mean : 28.8070**

**Gaus Width: 3.3512**



# B1L102S, U24-ch41, adc1

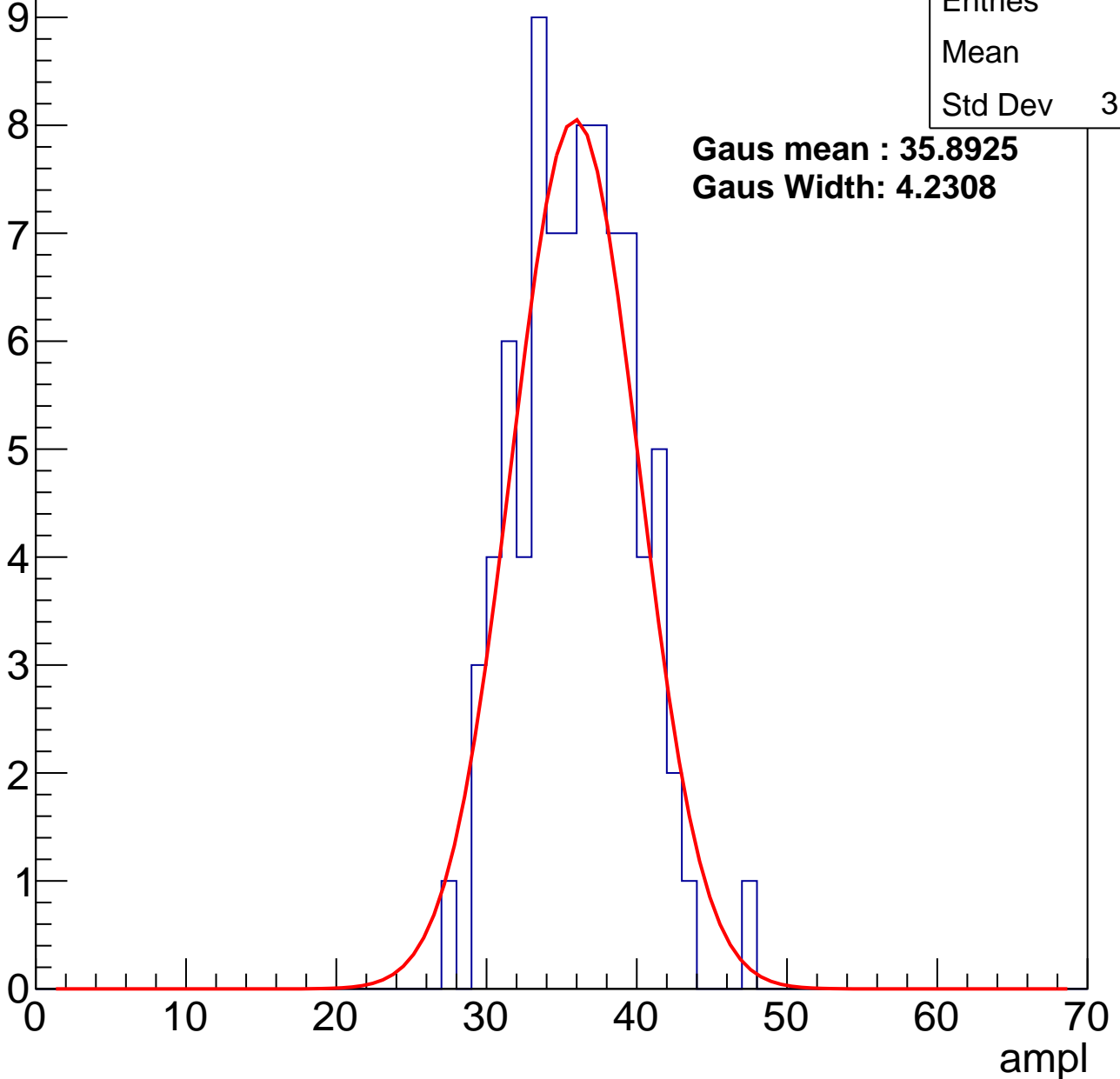
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	35.6
Std Dev	3.808

**Gaus mean : 35.8925**

**Gaus Width: 4.2308**



# B1L102S, U24-ch41, adc2

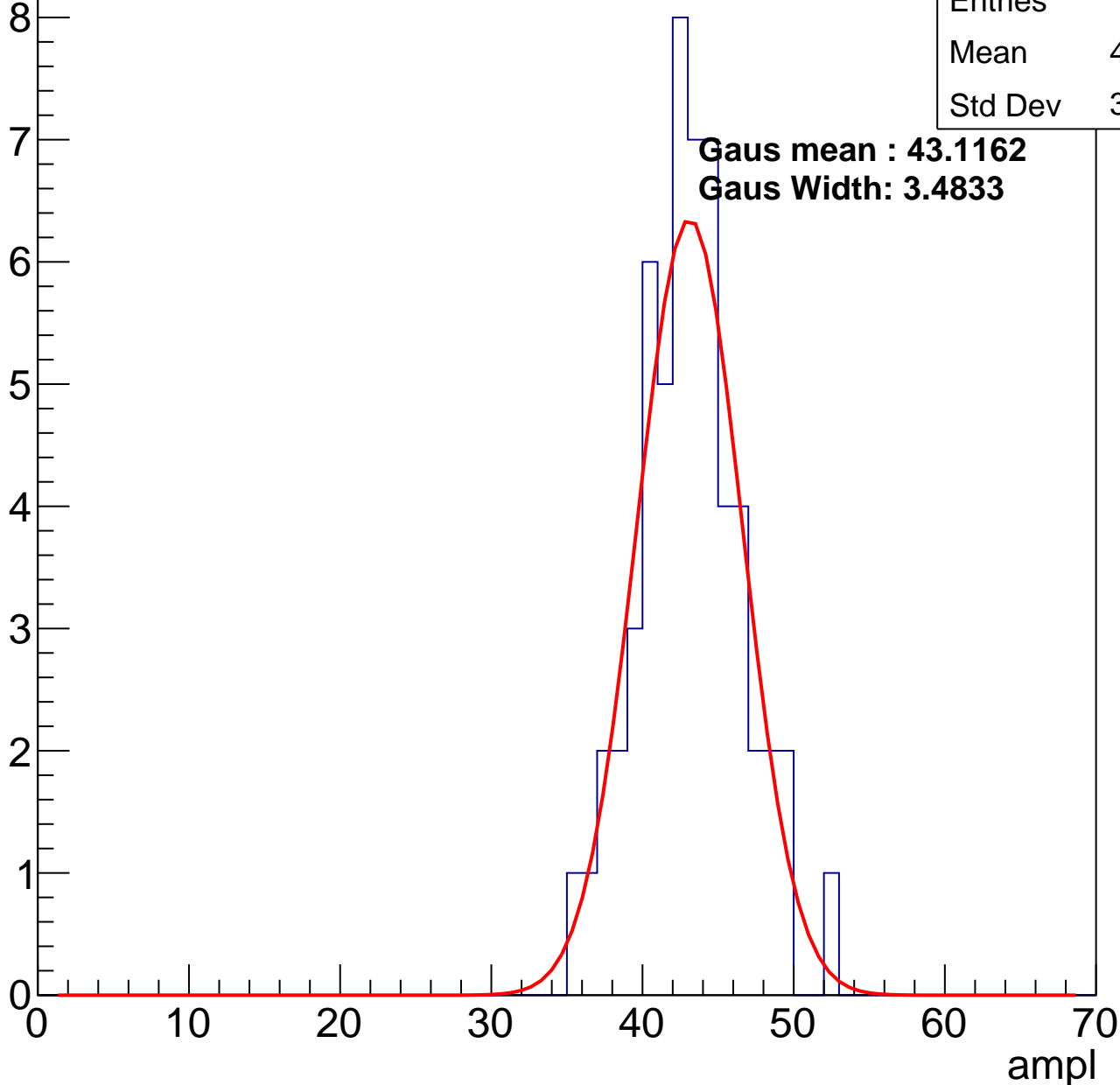
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.67
Std Dev	3.384

**Gaus mean : 43.1162**

**Gaus Width: 3.4833**

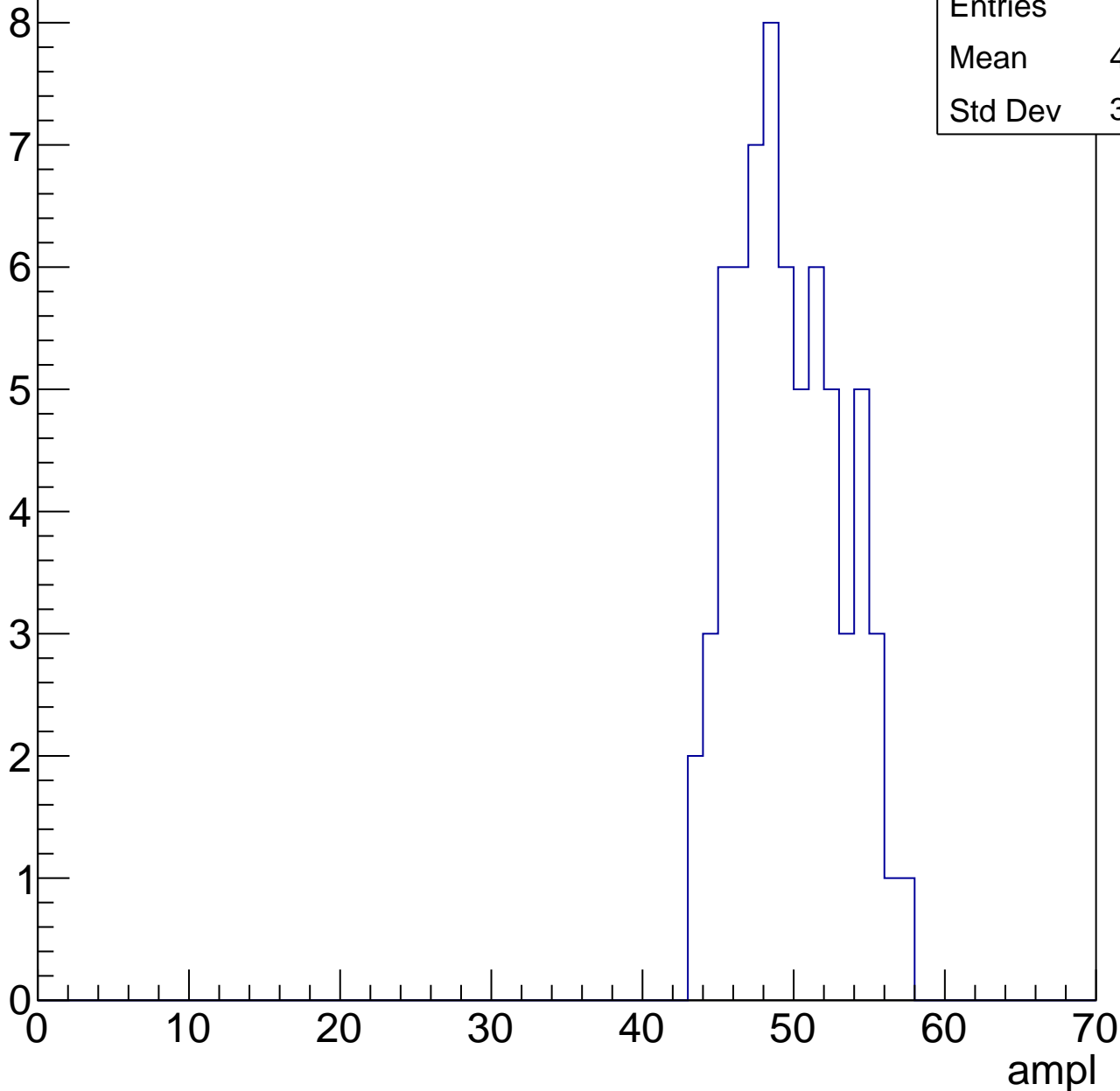


# B1L102S, U24-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.16
Std Dev	3.454



# B1L102S, U24-ch41, adc4

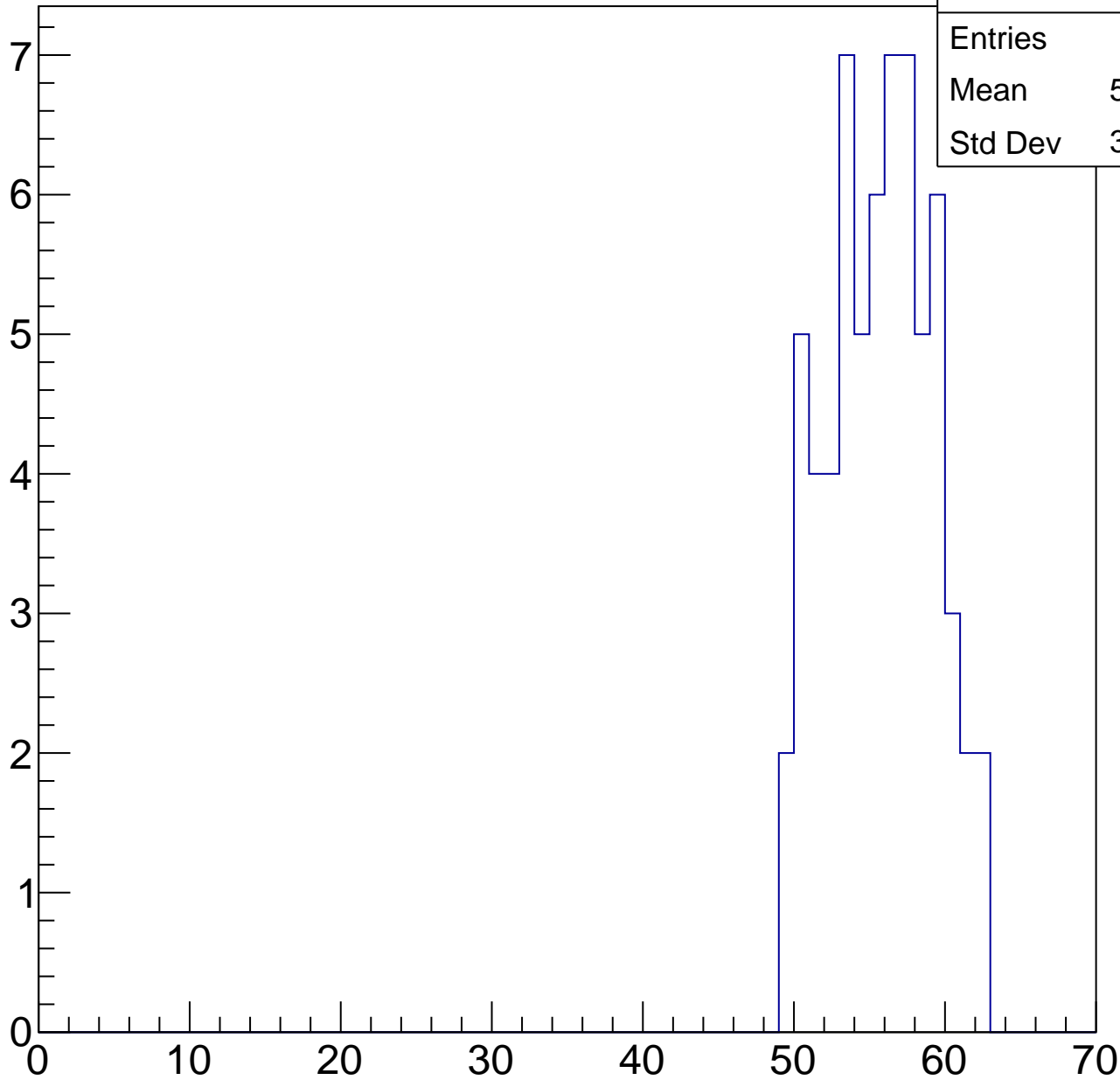
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	65
Mean	55.26
Std Dev	3.389

ampl

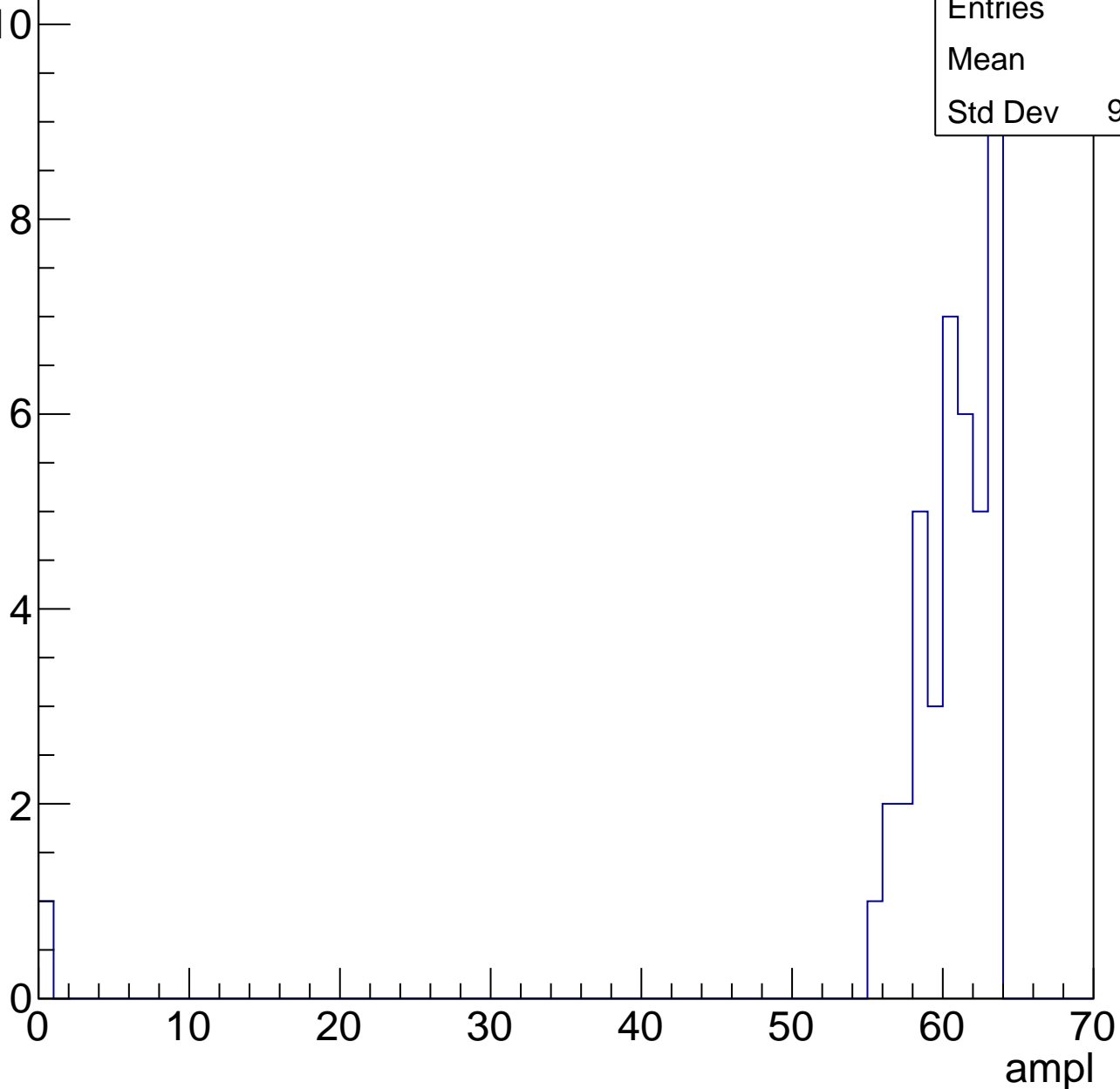


# B1L102S, U24-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

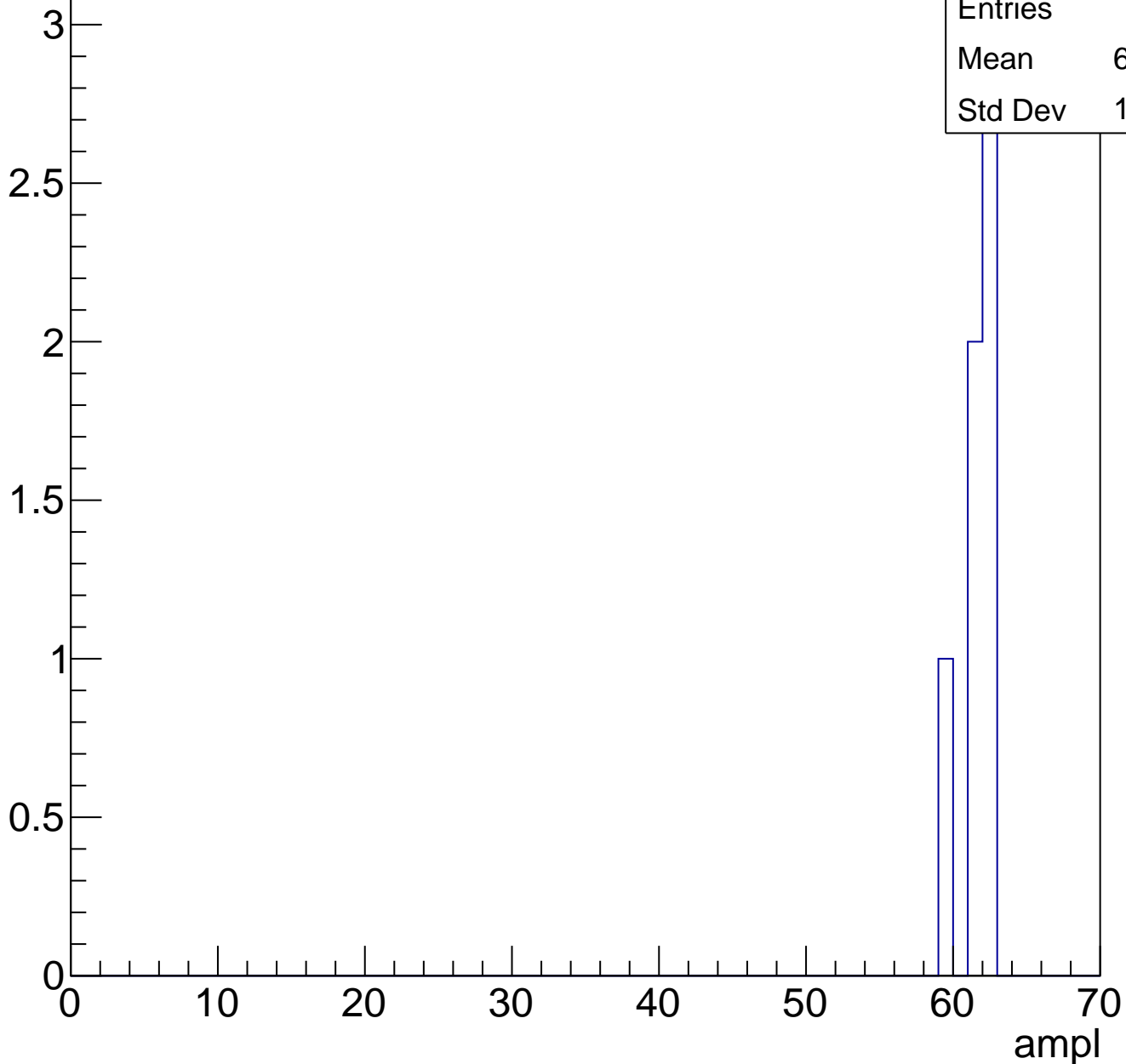
Entries	42
Mean	58.9
Std Dev	9.466



# B1L102S, U24-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch42, adc0

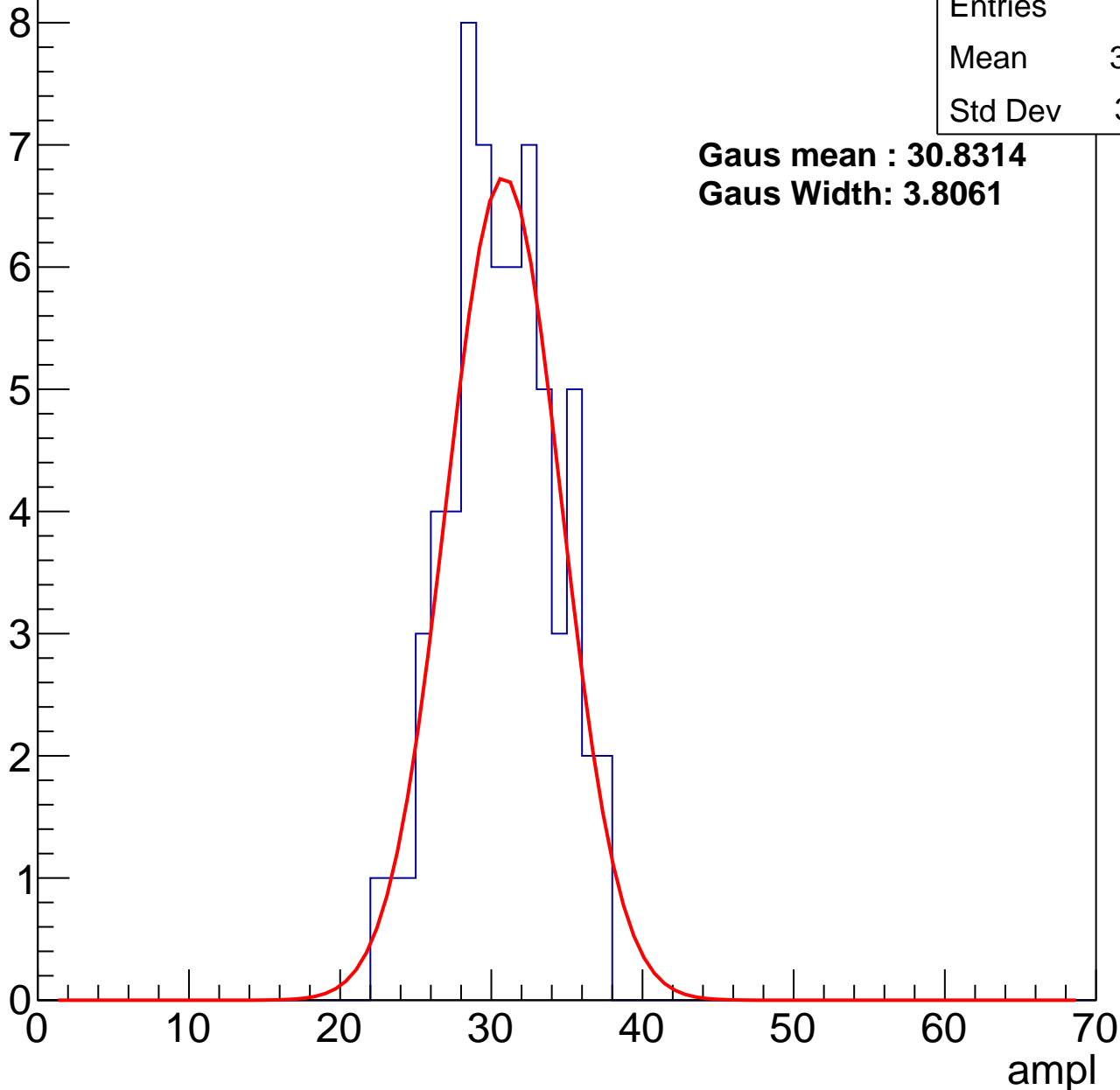
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.17
Std Dev	3.471

**Gaus mean : 30.8314**

**Gaus Width: 3.8061**



# B1L102S, U24-ch42, adc1

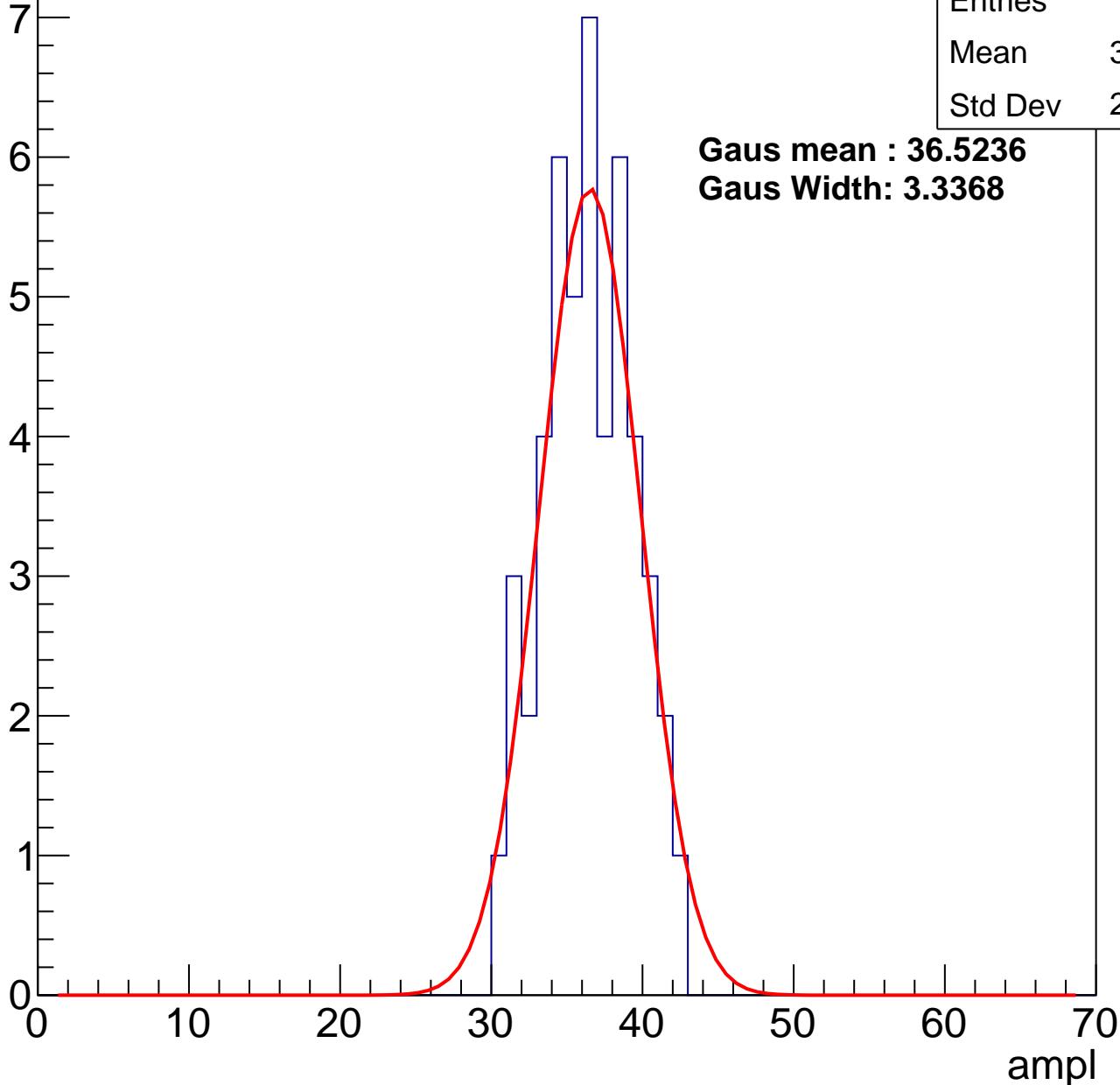
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	35.96
Std Dev	2.908

**Gaus mean : 36.5236**

**Gaus Width: 3.3368**



# B1L102S, U24-ch42, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	90
Mean	42.9
Std Dev	4.356

**Gaus mean : 43.6299**

**Gaus Width: 4.3713**

10

8

6

4

2

0

0

10

20

30

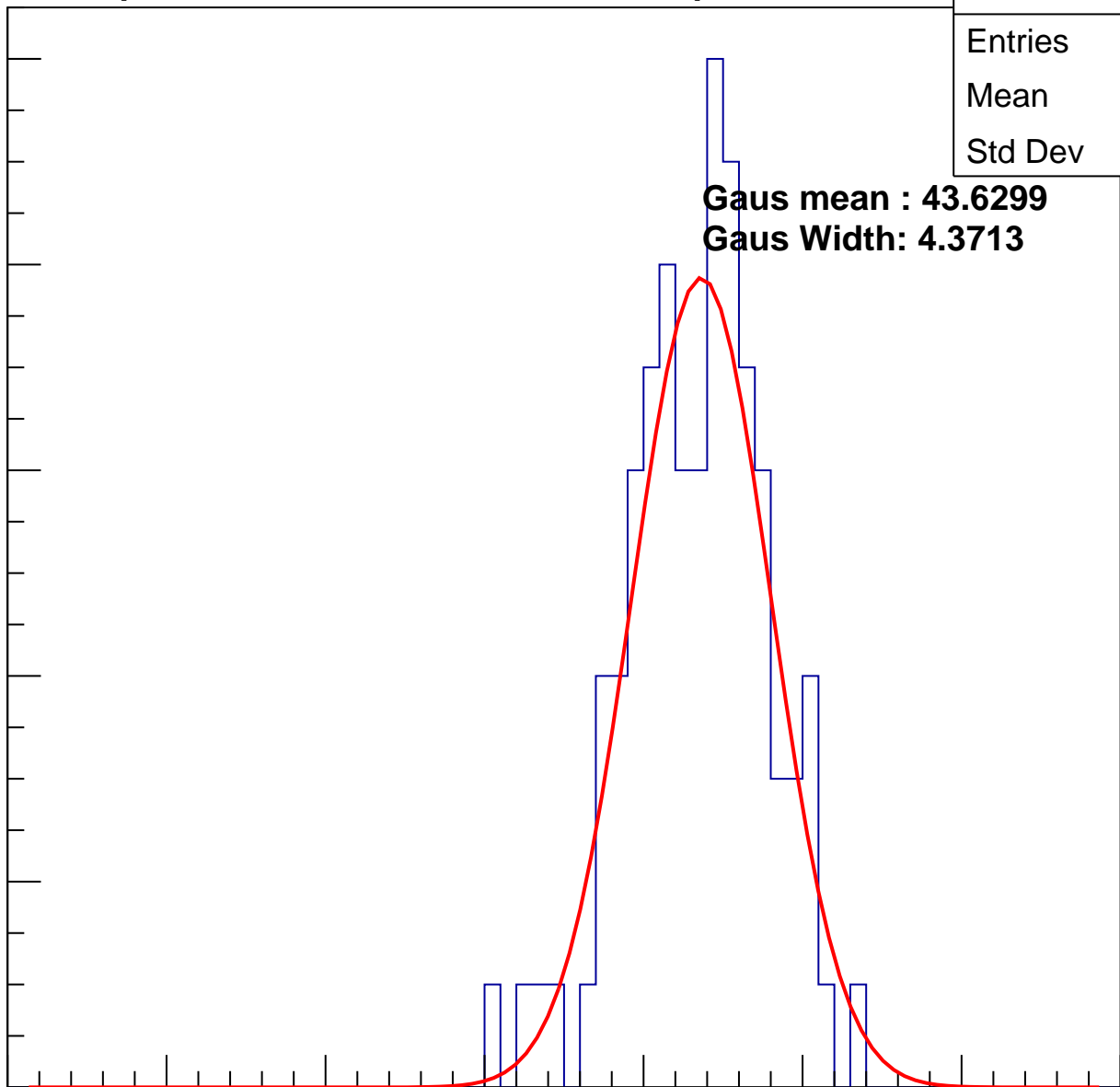
40

50

60

70

ampl

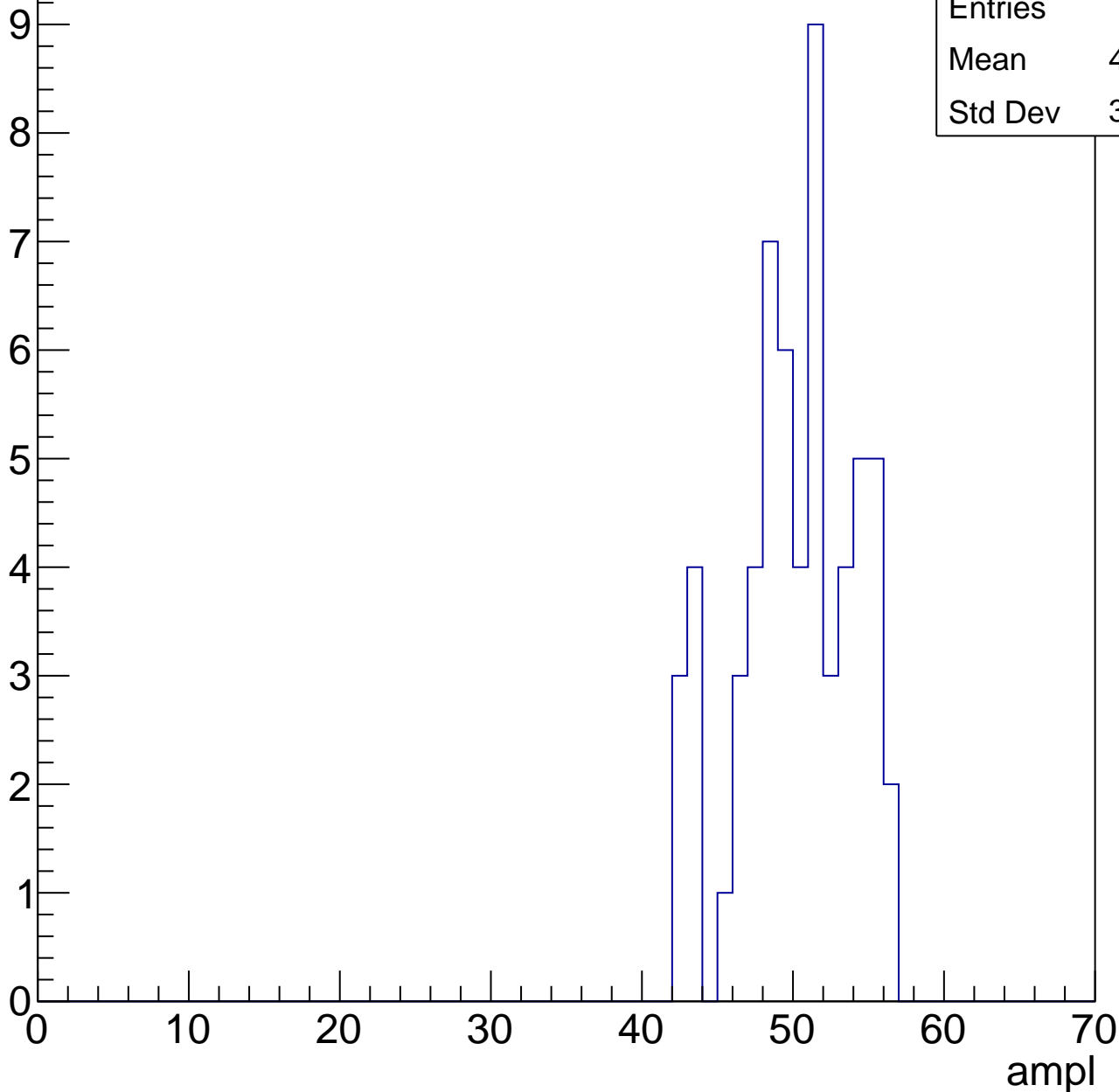


# B1L102S, U24-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.72
Std Dev	3.786

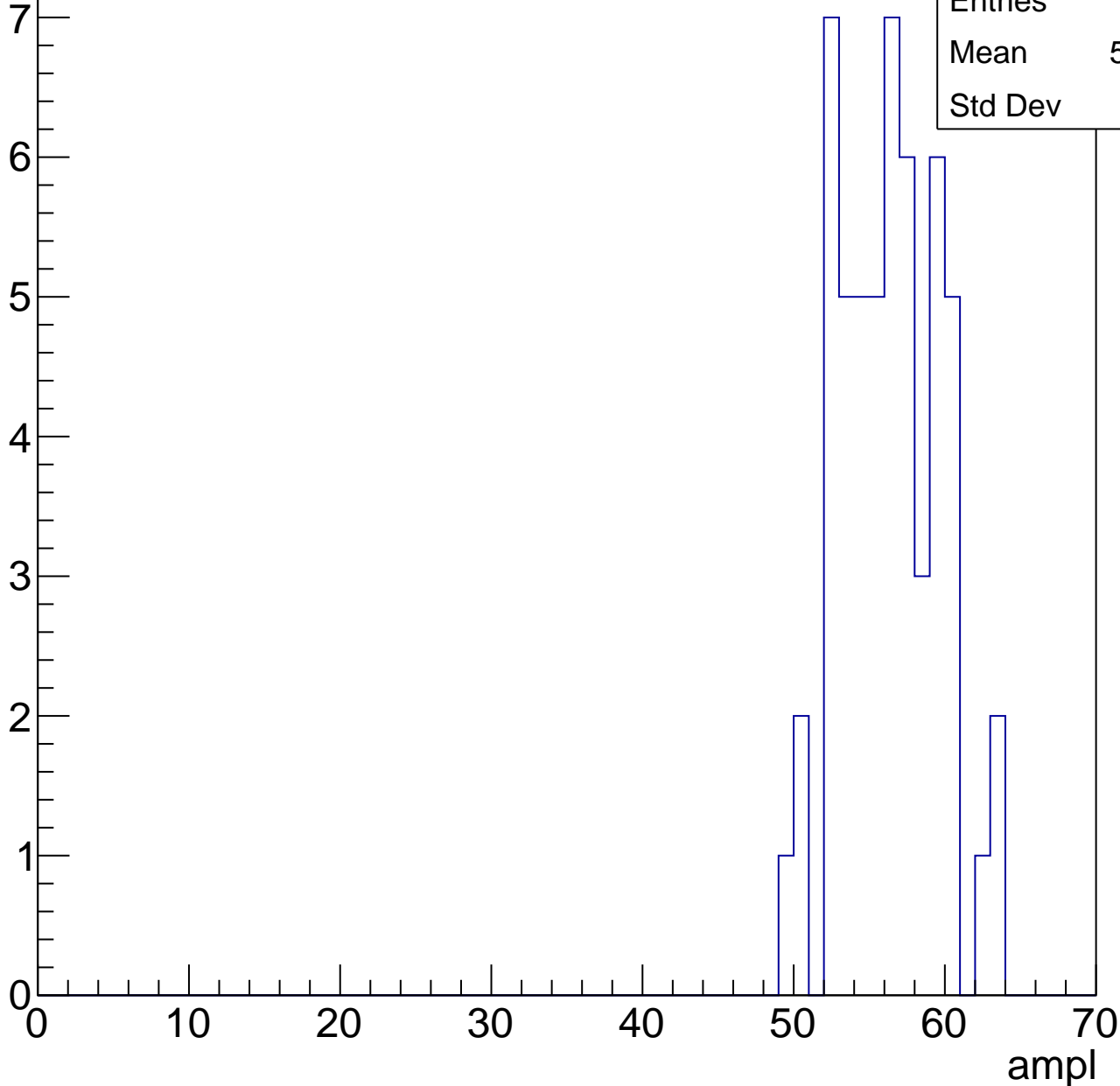


# B1L102S, U24-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.87
Std Dev	3.27

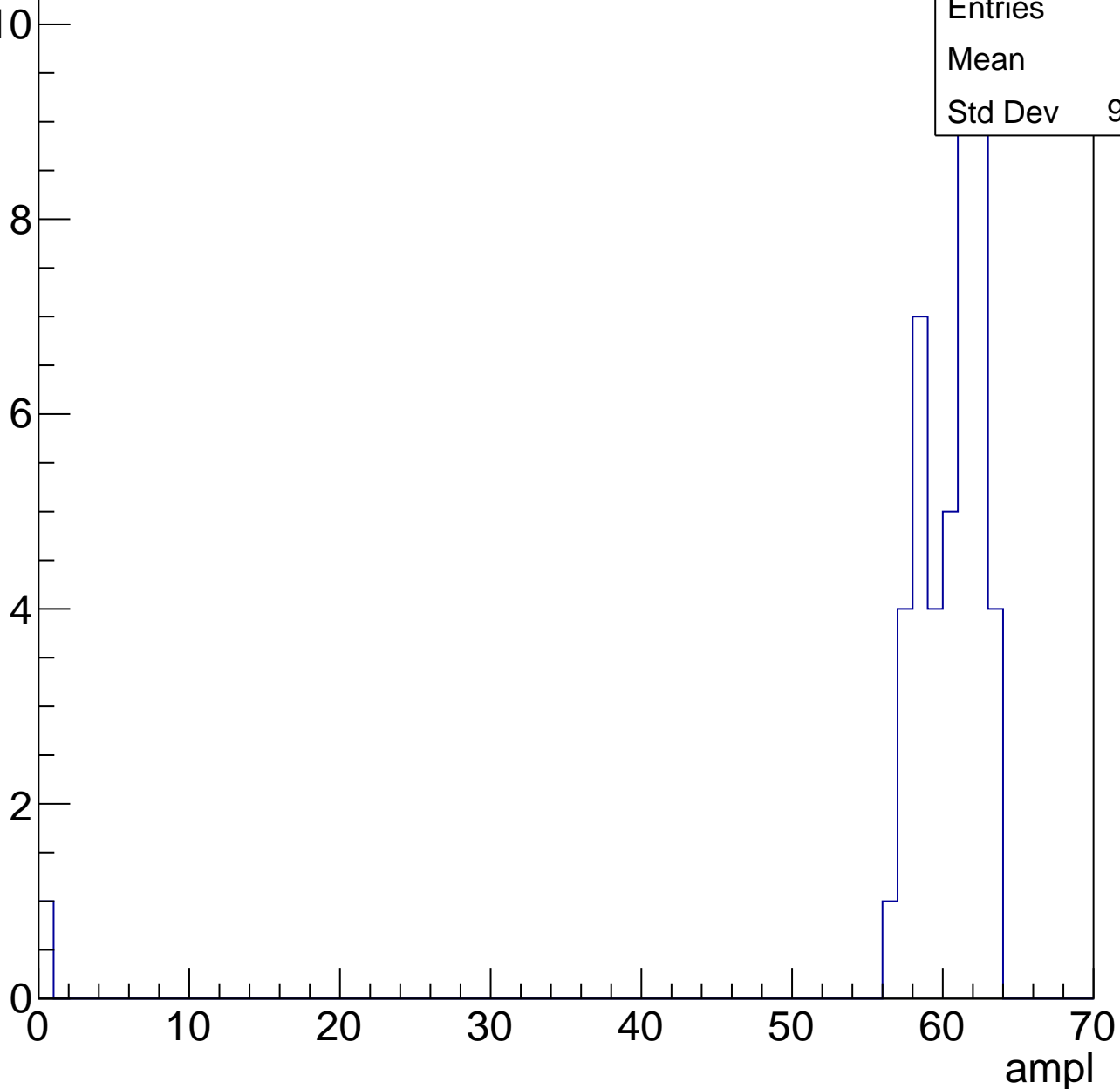


# B1L102S, U24-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.8
Std Dev	9.069



# B1L102S, U24-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	63
Std Dev	0



# B1L102S, U24-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	67
Mean	29.55
Std Dev	3.116

**Gaus mean : 29.8605**

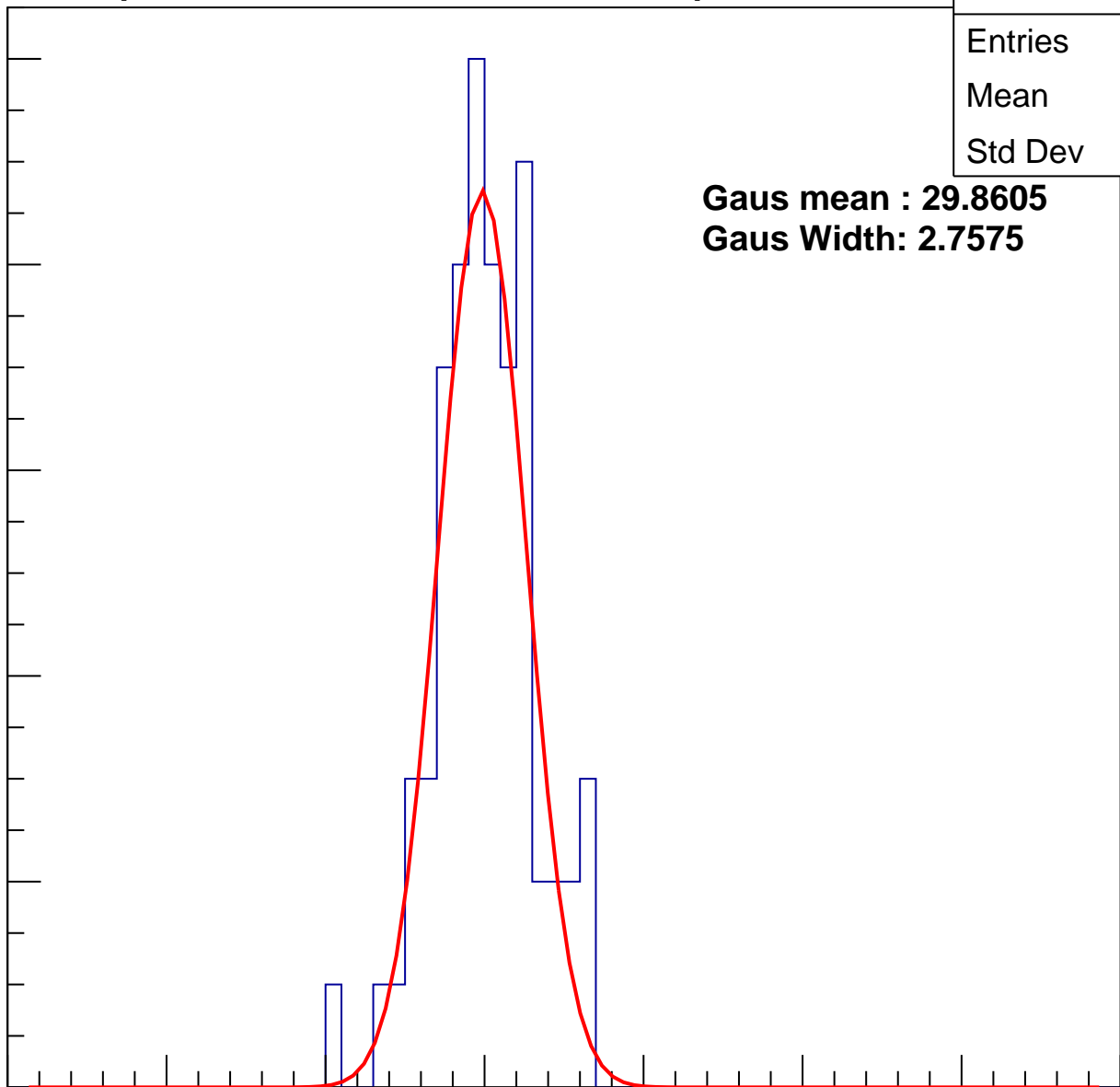
**Gaus Width: 2.7575**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch43, adc1

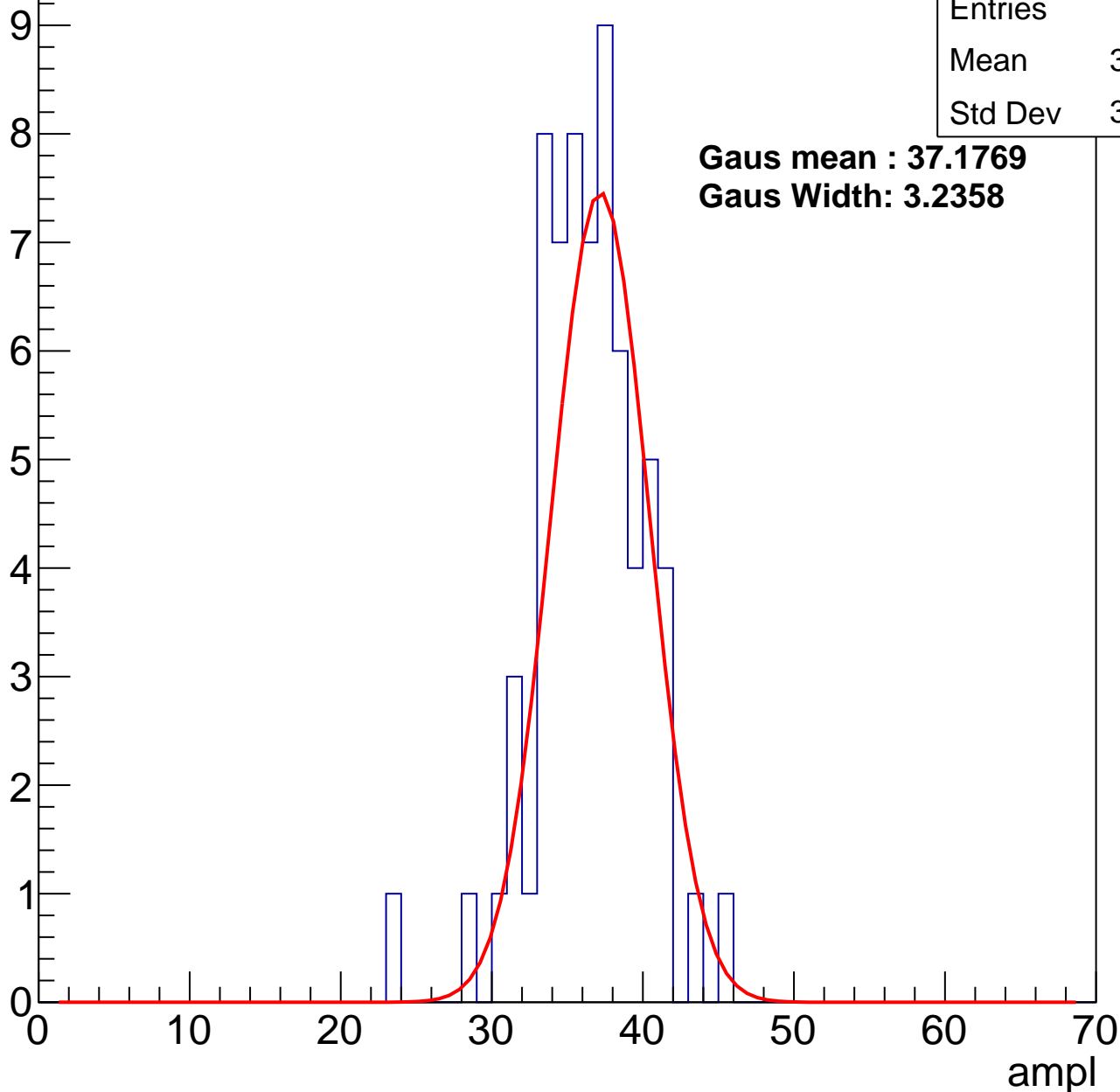
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.96
Std Dev	3.555

**Gaus mean : 37.1769**

**Gaus Width: 3.2358**



# B1L102S, U24-ch43, adc2

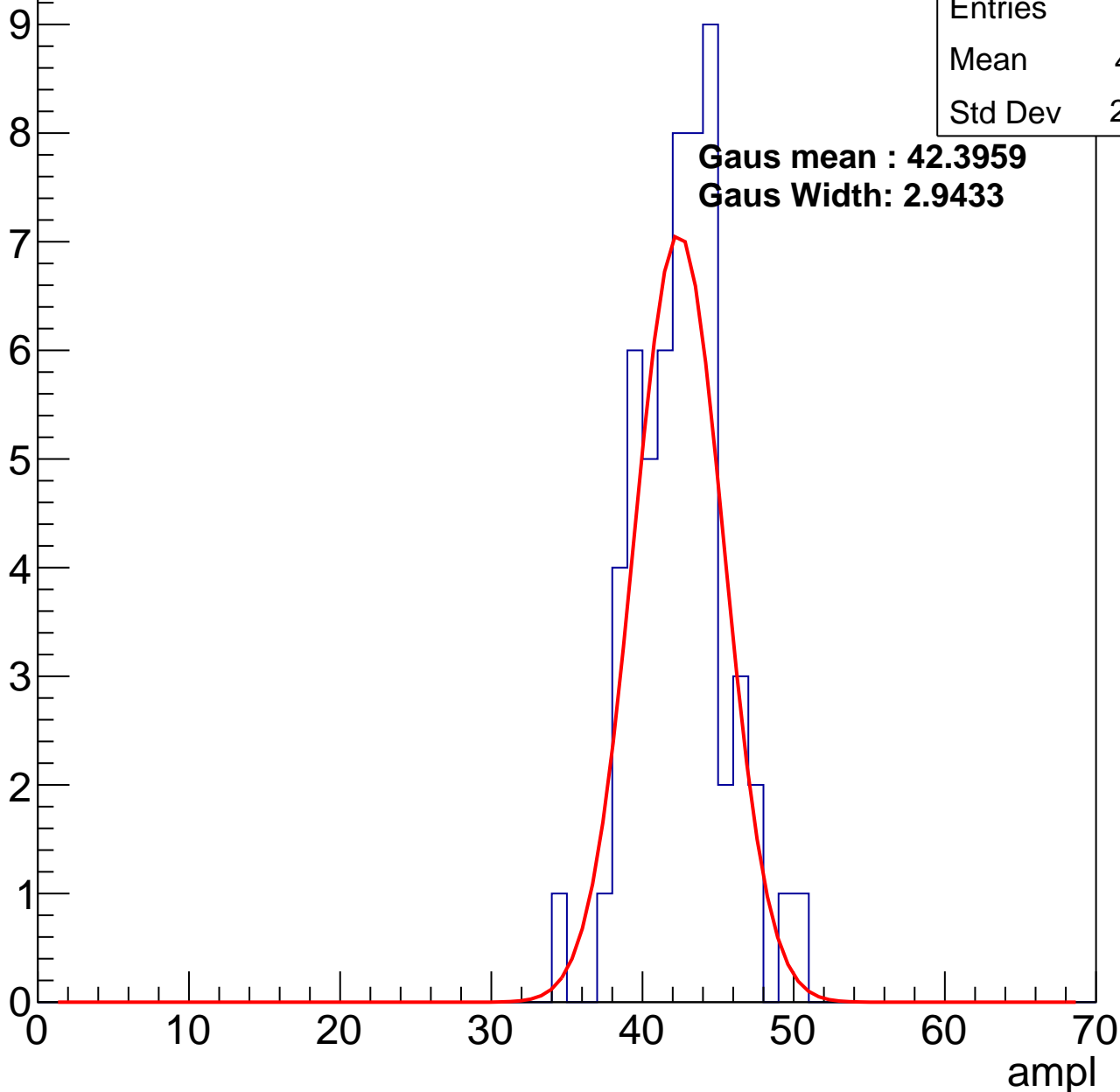
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.11
Std Dev	2.978

**Gaus mean : 42.3959**

**Gaus Width: 2.9433**

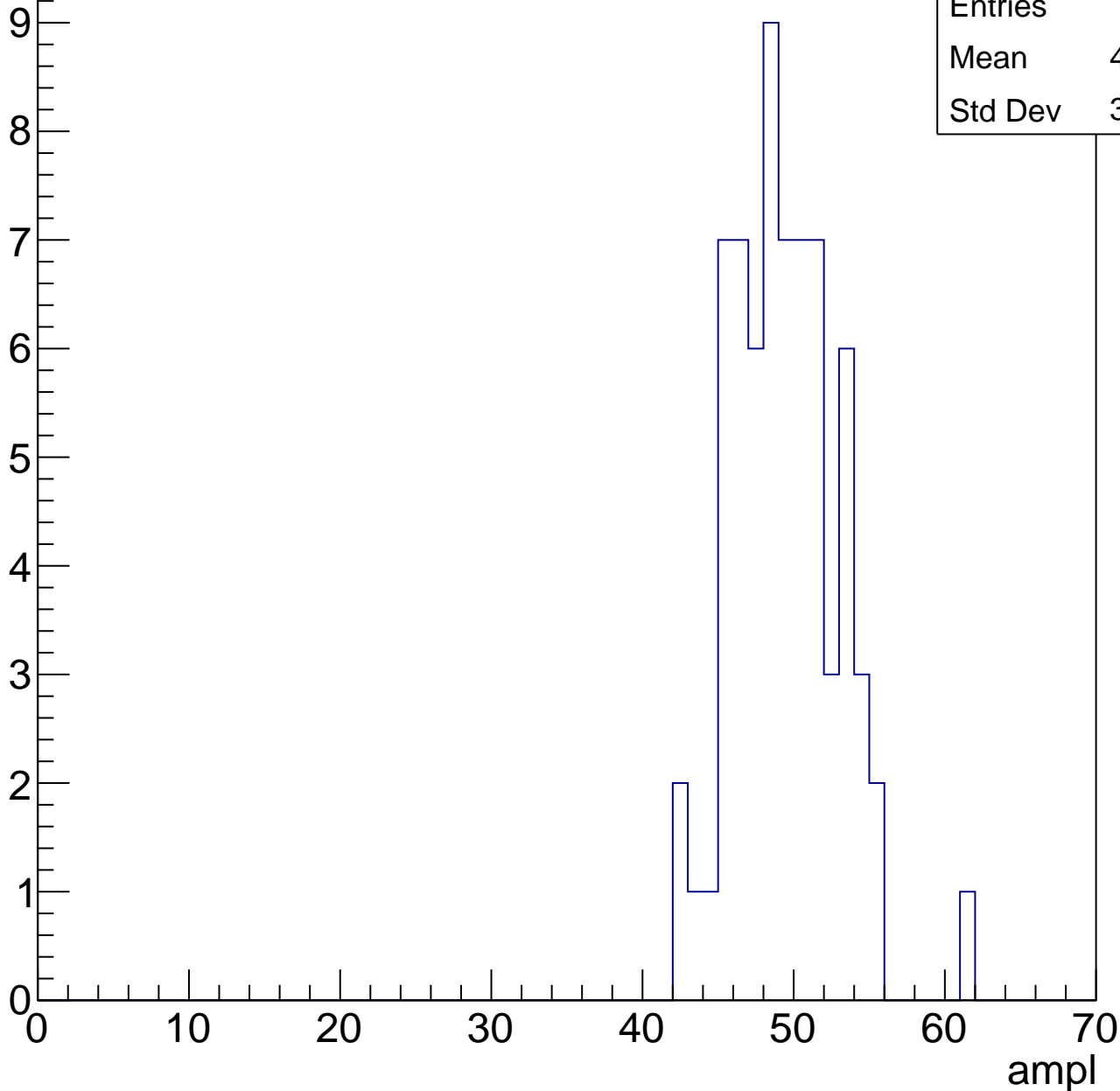


# B1L102S, U24-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

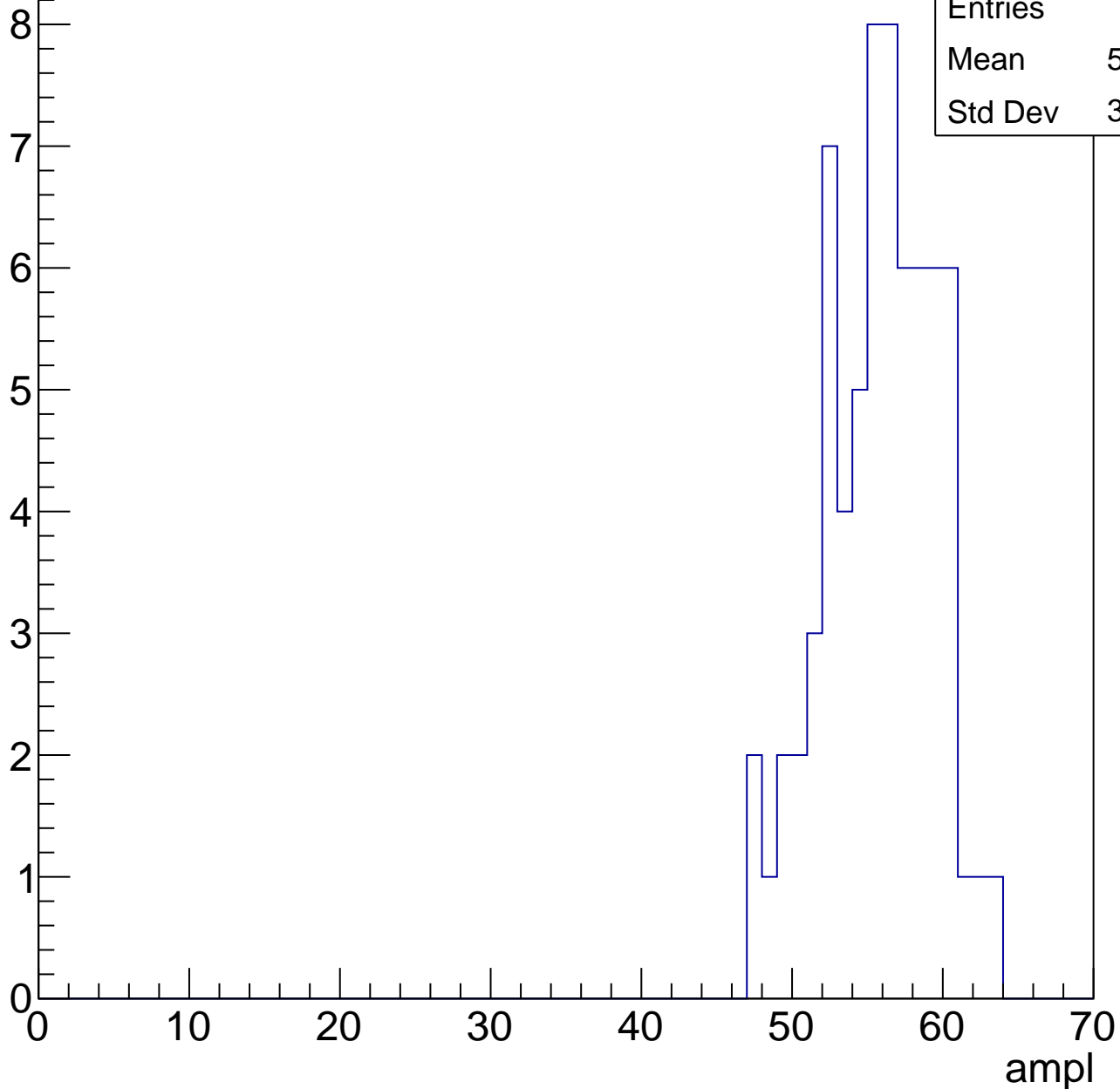
Entries	69
Mean	48.97
Std Dev	3.439



# B1L102S, U24-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



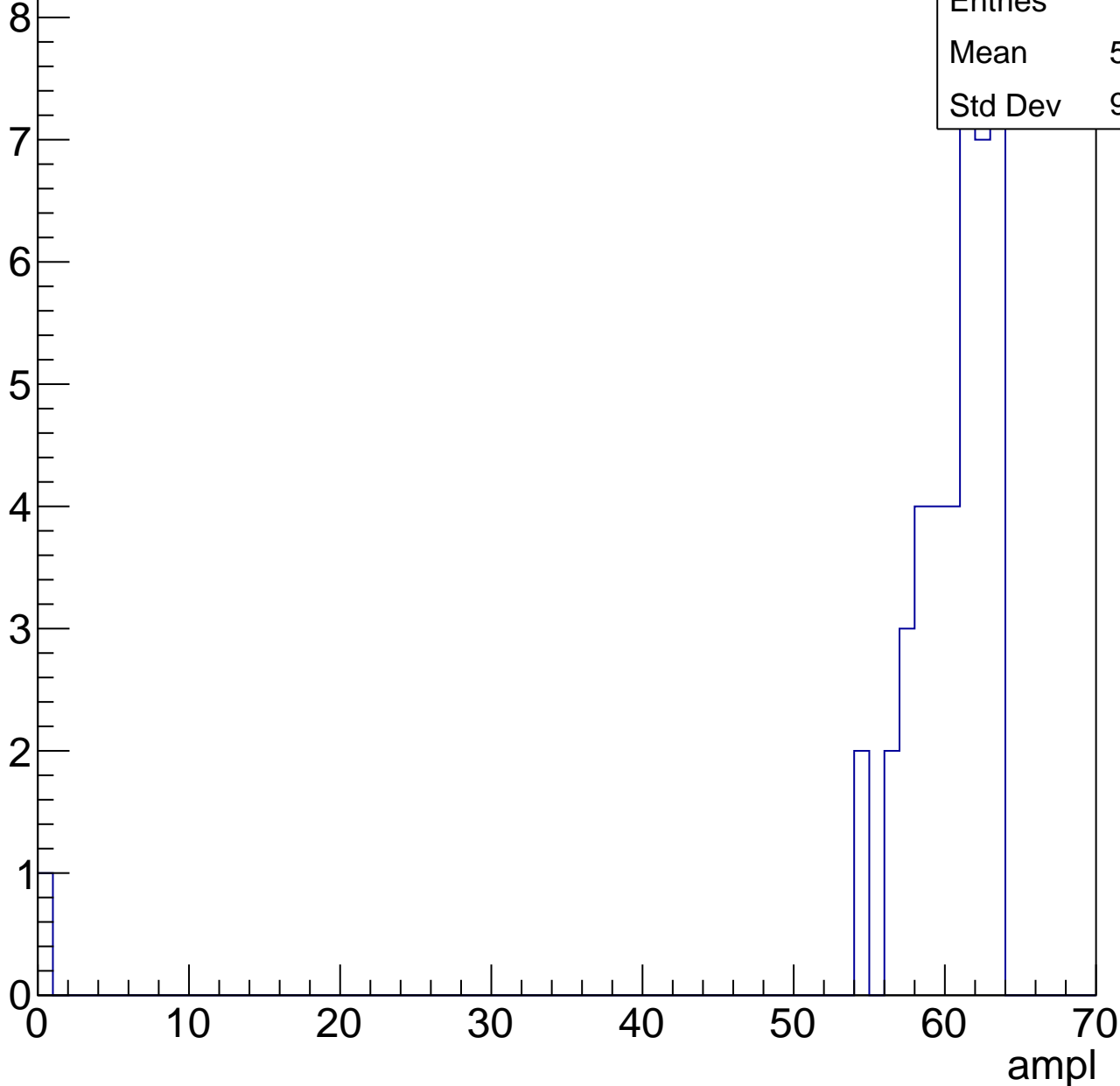
Entries	69
Mean	55.32
Std Dev	3.622

# B1L102S, U24-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

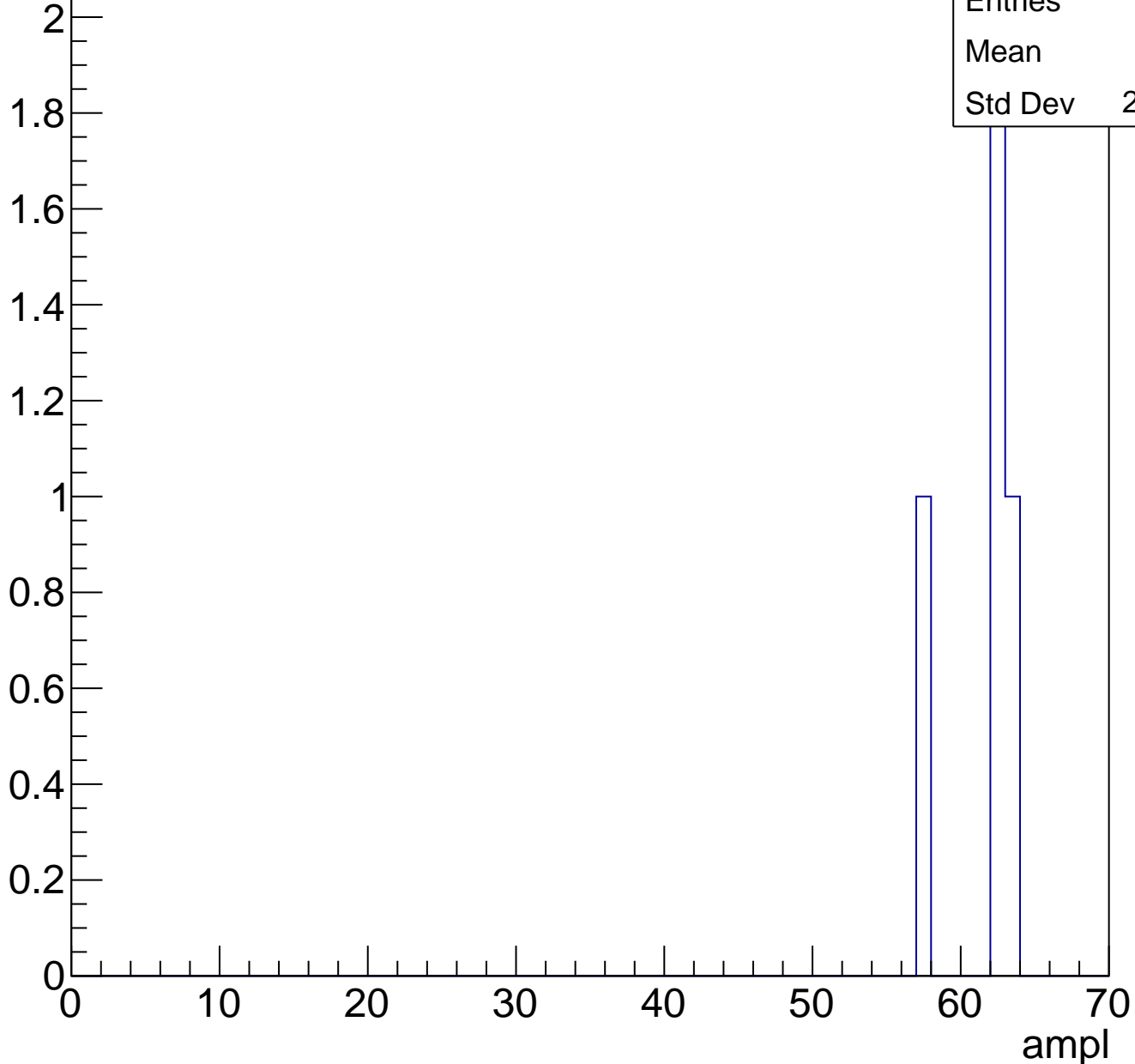
Entries	43
Mean	58.72
Std Dev	9.387



# B1L102S, U24-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch44, adc0

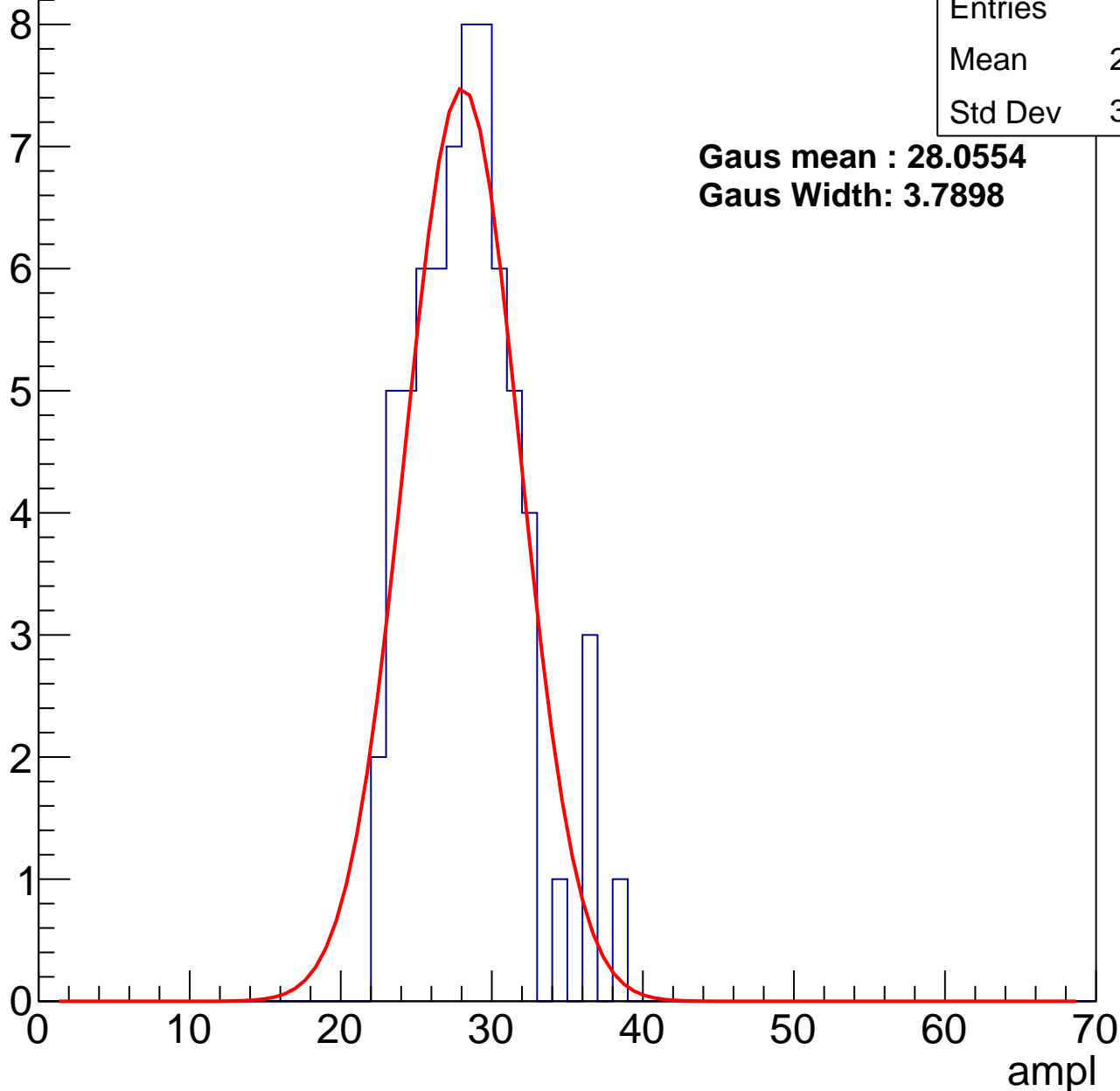
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.96
Std Dev	3.522

**Gaus mean : 28.0554**

**Gaus Width: 3.7898**



# B1L102S, U24-ch44, adc1

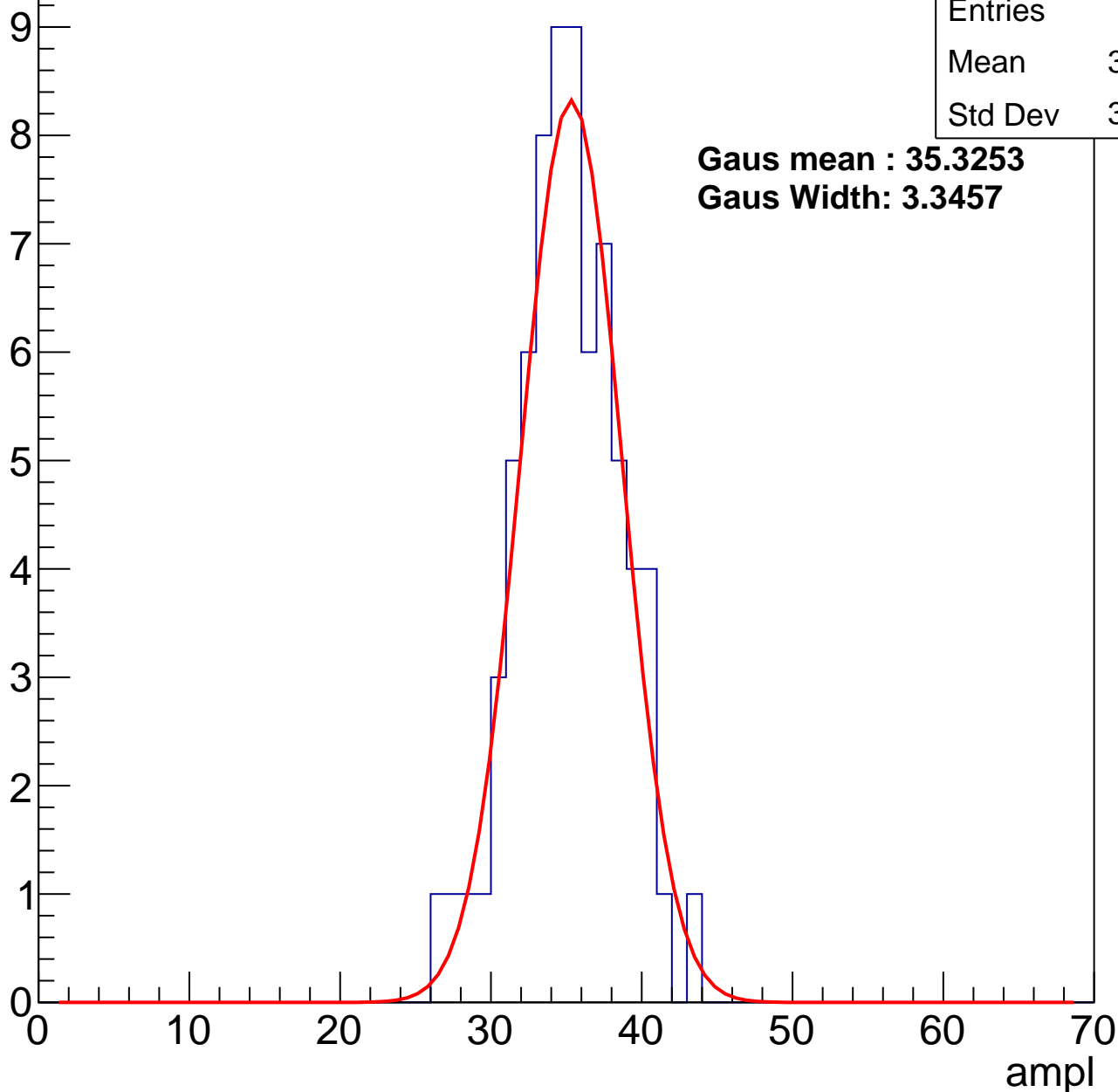
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.68
Std Dev	3.366

**Gaus mean : 35.3253**

**Gaus Width: 3.3457**



# B1L102S, U24-ch44, adc2

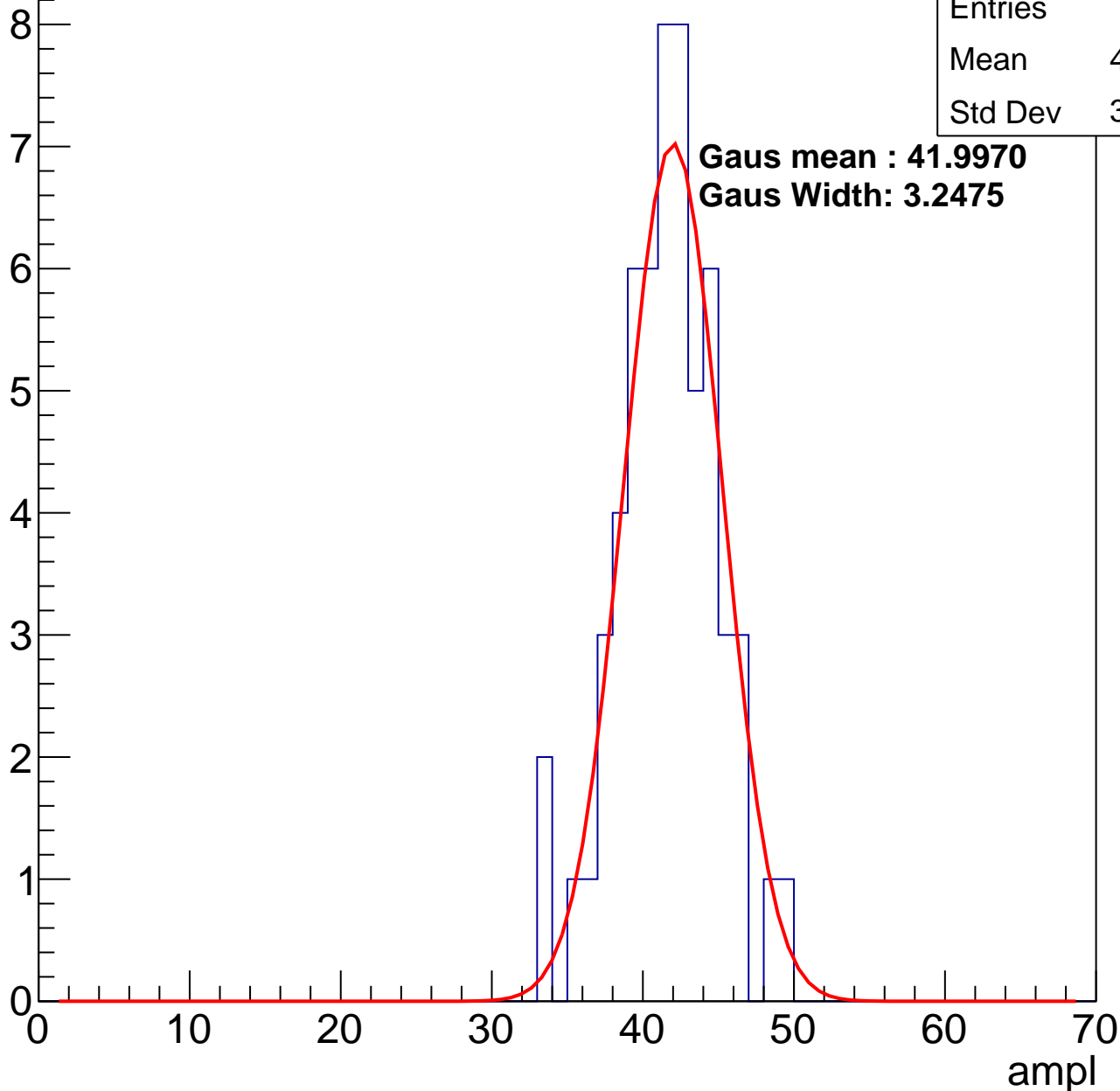
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.16
Std Dev	3.268

**Gaus mean : 41.9970**

**Gaus Width: 3.2475**

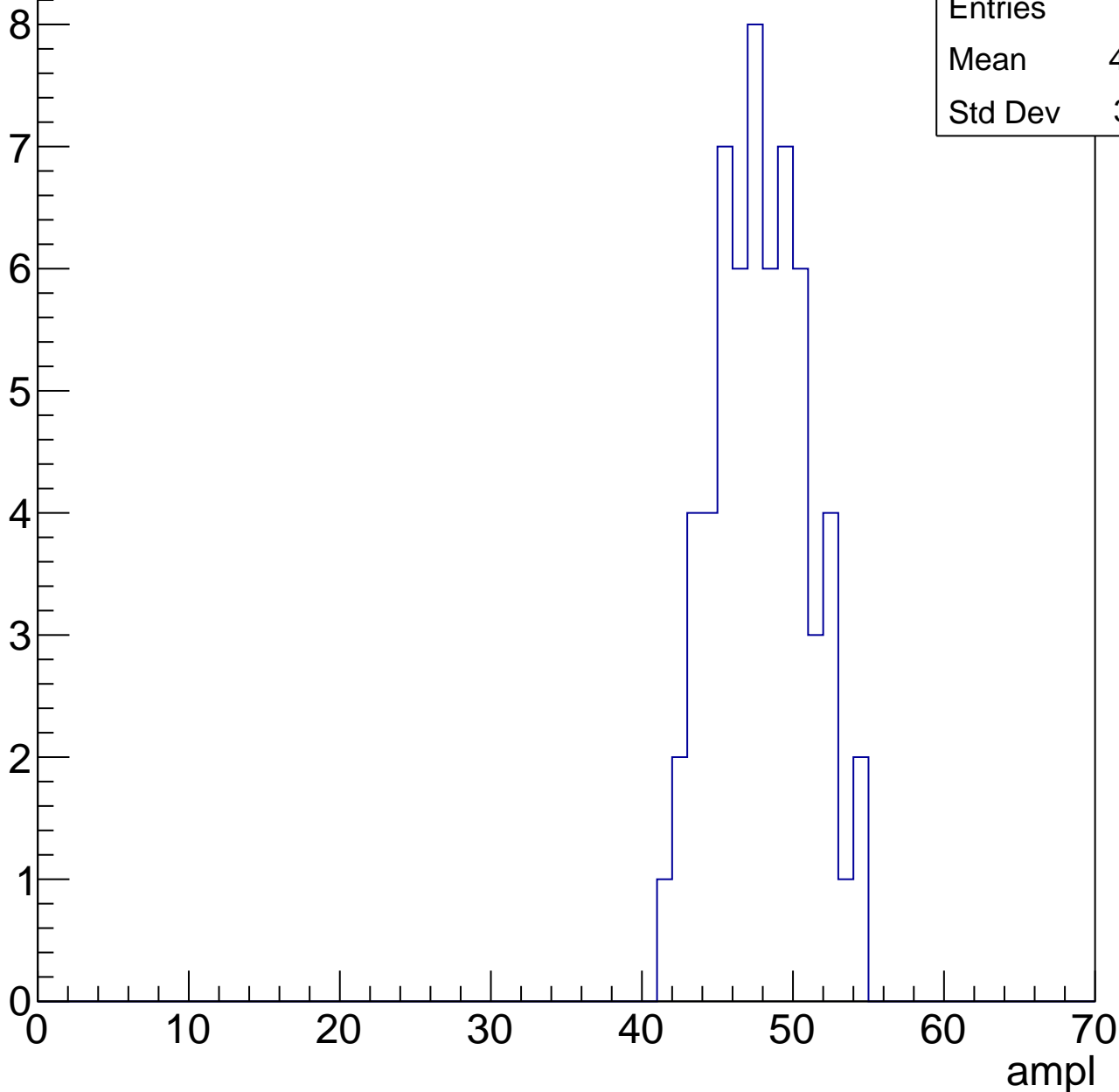


# B1L102S, U24-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.43
Std Dev	3.081

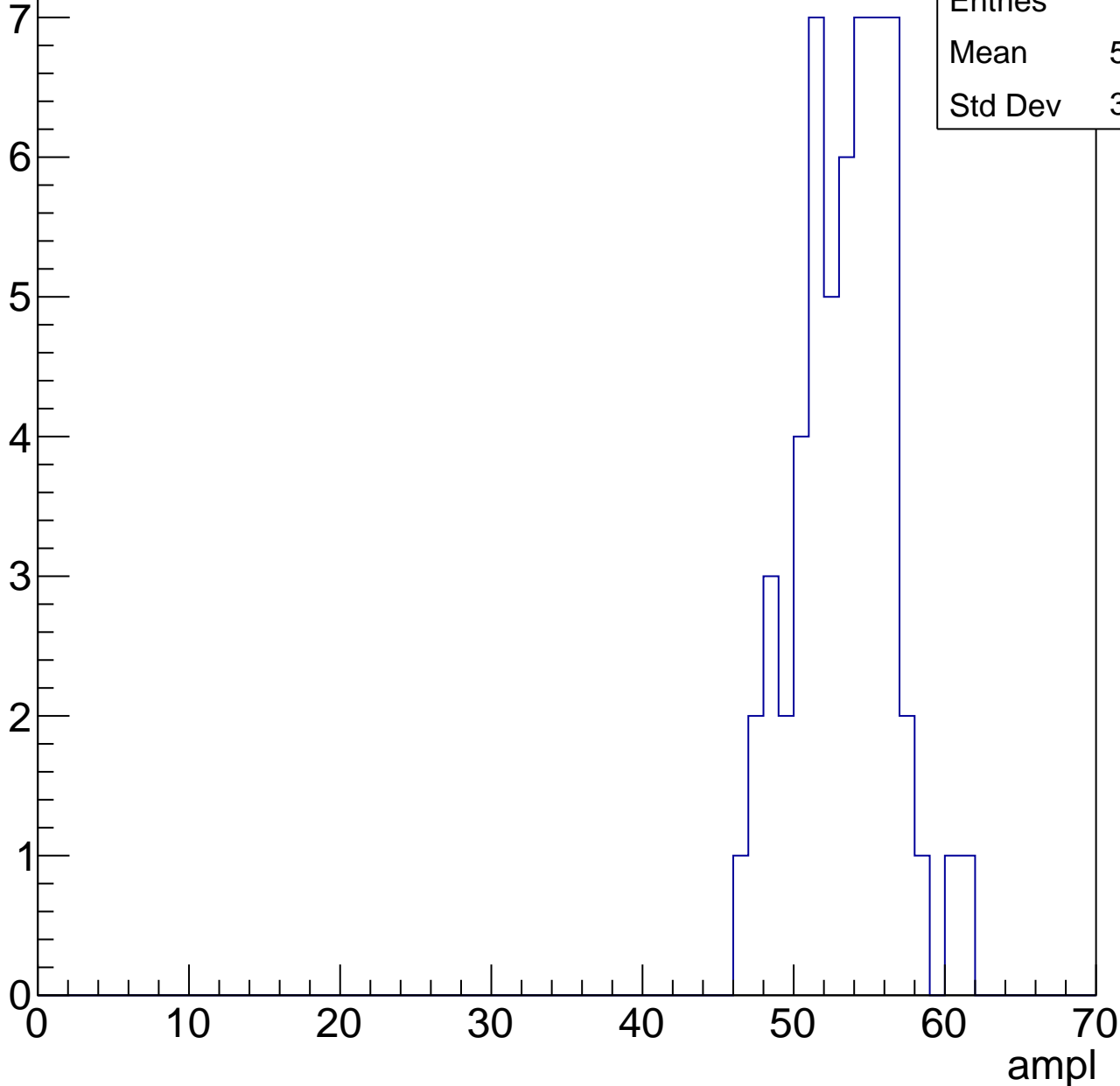


# B1L102S, U24-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

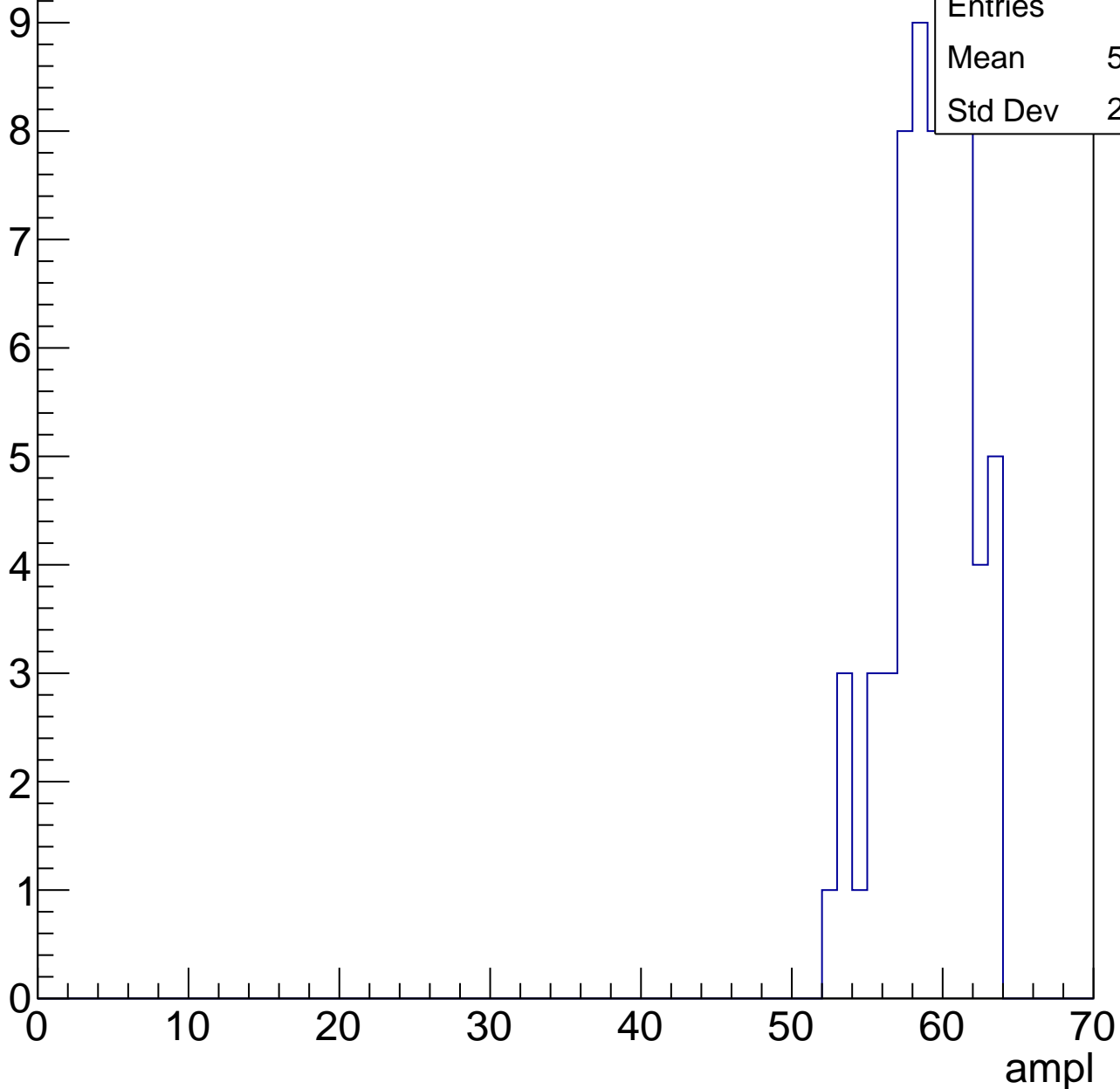
Entries	56
Mean	52.95
Std Dev	3.176



# B1L102S, U24-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

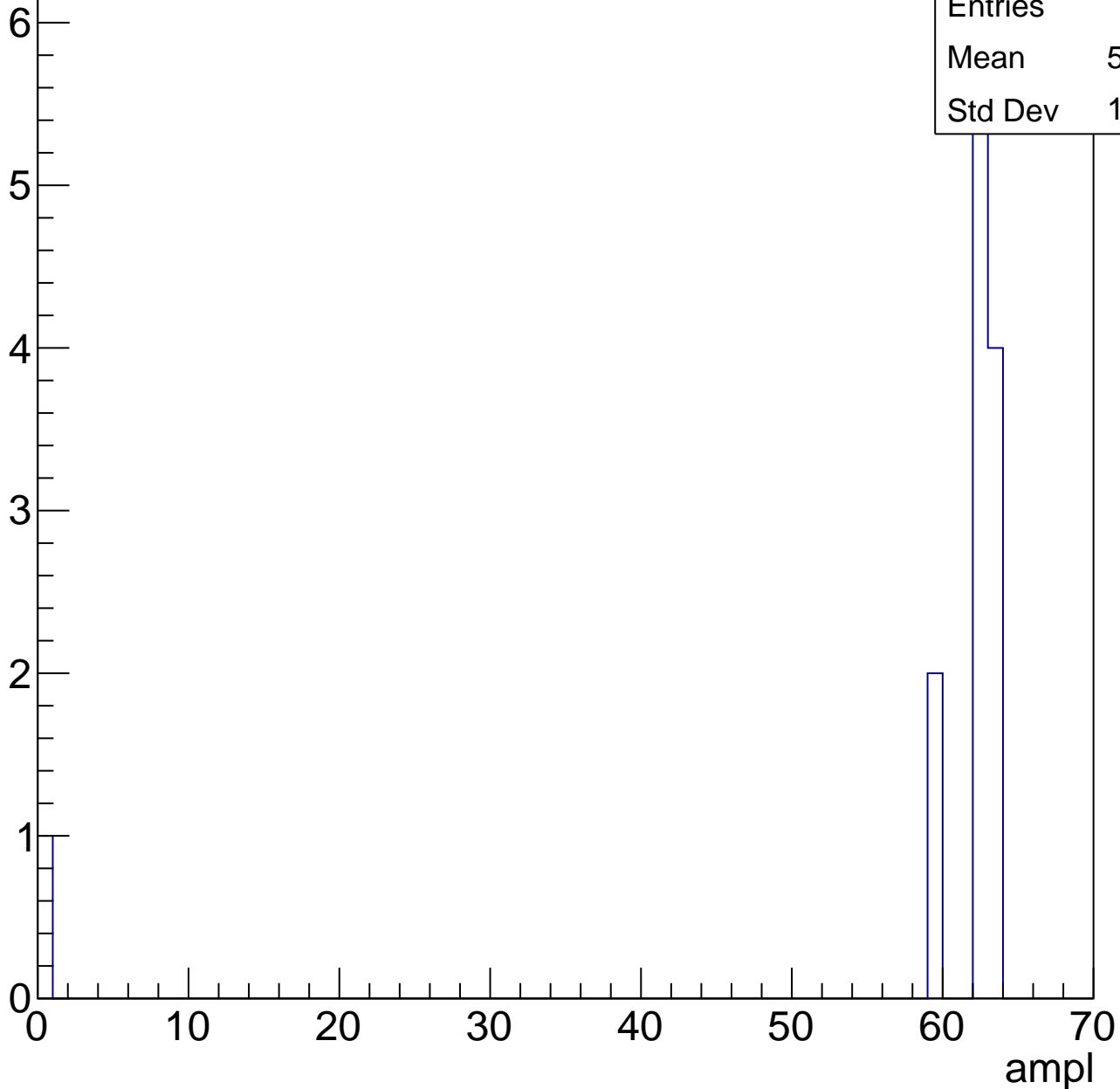


# B1L102S, U24-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57.08
Std Dev	16.53

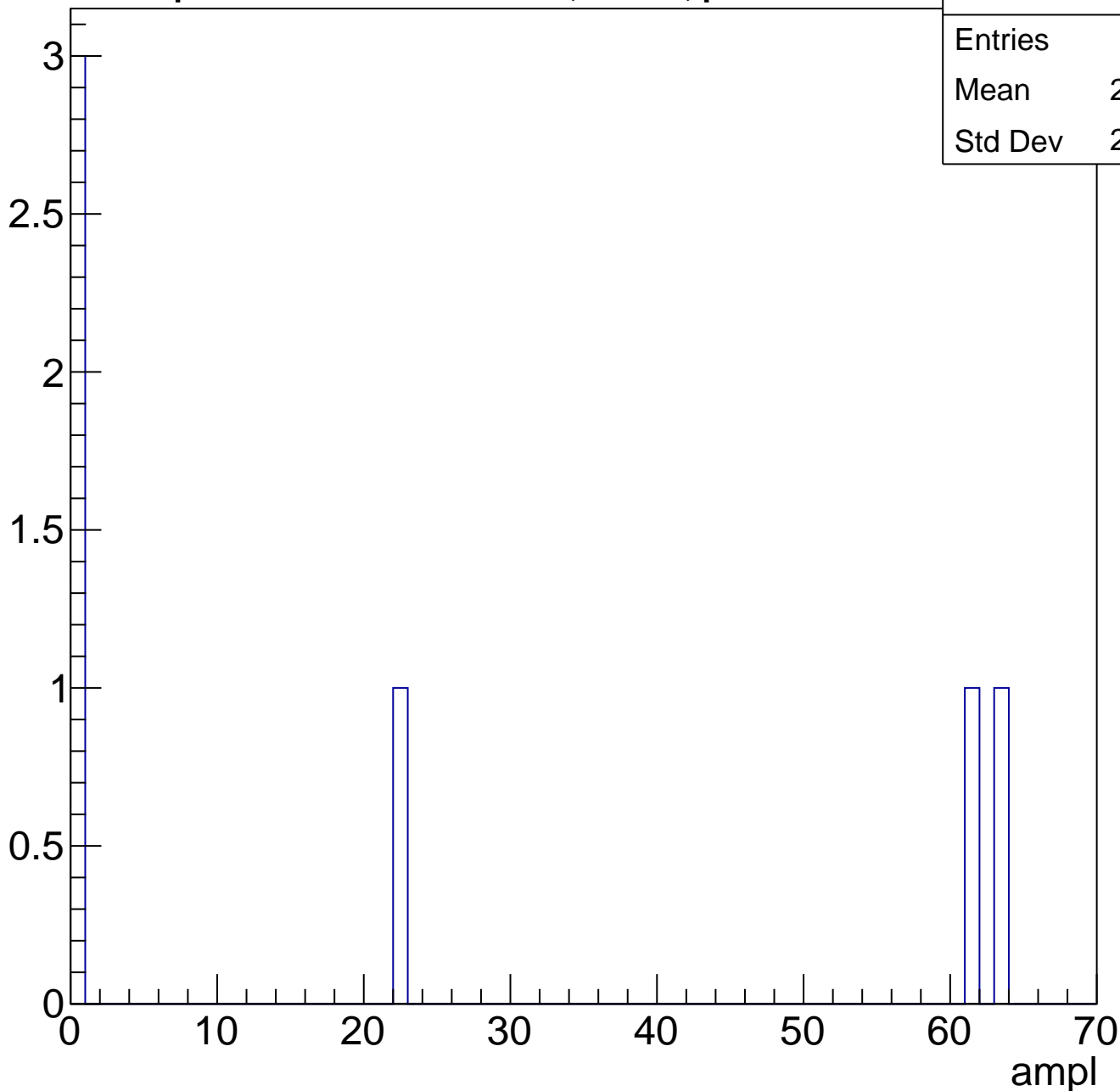




# B1L102S, U24-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	6
Mean	24.33
Std Dev	27.75

# B1L102S, U24-ch45, adc0

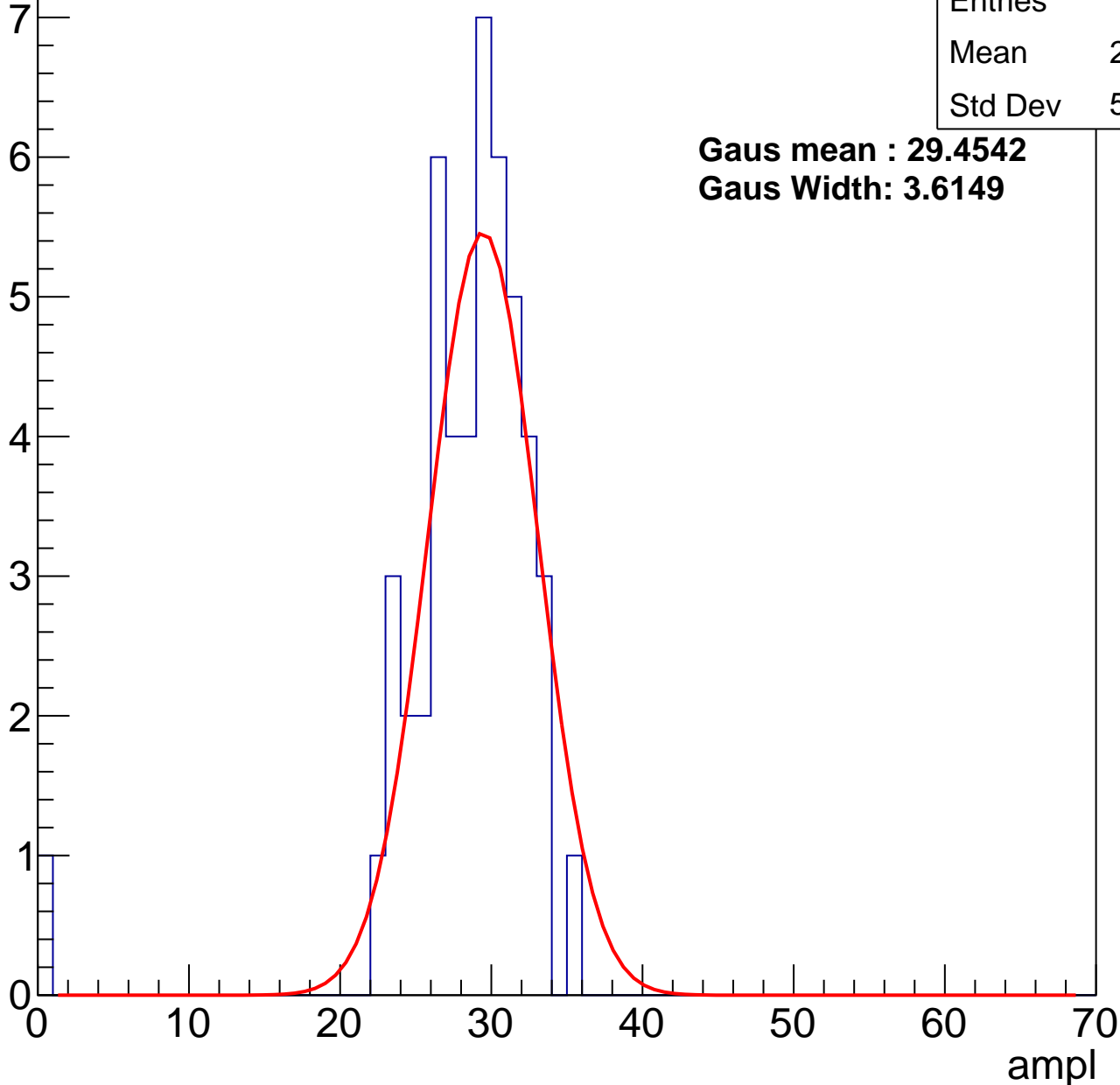
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	27.86
Std Dev	5.022

**Gaus mean : 29.4542**

**Gaus Width: 3.6149**



# B1L102S, U24-ch45, adc1

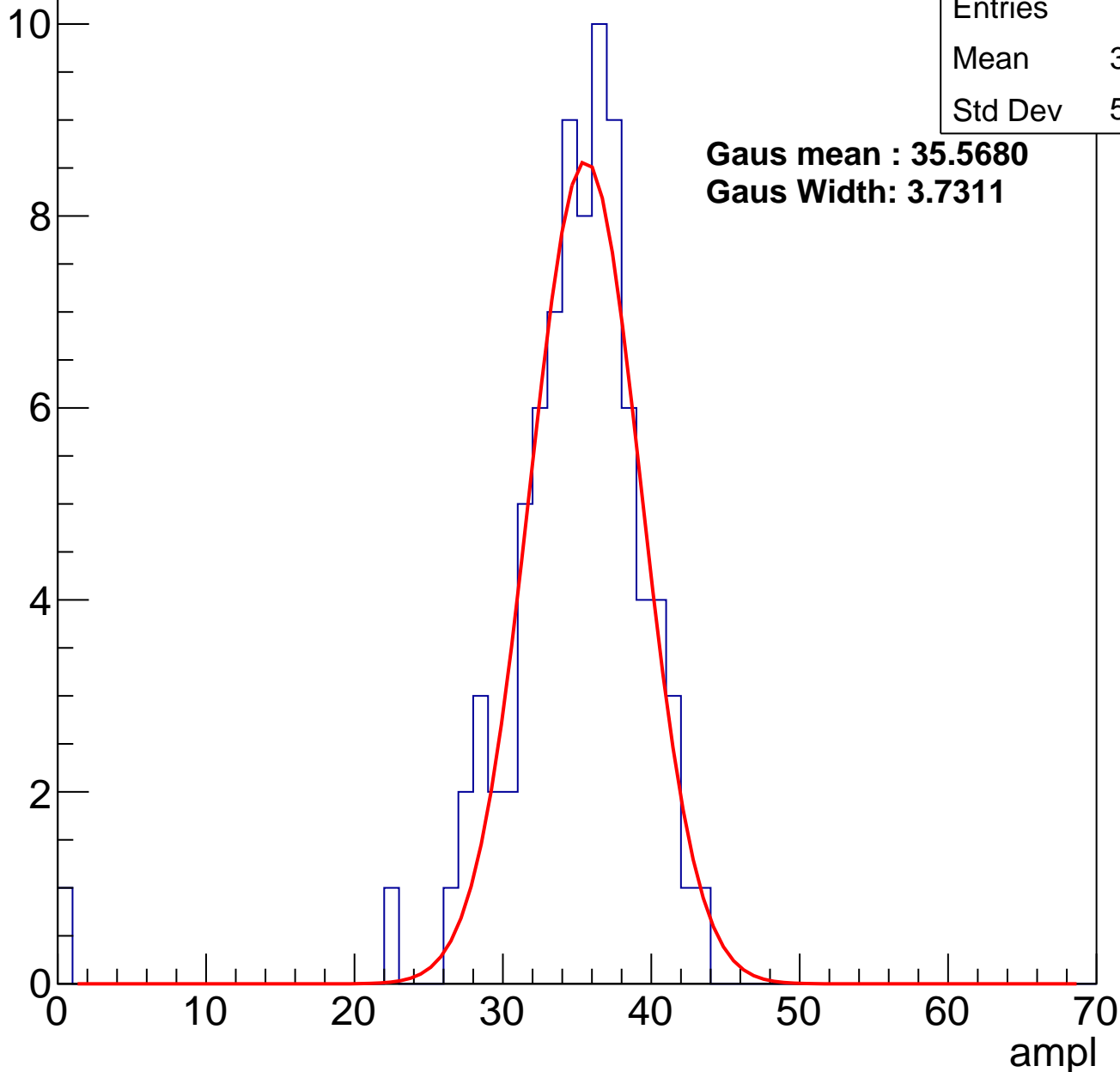
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	34.27
Std Dev	5.405

**Gaus mean : 35.5680**

**Gaus Width: 3.7311**

Entry



# B1L102S, U24-ch45, adc2

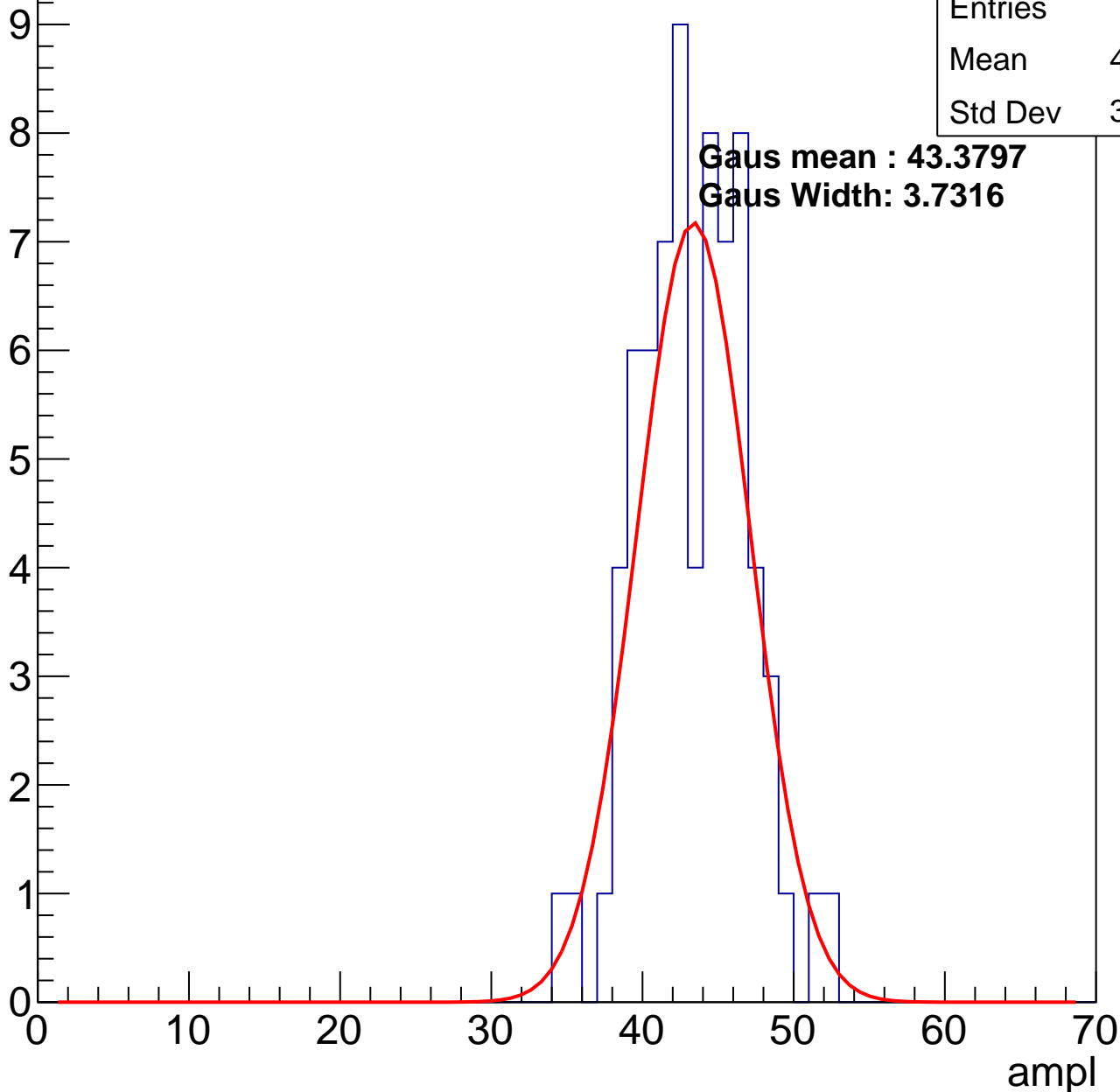
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.89
Std Dev	3.526

**Gaus mean : 43.3797**

**Gaus Width: 3.7316**

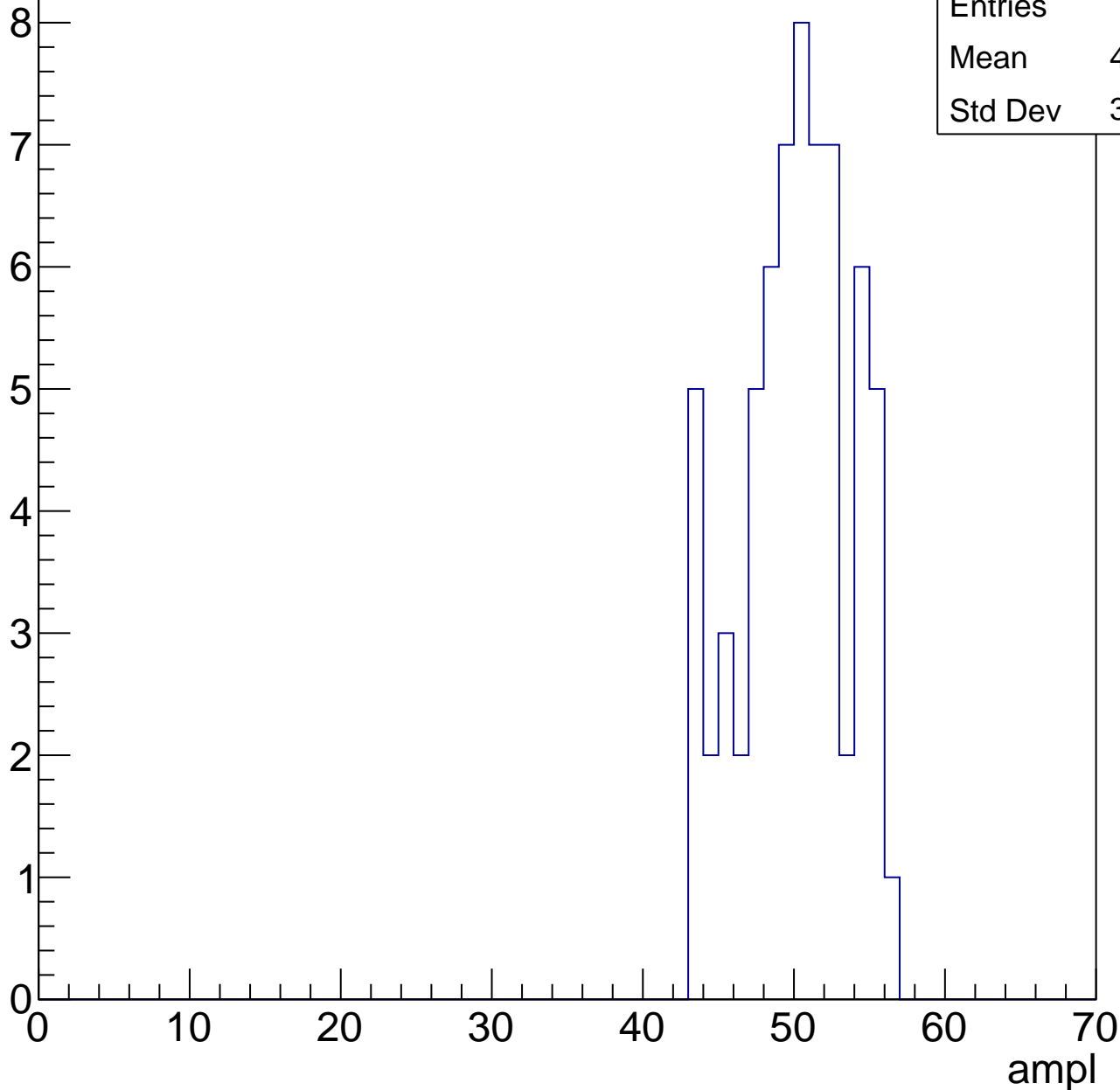


# B1L102S, U24-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.67
Std Dev	3.487

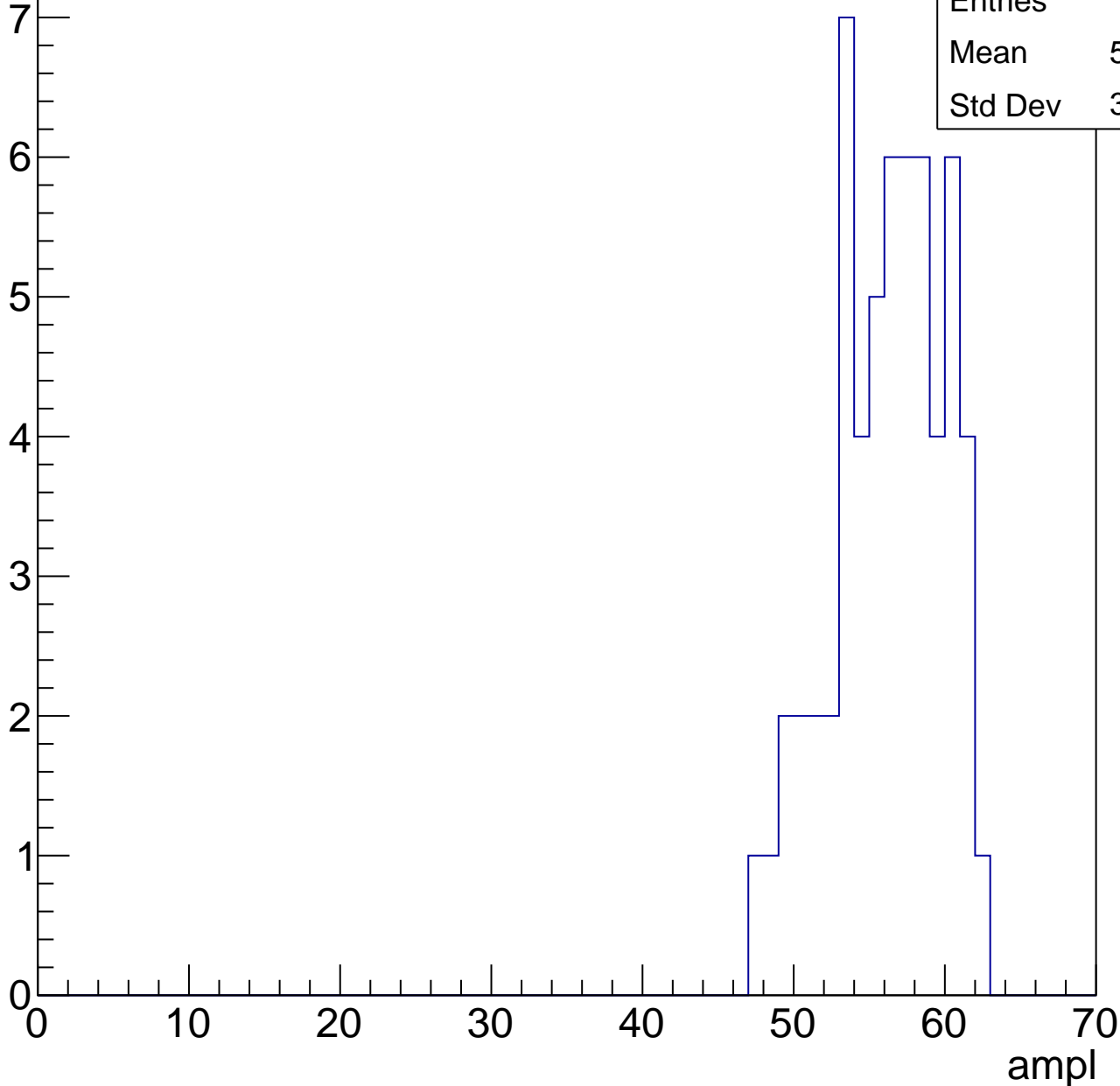


# B1L102S, U24-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.75
Std Dev	3.625

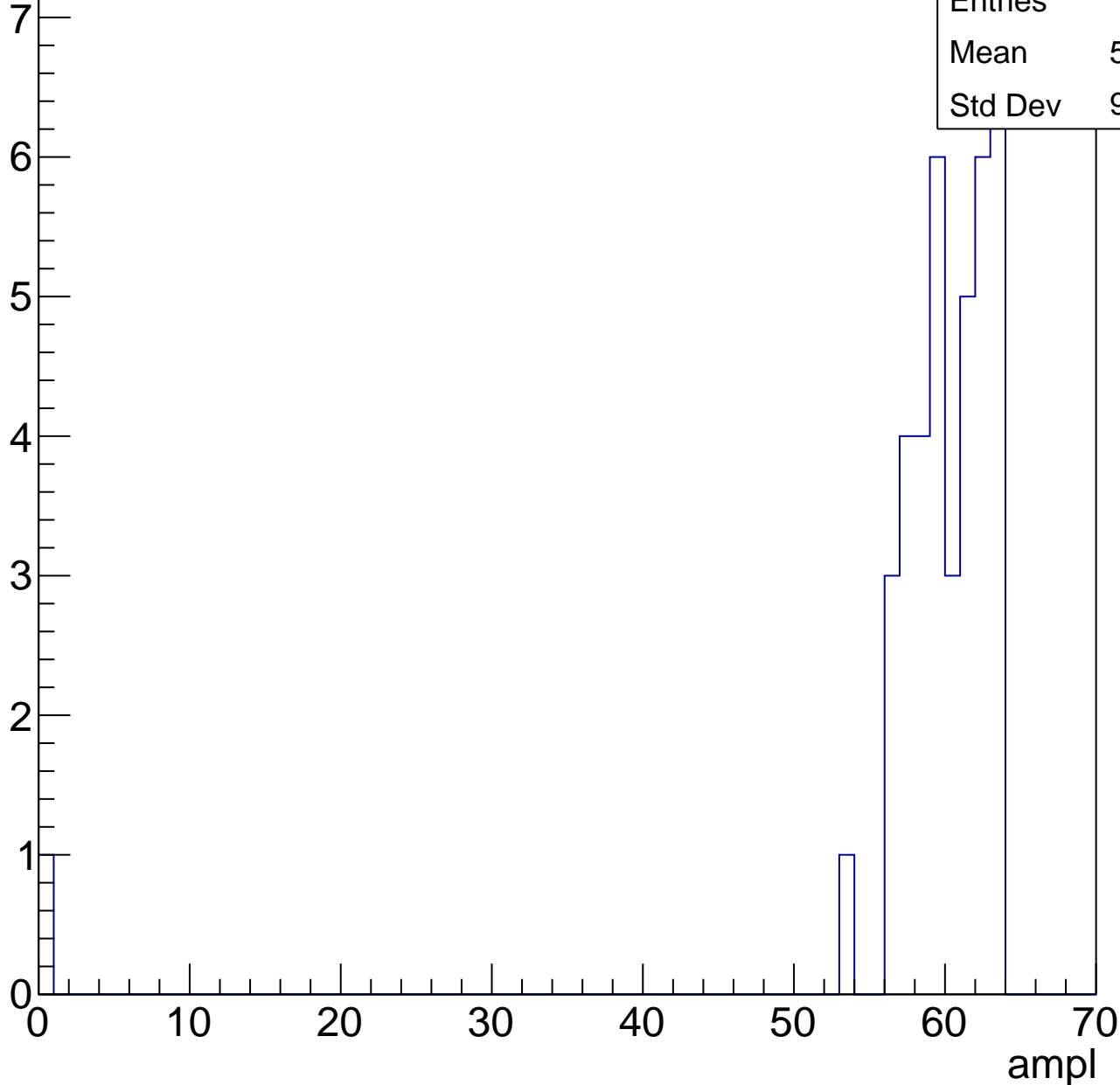


# B1L102S, U24-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.33
Std Dev	9.663



# B1L102S, U24-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch46, adc0

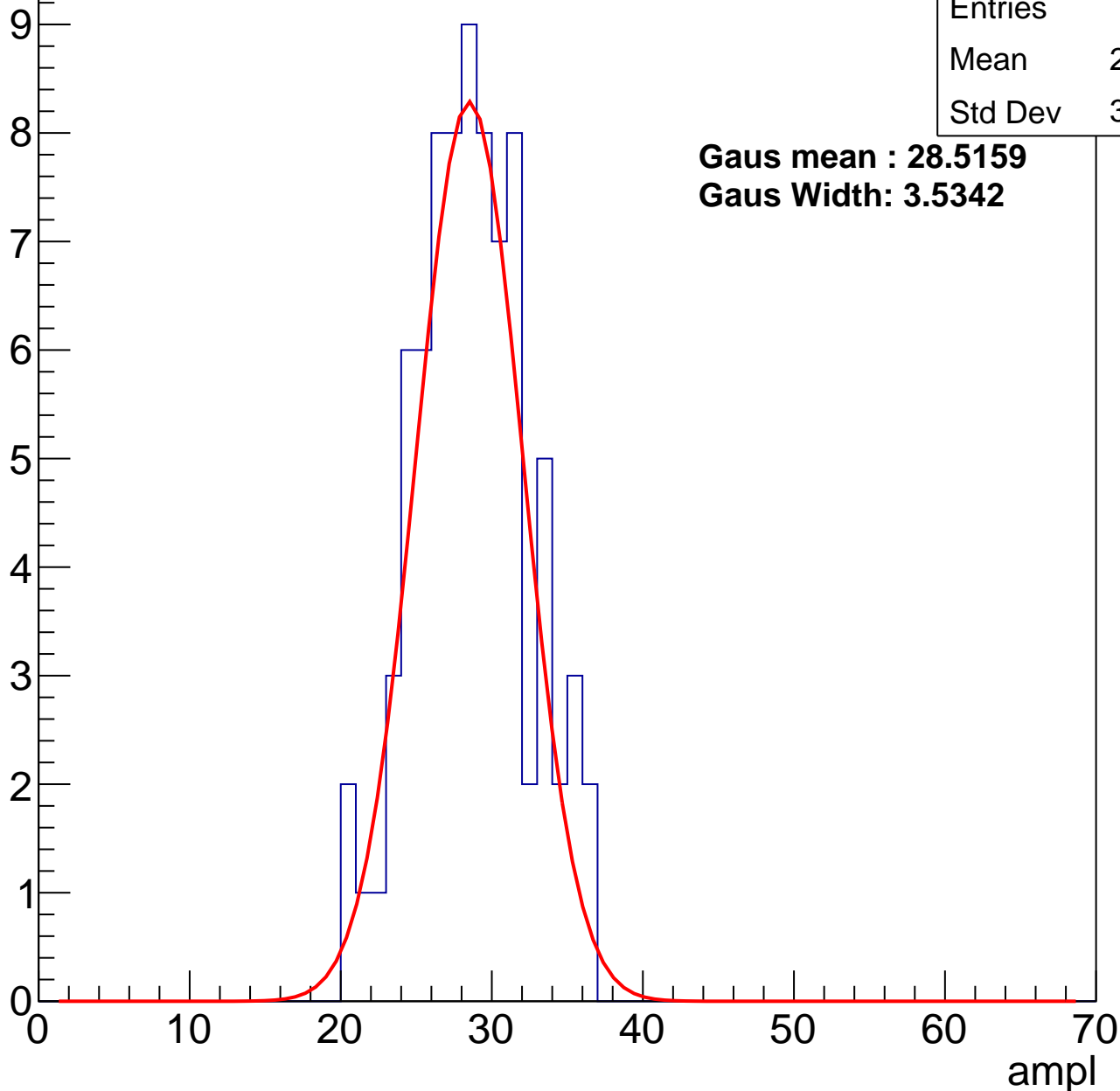
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.22
Std Dev	3.662

**Gaus mean : 28.5159**

**Gaus Width: 3.5342**



# B1L102S, U24-ch46, adc1

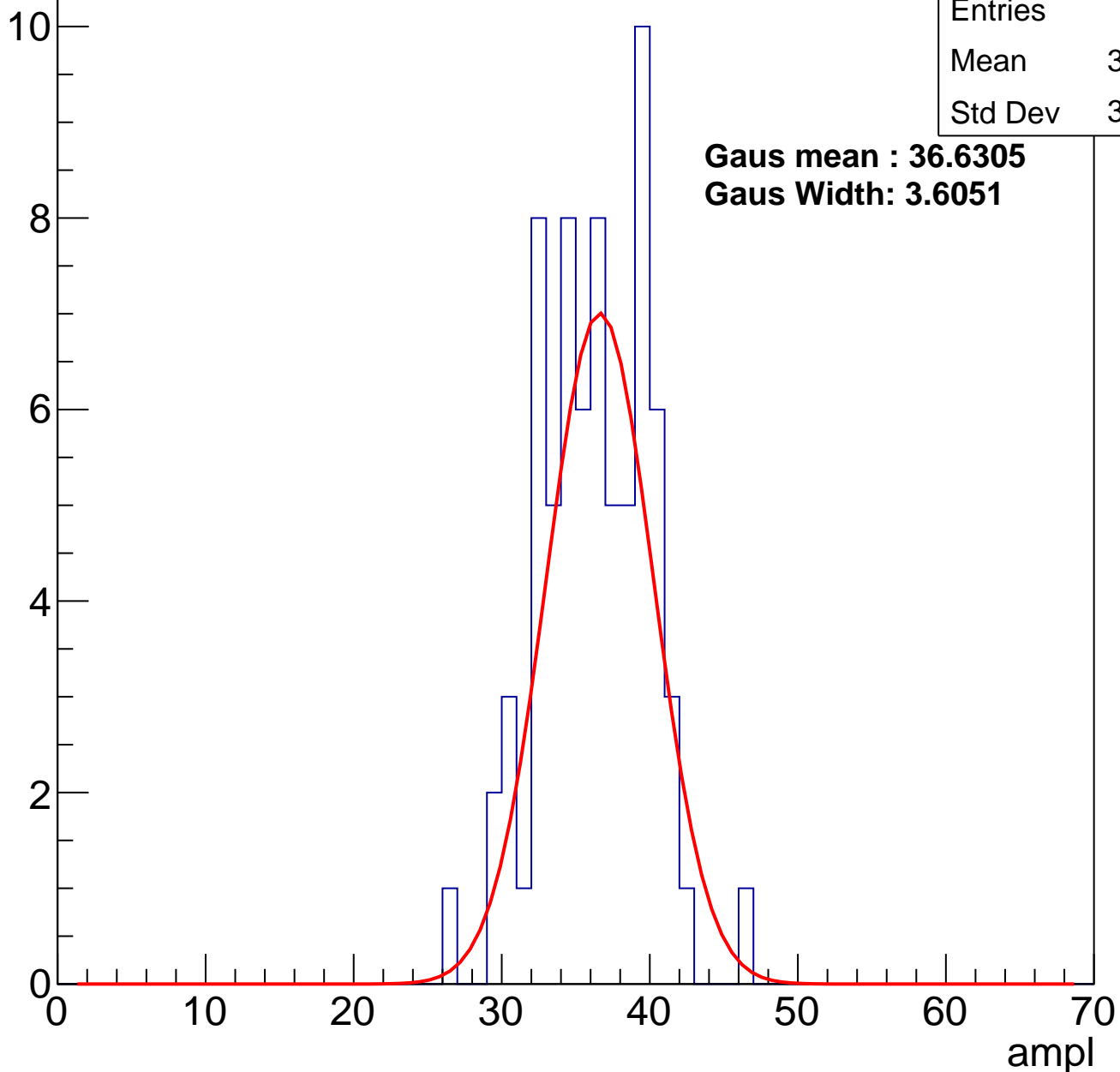
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	35.78
Std Dev	3.627

**Gaus mean : 36.6305**

**Gaus Width: 3.6051**

Entry



# B1L102S, U24-ch46, adc2

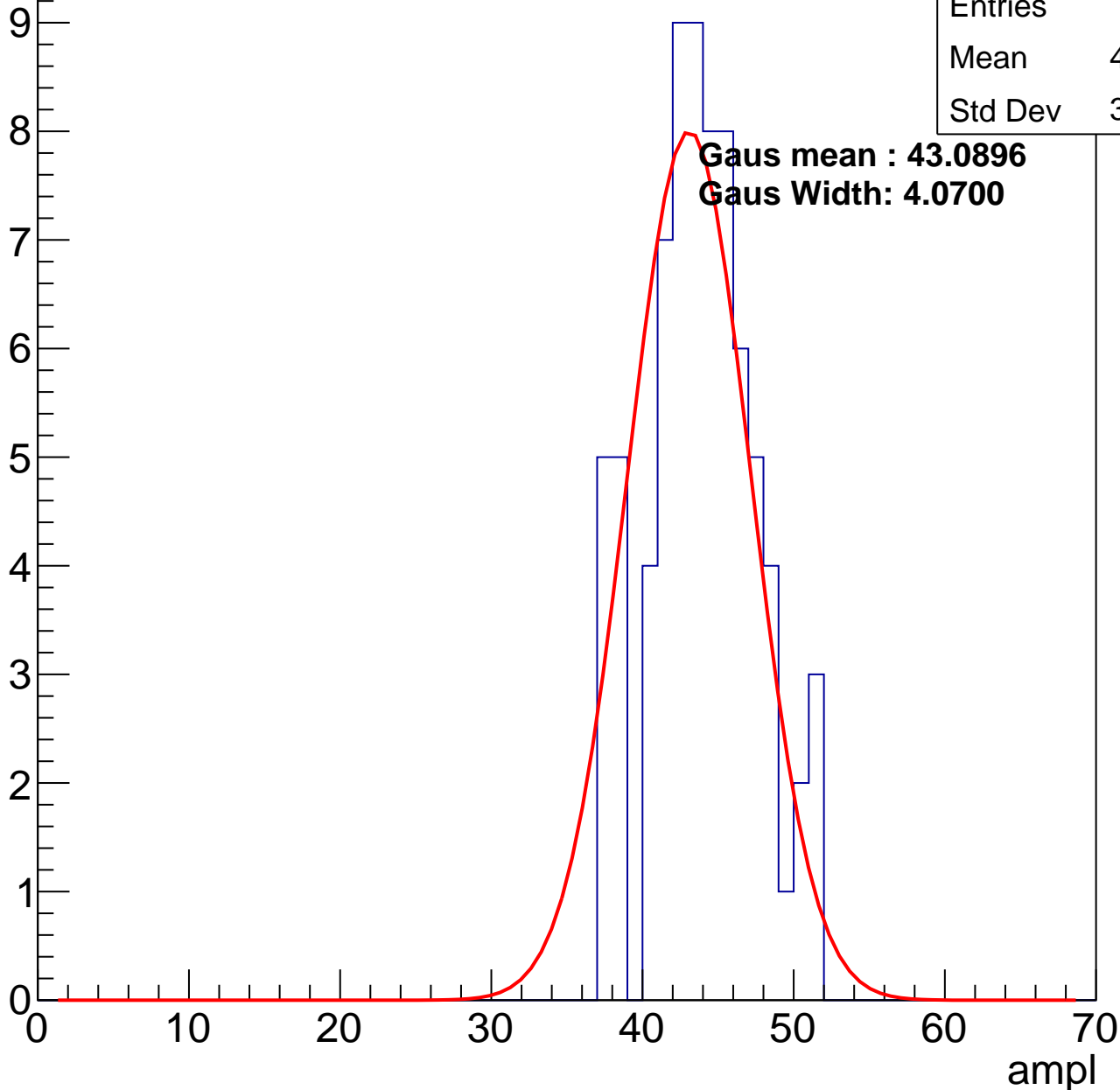
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.47
Std Dev	3.556

**Gaus mean : 43.0896**

**Gaus Width: 4.0700**

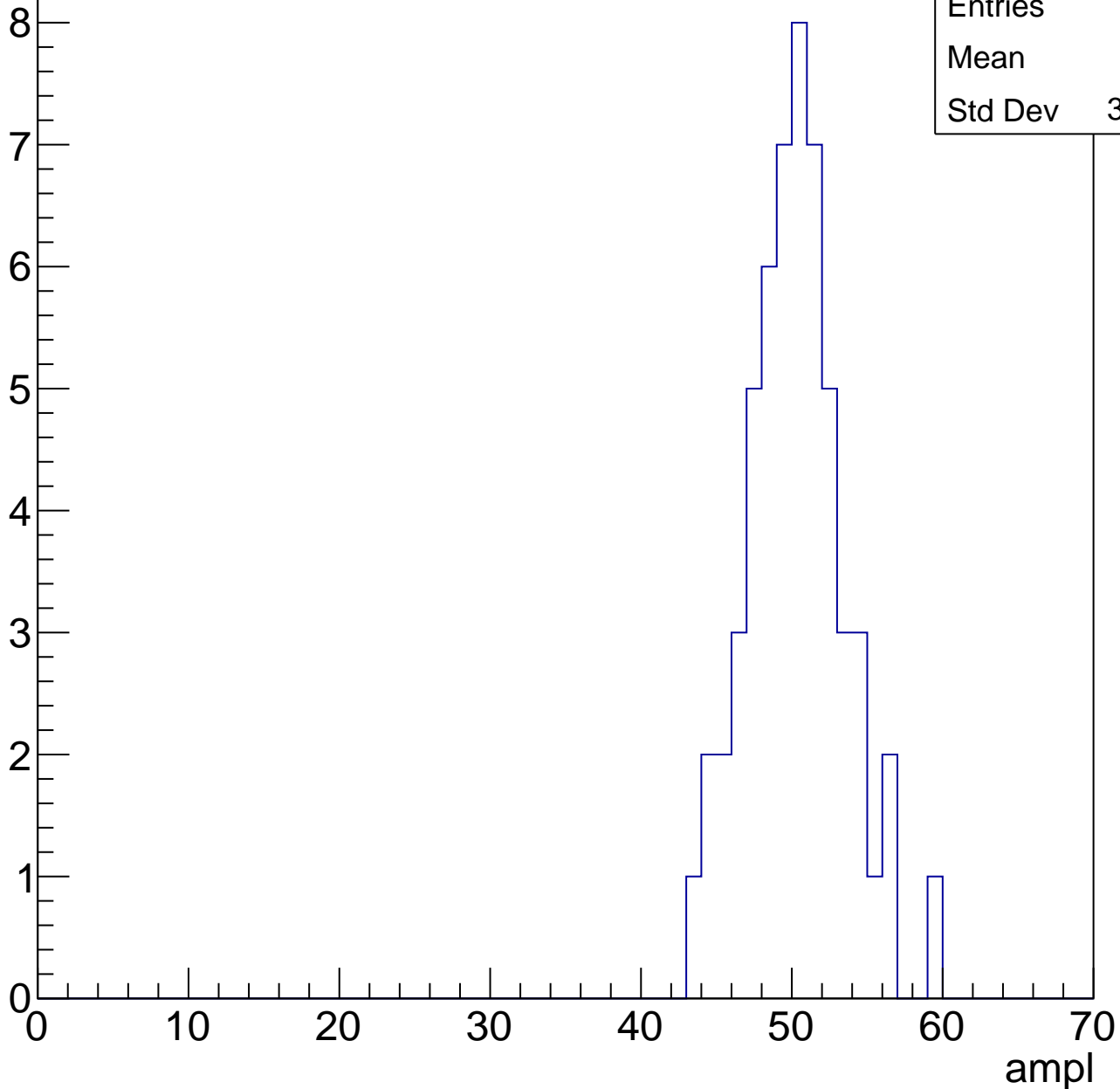


# B1L102S, U24-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.8
Std Dev	3.198

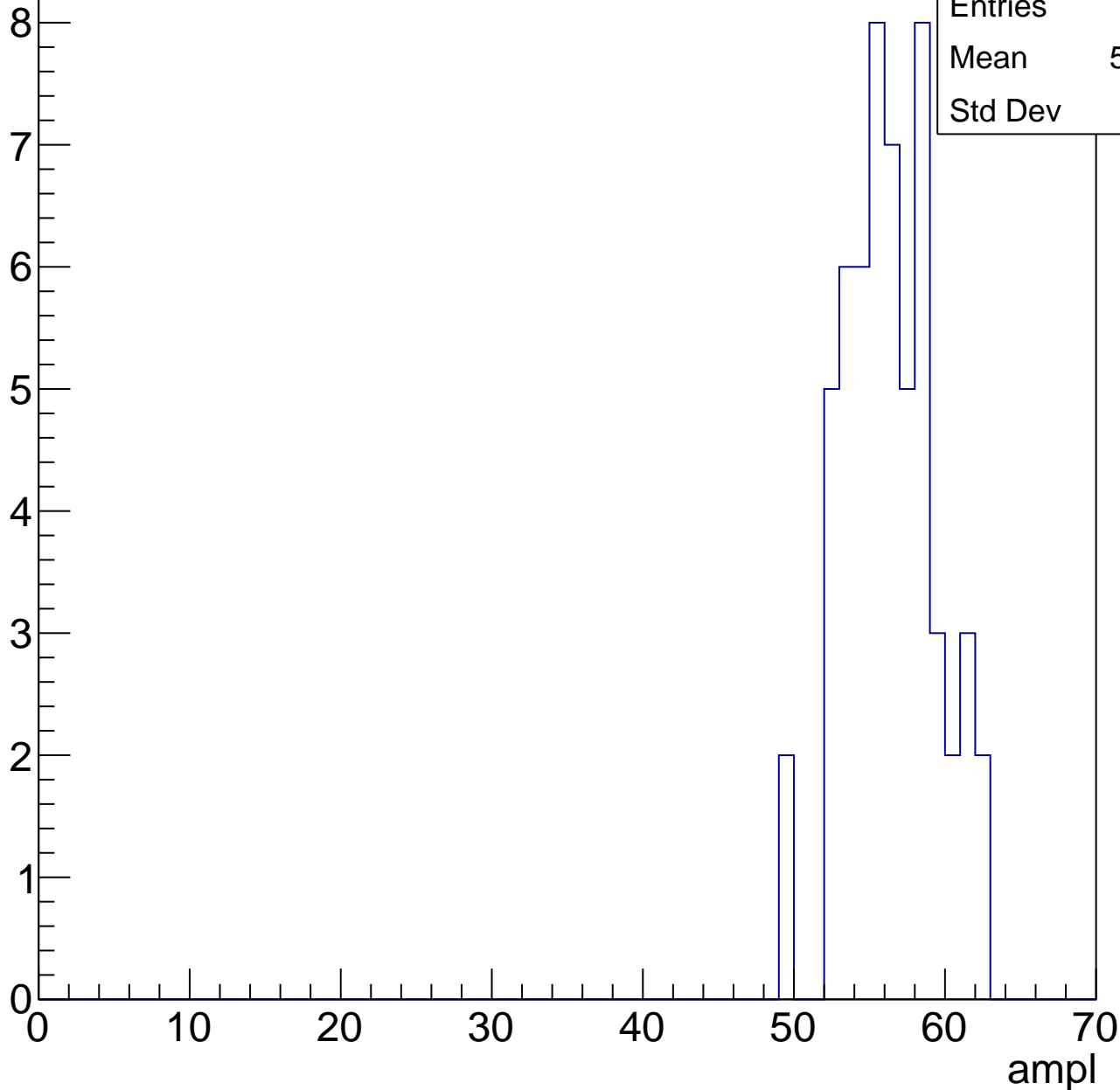


# B1L102S, U24-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

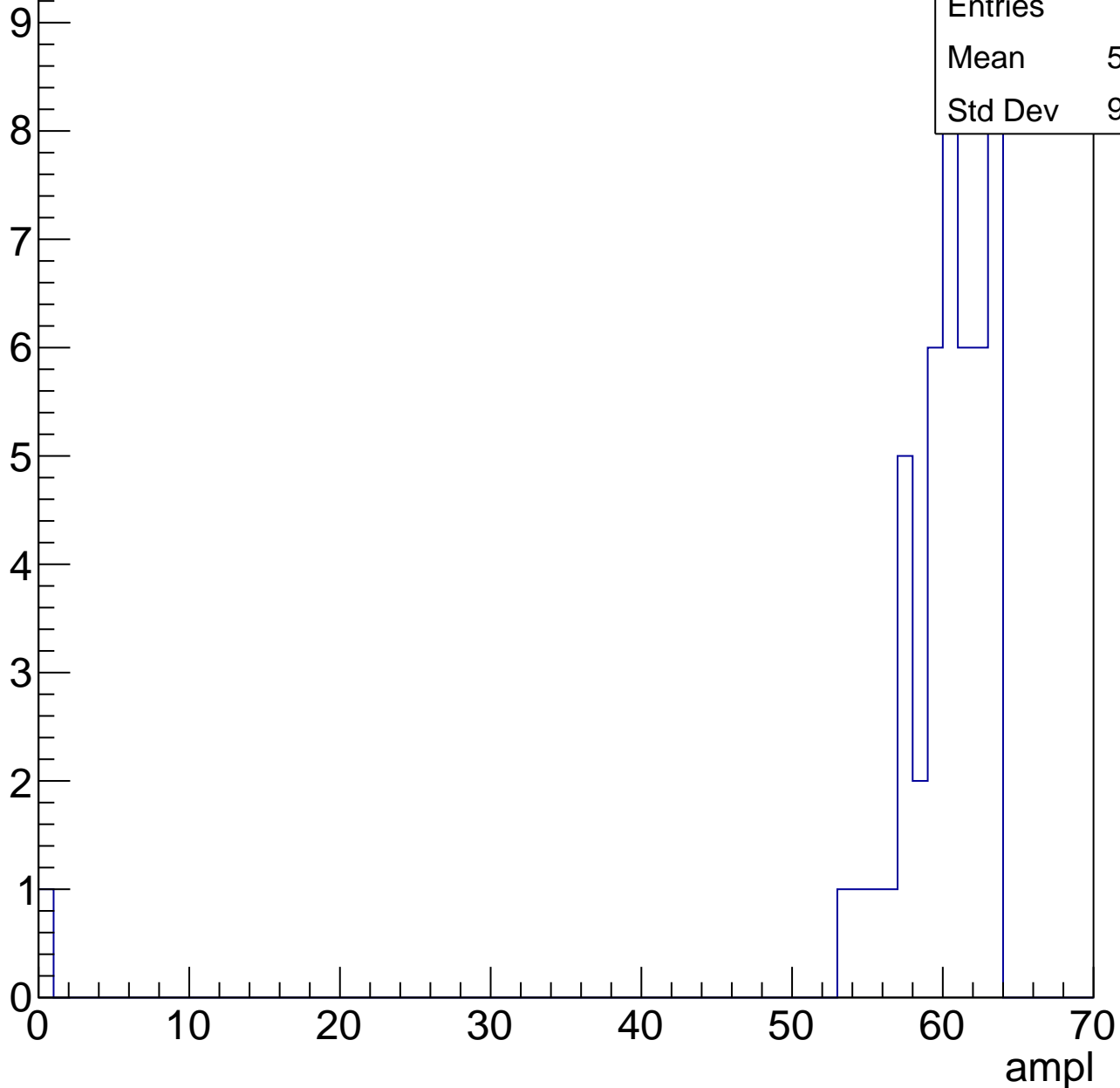
Entries	57
Mean	55.88
Std Dev	2.98



# B1L102S, U24-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch47, adc0

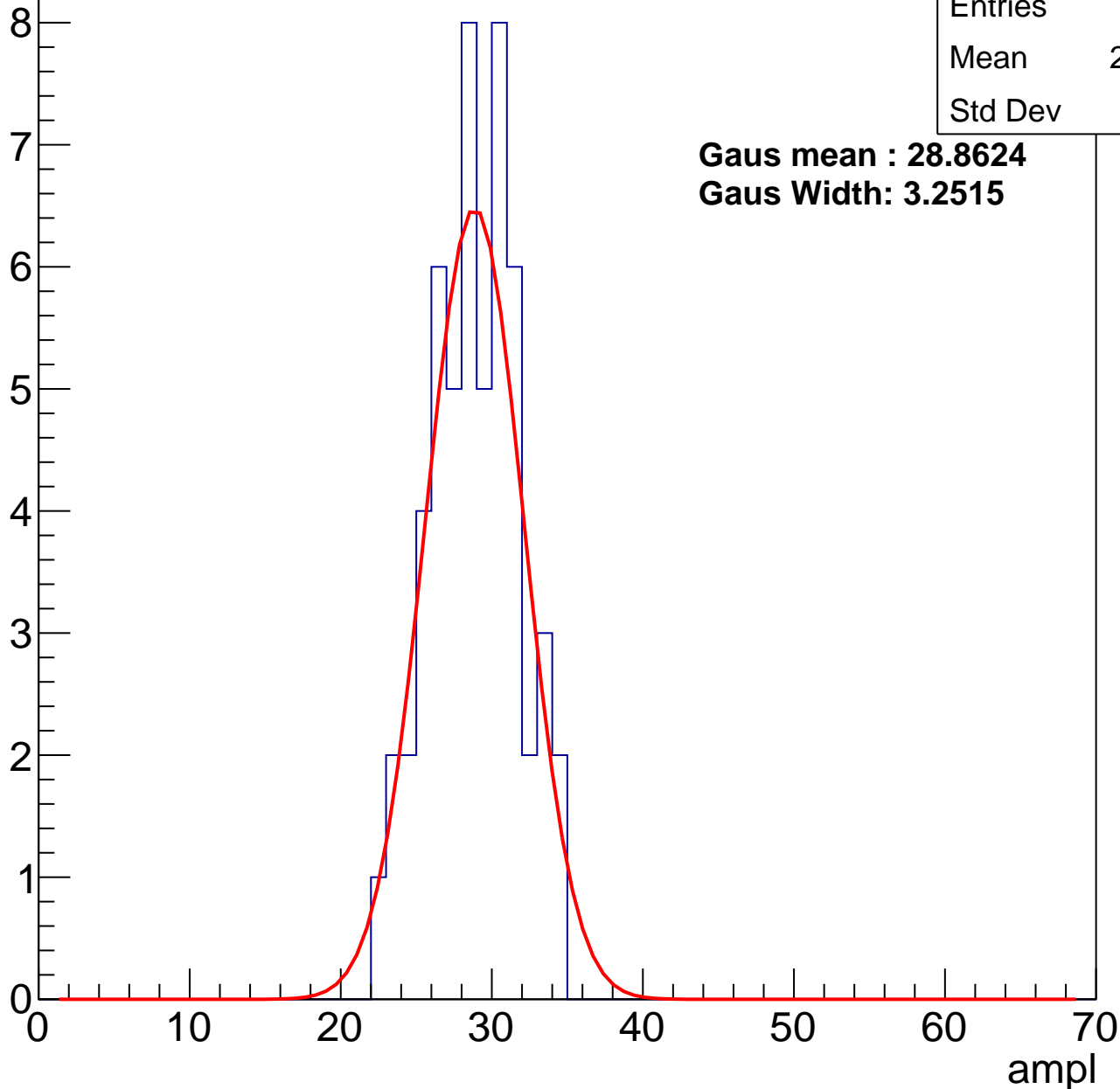
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	28.39
Std Dev	2.87

**Gaus mean : 28.8624**

**Gaus Width: 3.2515**



# B1L102S, U24-ch47, adc1

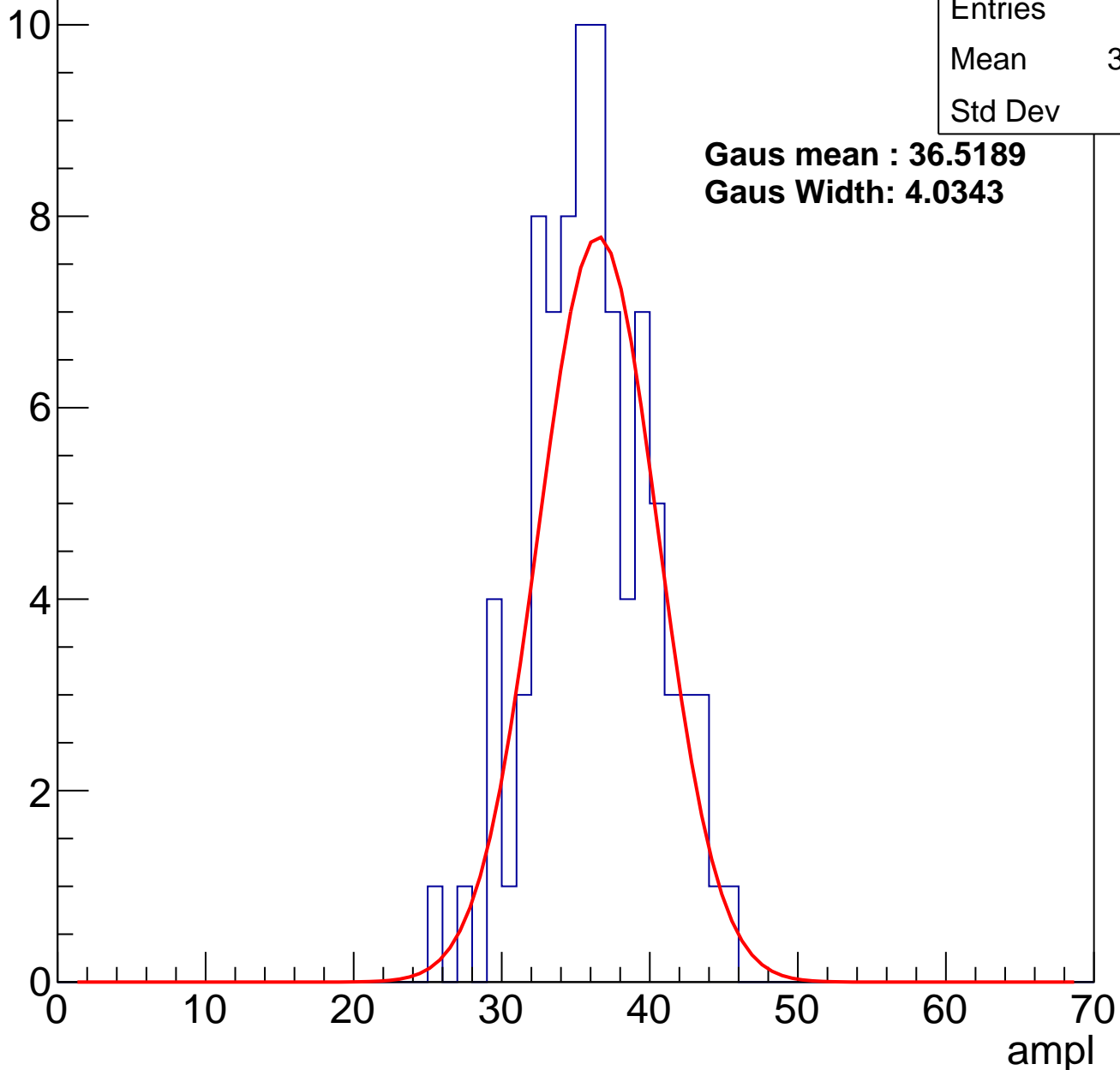
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	87
Mean	35.76
Std Dev	3.98

**Gaus mean : 36.5189**

**Gaus Width: 4.0343**

Entry



# B1L102S, U24-ch47, adc2

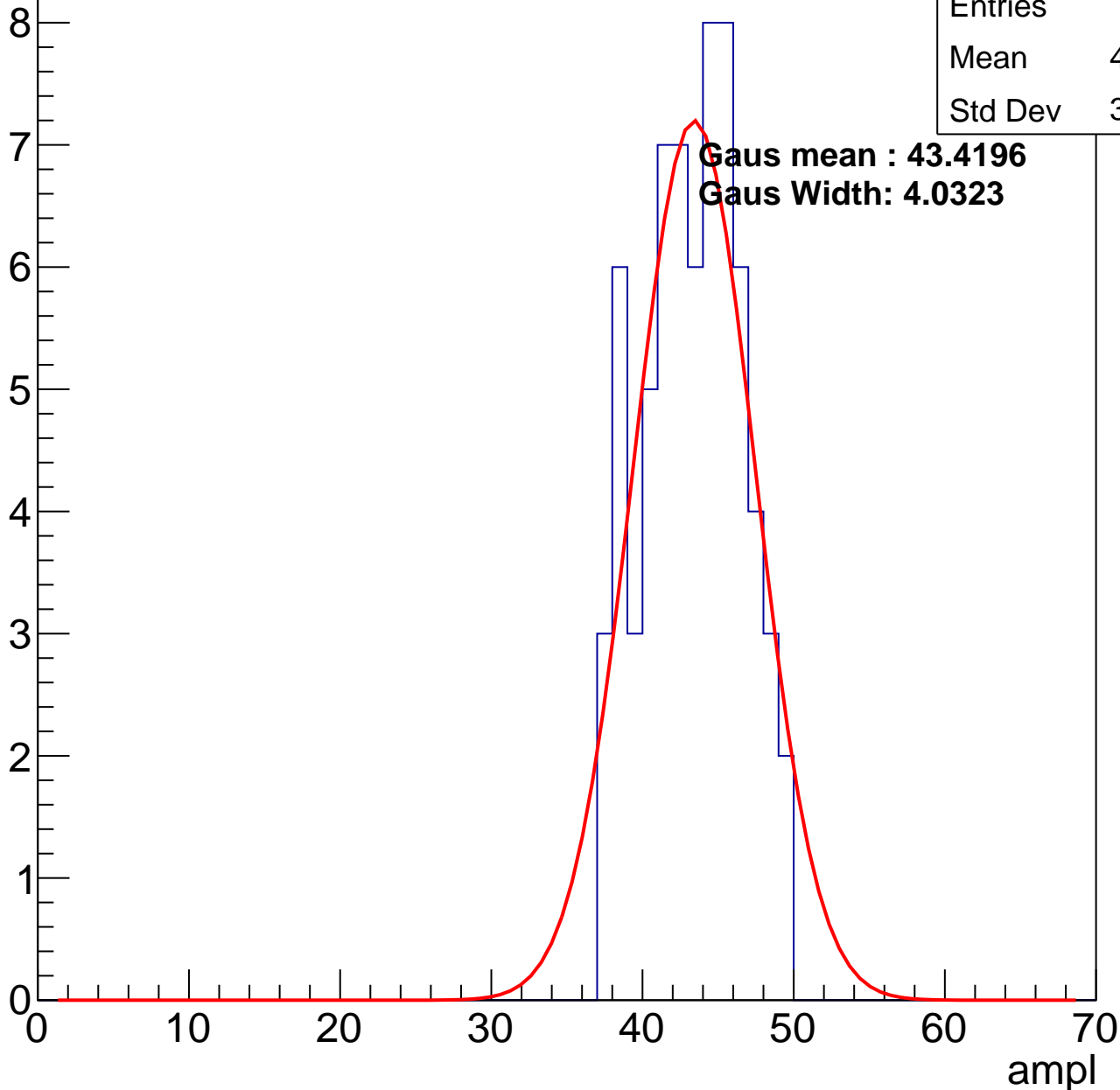
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.84
Std Dev	3.184

**Gaus mean : 43.4196**

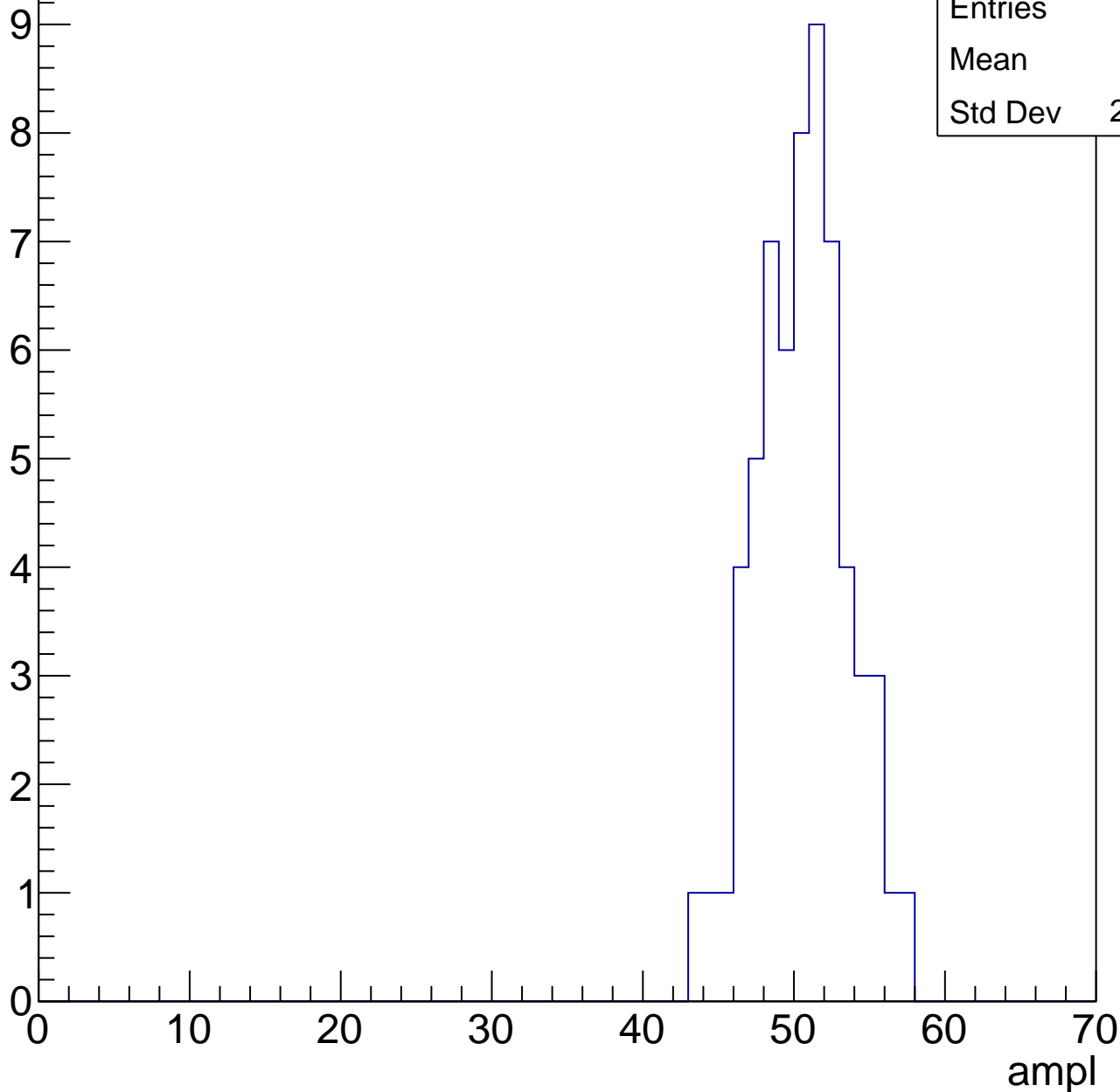
**Gaus Width: 4.0323**



# B1L102S, U24-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

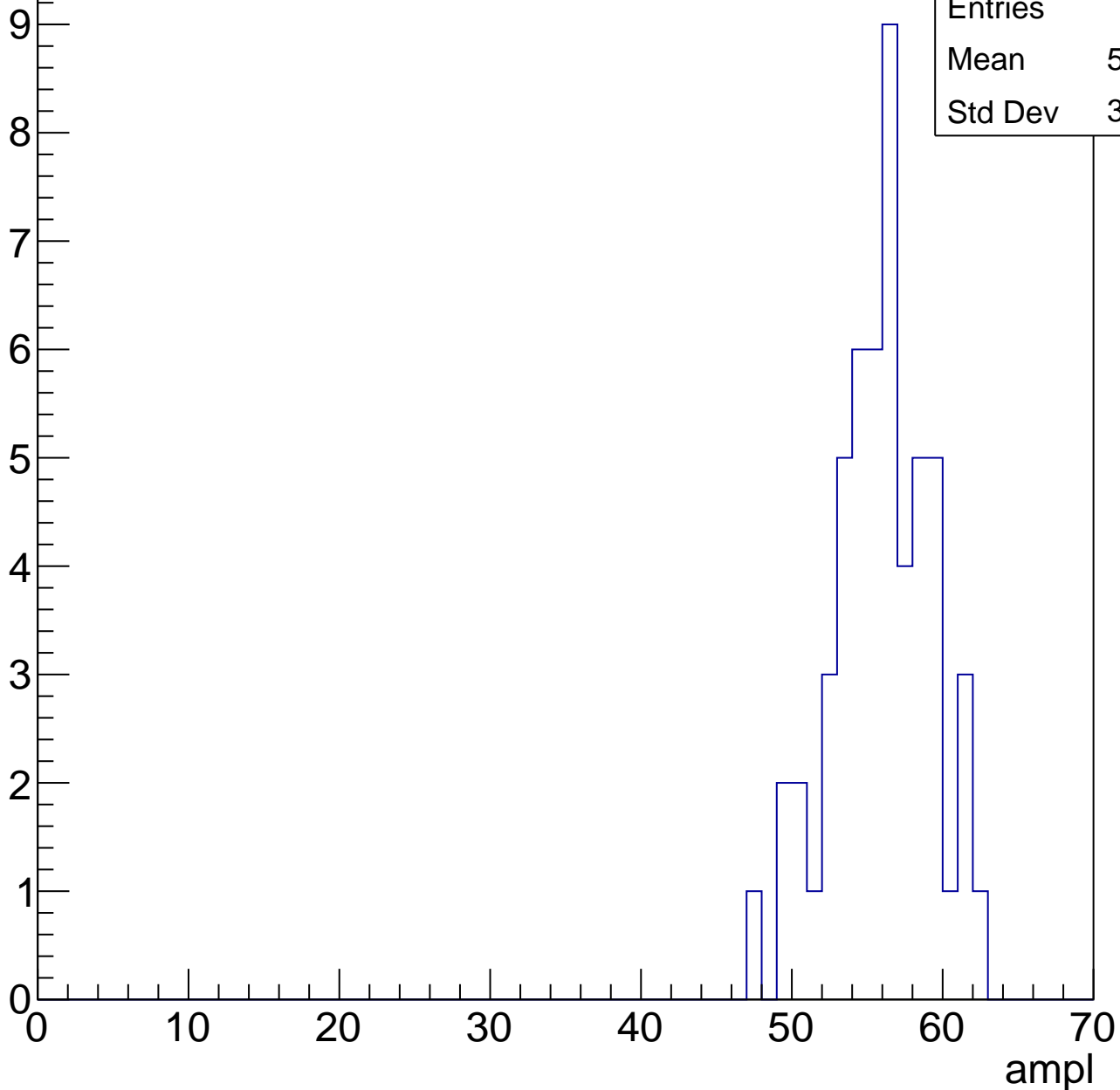
Entry



# B1L102S, U24-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

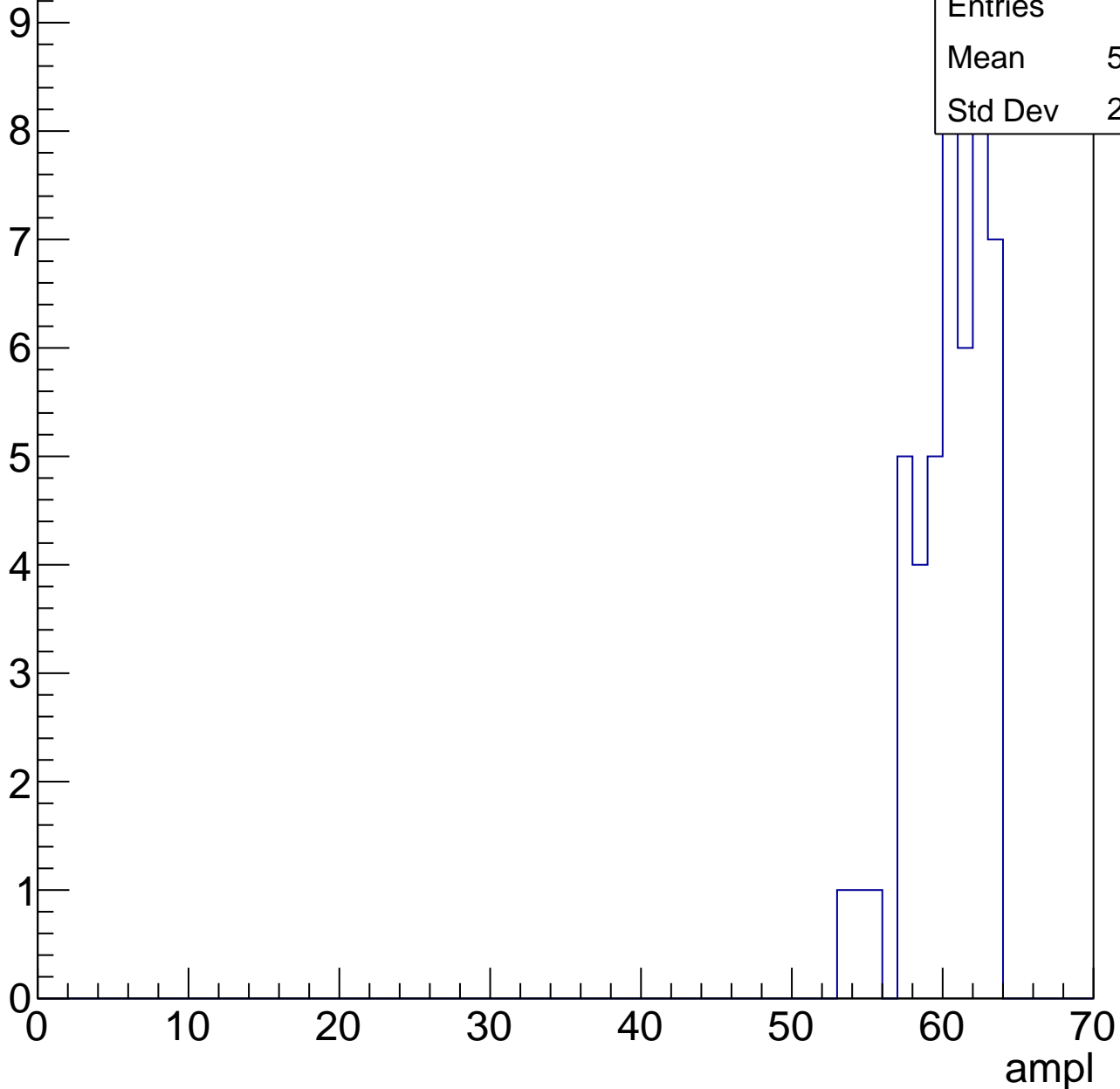


Entries	54
Mean	55.43
Std Dev	3.269

# B1L102S, U24-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

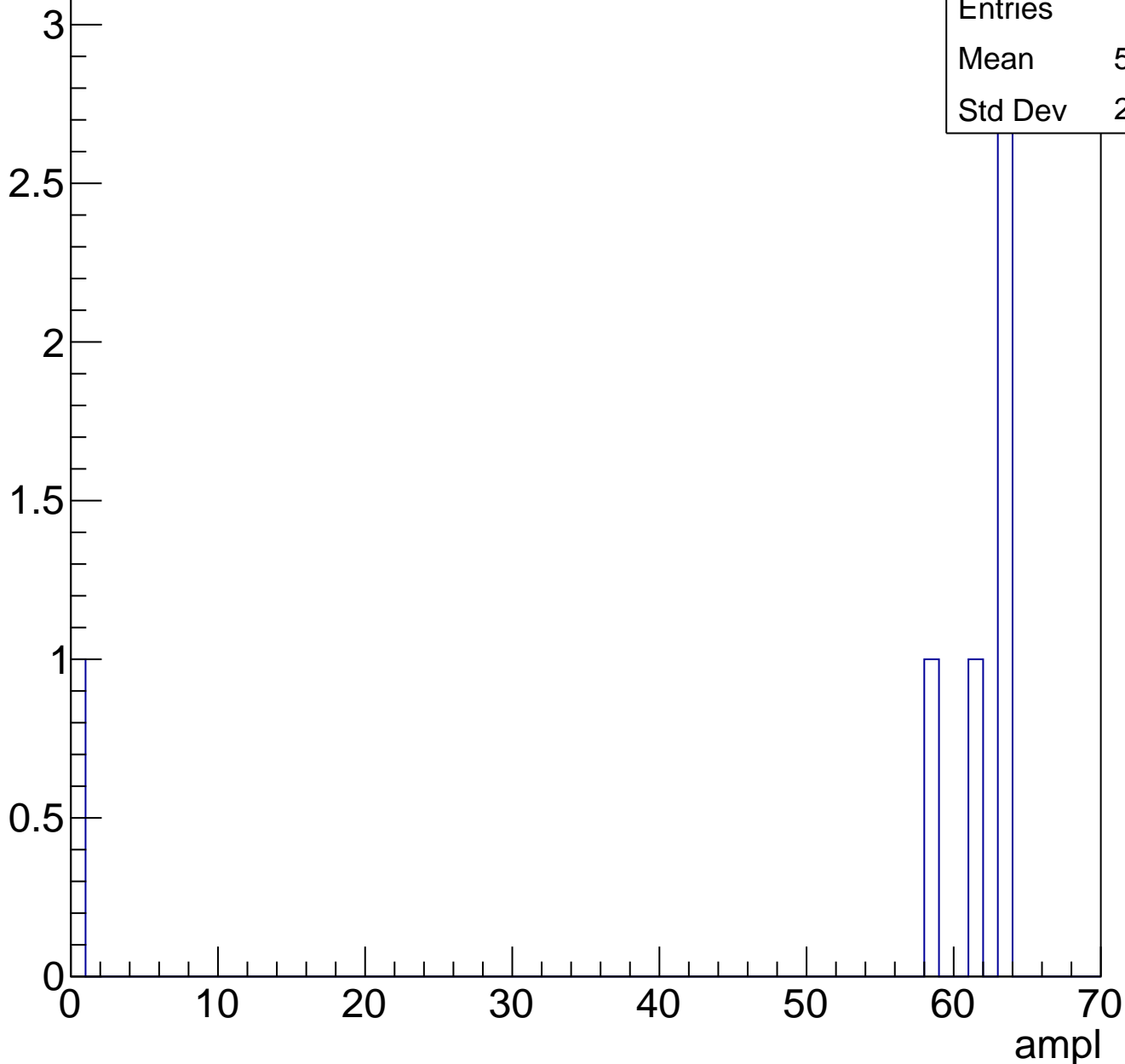
Entry



# B1L102S, U24-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch48, adc0

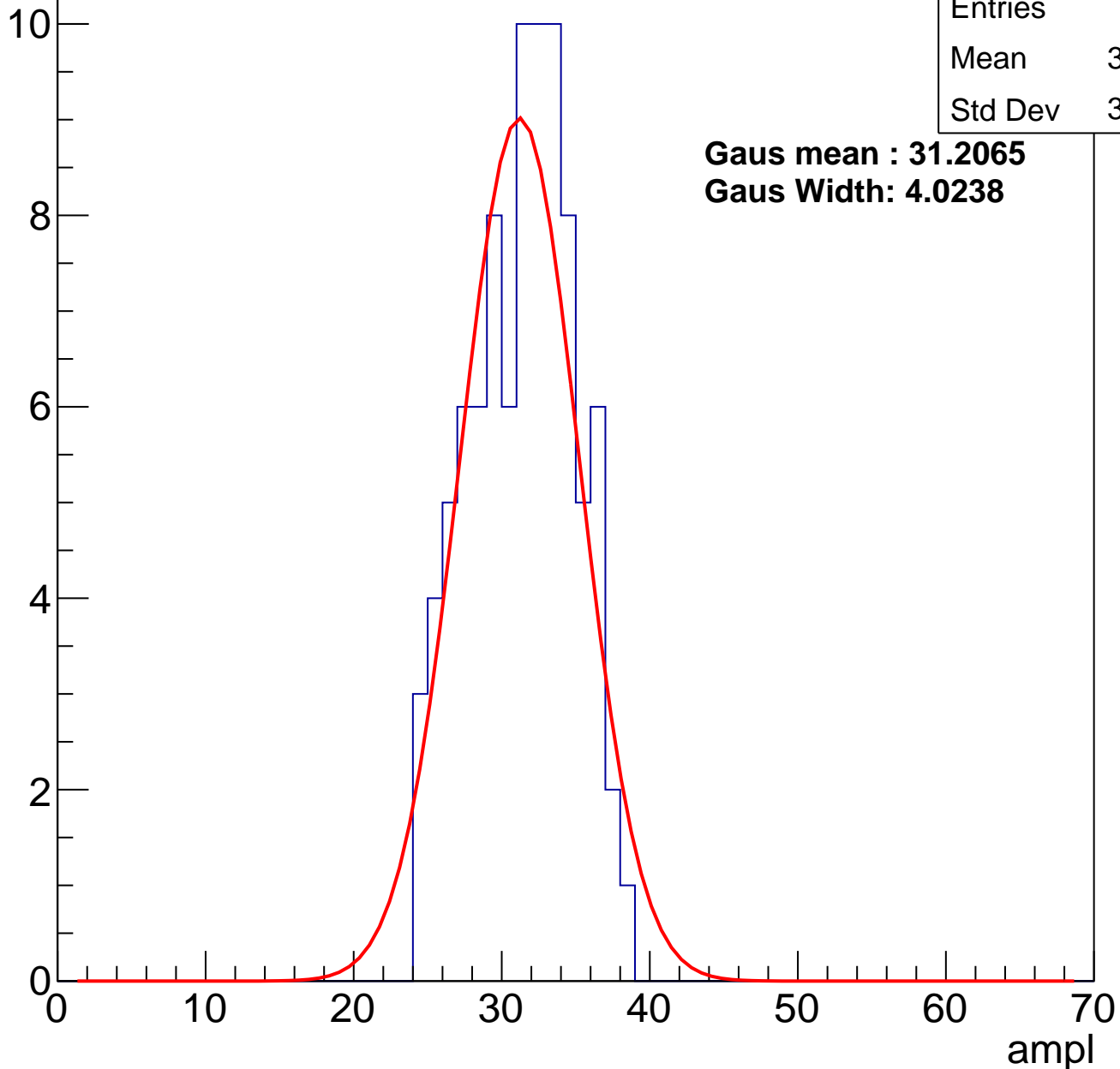
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	90
Mean	30.88
Std Dev	3.457

**Gaus mean : 31.2065**

**Gaus Width: 4.0238**

Entry



# B1L102S, U24-ch48, adc1

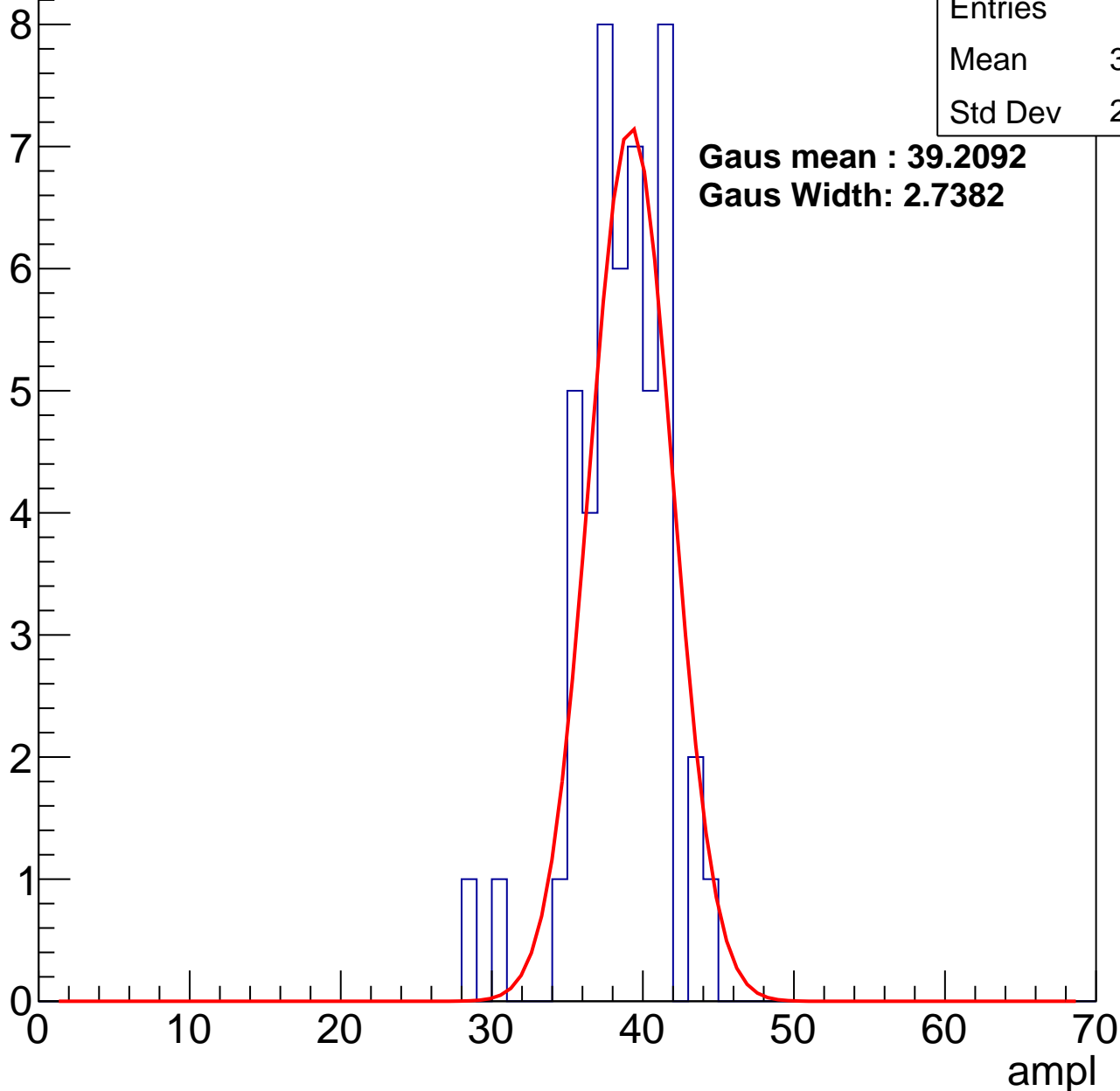
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	38.08
Std Dev	2.975

**Gaus mean : 39.2092**

**Gaus Width: 2.7382**



# B1L102S, U24-ch48, adc2

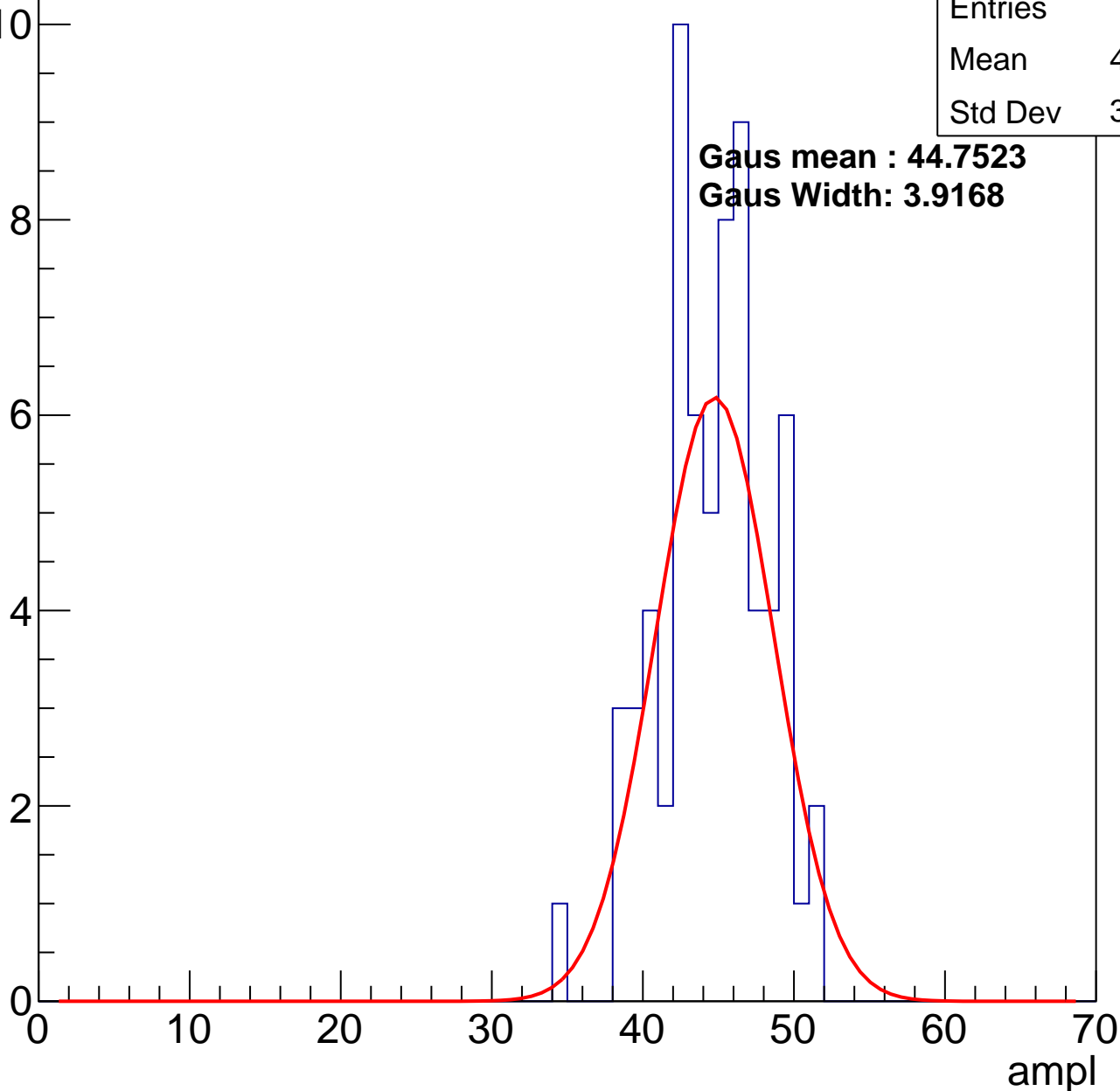
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.19
Std Dev	3.507

**Gaus mean : 44.7523**

**Gaus Width: 3.9168**

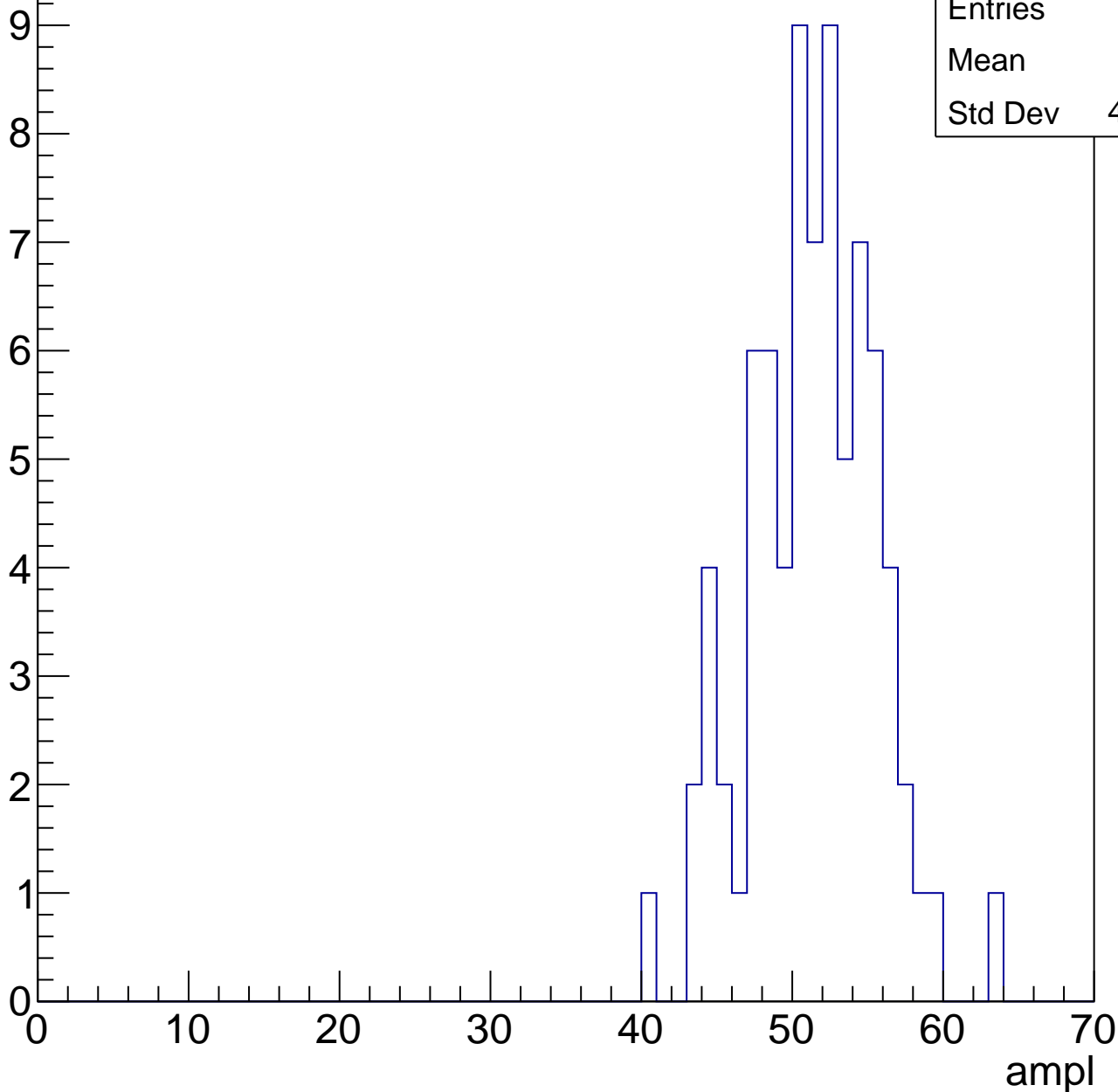


# B1L102S, U24-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	50.9
Std Dev	4.134

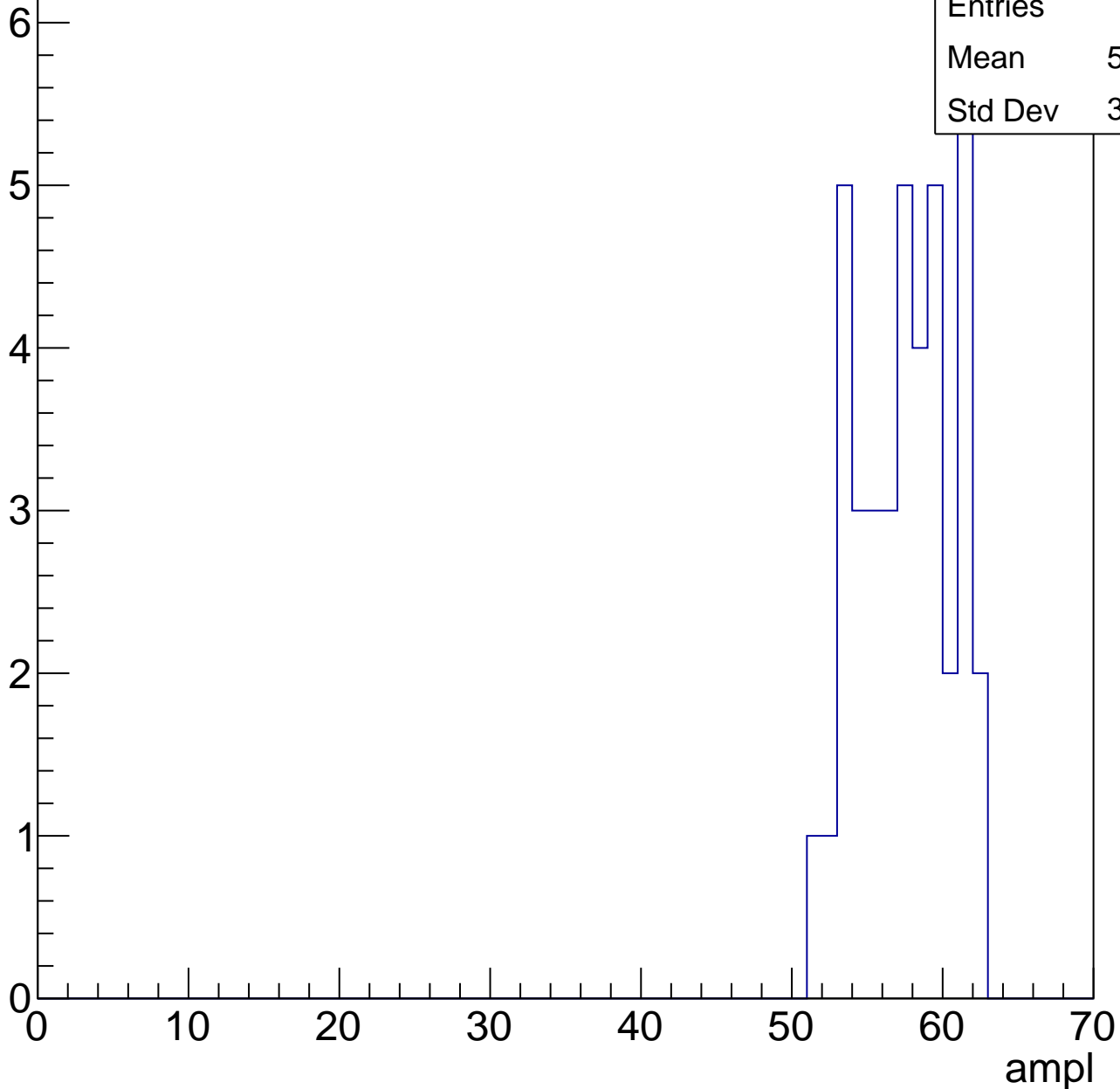


# B1L102S, U24-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

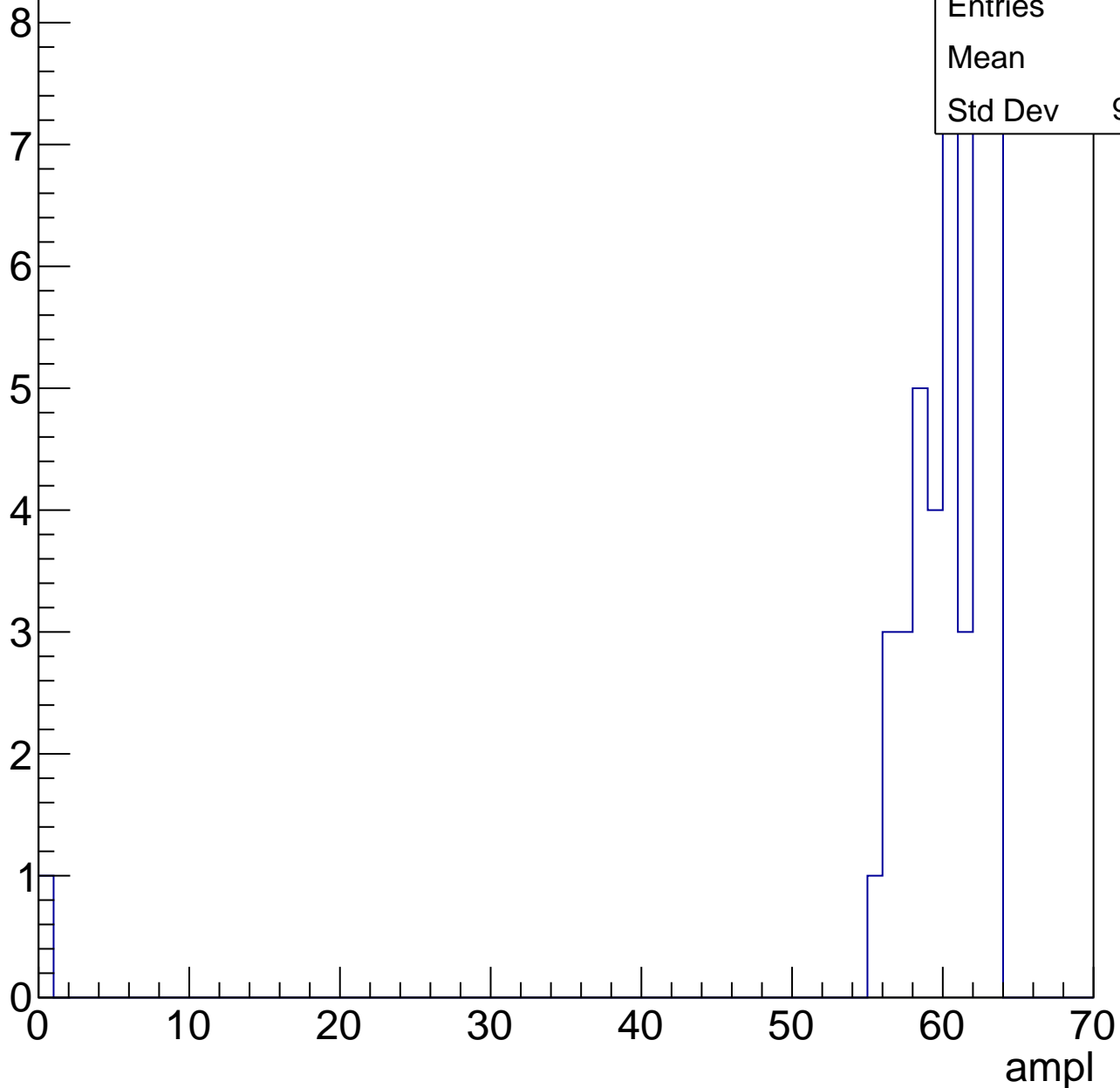
Entries	40
Mean	57.12
Std Dev	3.043



# B1L102S, U24-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B1L102S, U24-ch49, adc0

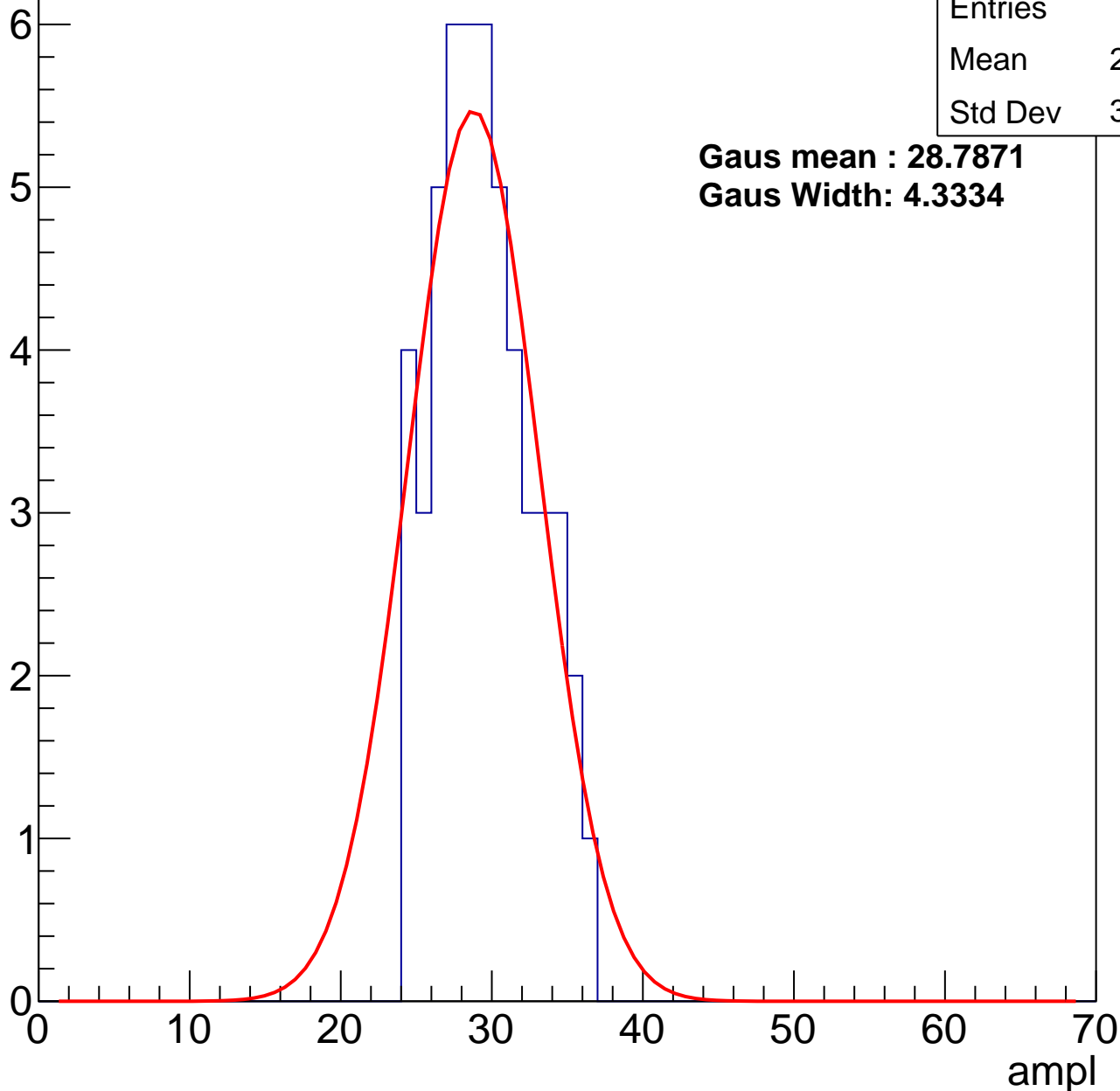
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	29.06
Std Dev	3.177

**Gaus mean : 28.7871**

**Gaus Width: 4.3334**



# B1L102S, U24-ch49, adc1

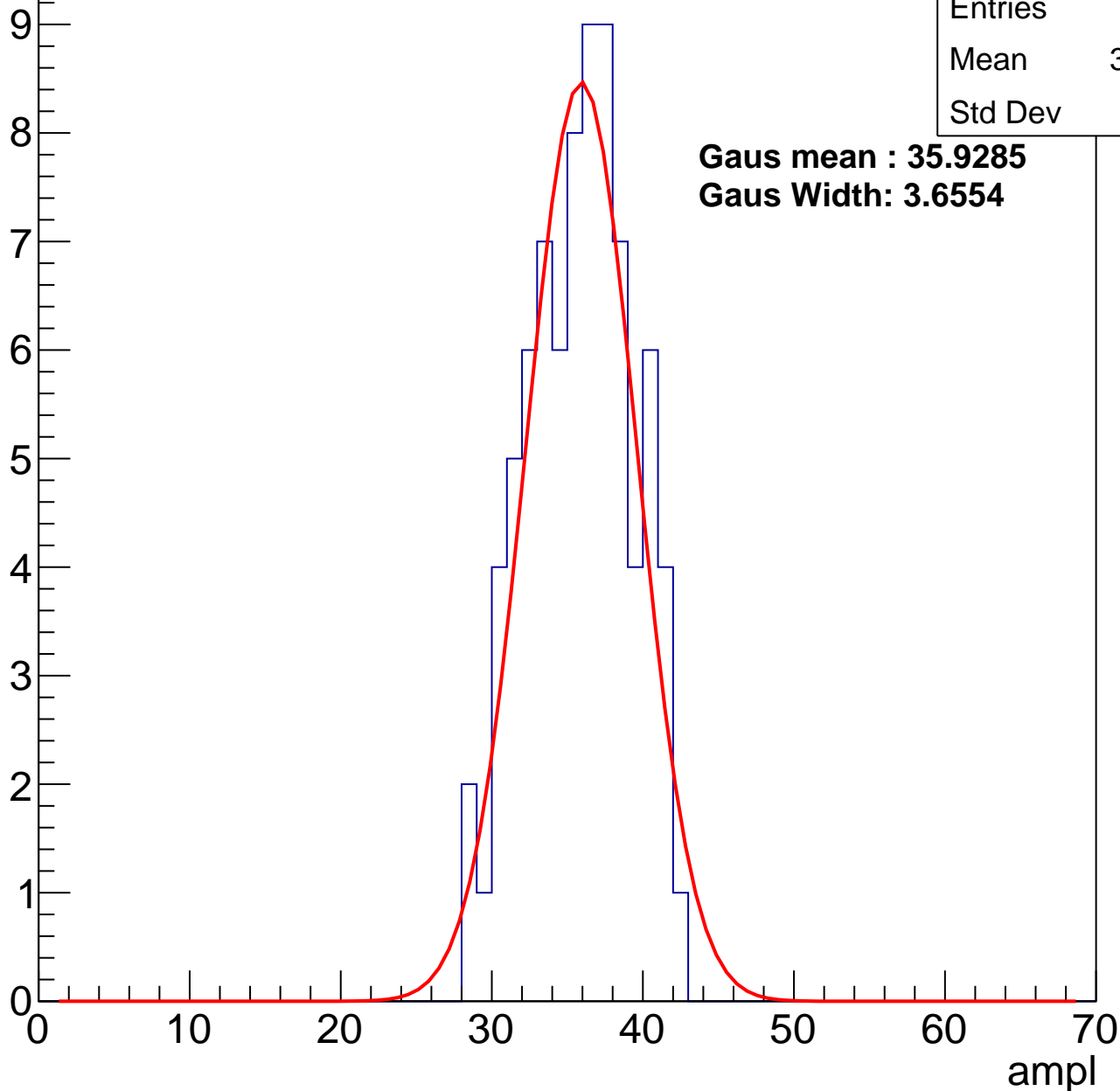
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.34
Std Dev	3.39

**Gaus mean : 35.9285**

**Gaus Width: 3.6554**



# B1L102S, U24-ch49, adc2

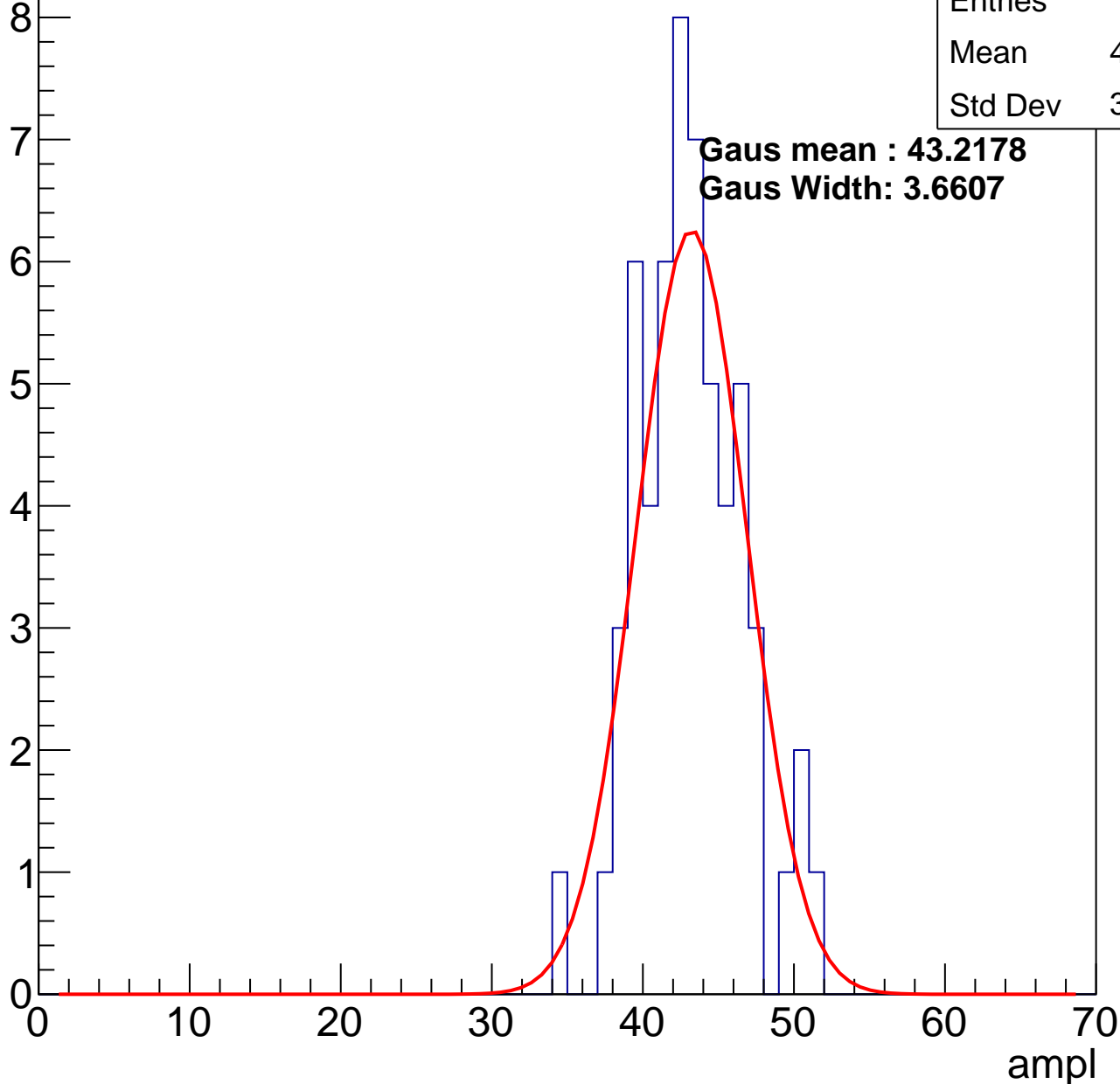
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.68
Std Dev	3.409

**Gaus mean : 43.2178**

**Gaus Width: 3.6607**

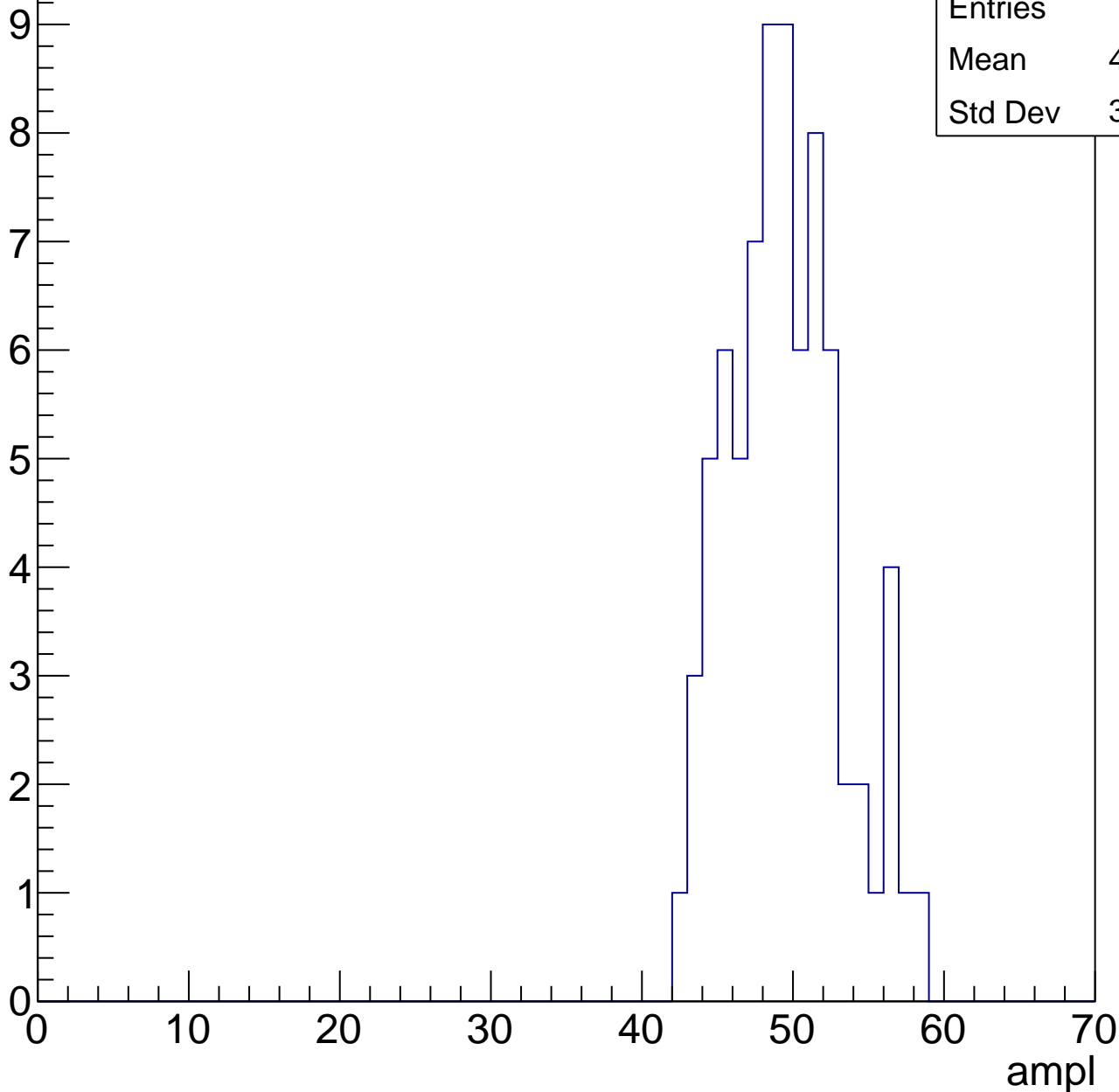


# B1L102S, U24-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

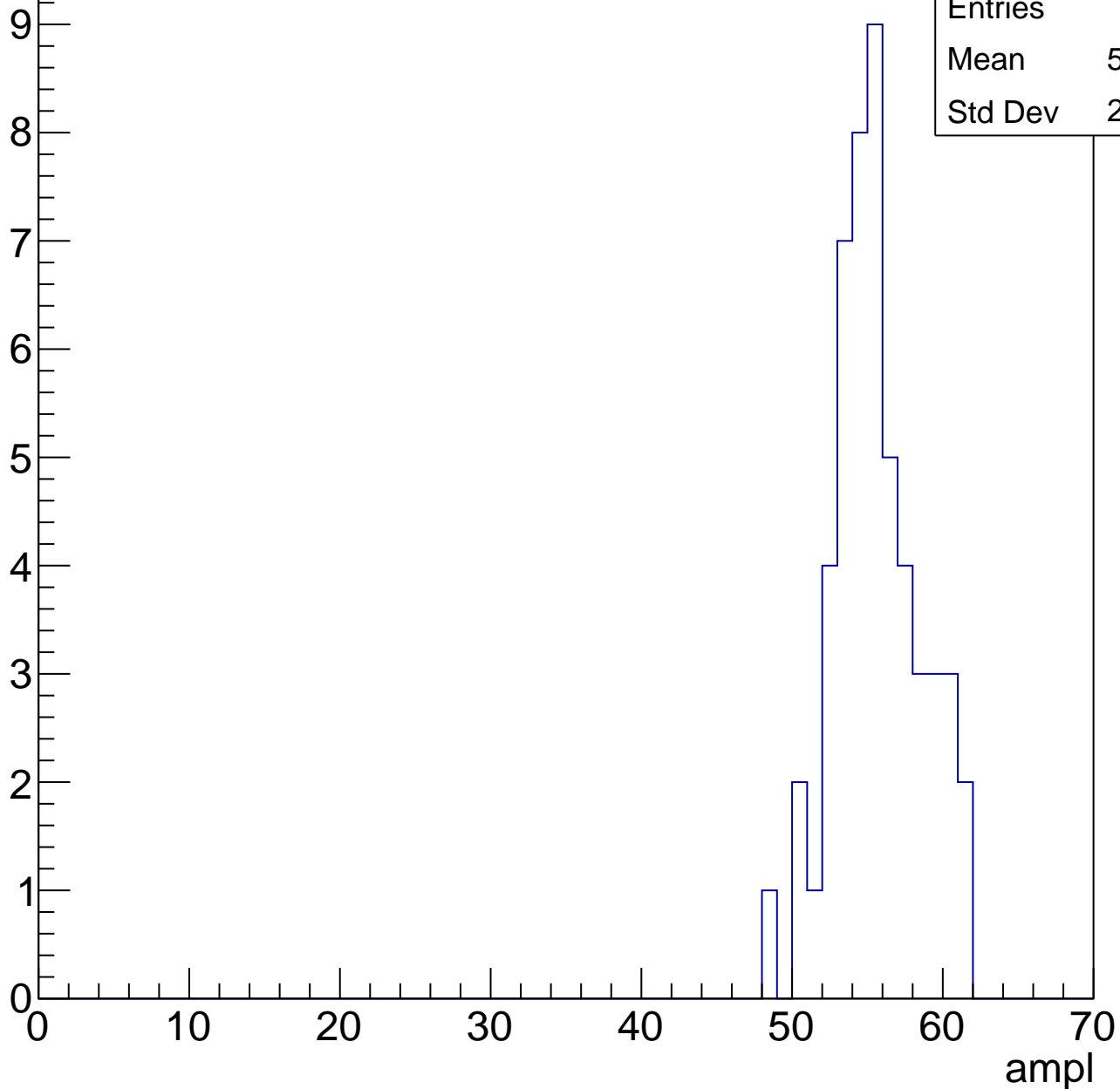
Entries	76
Mean	48.96
Std Dev	3.647



# B1L102S, U24-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



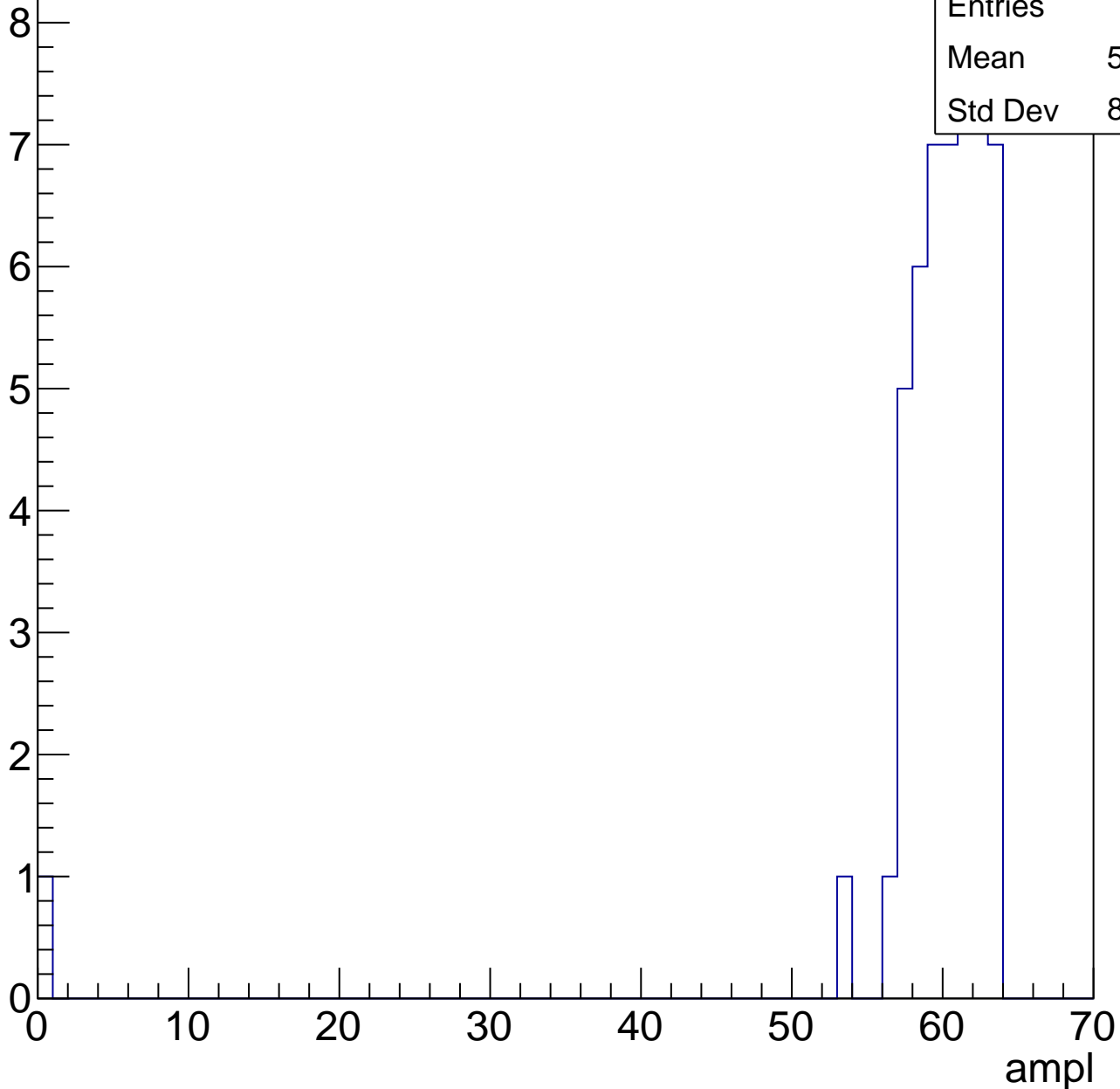
Entries	52
Mean	55.12
Std Dev	2.873

# B1L102S, U24-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.82
Std Dev	8.602



# B1L102S, U24-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.6

Std Dev

0.4899



# B1L102S, U24-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U24-ch50, adc0

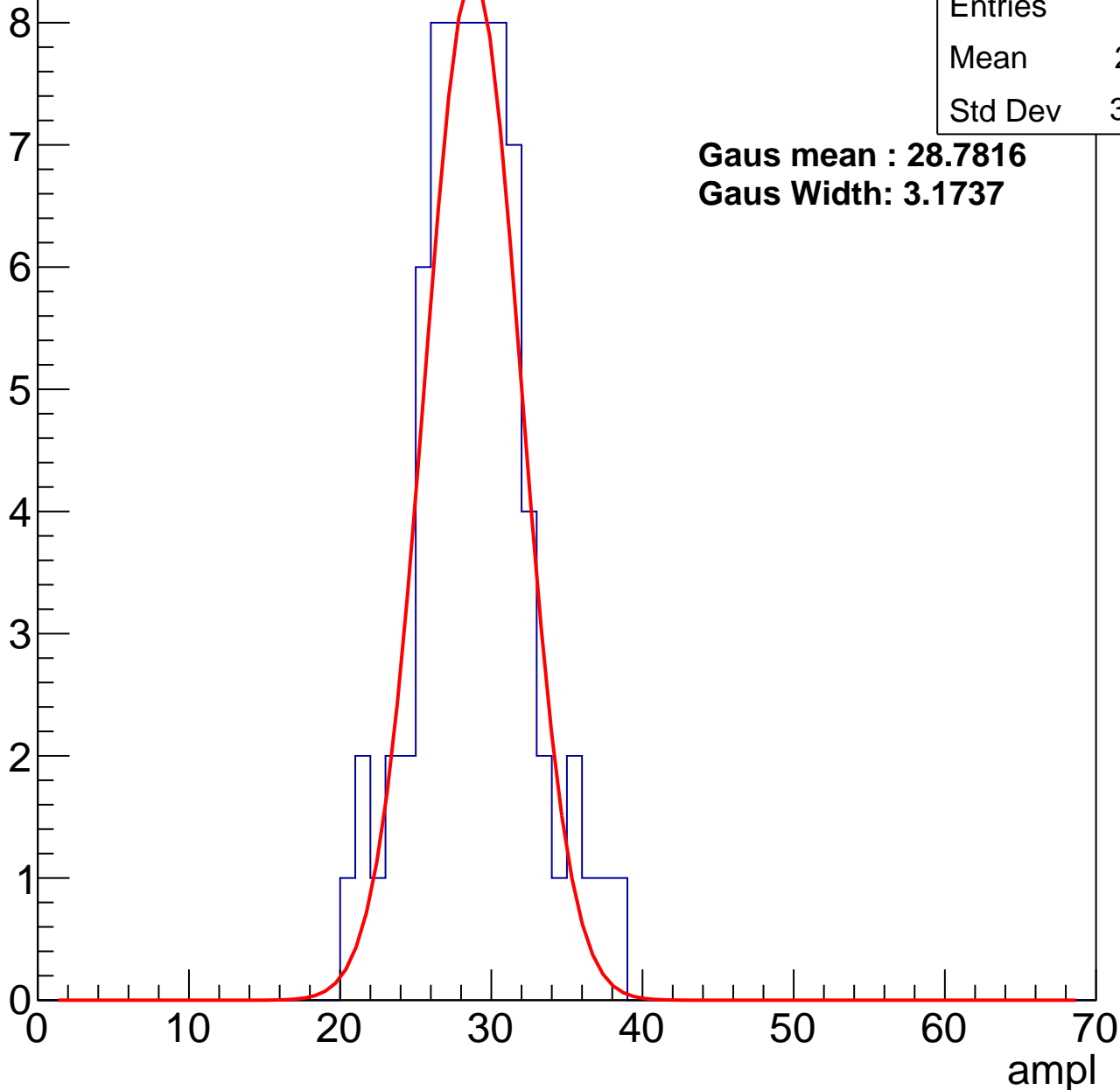
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.41
Std Dev	3.622

**Gaus mean : 28.7816**

**Gaus Width: 3.1737**



# B1L102S, U24-ch50, adc1

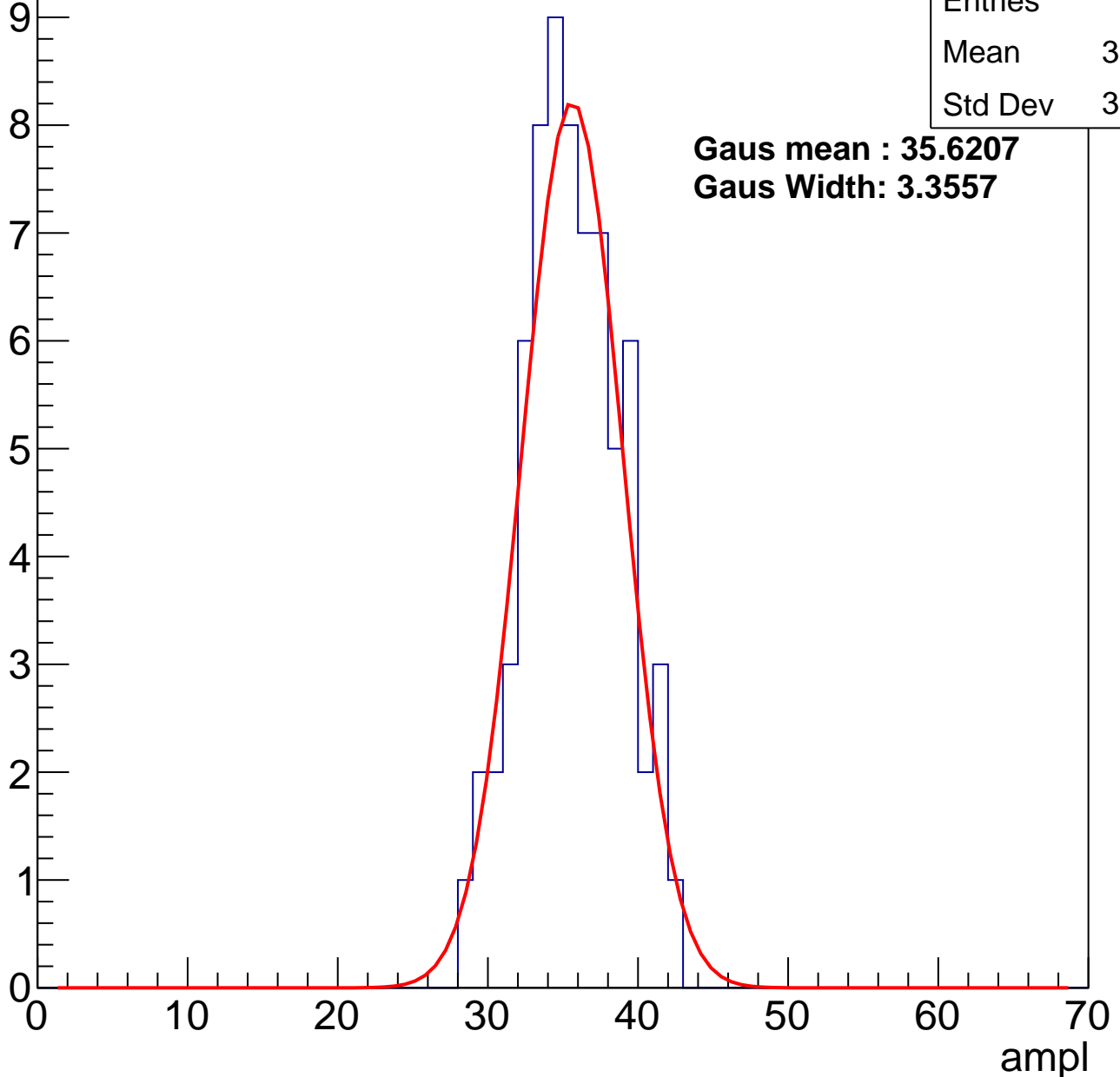
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.16
Std Dev	3.152

**Gaus mean : 35.6207**

**Gaus Width: 3.3557**



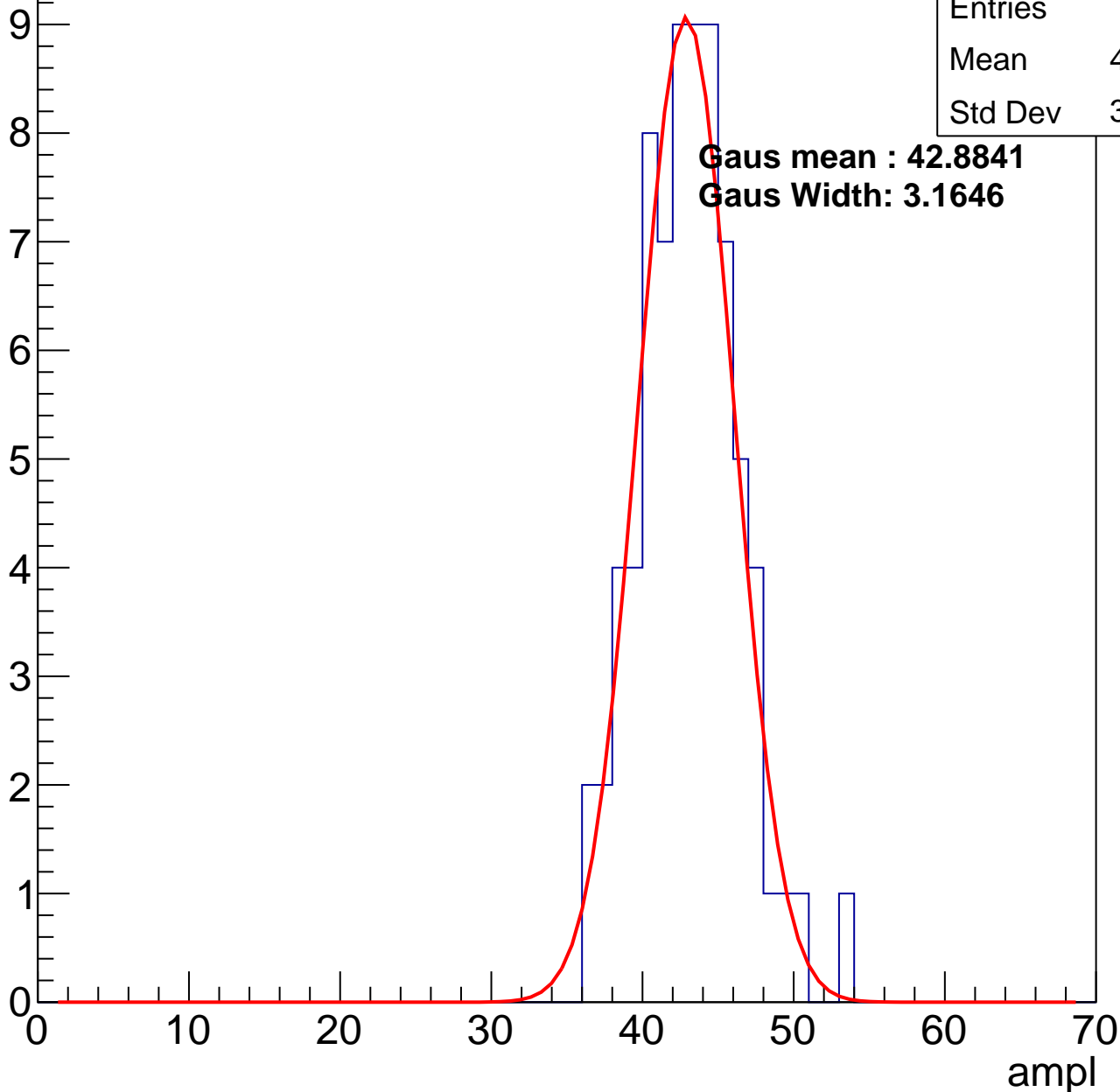
# B1L102S, U24-ch50, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.64
Std Dev	3.266

**Gaus mean : 42.8841**  
**Gaus Width: 3.1646**

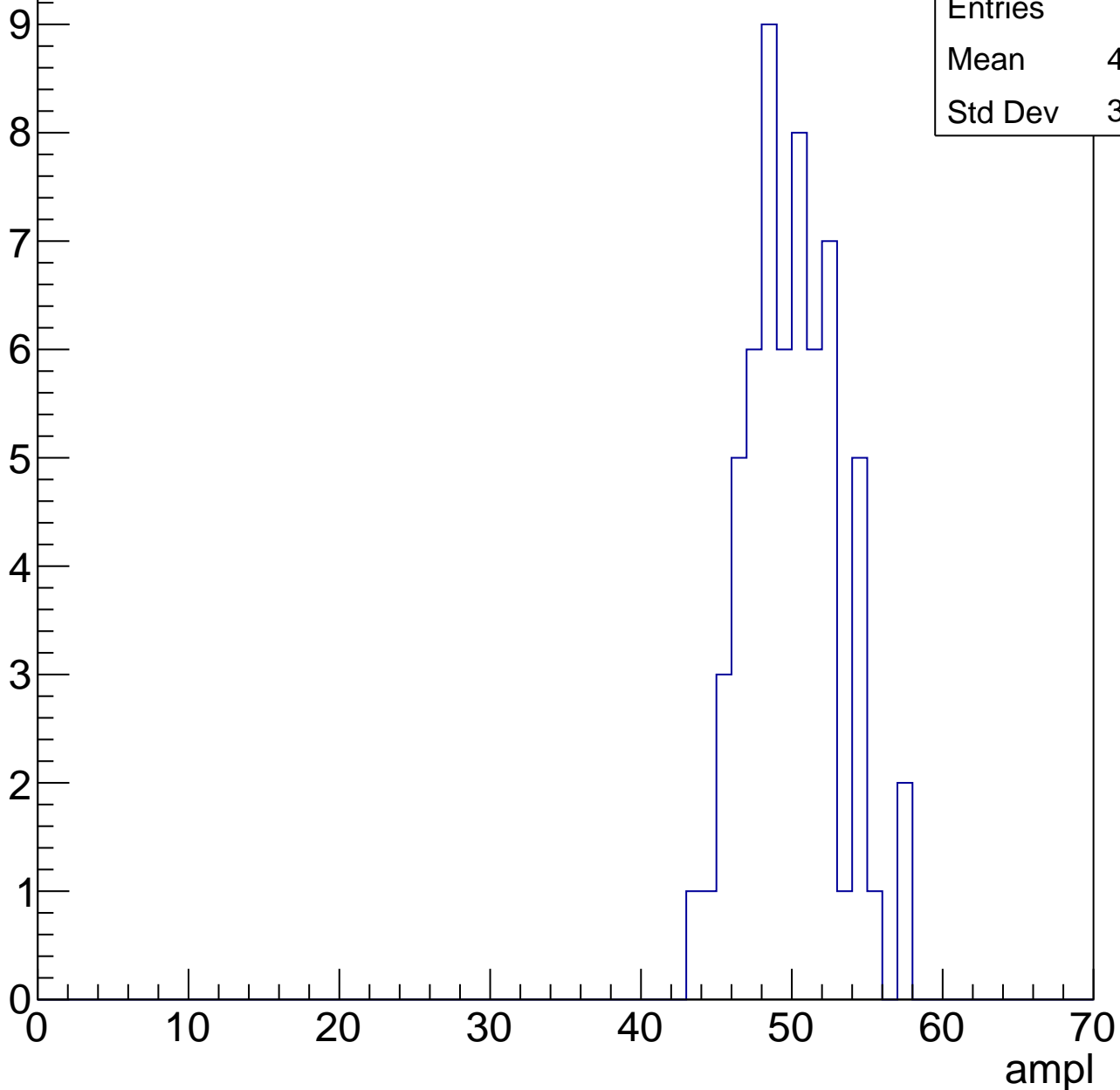


# B1L102S, U24-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

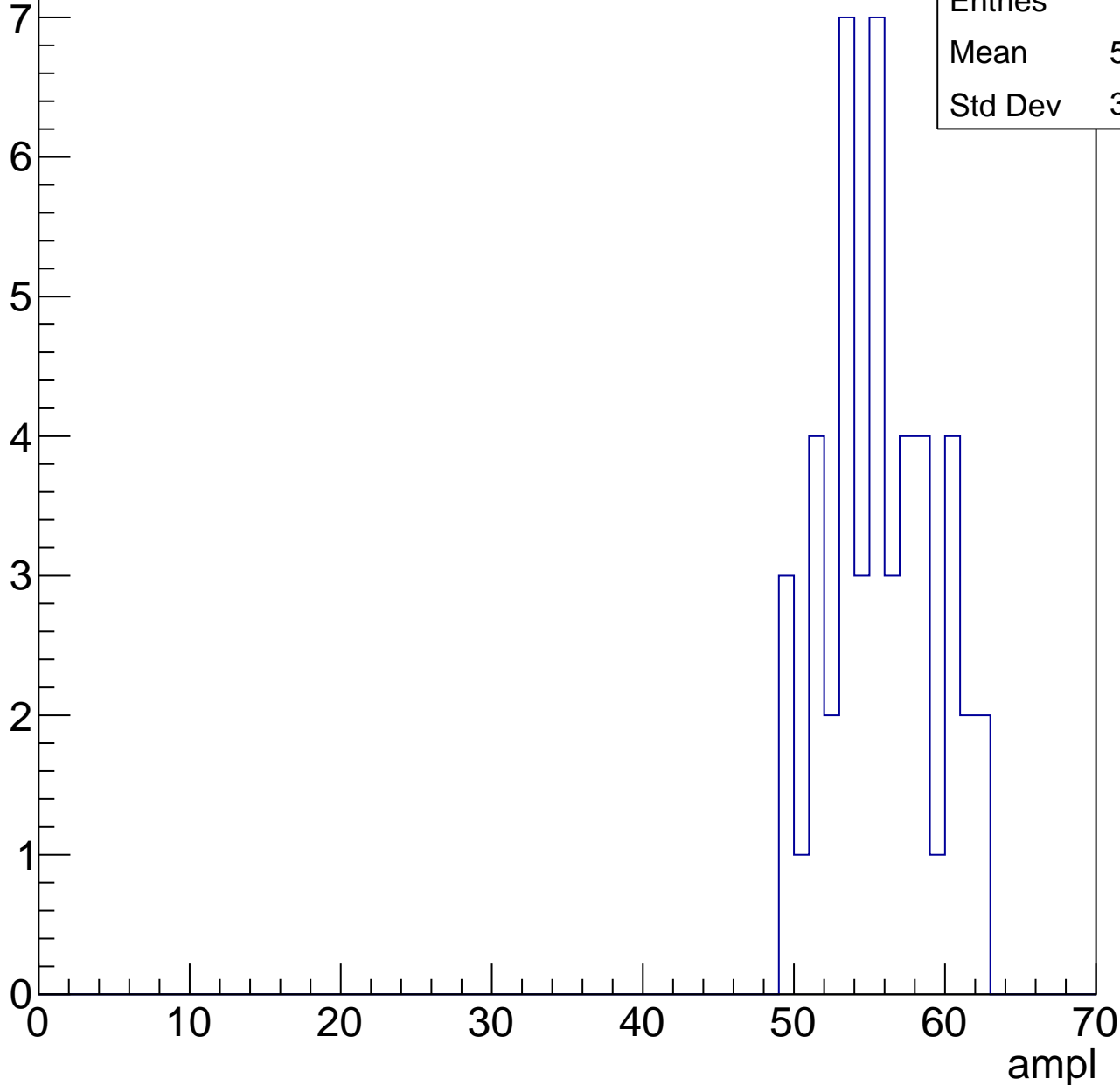
Entries	61
Mean	49.54
Std Dev	3.055



# B1L102S, U24-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

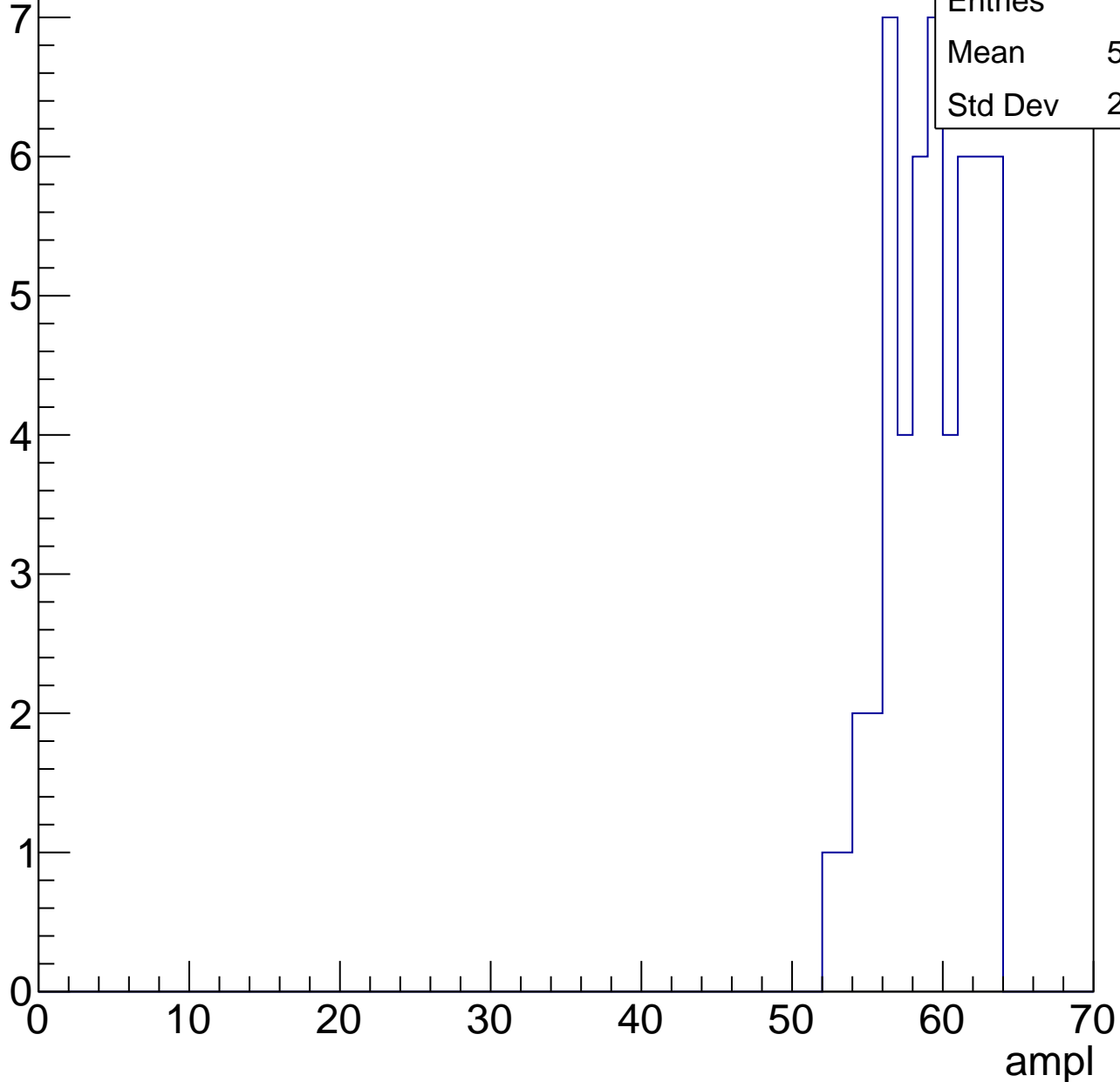
Entry



# B1L102S, U24-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

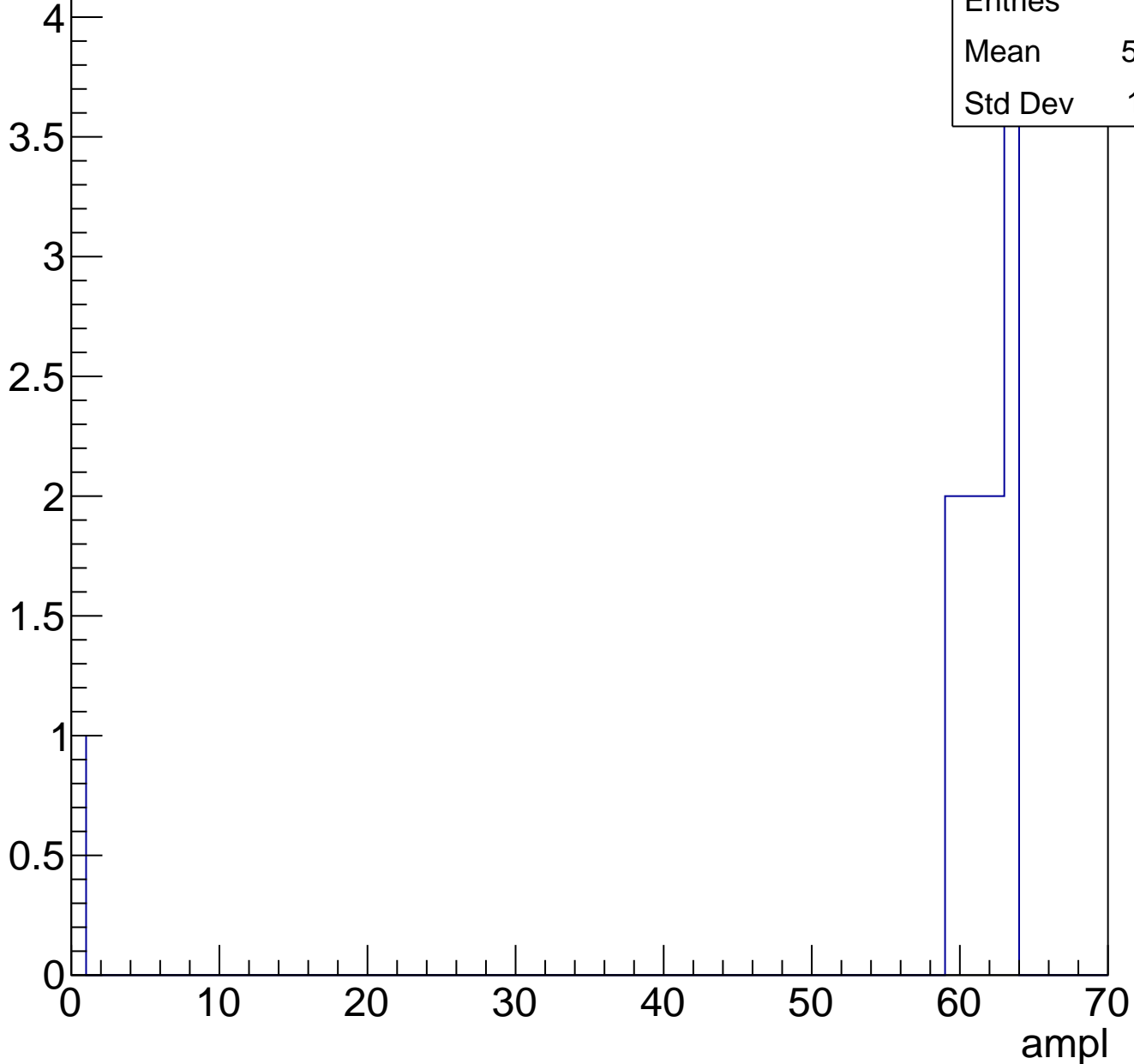
Entry



# B1L102S, U24-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch51, adc0

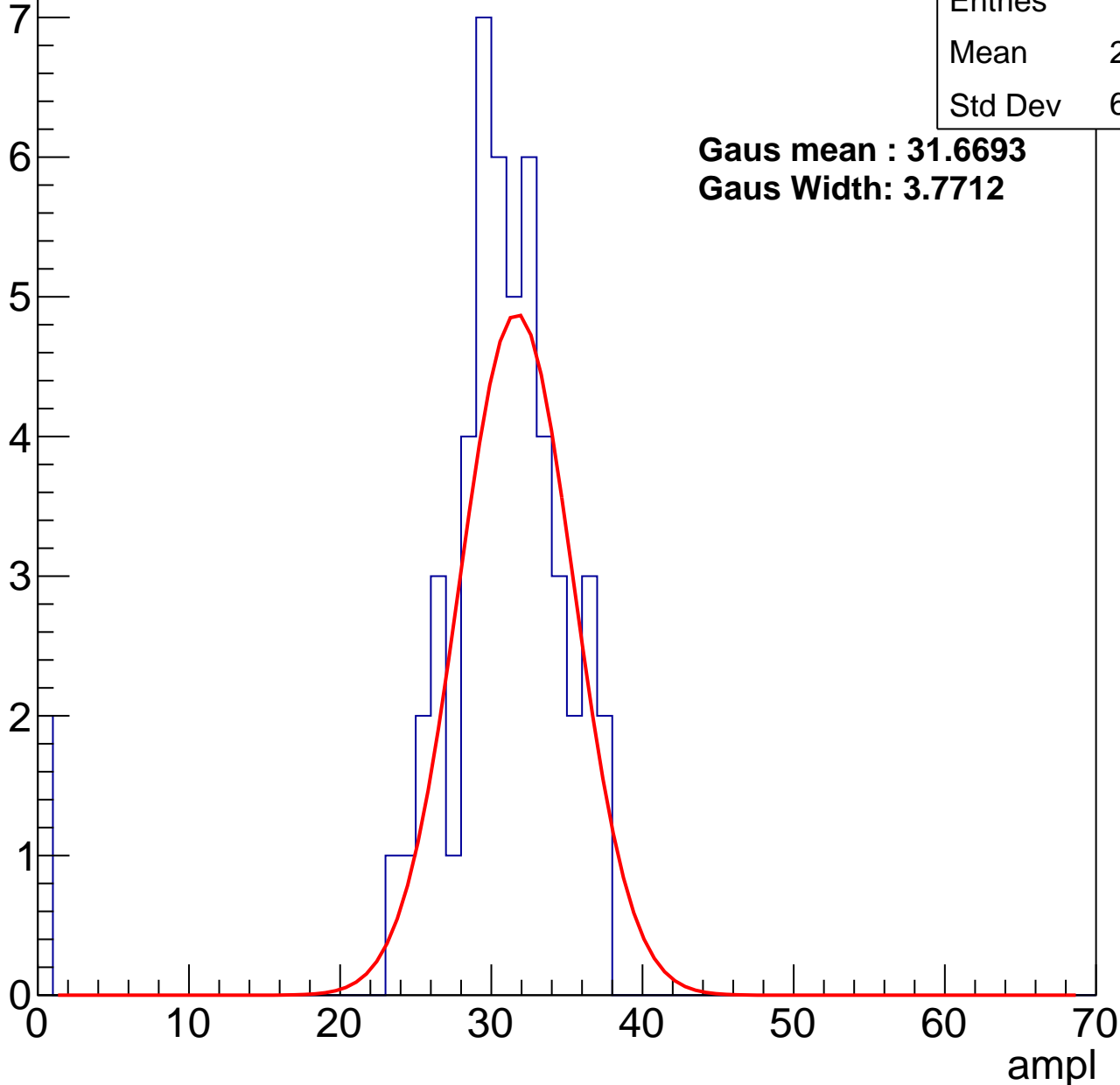
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	29.42
Std Dev	6.746

**Gaus mean : 31.6693**

**Gaus Width: 3.7712**



# B1L102S, U24-ch51, adc1

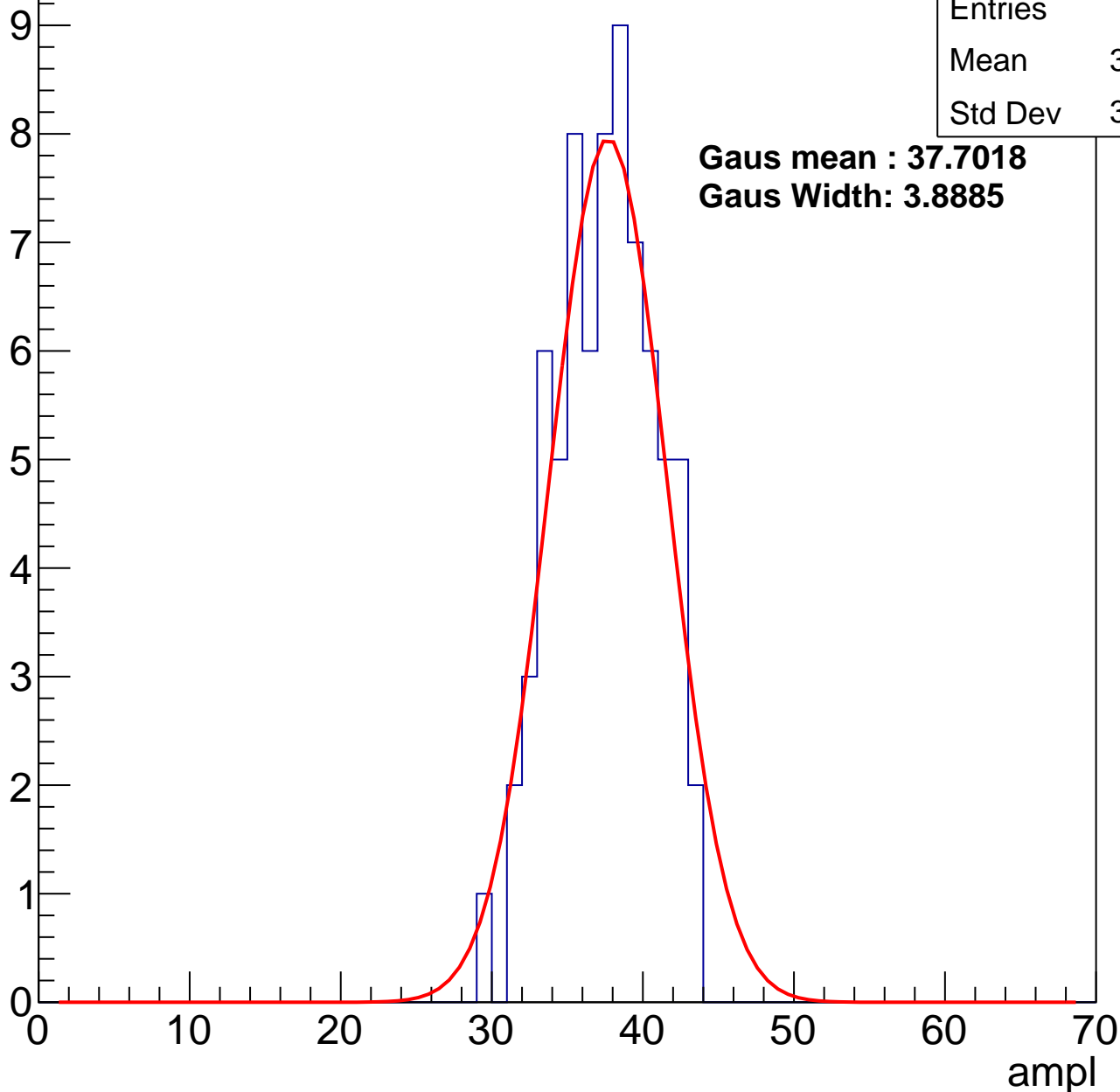
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.03
Std Dev	3.222

**Gaus mean : 37.7018**

**Gaus Width: 3.8885**



# B1L102S, U24-ch51, adc2

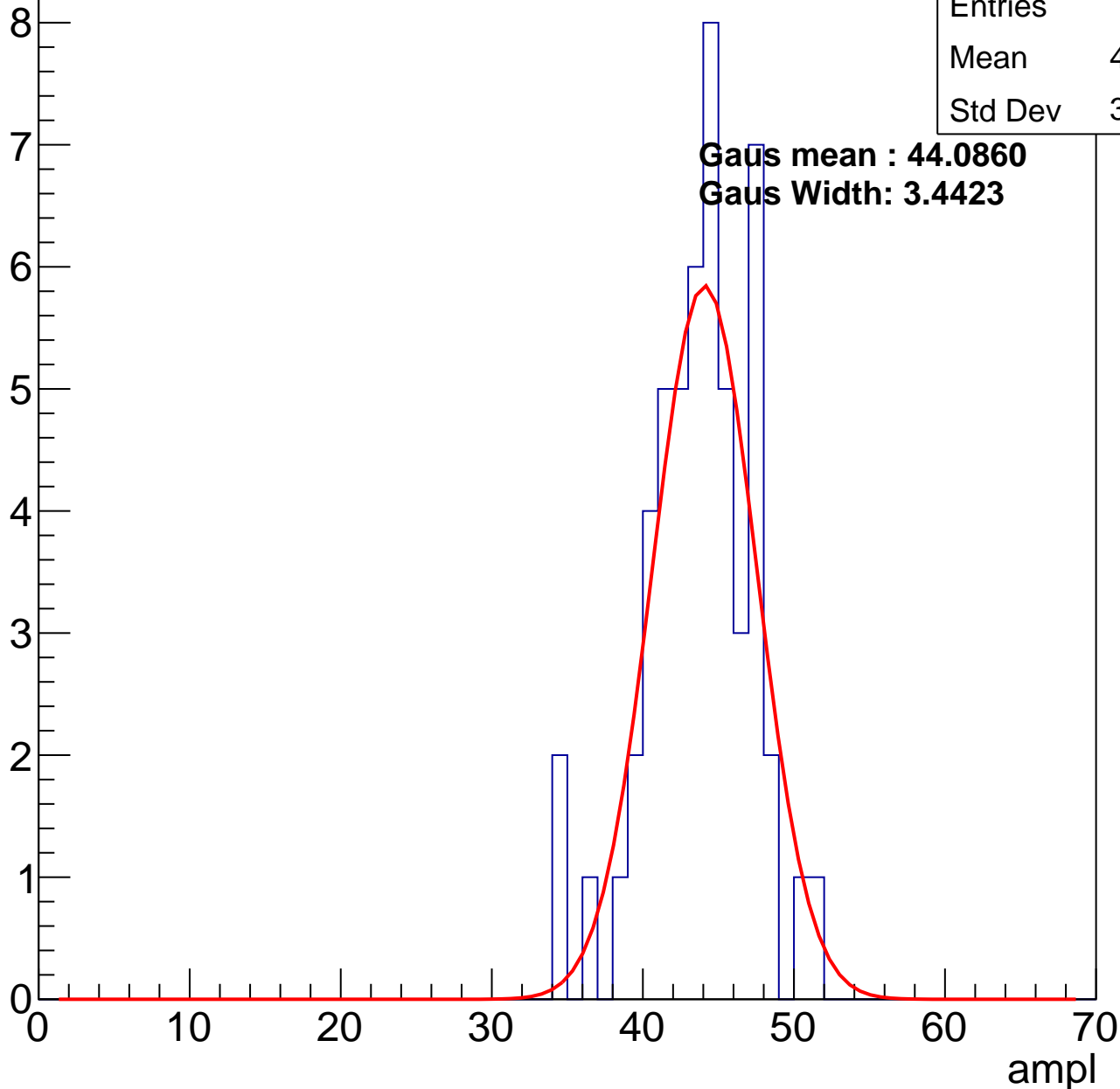
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.28
Std Dev	3.515

**Gaus mean : 44.0860**

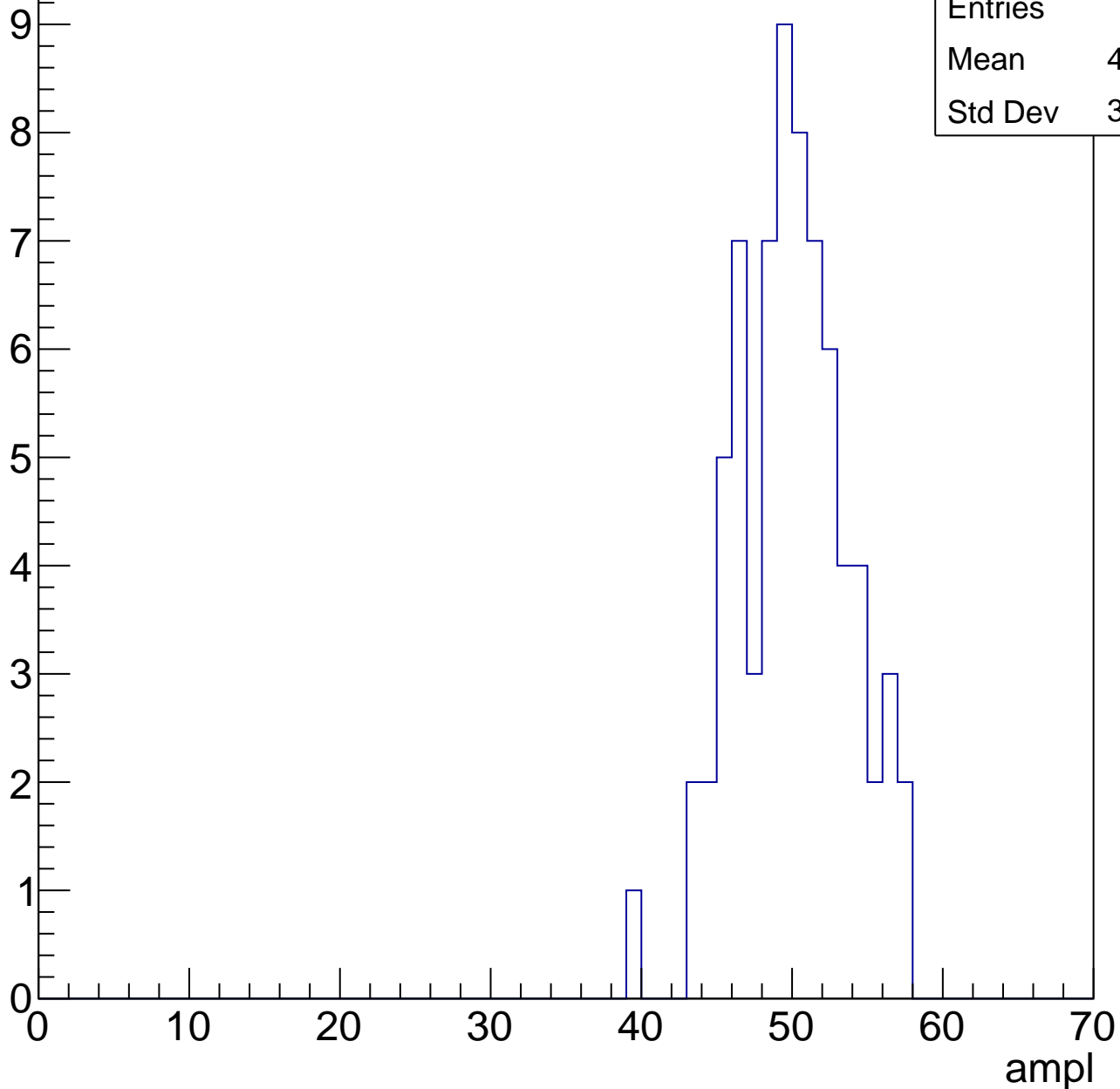
**Gaus Width: 3.4423**



# B1L102S, U24-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

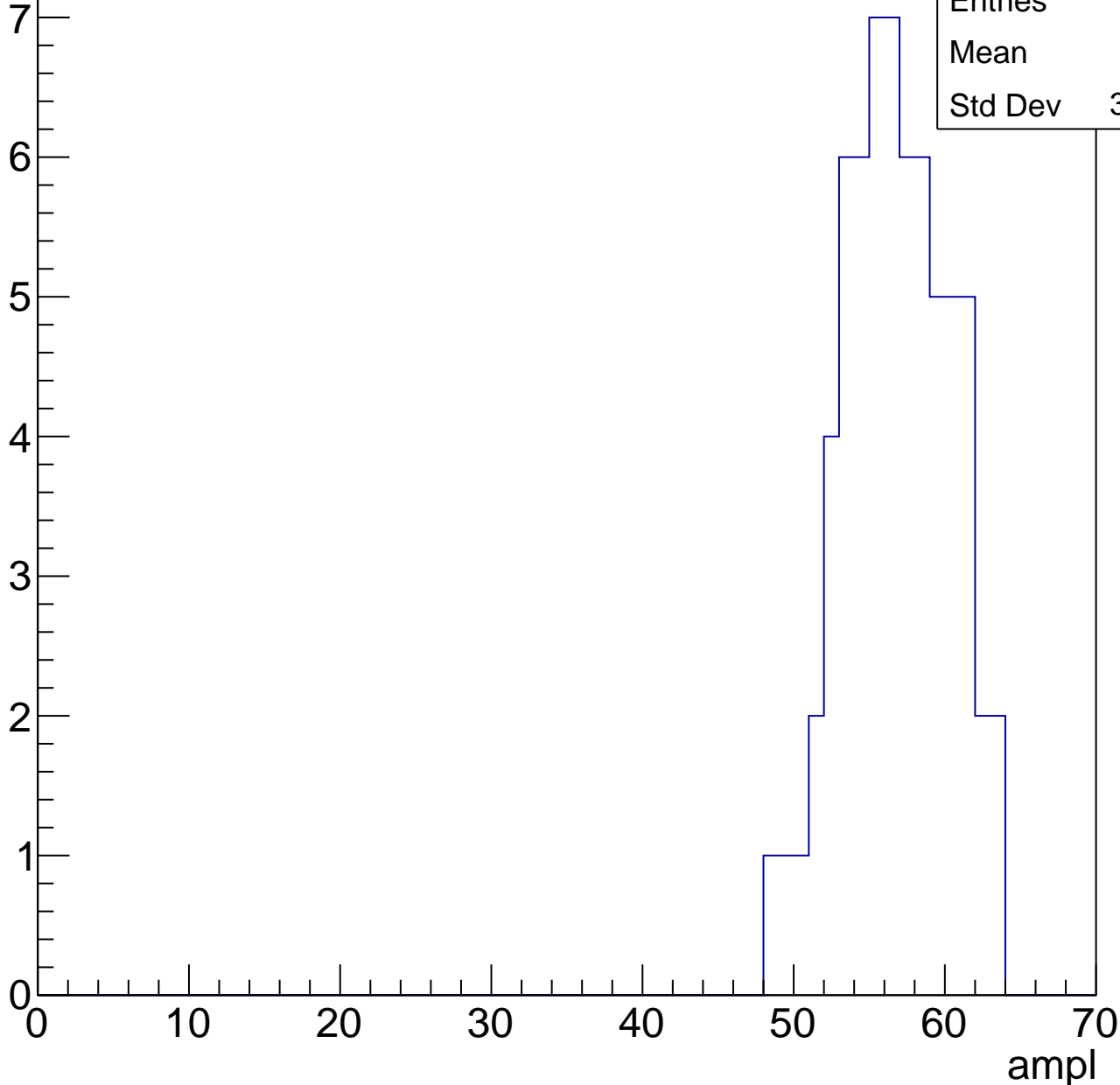


# B1L102S, U24-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	56.3
Std Dev	3.464

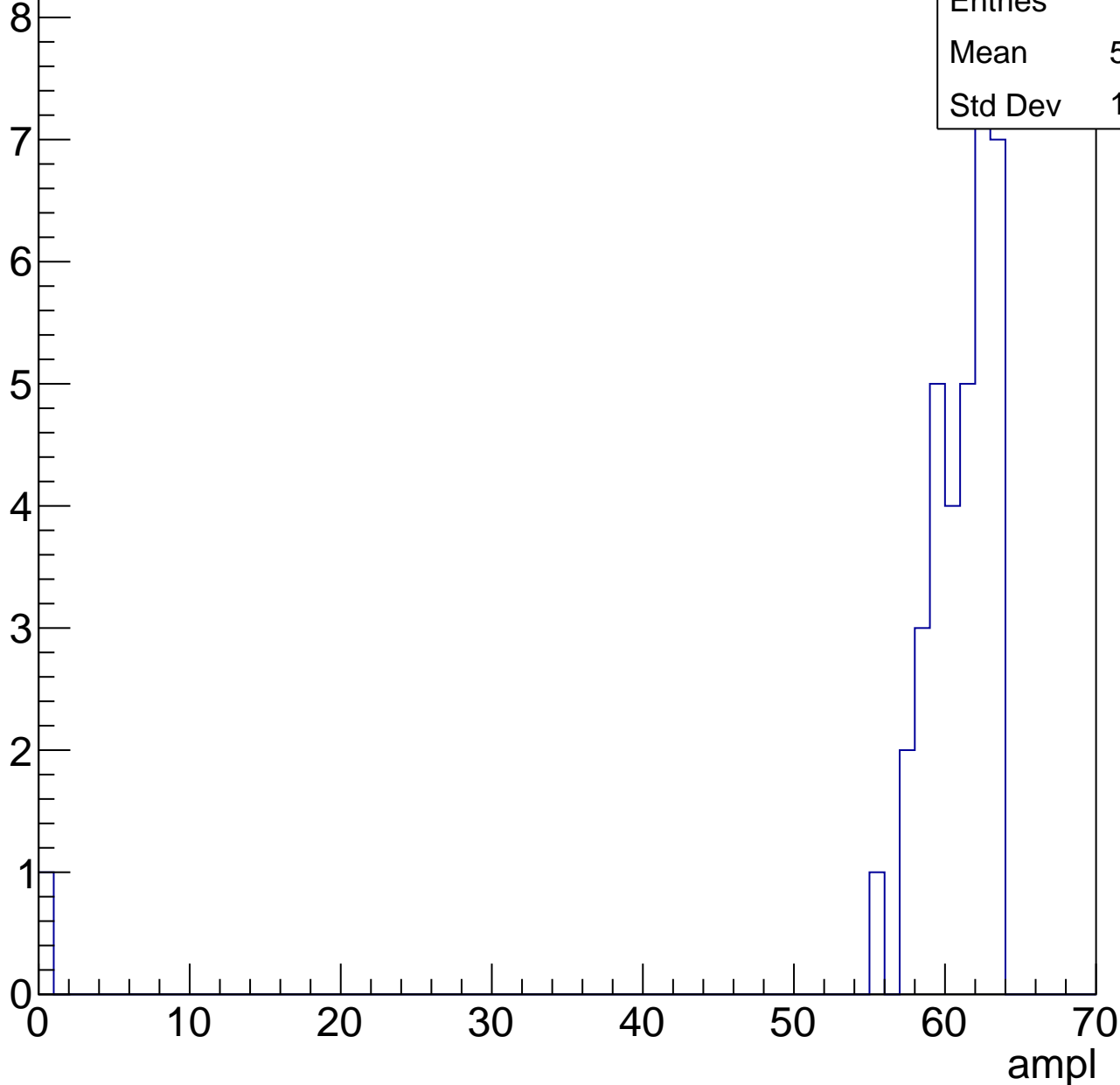


# B1L102S, U24-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

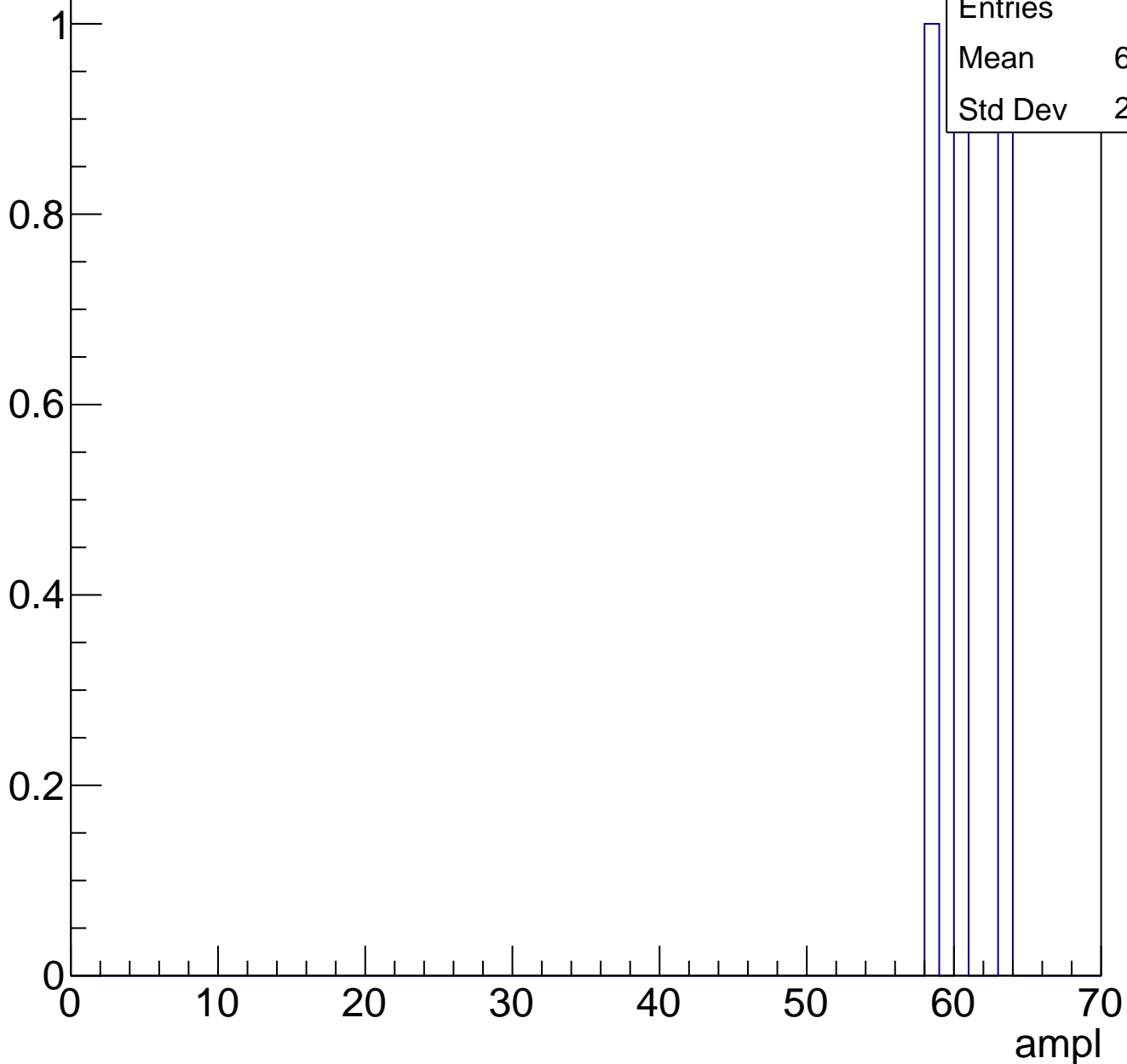
Entries	36
Mean	58.89
Std Dev	10.16



# B1L102S, U24-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch52, adc0

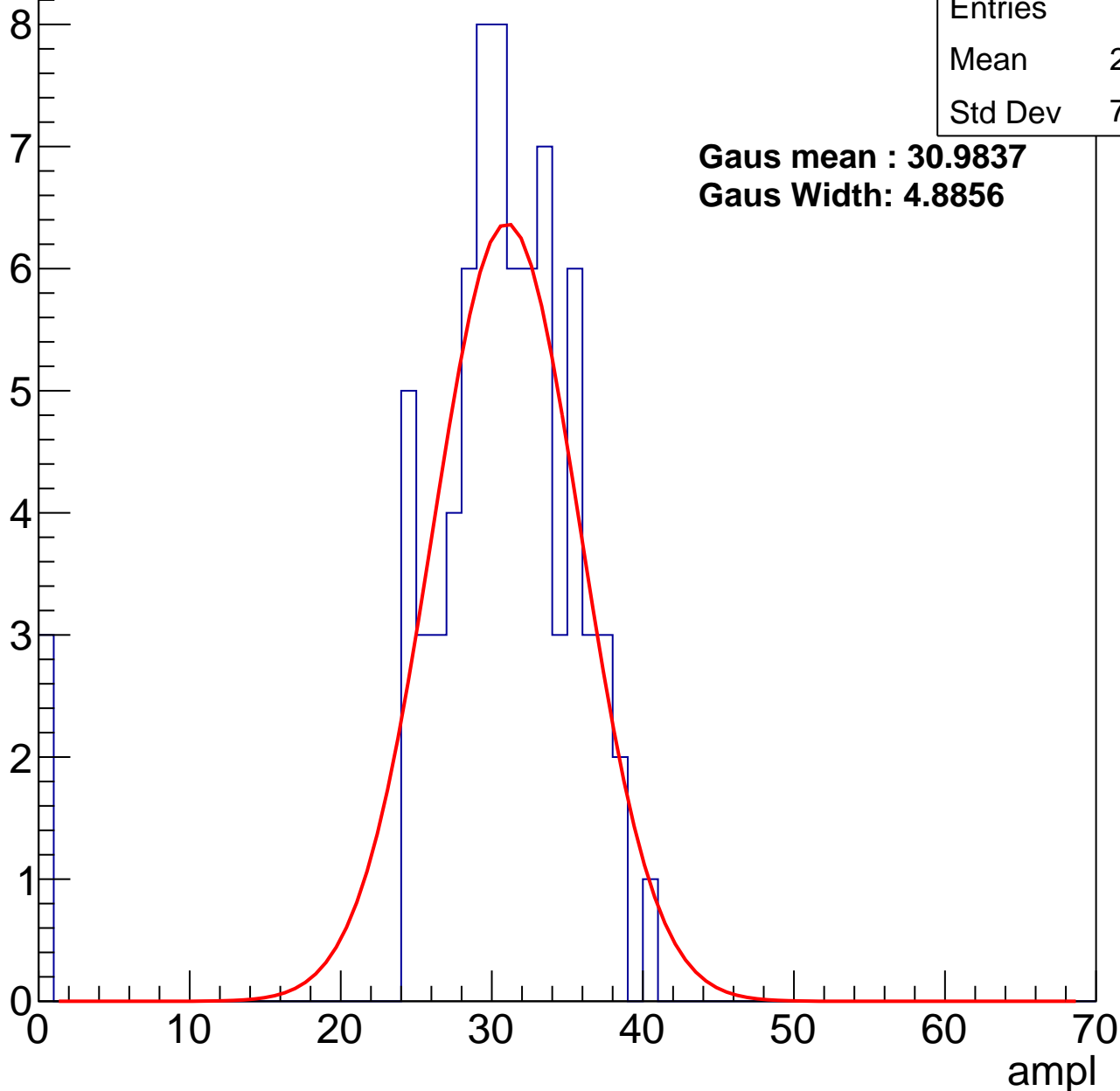
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.57
Std Dev	7.053

**Gaus mean : 30.9837**

**Gaus Width: 4.8856**



# B1L102S, U24-ch52, adc1

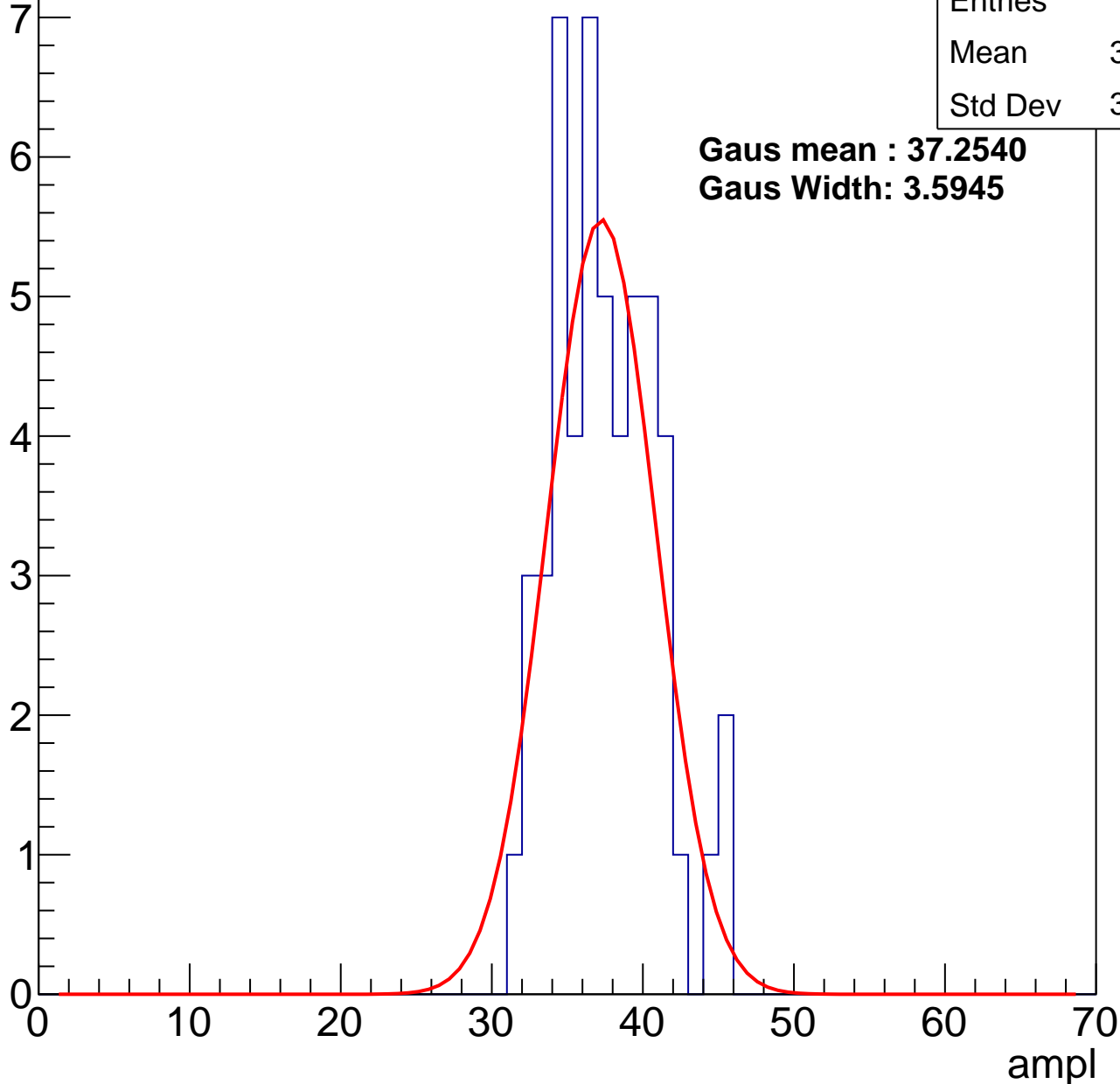
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	37.08
Std Dev	3.339

**Gaus mean : 37.2540**

**Gaus Width: 3.5945**



# B1L102S, U24-ch52, adc2

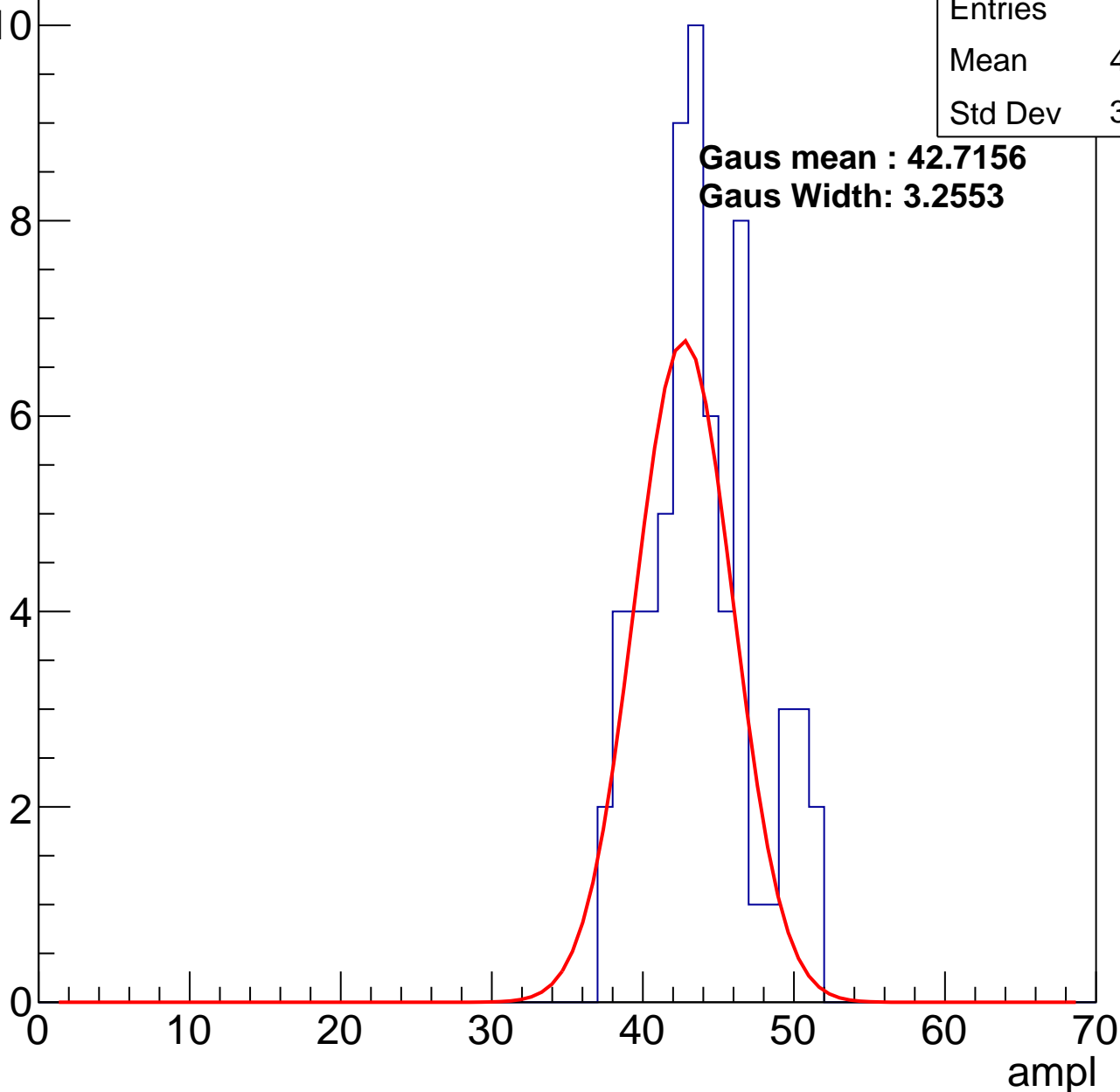
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.35
Std Dev	3.505

**Gaus mean : 42.7156**

**Gaus Width: 3.2553**

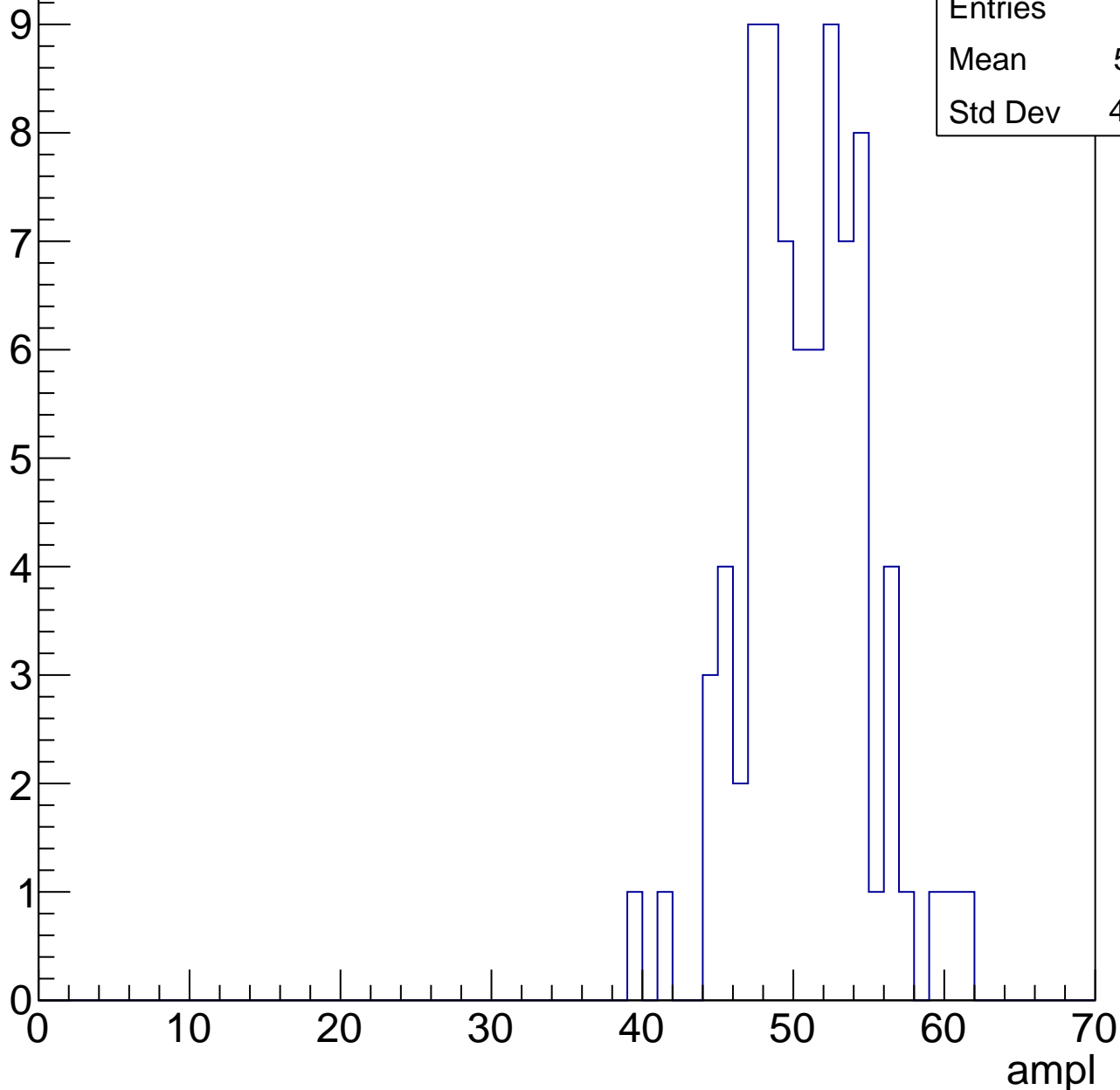


# B1L102S, U24-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	50.31
Std Dev	4.024

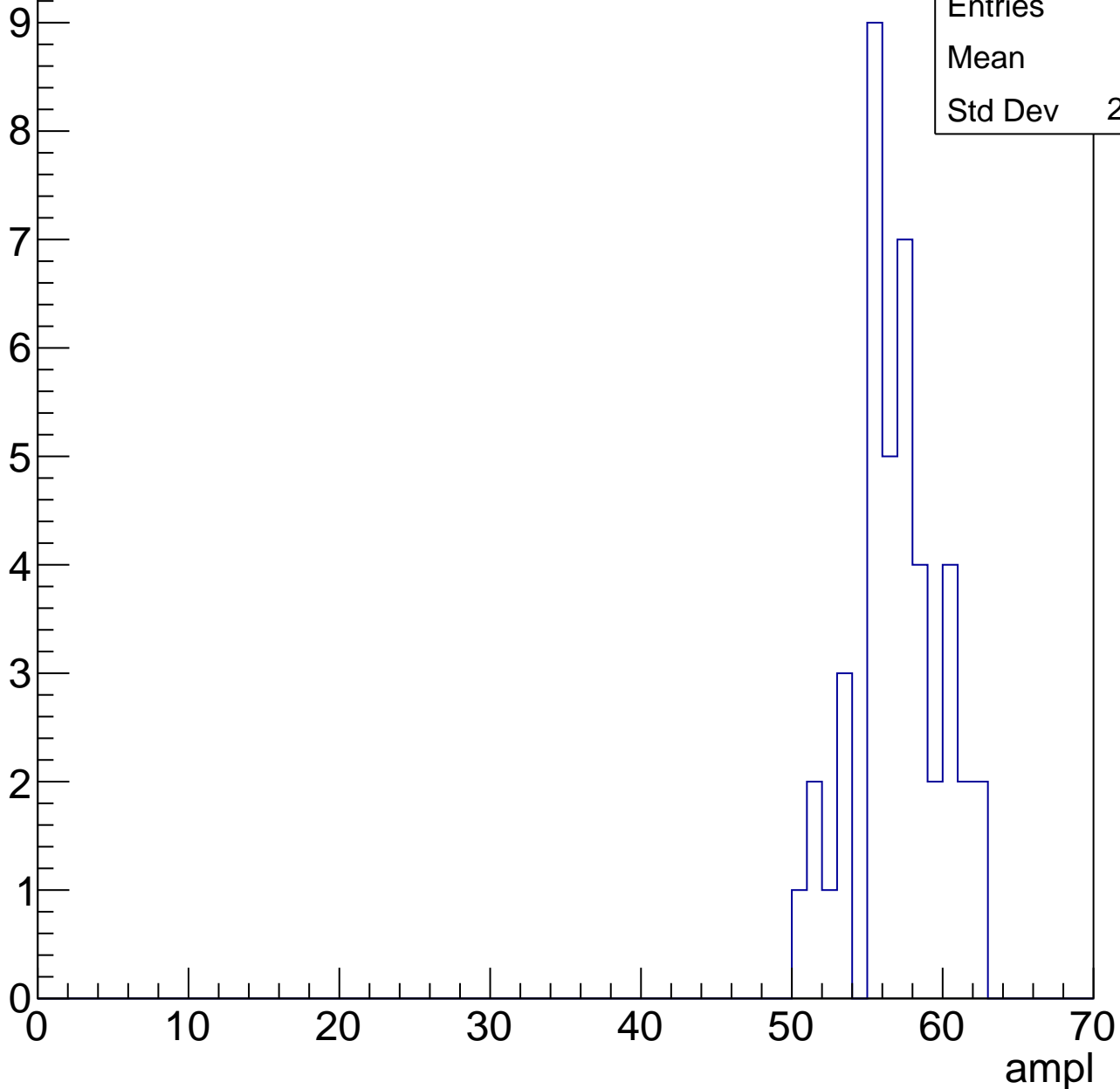


# B1L102S, U24-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	56.5
Std Dev	2.905

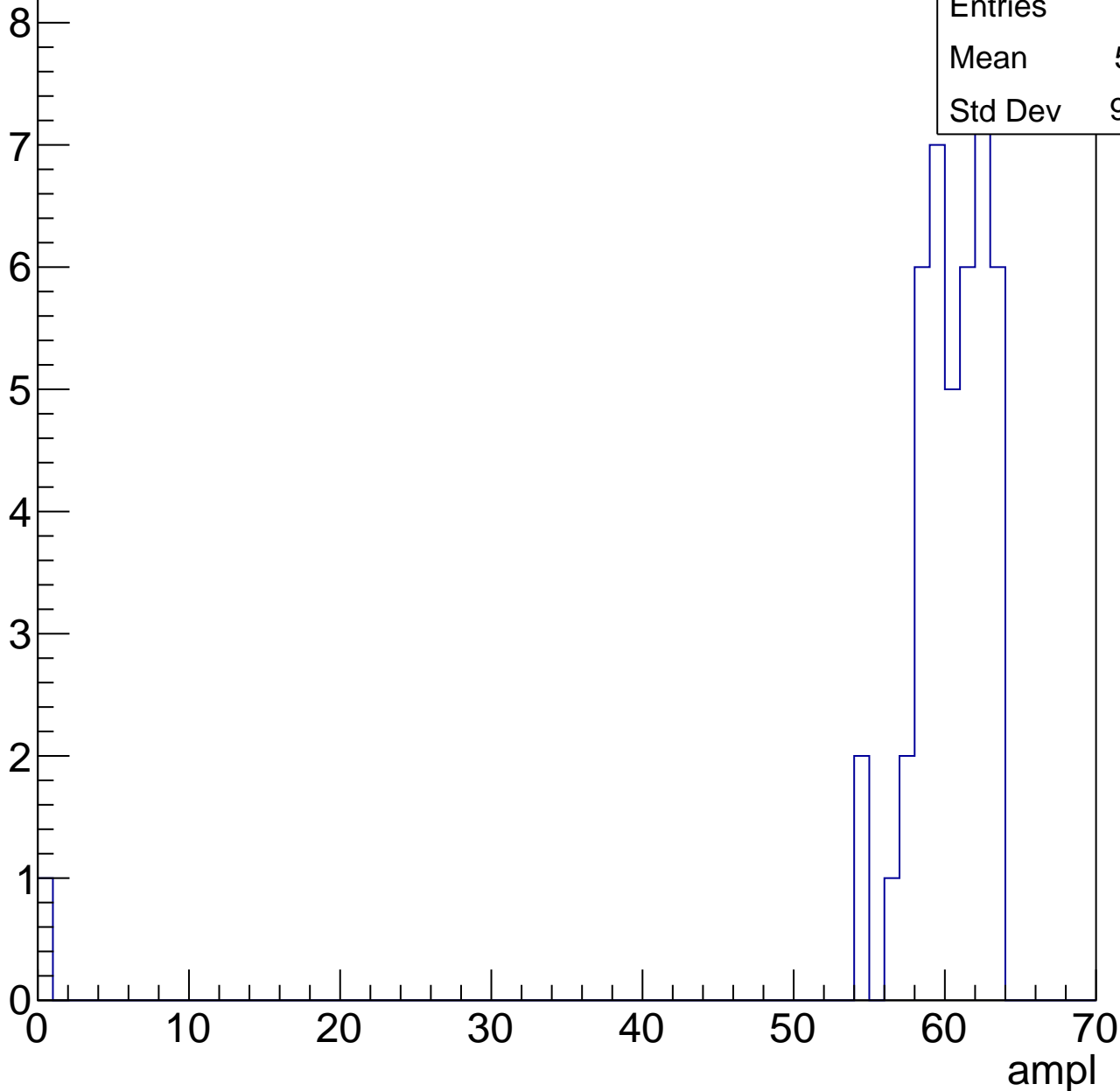


# B1L102S, U24-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

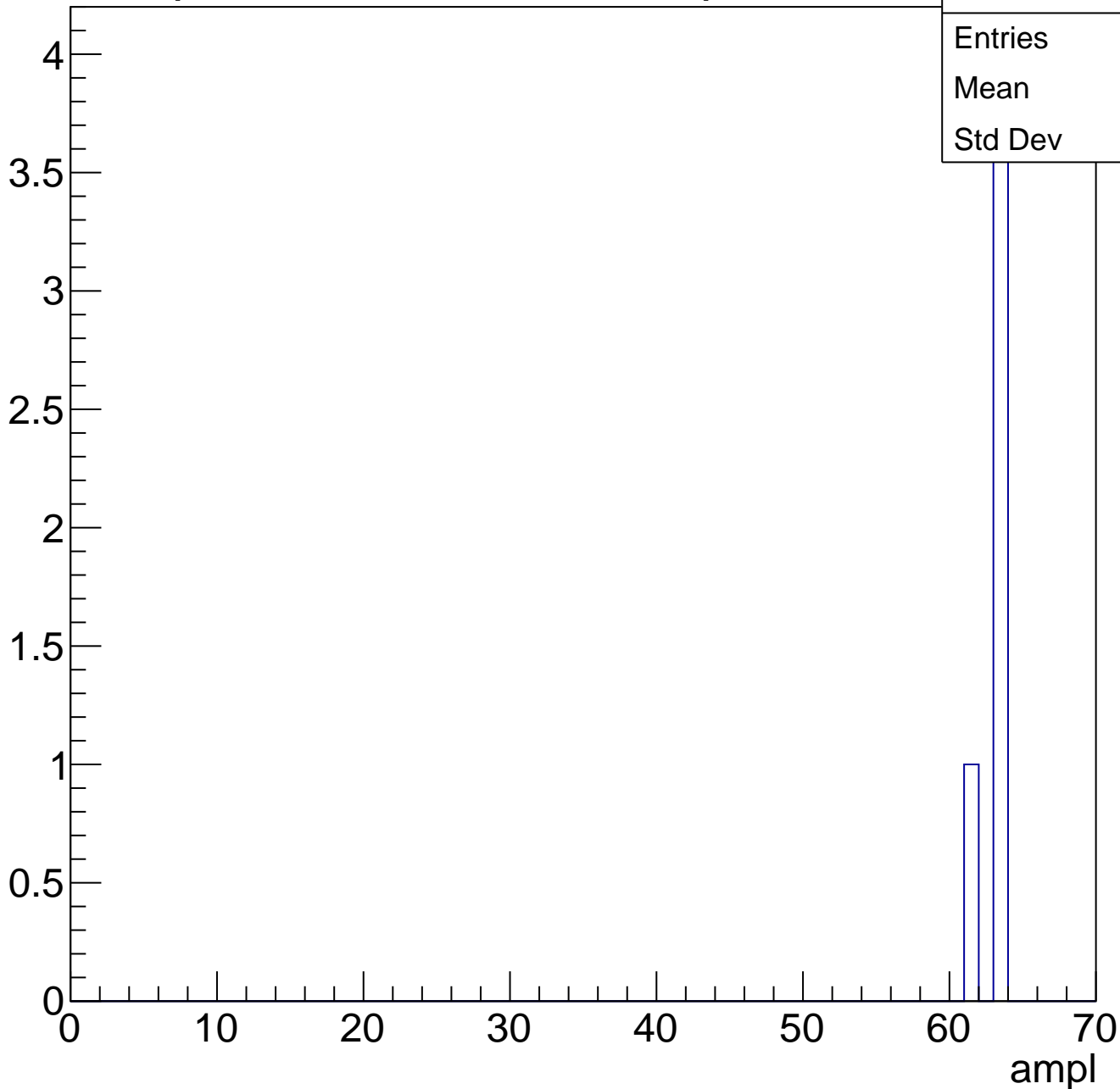
Entries	44
Mean	58.61
Std Dev	9.225



# B1L102S, U24-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch53, adc0

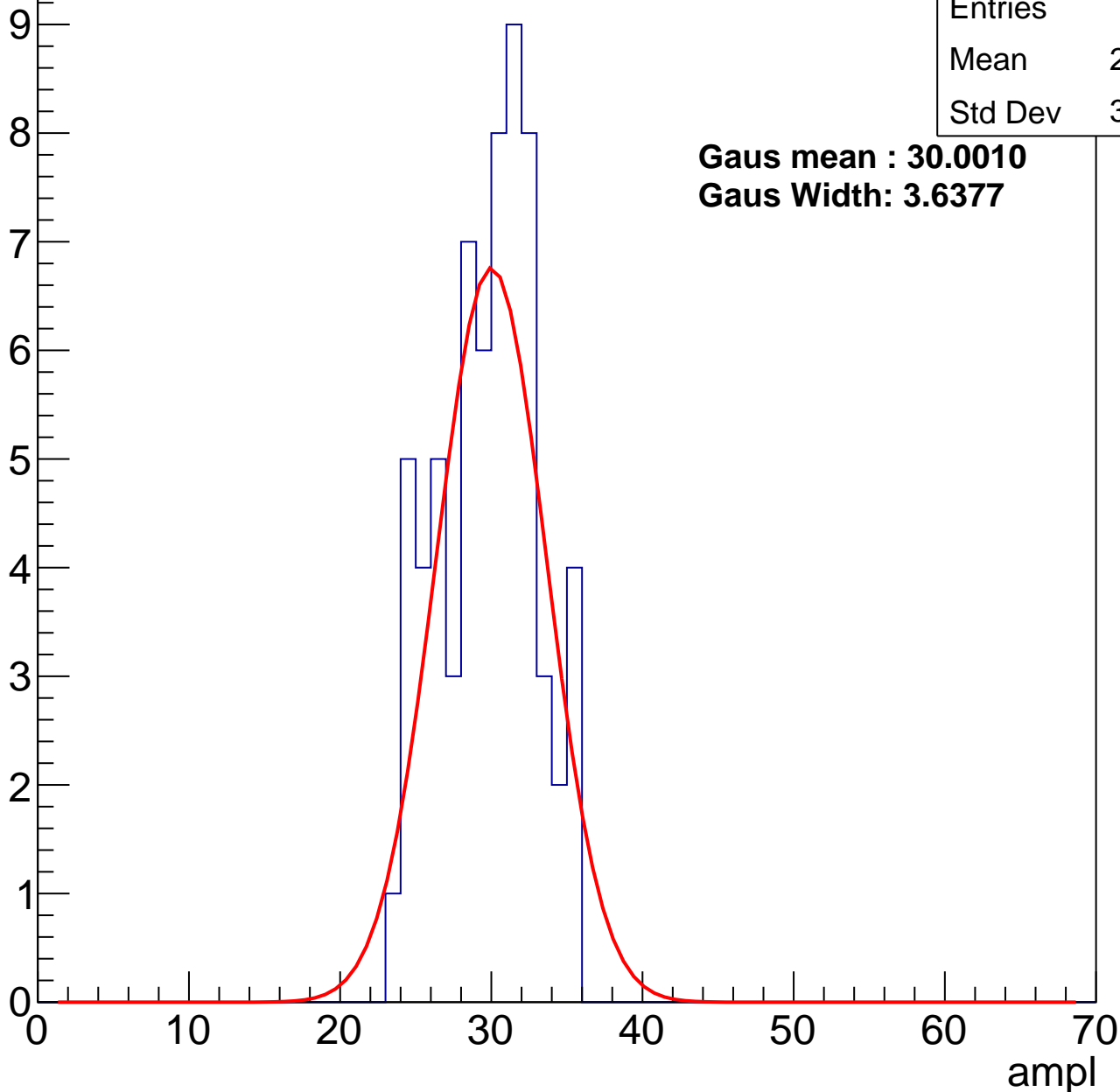
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.32
Std Dev	3.138

**Gaus mean : 30.0010**

**Gaus Width: 3.6377**



# B1L102S, U24-ch53, adc1

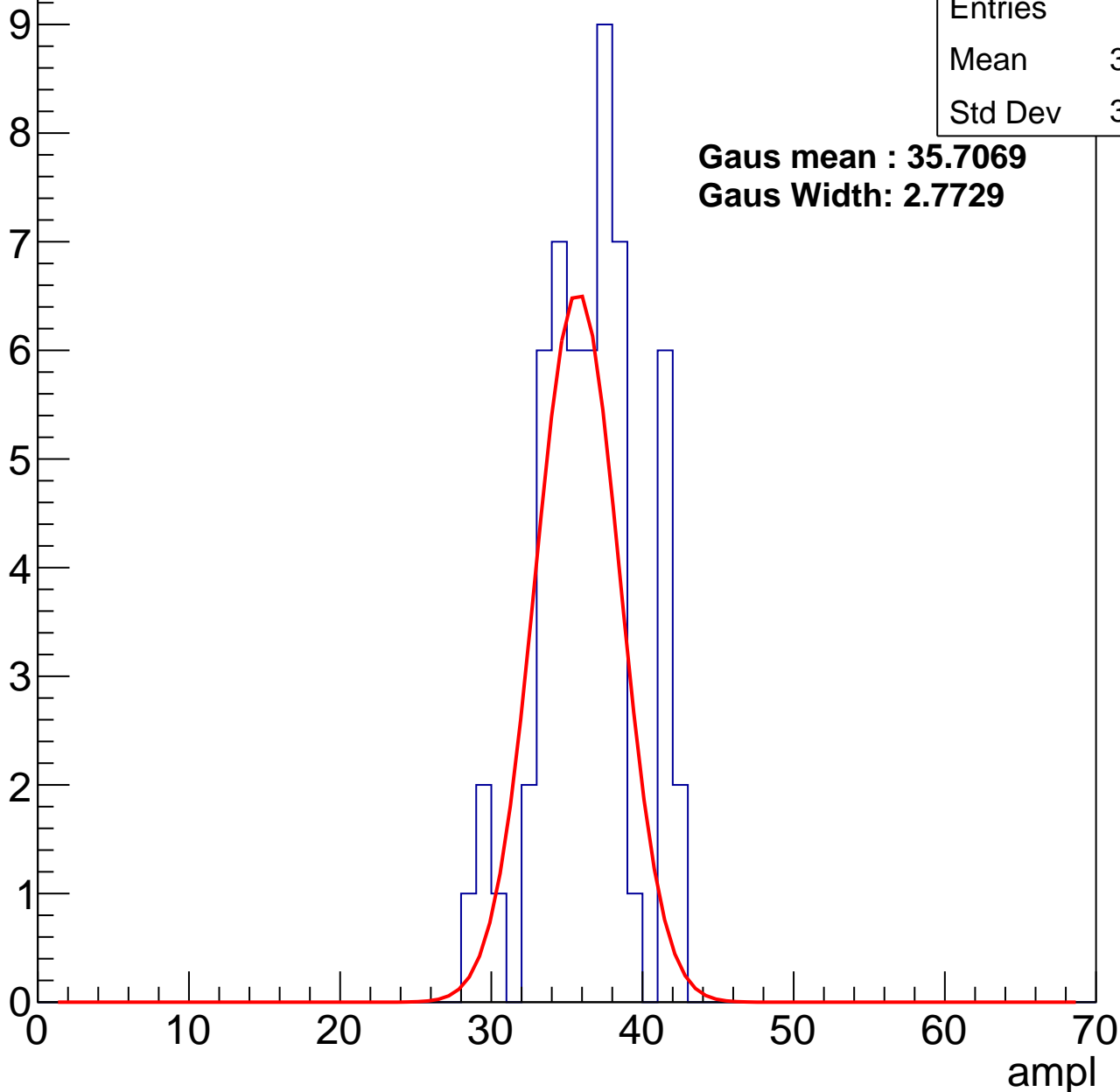
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.89
Std Dev	3.233

**Gaus mean : 35.7069**

**Gaus Width: 2.7729**



# B1L102S, U24-ch53, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	76
Mean	42.38
Std Dev	4.01

**Gaus mean : 43.2483**

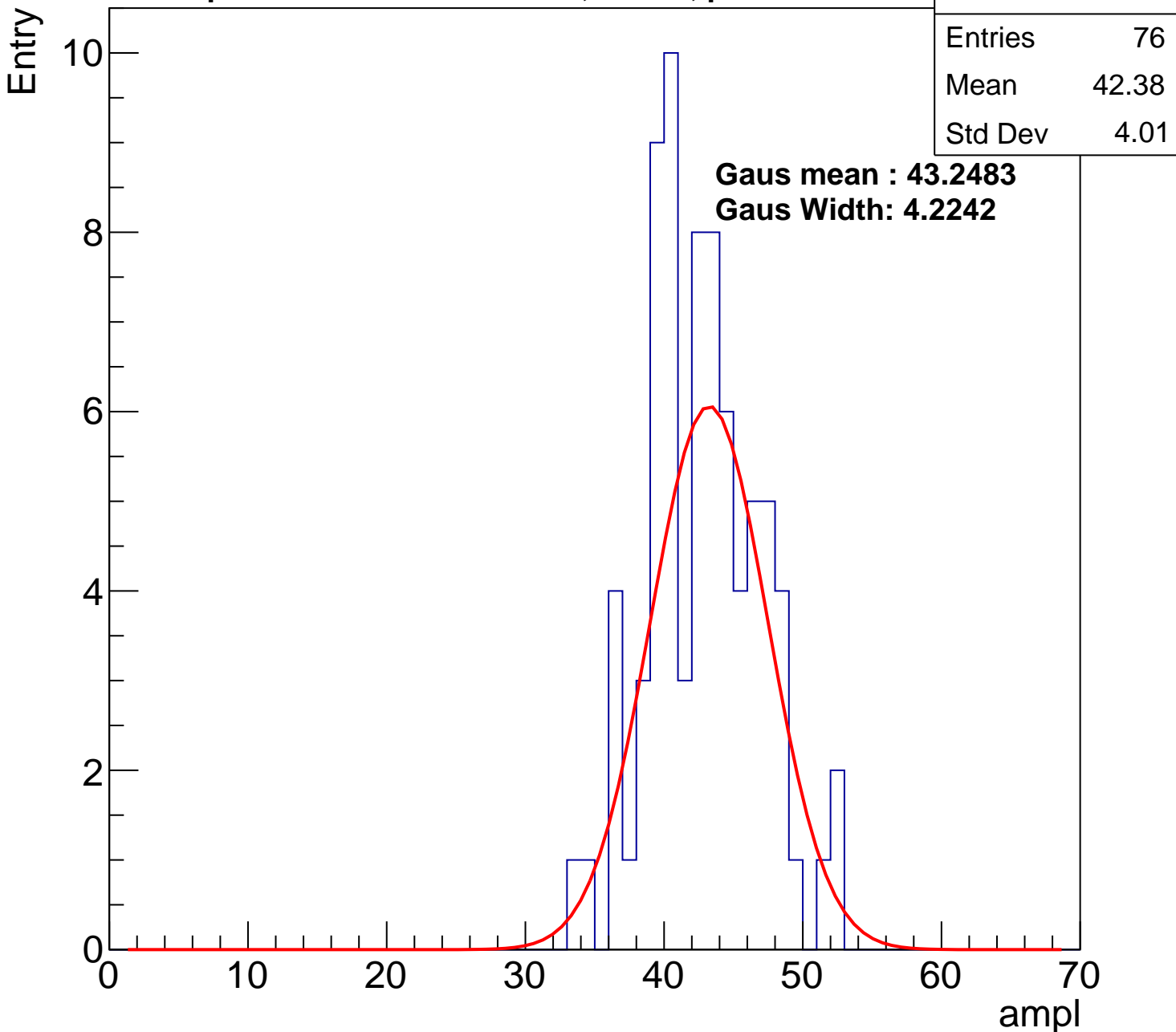
**Gaus Width: 4.2242**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

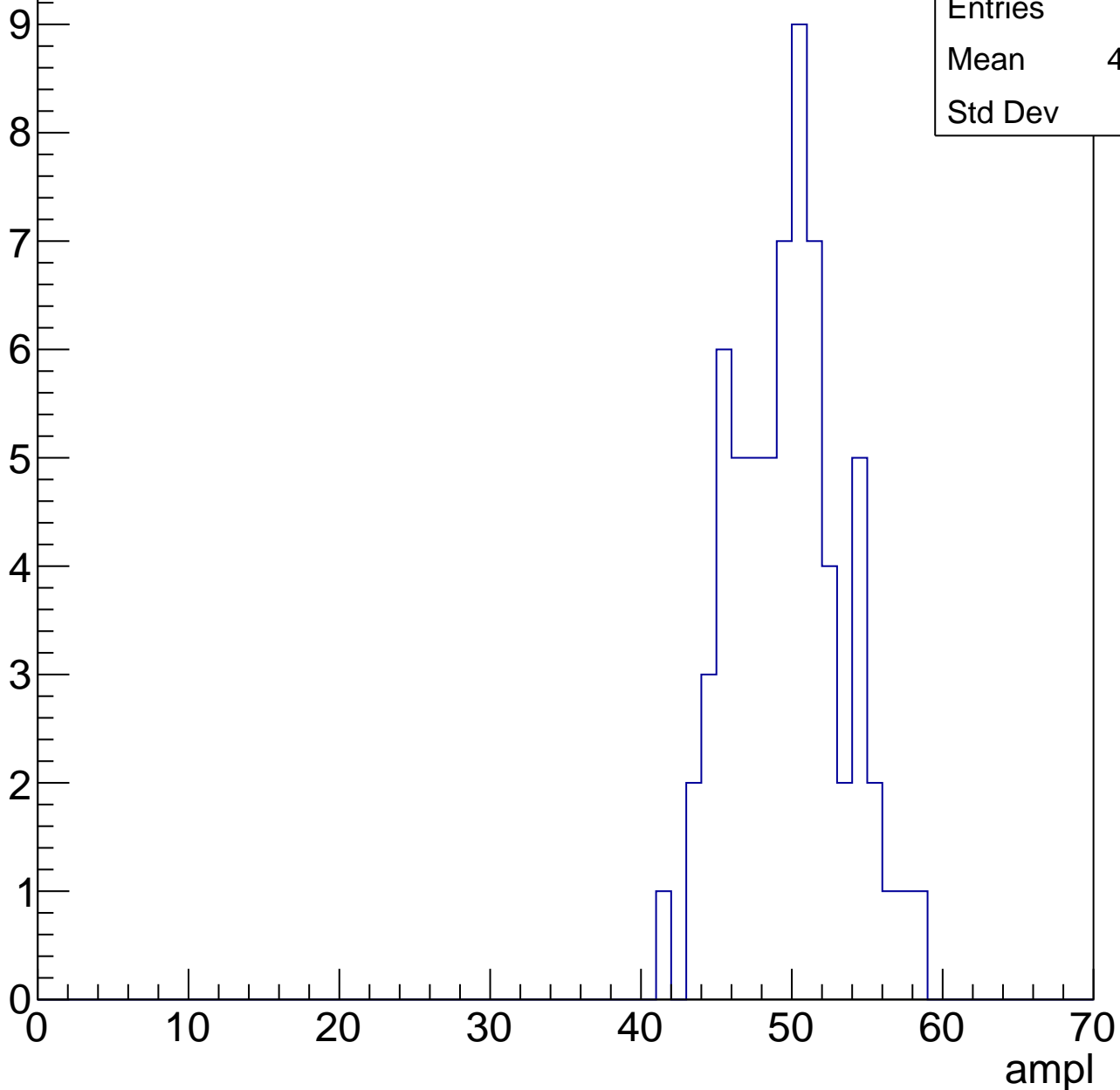


# B1L102S, U24-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.23
Std Dev	3.63

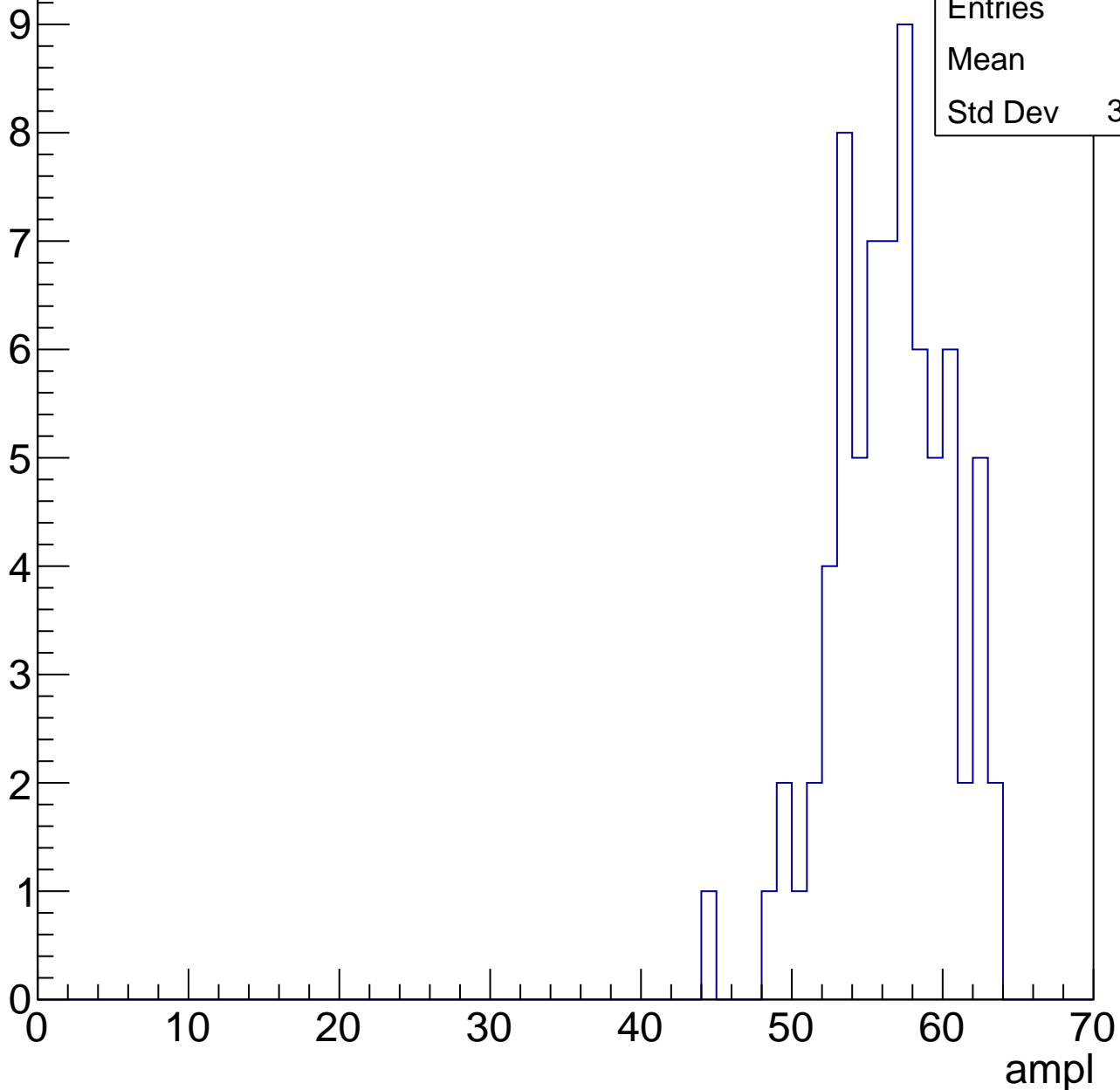


# B1L102S, U24-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	56.1
Std Dev	3.804

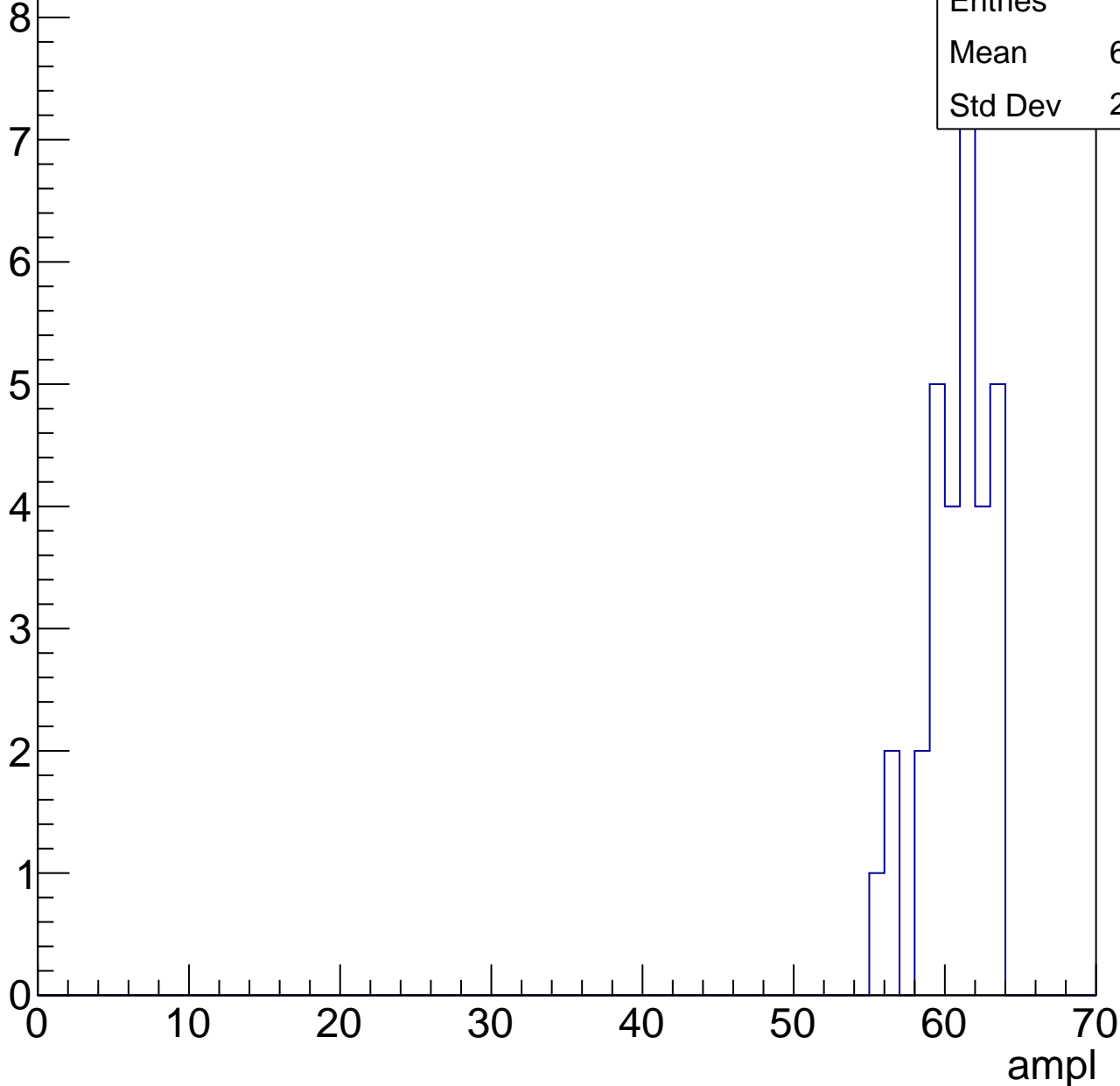


# B1L102S, U24-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

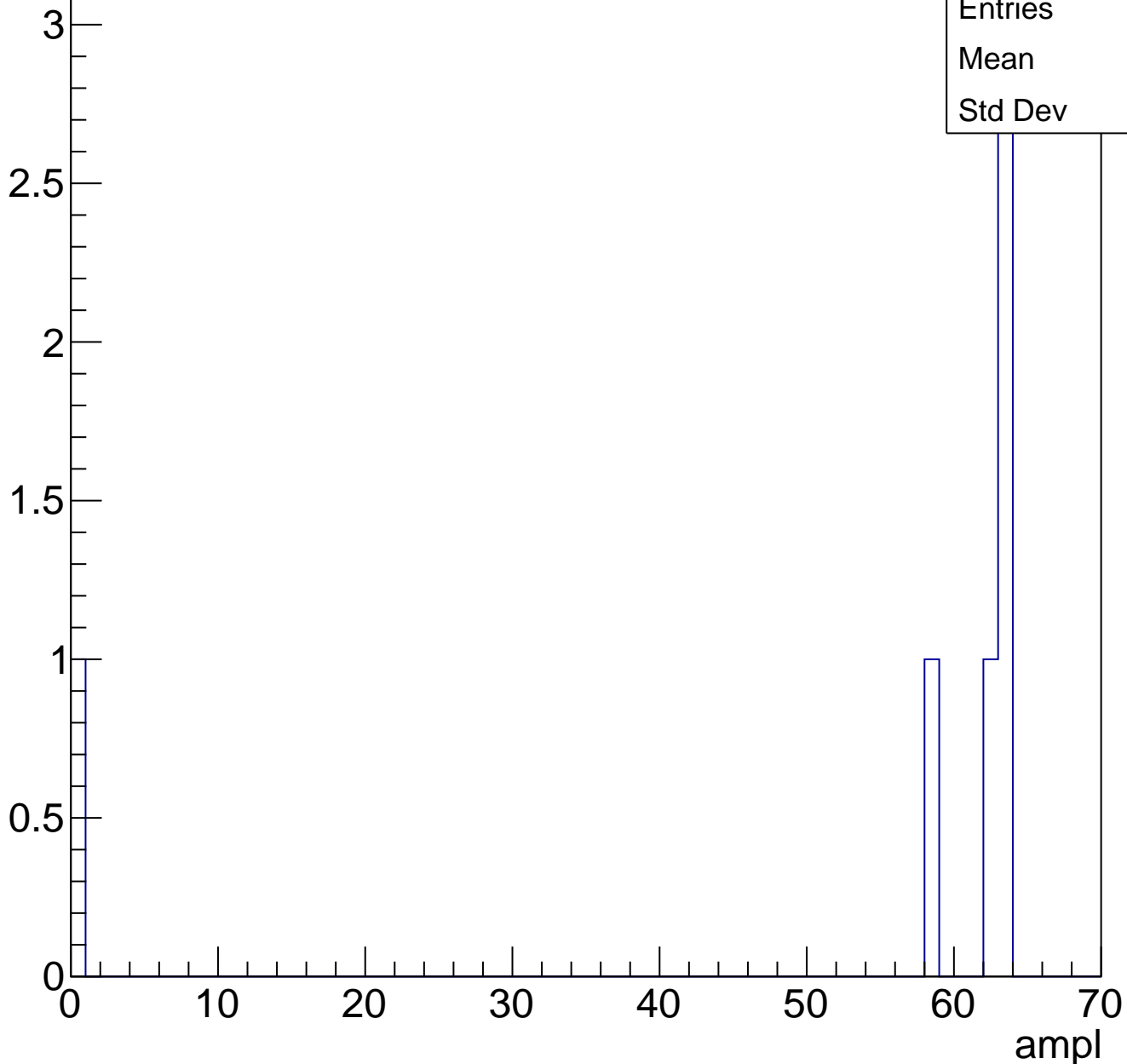
Entries	31
Mean	60.29
Std Dev	2.098



# B1L102S, U24-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch54, adc0

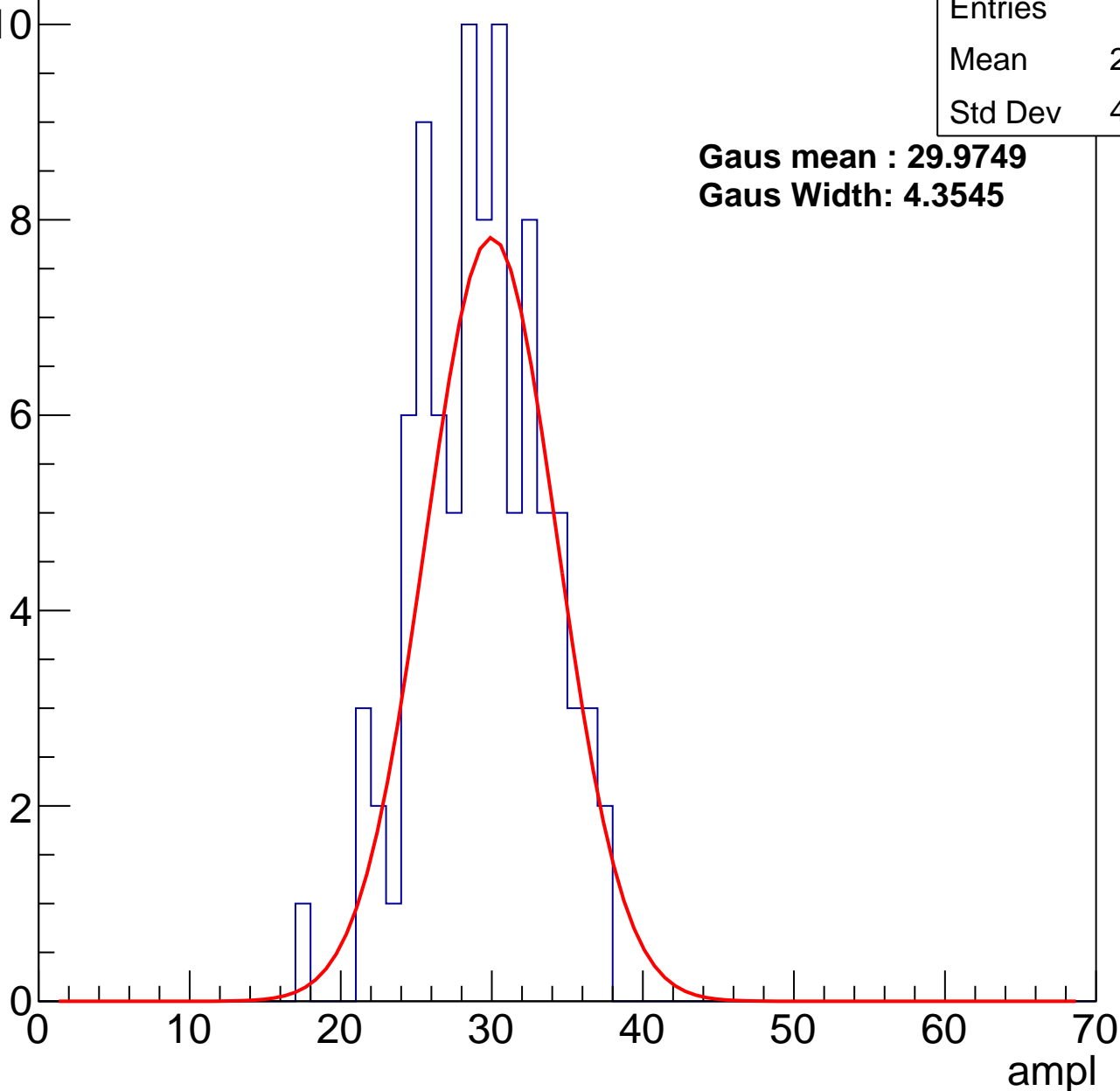
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	92
Mean	28.83
Std Dev	4.085

**Gaus mean : 29.9749**

**Gaus Width: 4.3545**



# B1L102S, U24-ch54, adc1

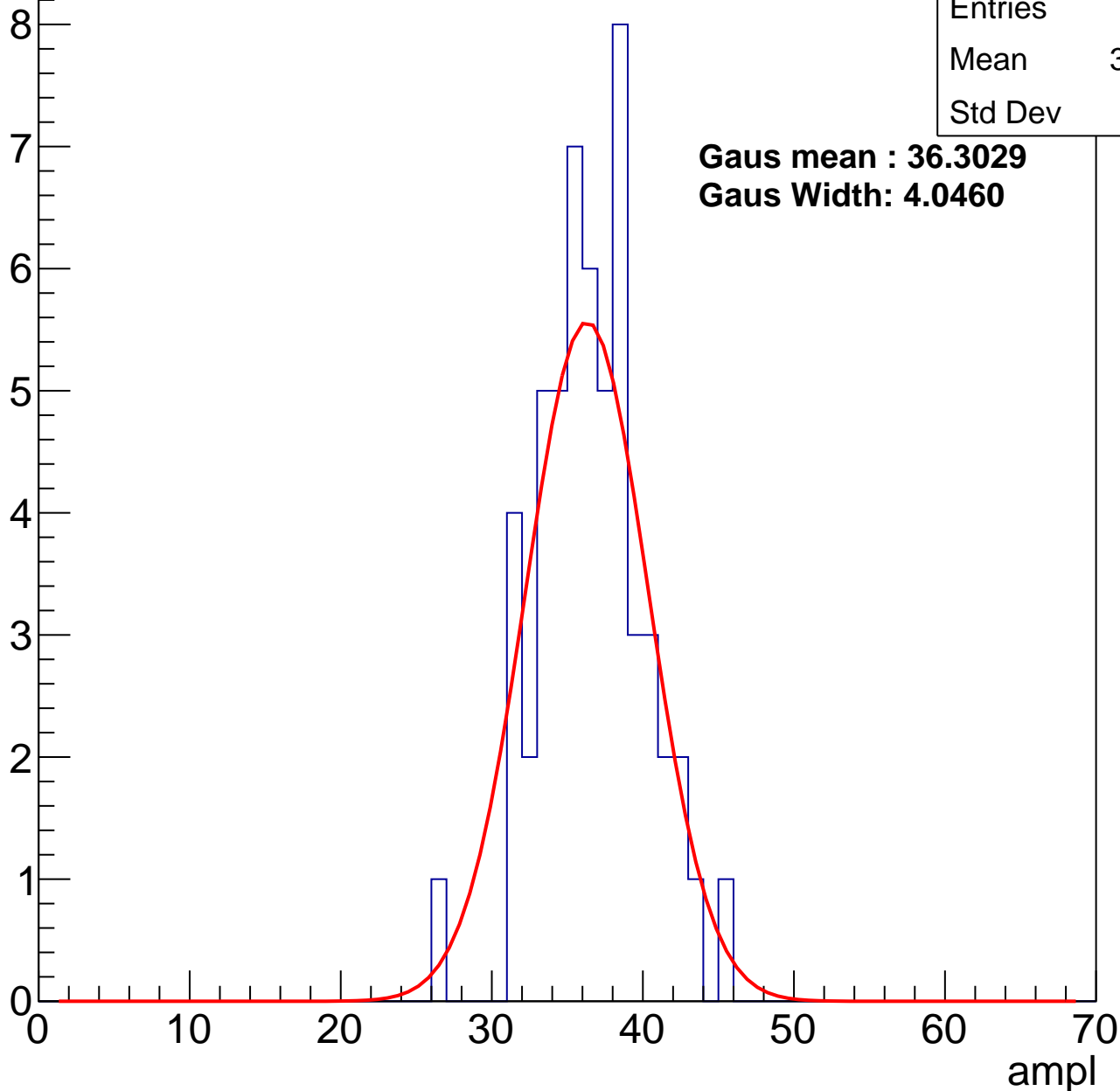
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.18
Std Dev	3.48

**Gaus mean : 36.3029**

**Gaus Width: 4.0460**



# B1L102S, U24-ch54, adc2

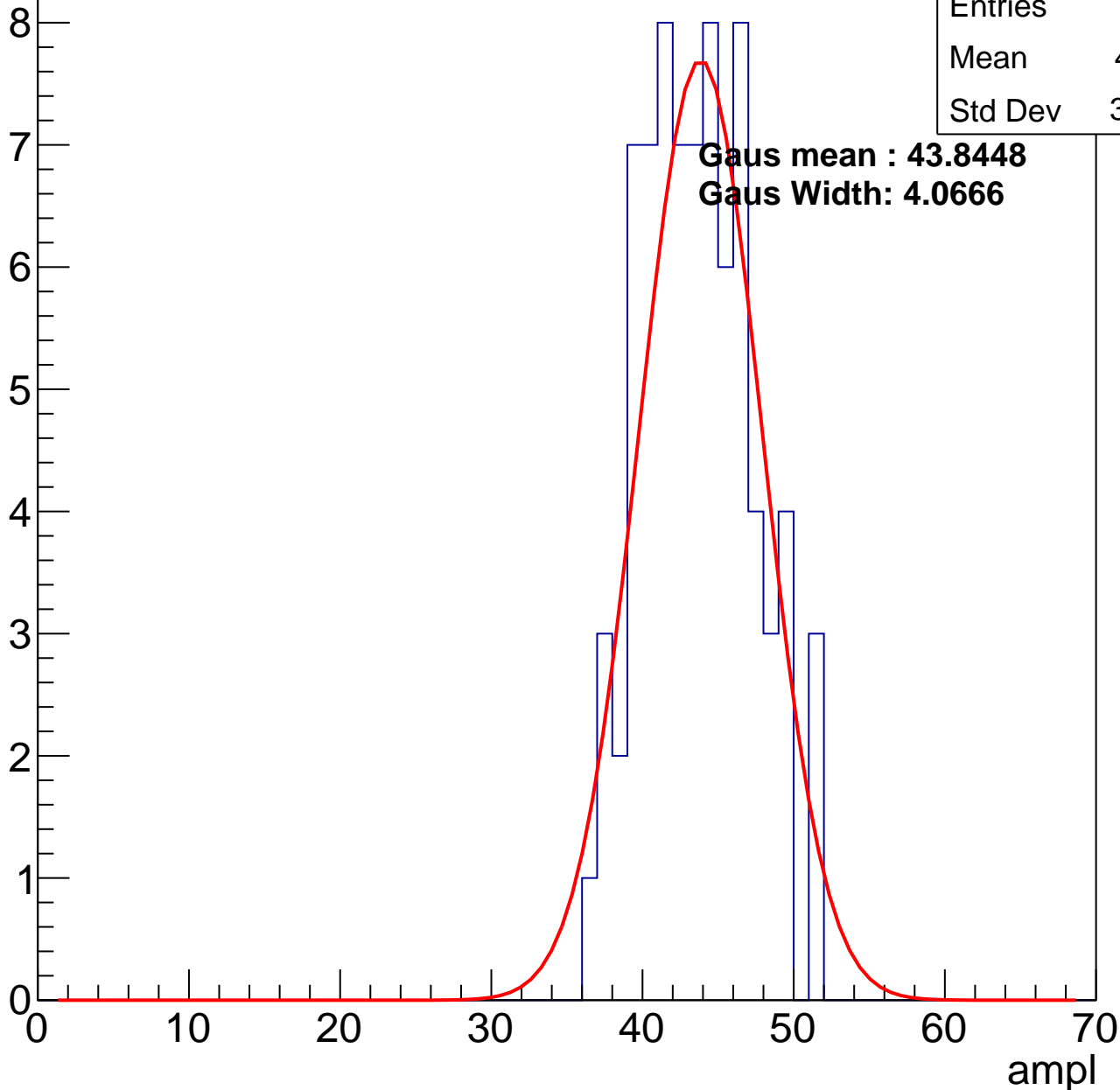
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	43.21
Std Dev	3.575

**Gaus mean : 43.8448**

**Gaus Width: 4.0666**

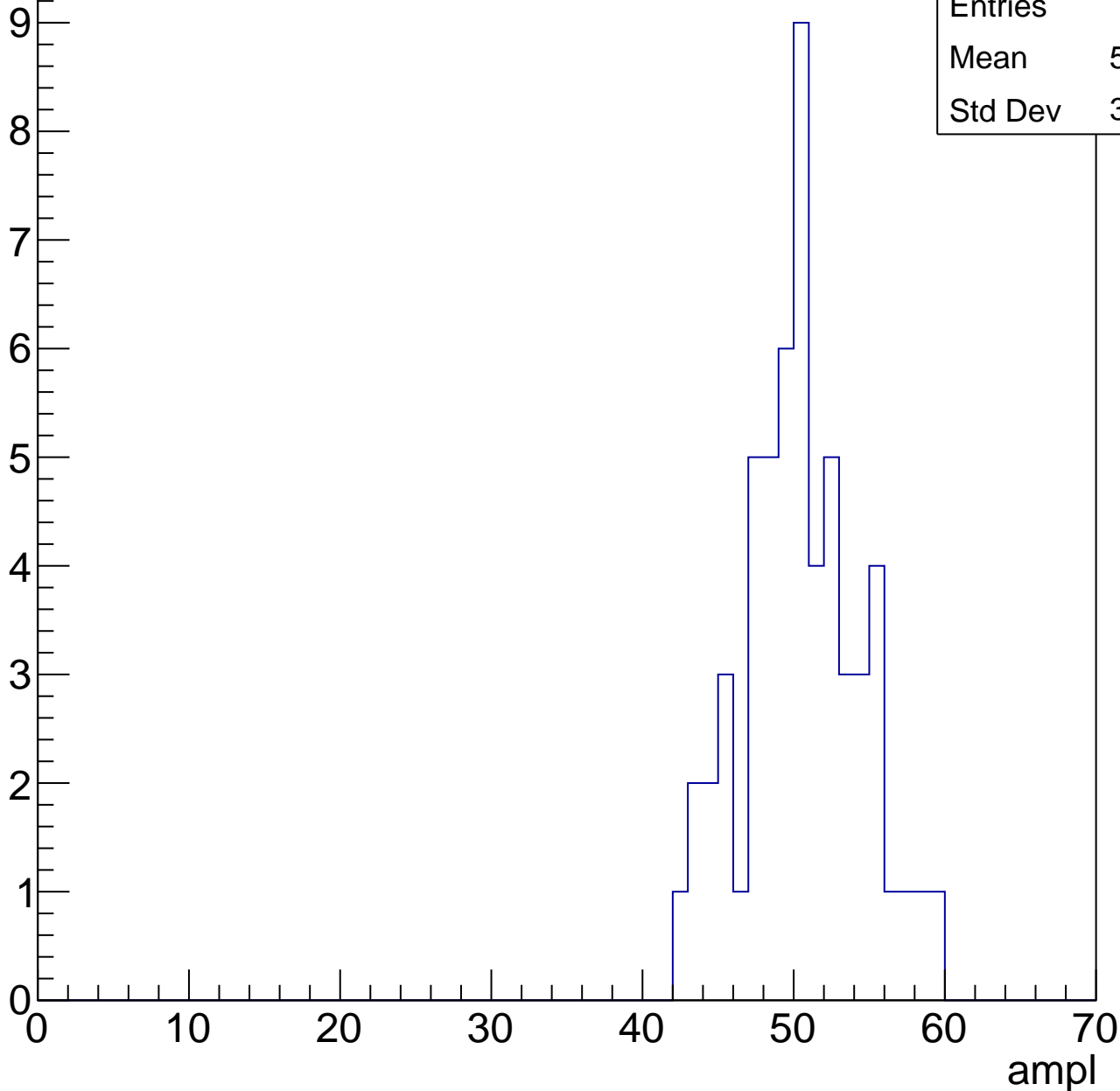


# B1L102S, U24-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	50.02
Std Dev	3.804



# B1L102S, U24-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

6

5

4

3

2

1

0

Entries 58

Mean 55.19

Std Dev 3.608

0

10

20

30

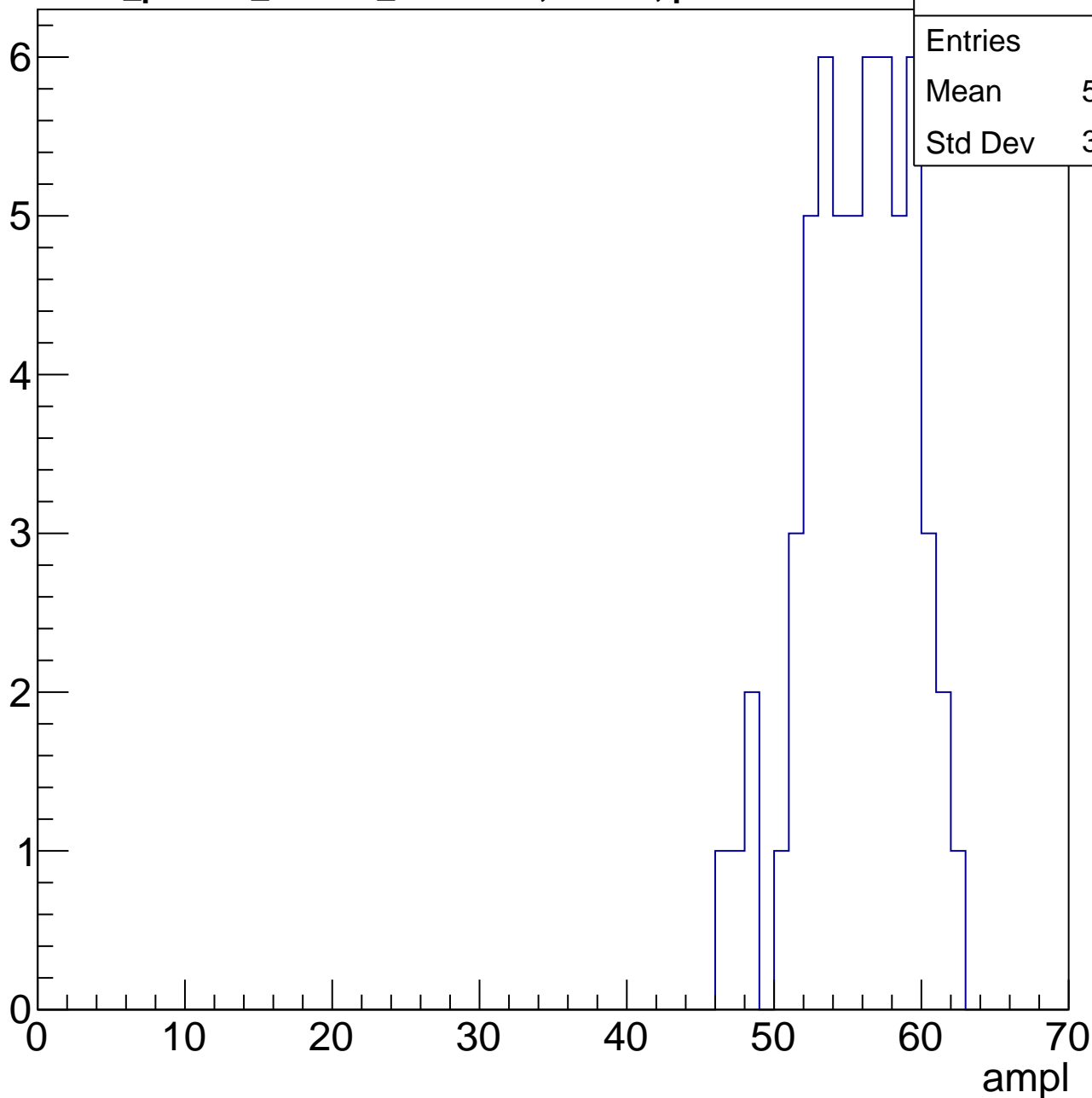
40

50

60

70

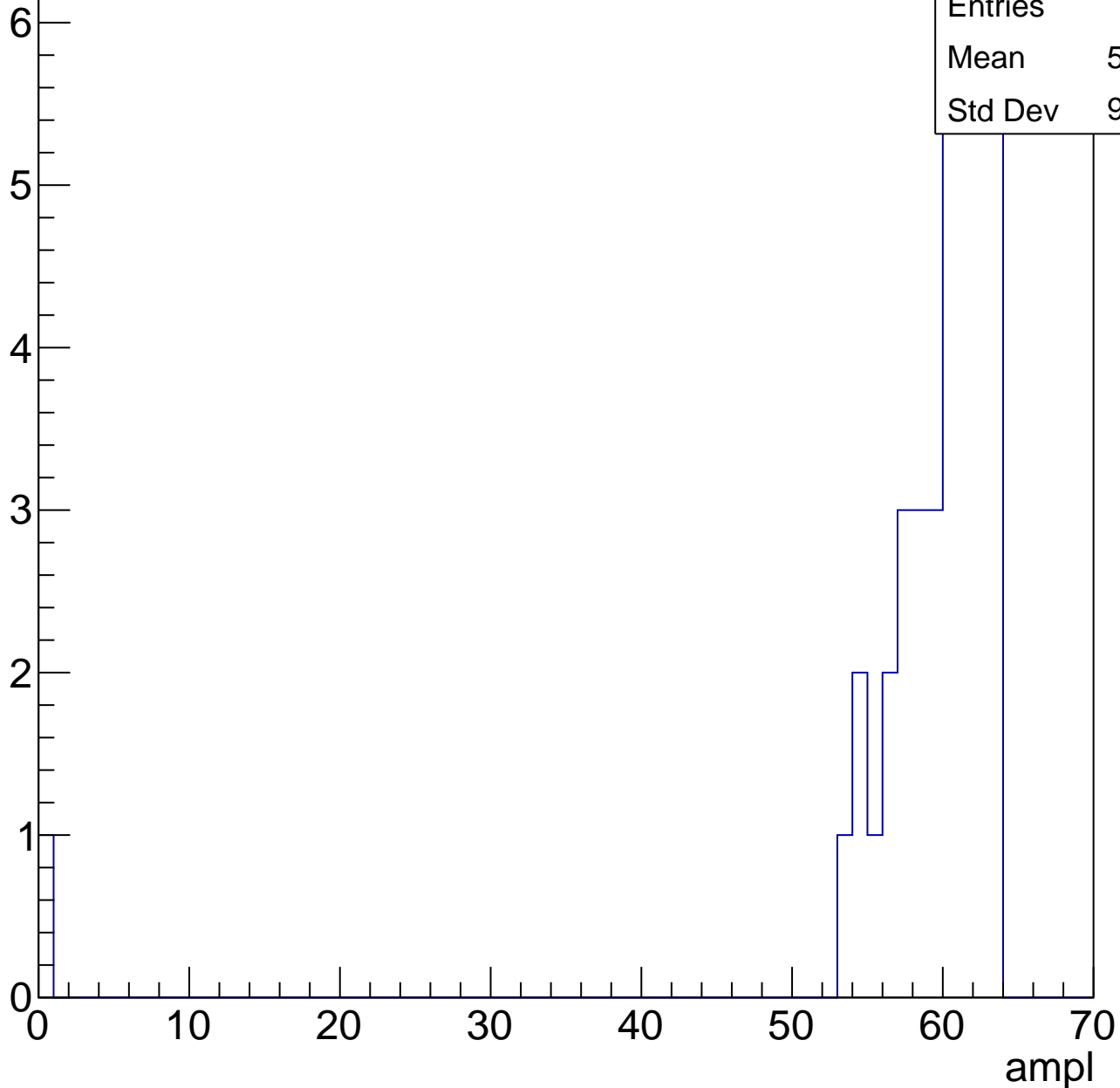
ampl



# B1L102S, U24-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

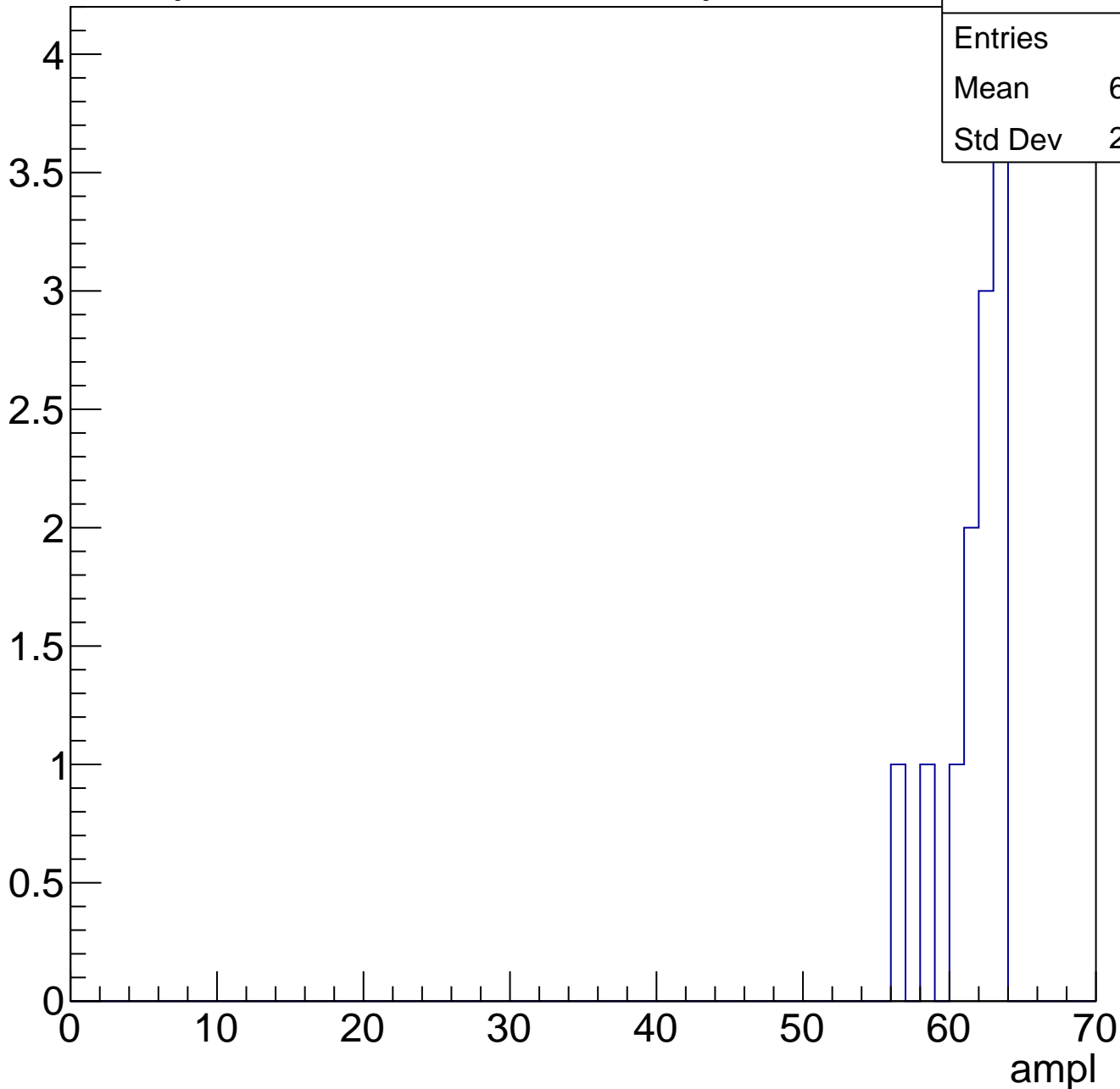
Entry



# B1L102S, U24-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	12
Mean	61.17
Std Dev	2.115



# B1L102S, U24-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch55, adc0

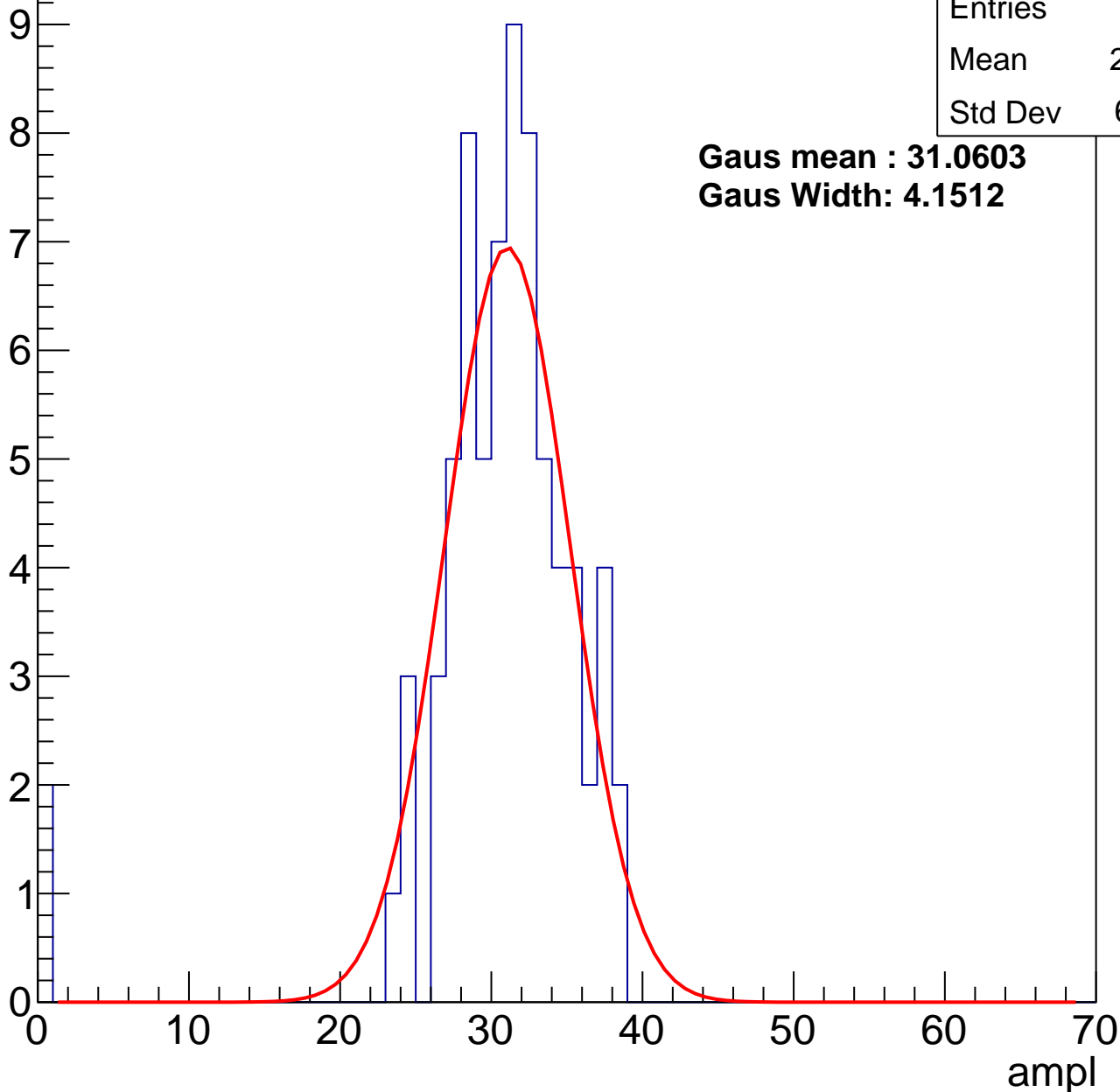
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.99
Std Dev	6.161

**Gaus mean : 31.0603**

**Gaus Width: 4.1512**



# B1L102S, U24-ch55, adc1

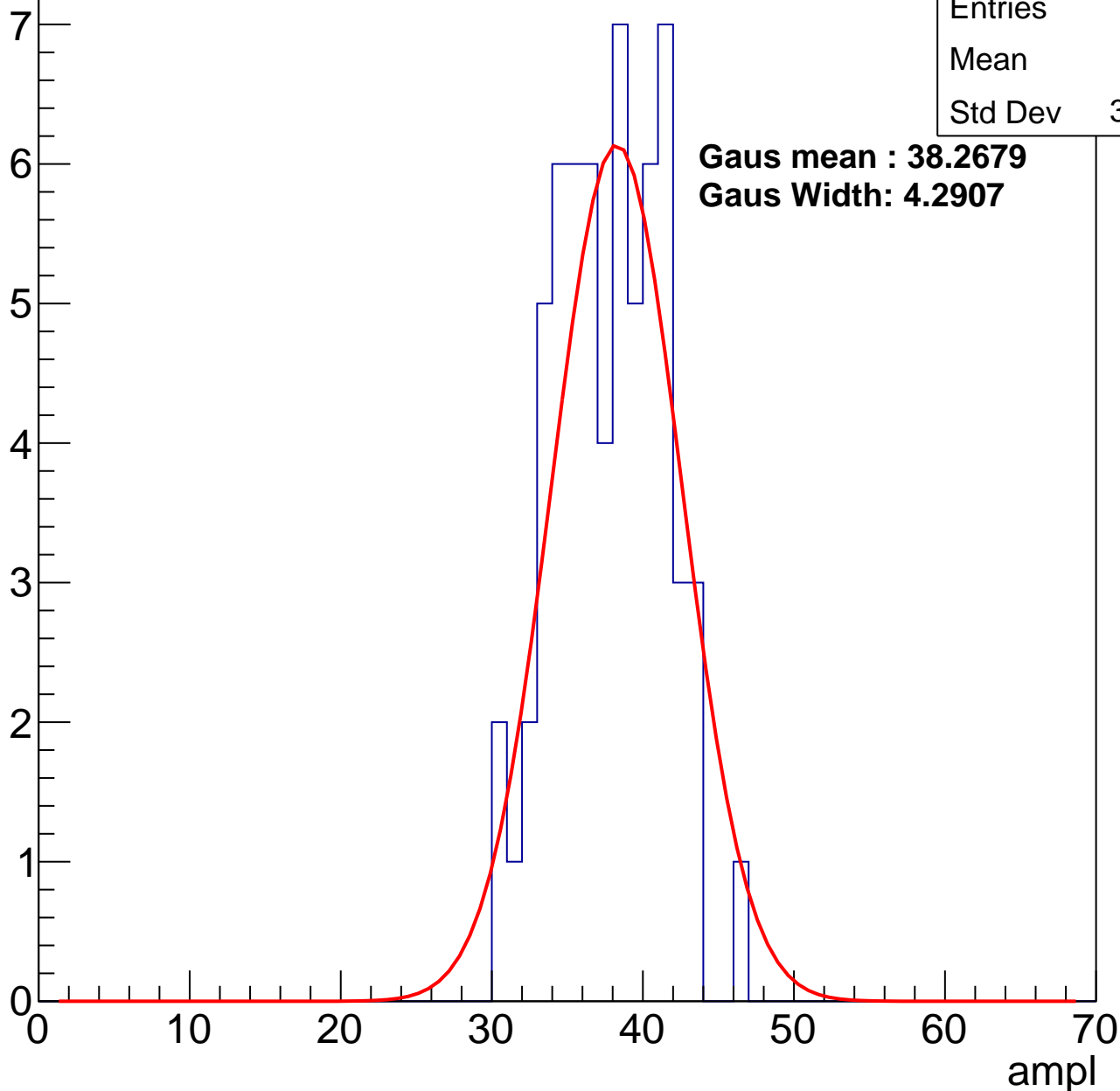
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.3
Std Dev	3.534

**Gaus mean : 38.2679**

**Gaus Width: 4.2907**



# B1L102S, U24-ch55, adc2

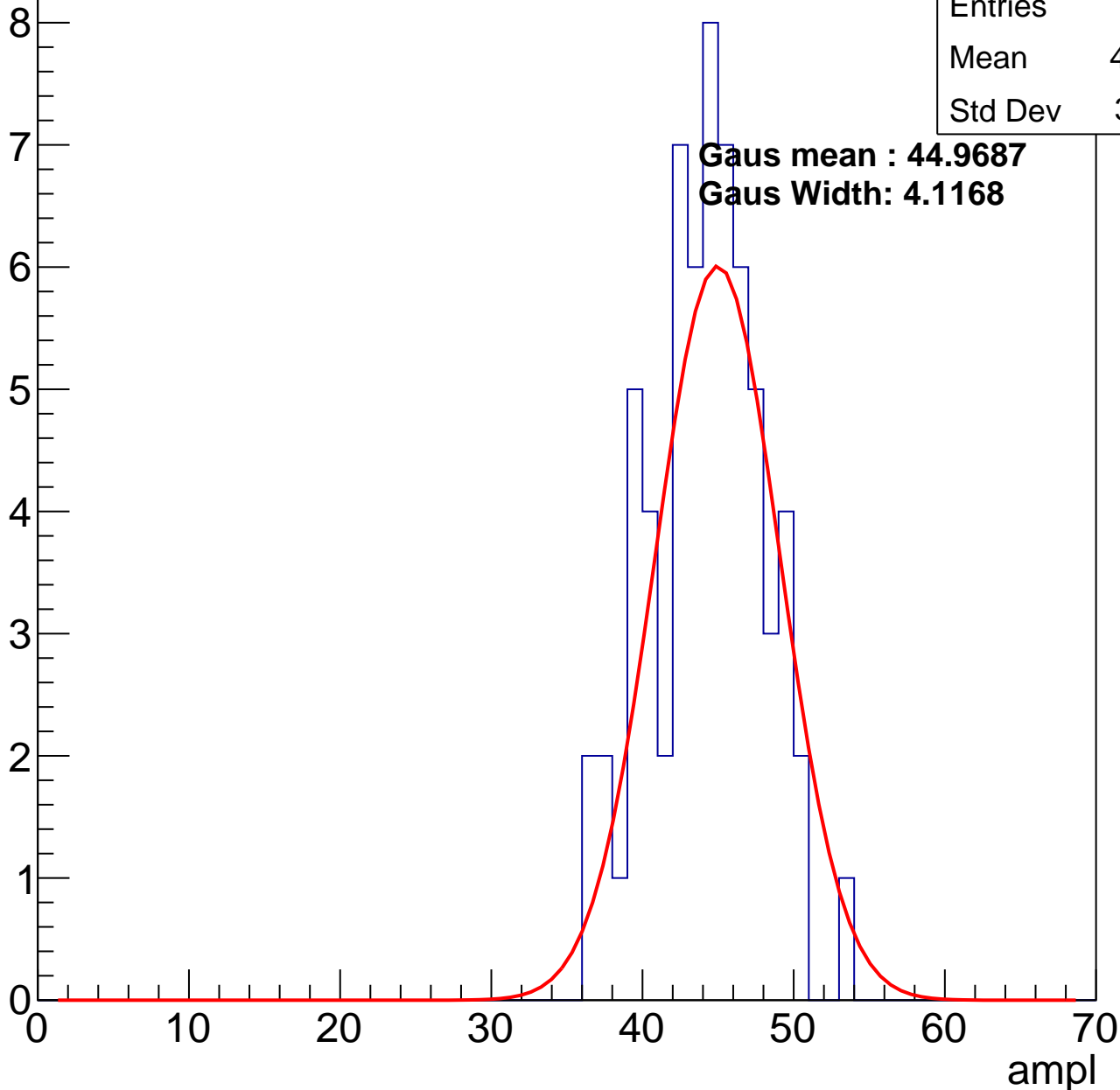
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.75
Std Dev	3.671

**Gaus mean : 44.9687**

**Gaus Width: 4.1168**

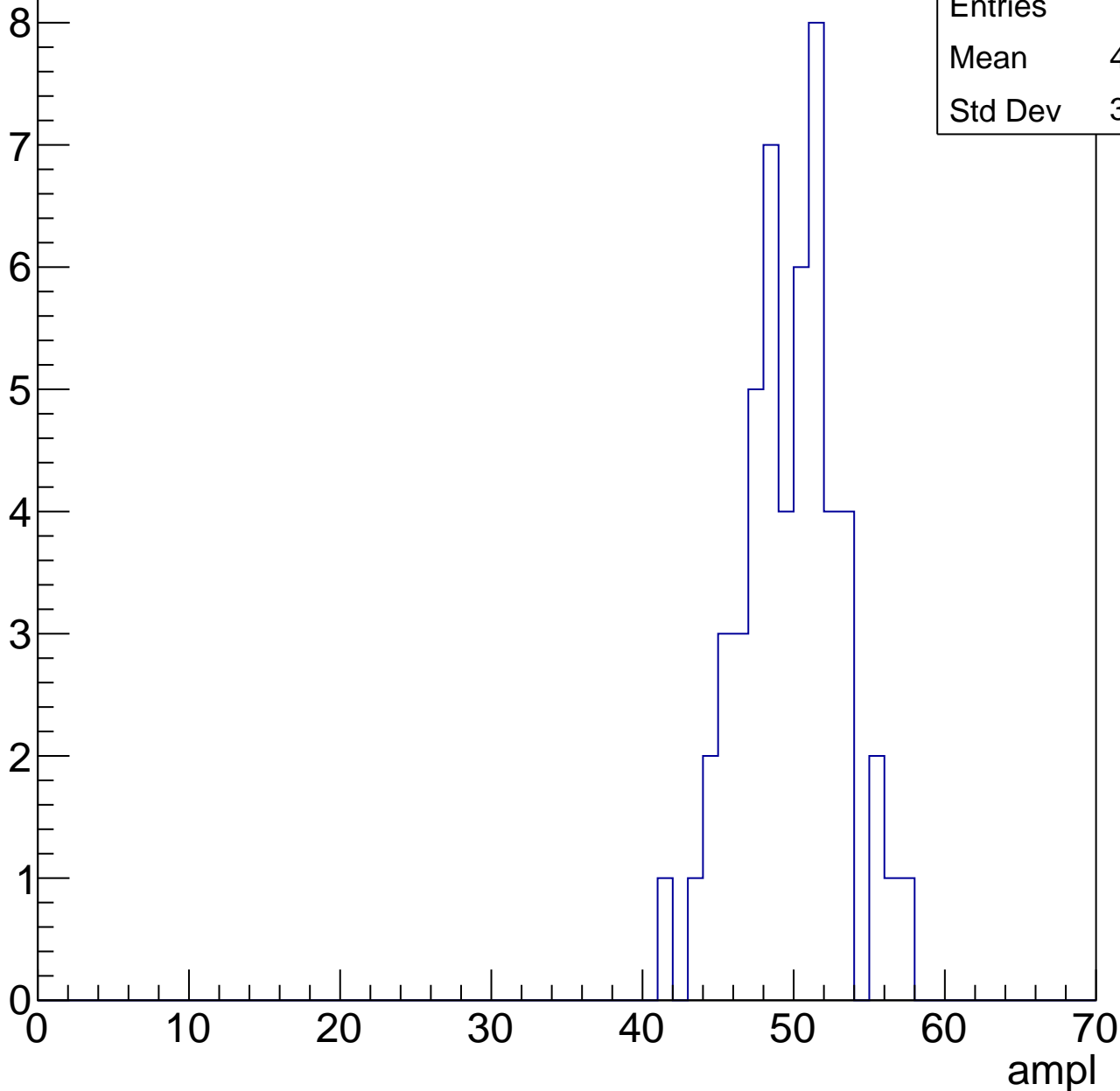


# B1L102S, U24-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.29
Std Dev	3.313



# B1L102S, U24-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	56.12
Std Dev	3.384

Entry

10

8

6

4

2

0

0

10

20

30

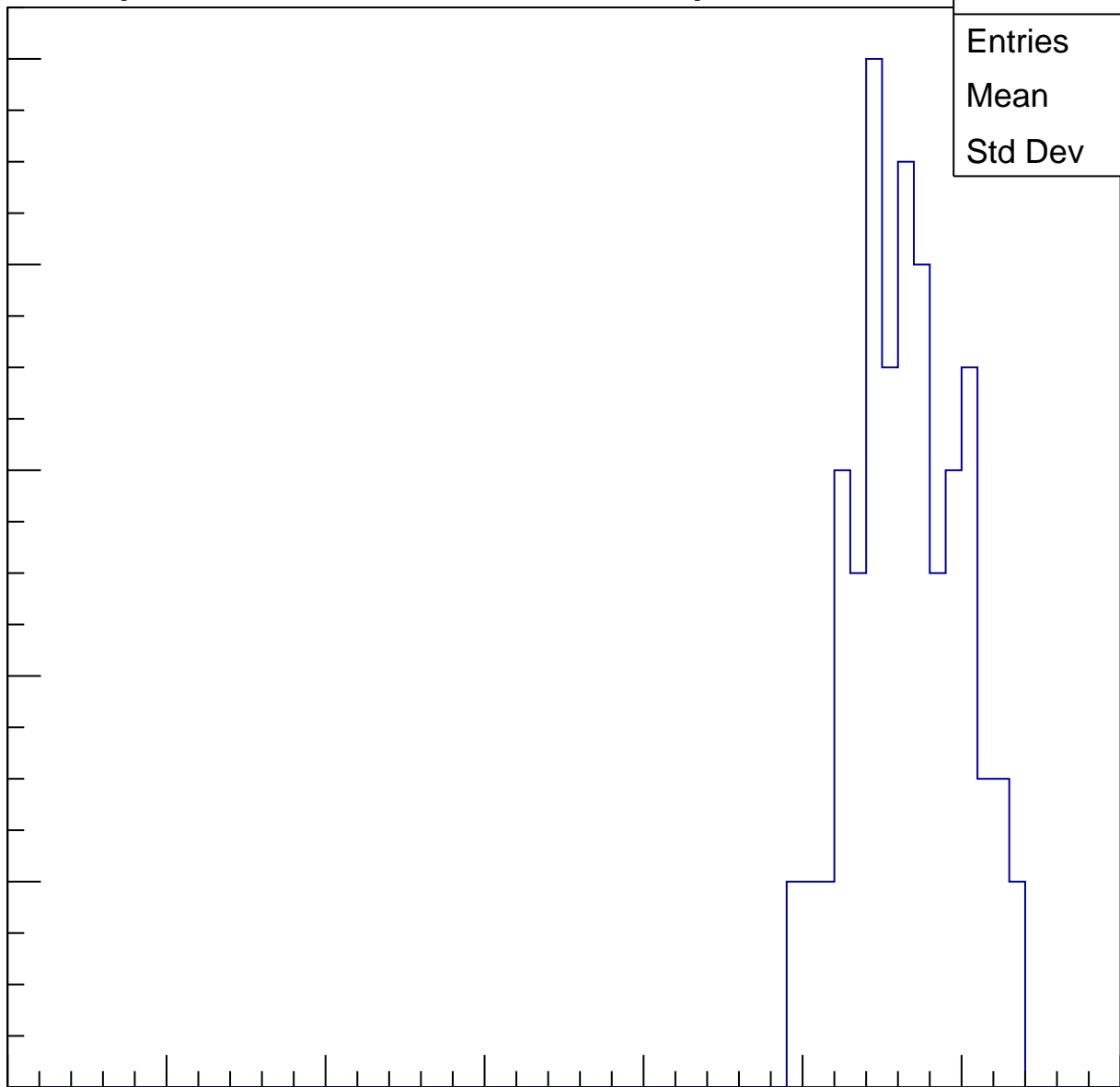
40

50

60

ampl

70

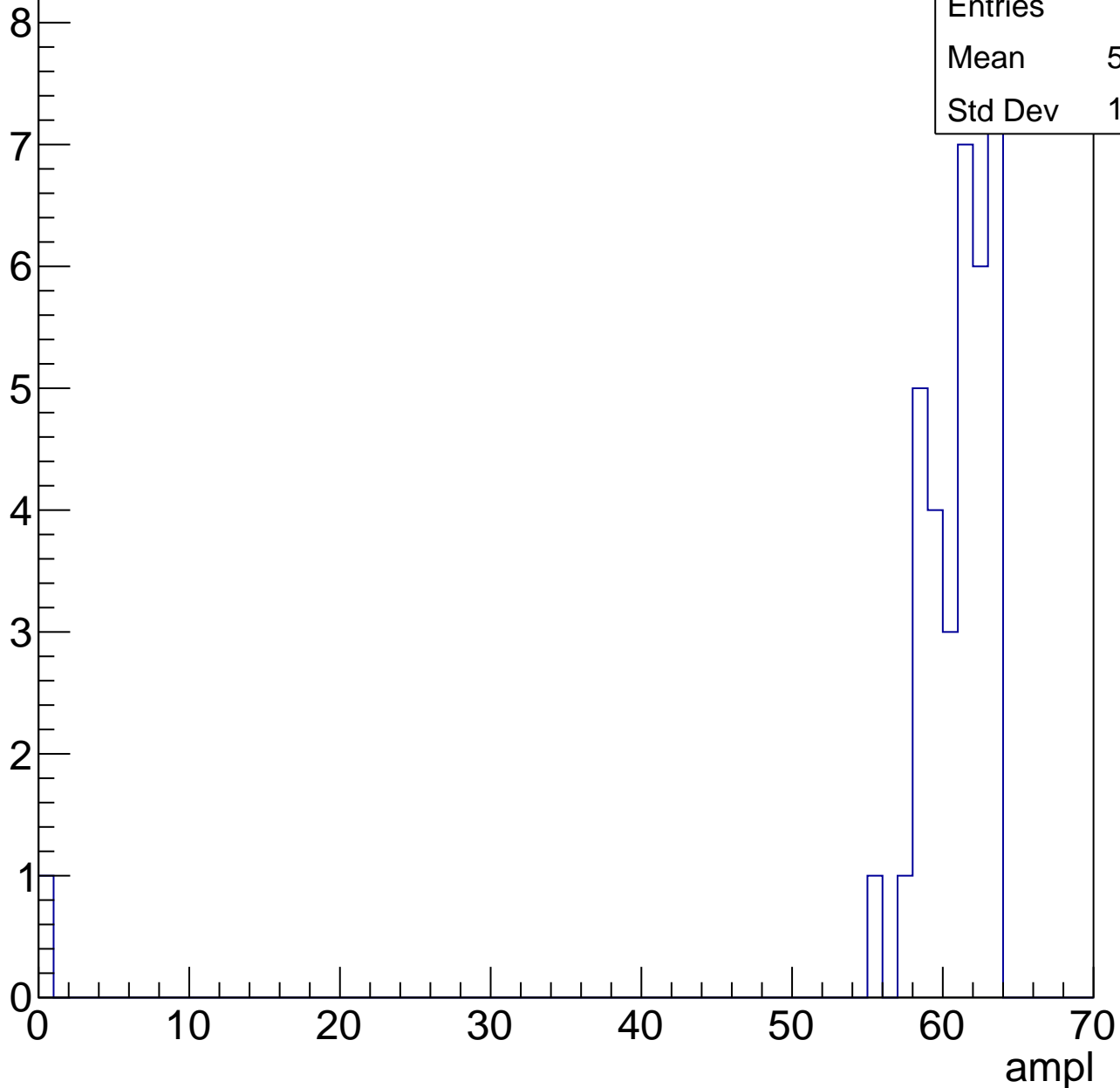


# B1L102S, U24-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.92
Std Dev	10.16



# B1L102S, U24-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch56, adc0

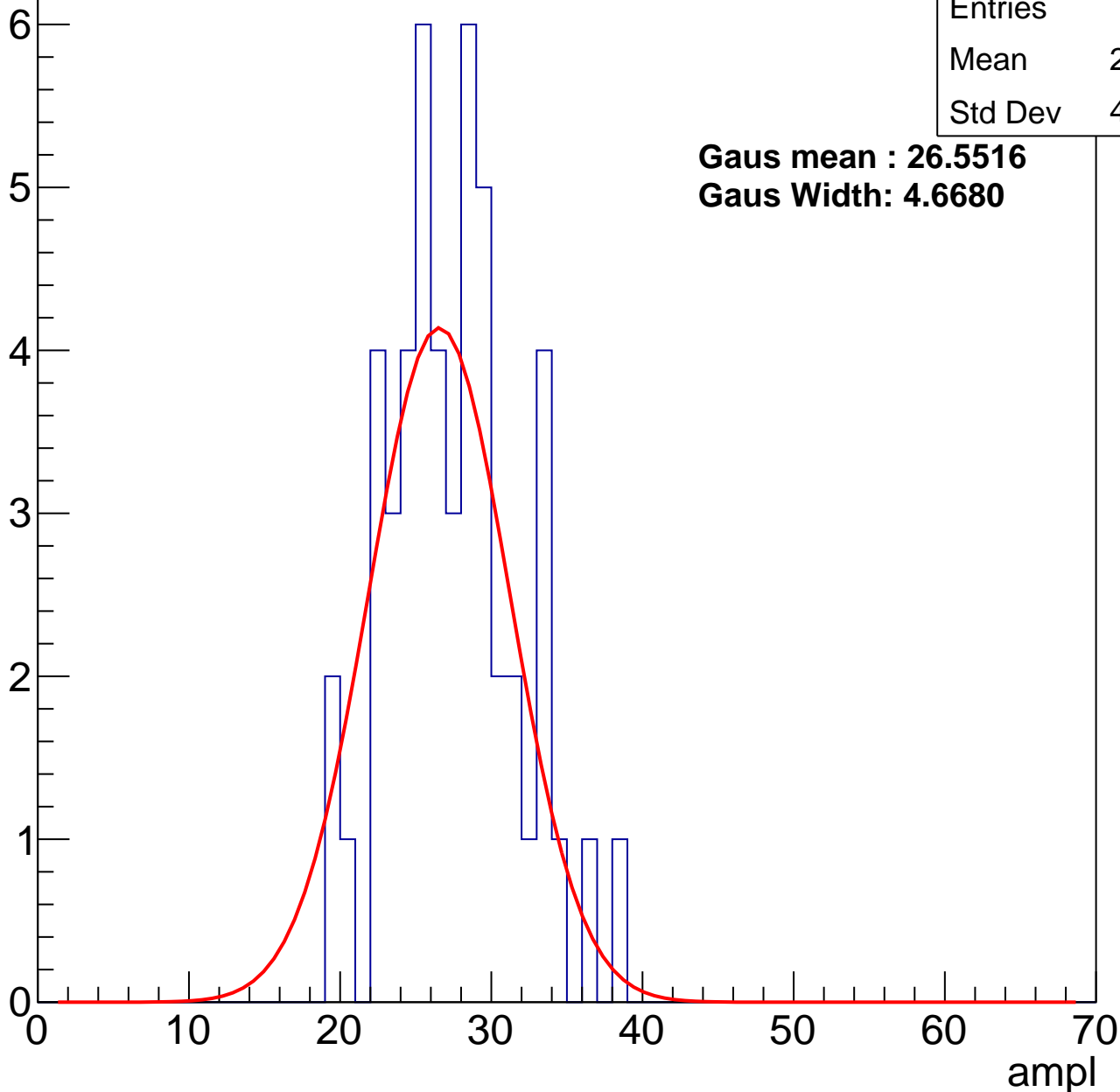
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	27.06
Std Dev	4.207

**Gaus mean : 26.5516**

**Gaus Width: 4.6680**



# B1L102S, U24-ch56, adc1

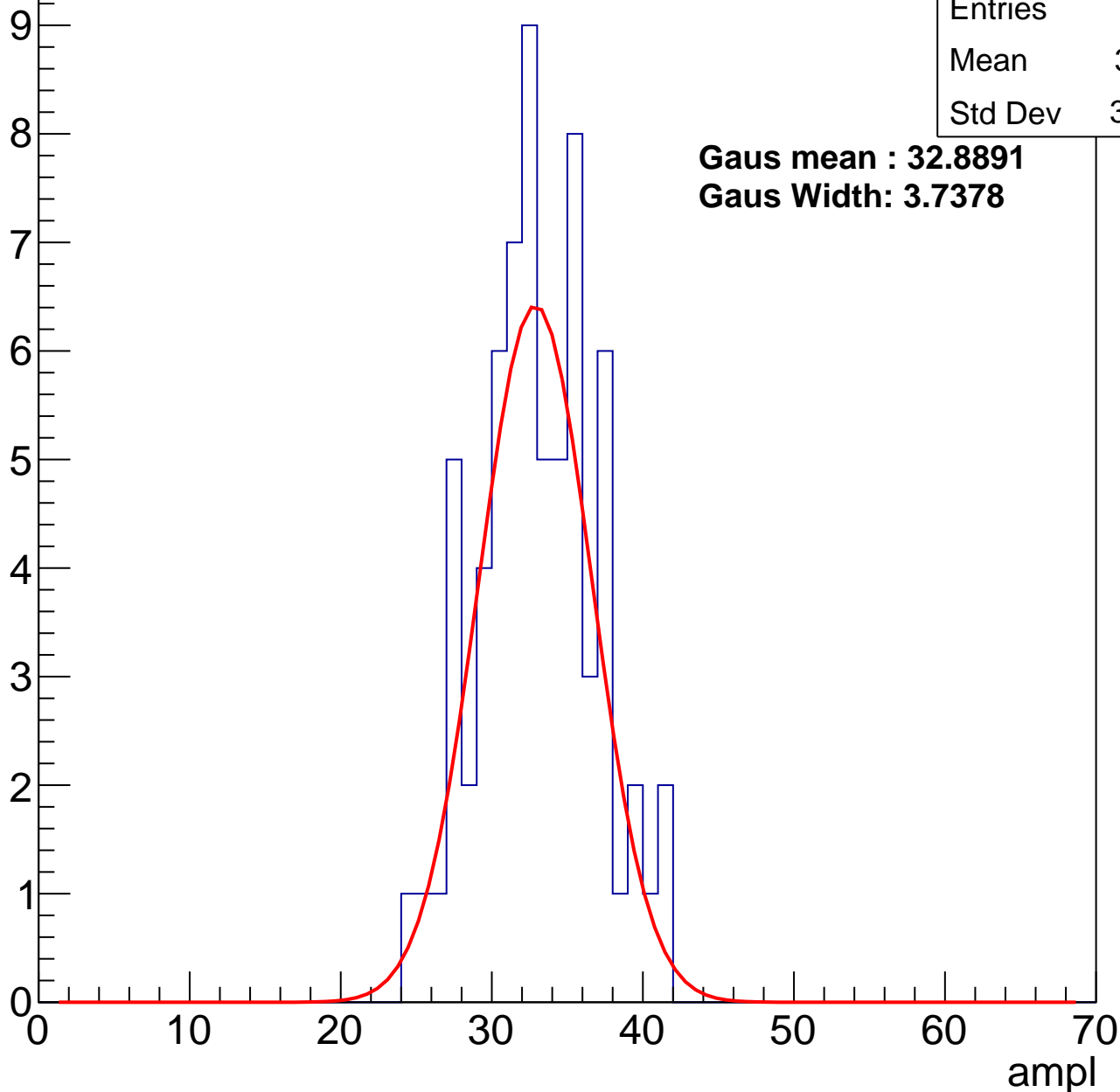
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	32.61
Std Dev	3.812

**Gaus mean : 32.8891**

**Gaus Width: 3.7378**



# B1L102S, U24-ch56, adc2

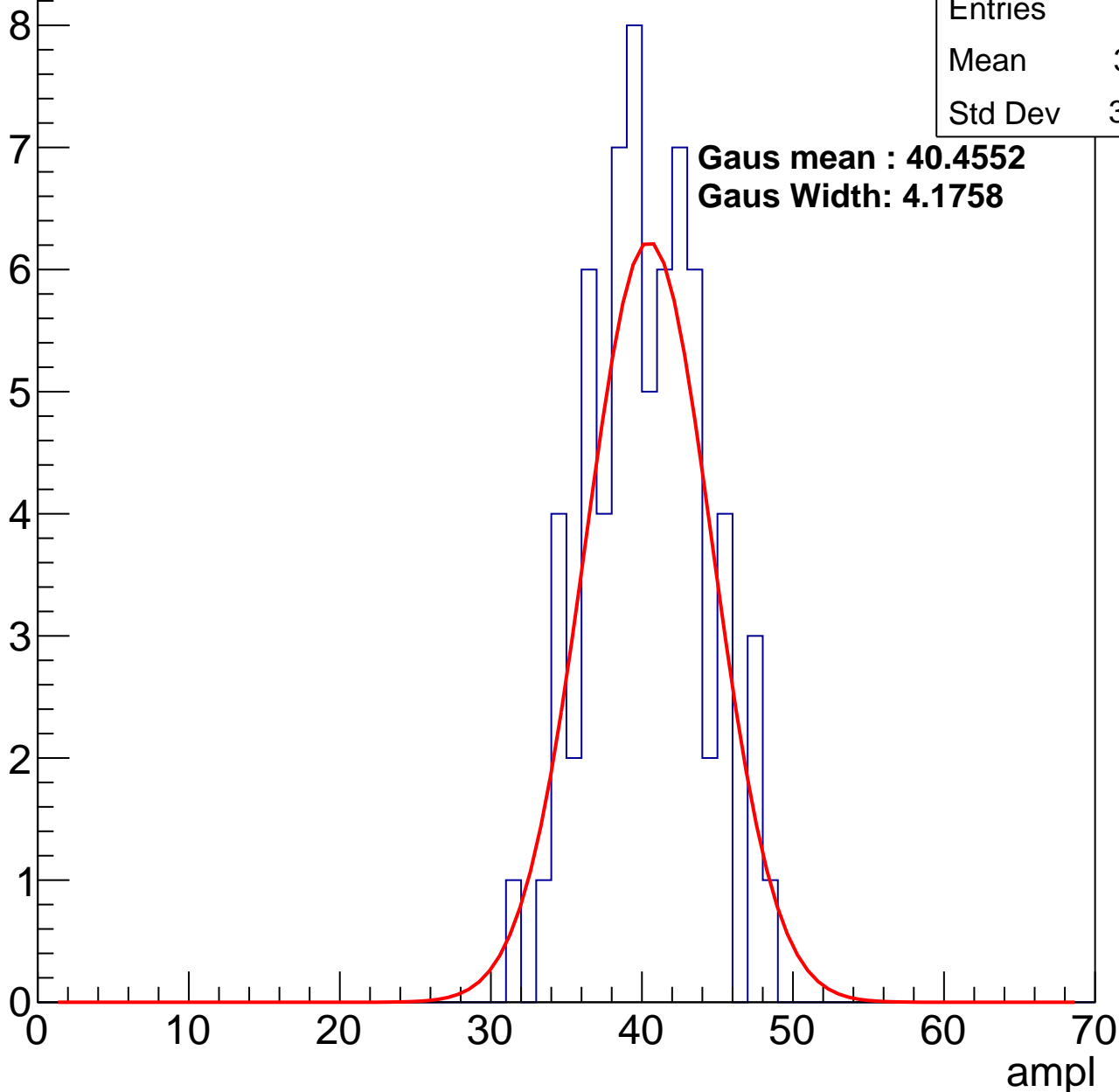
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	39.81
Std Dev	3.707

**Gaus mean : 40.4552**

**Gaus Width: 4.1758**

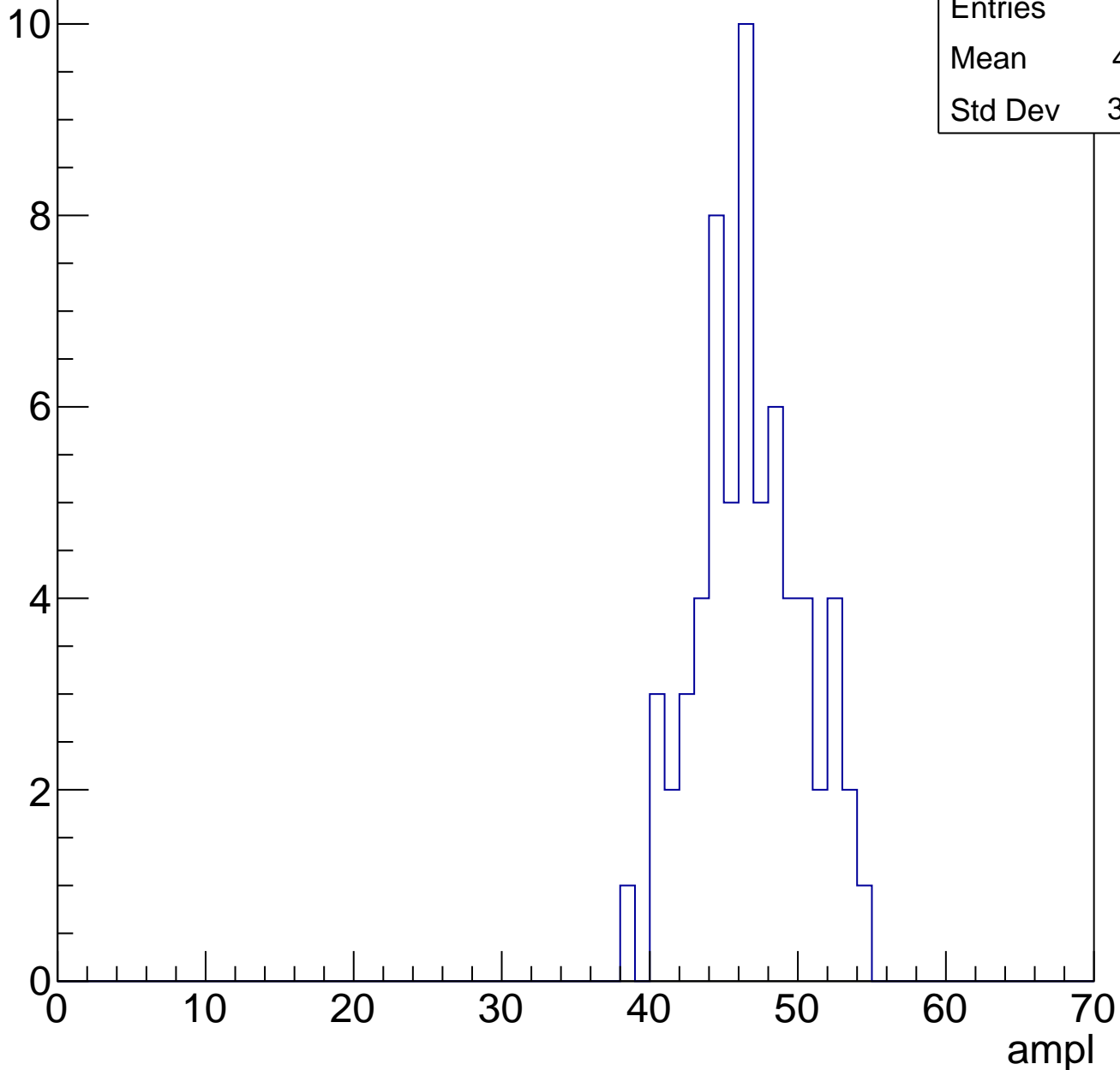


# B1L102S, U24-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	64
Mean	46.31
Std Dev	3.583

Entry

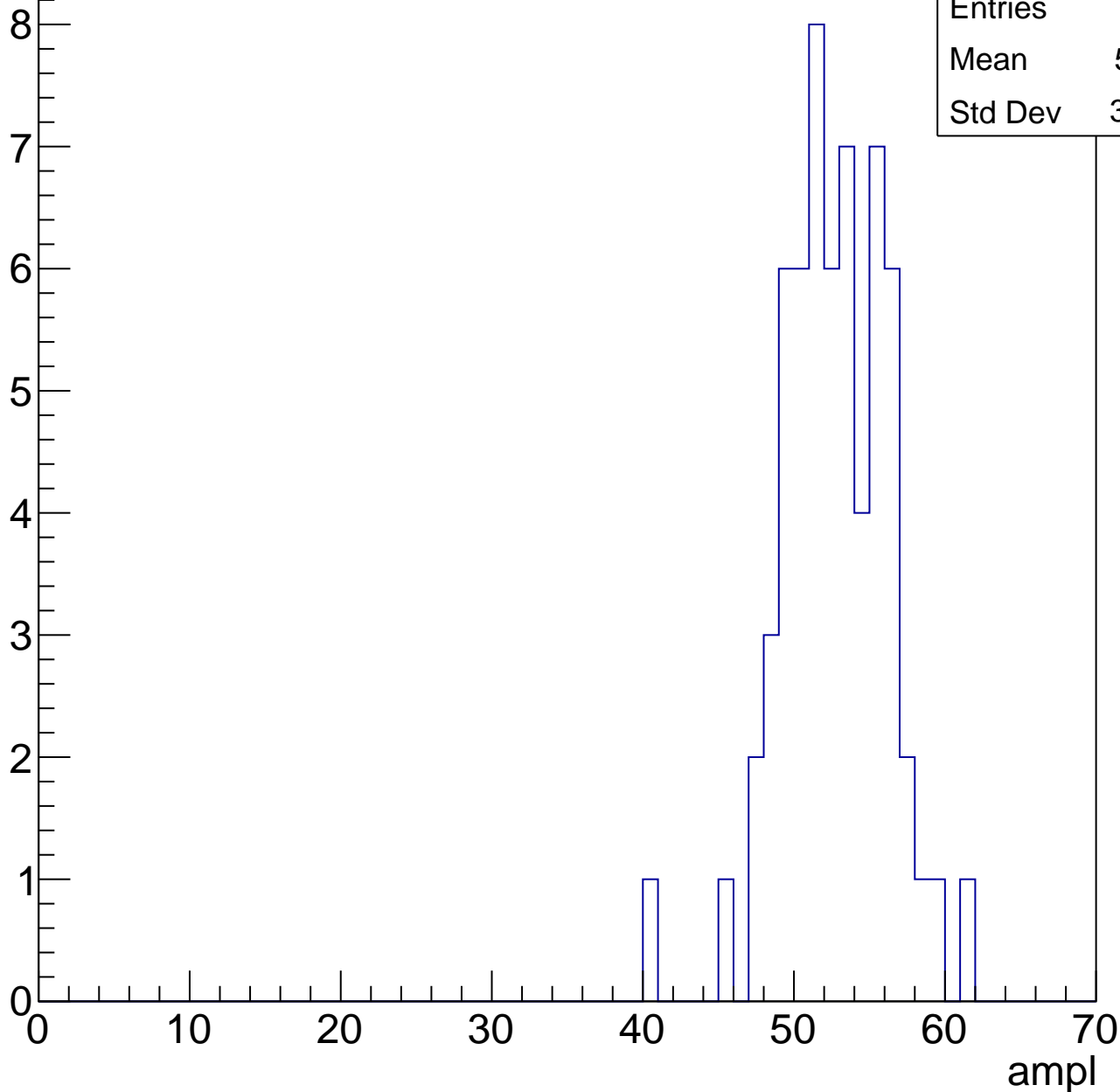


# B1L102S, U24-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	52.21
Std Dev	3.529

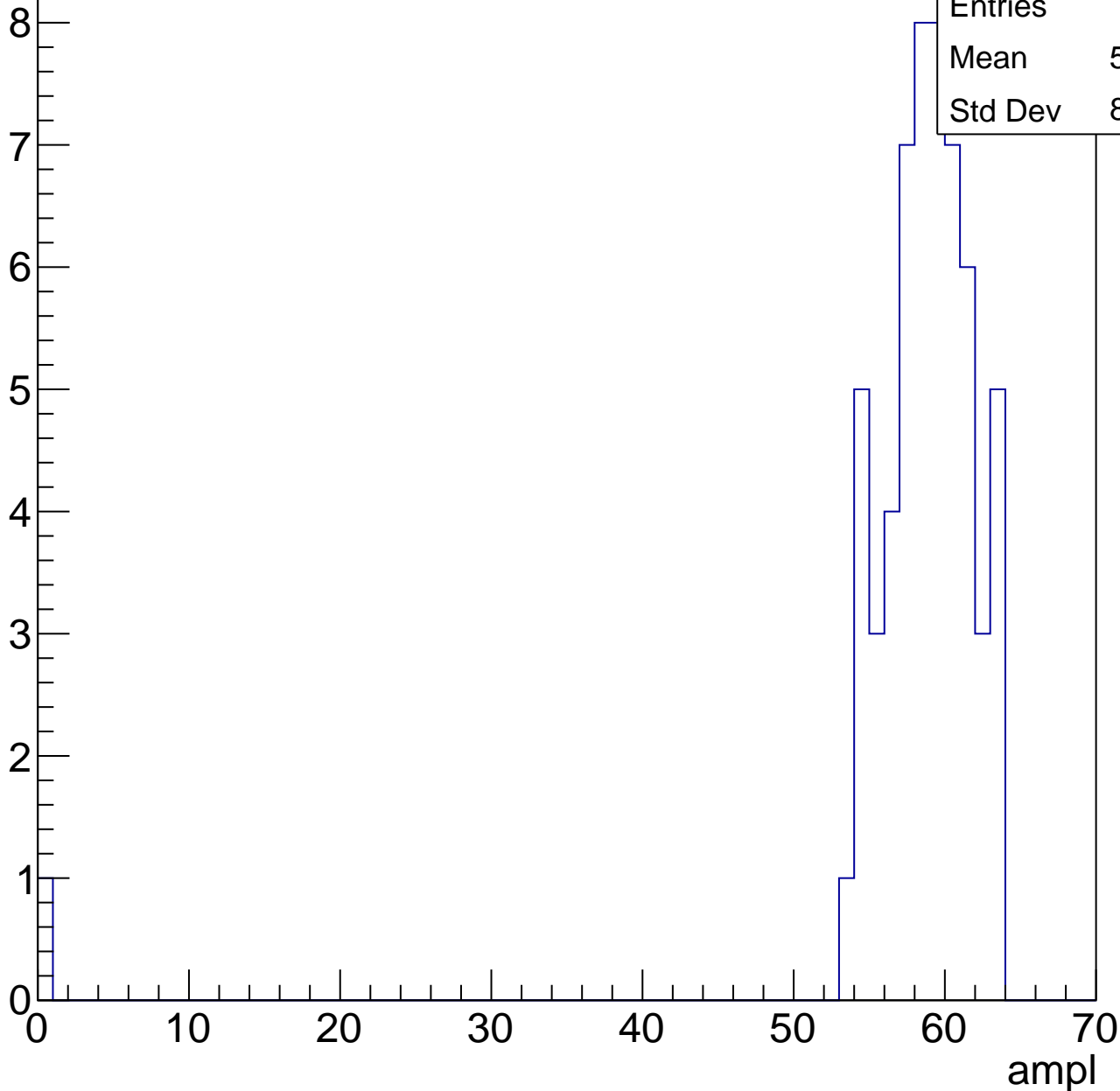


# B1L102S, U24-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.48
Std Dev	8.058

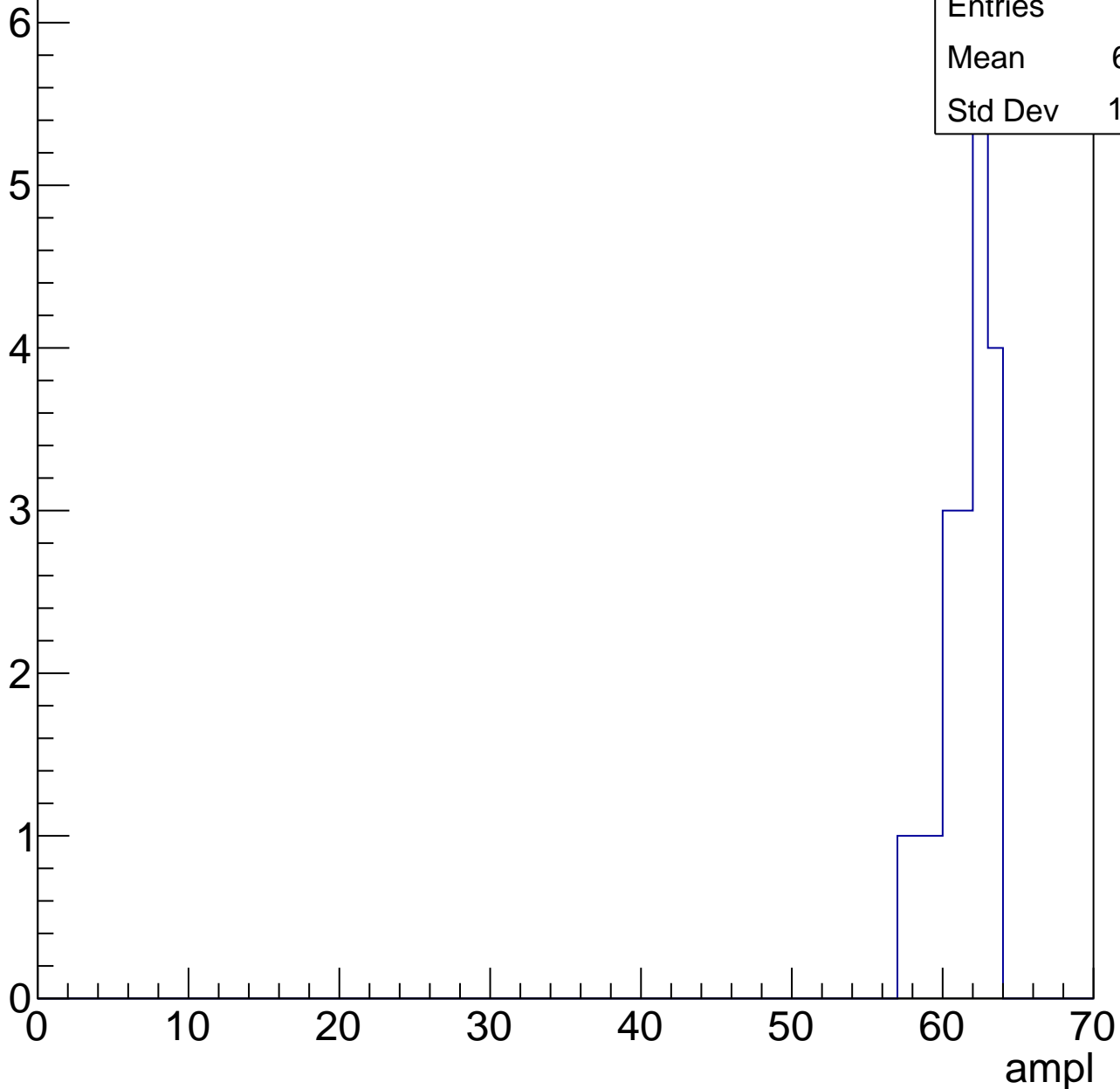


# B1L102S, U24-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.11
Std Dev	1.683





# B1L102S, U24-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch57, adc0

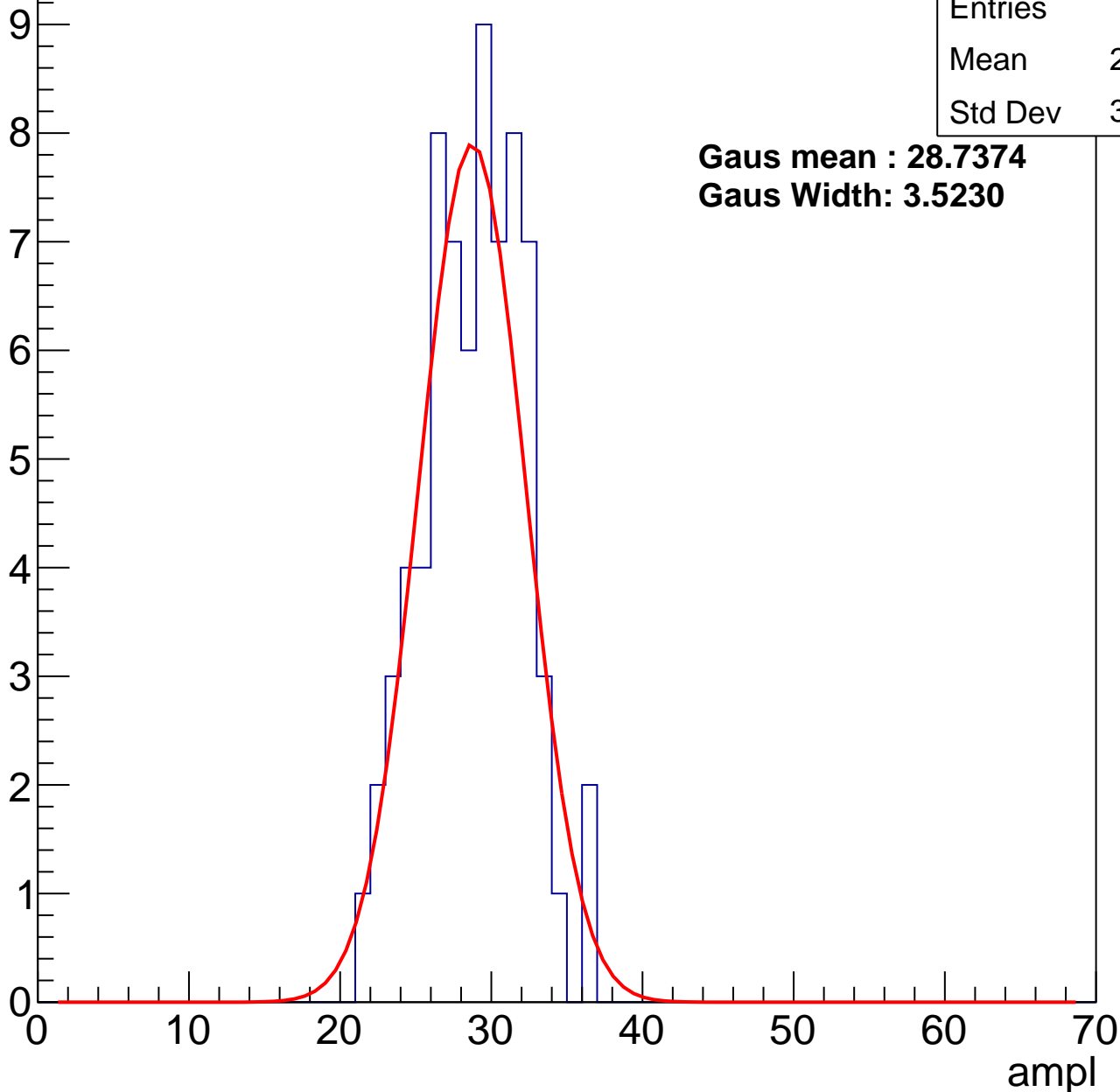
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.38
Std Dev	3.302

**Gaus mean : 28.7374**

**Gaus Width: 3.5230**



# B1L102S, U24-ch57, adc1

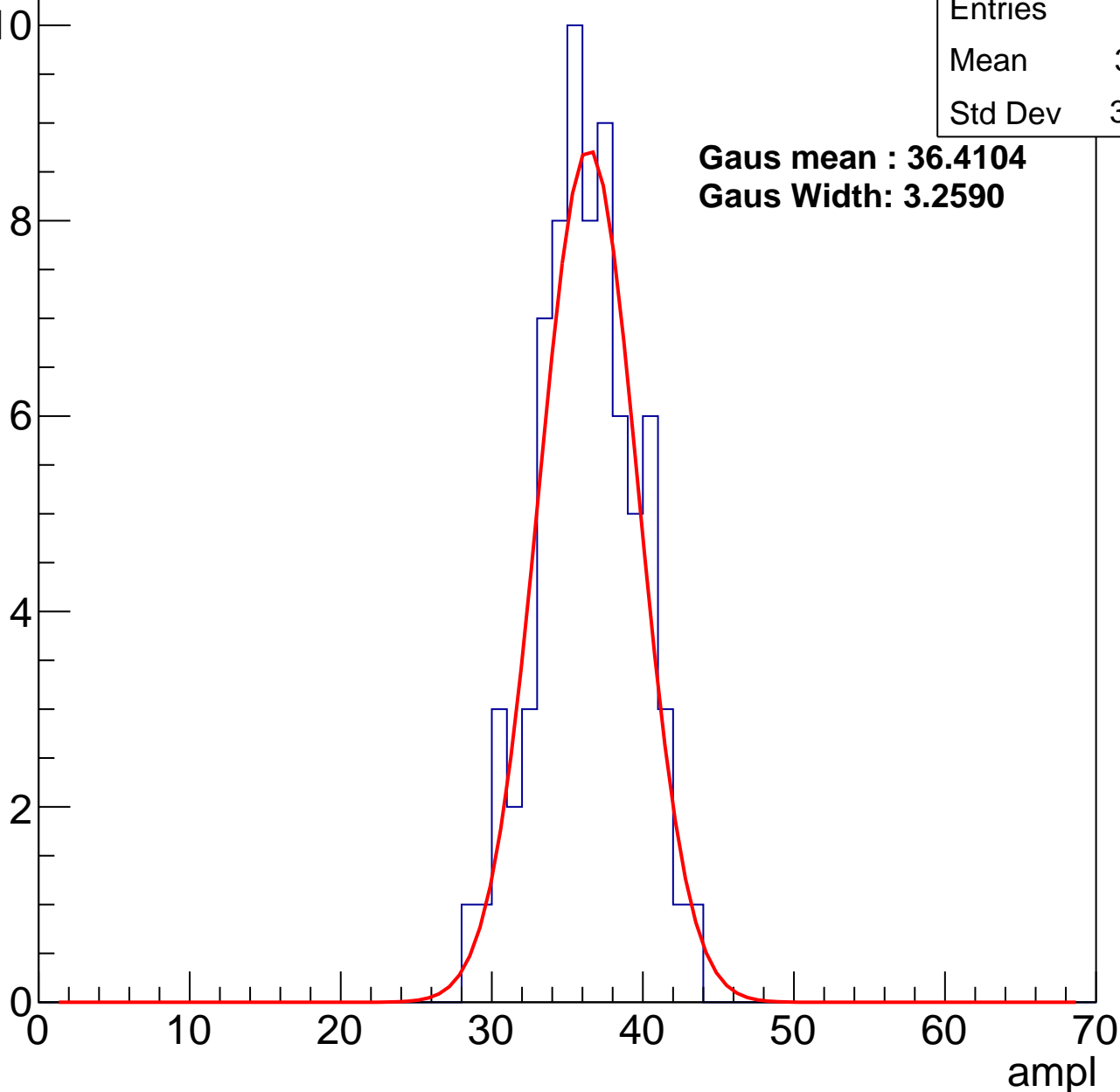
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.81
Std Dev	3.195

**Gaus mean : 36.4104**

**Gaus Width: 3.2590**



# B1L102S, U24-ch57, adc2

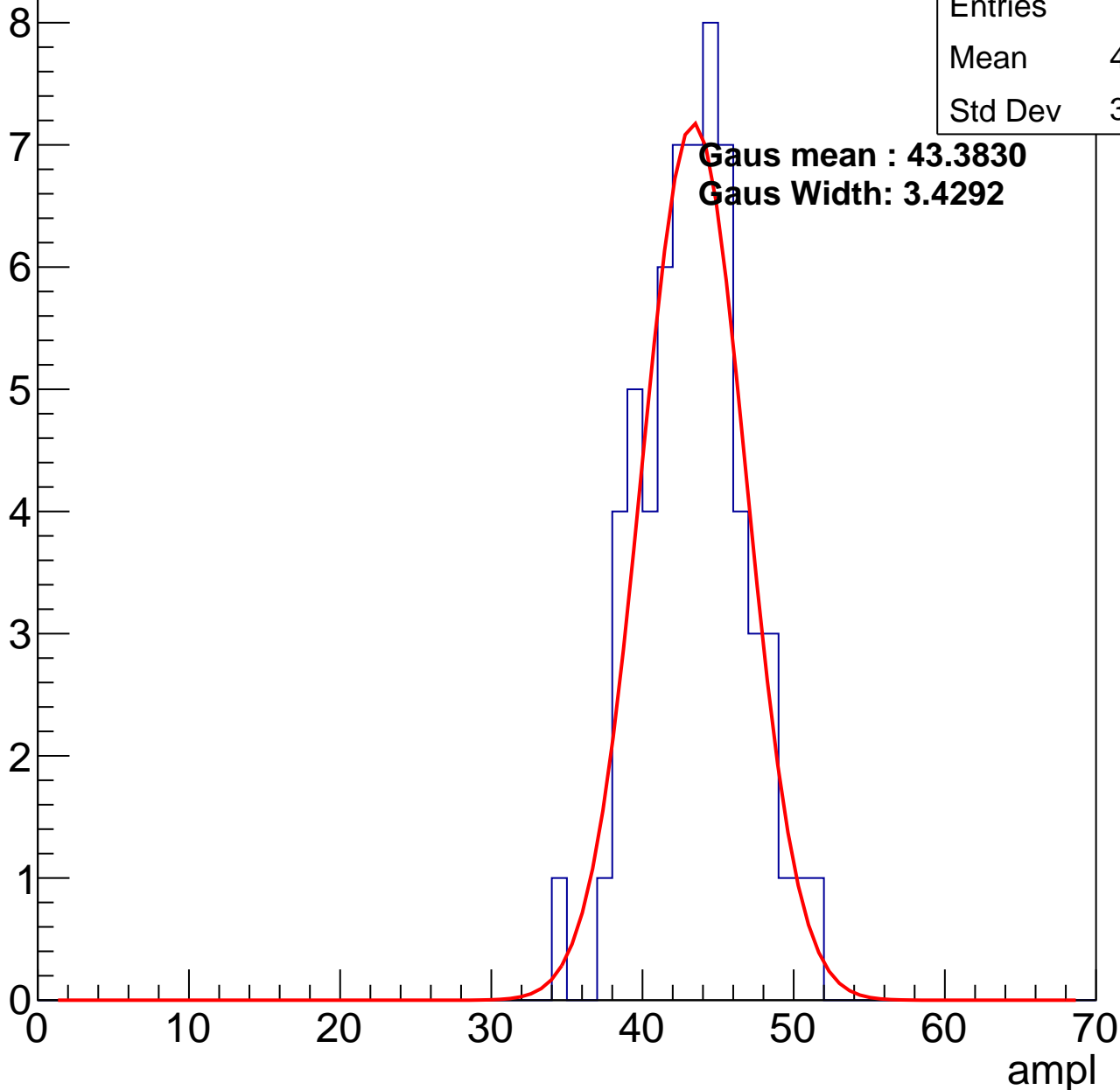
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.94
Std Dev	3.352

**Gaus mean : 43.3830**

**Gaus Width: 3.4292**

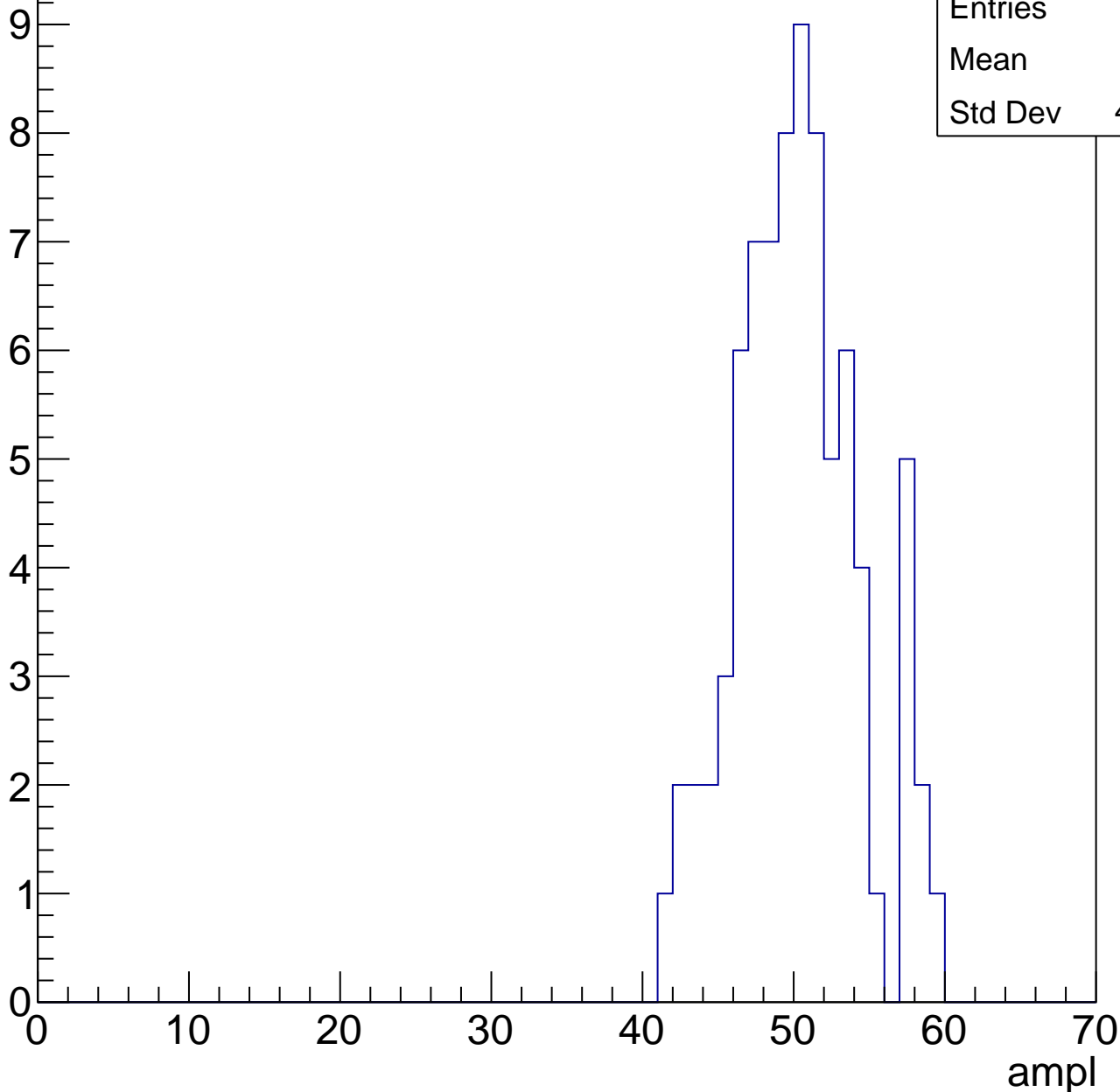


# B1L102S, U24-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

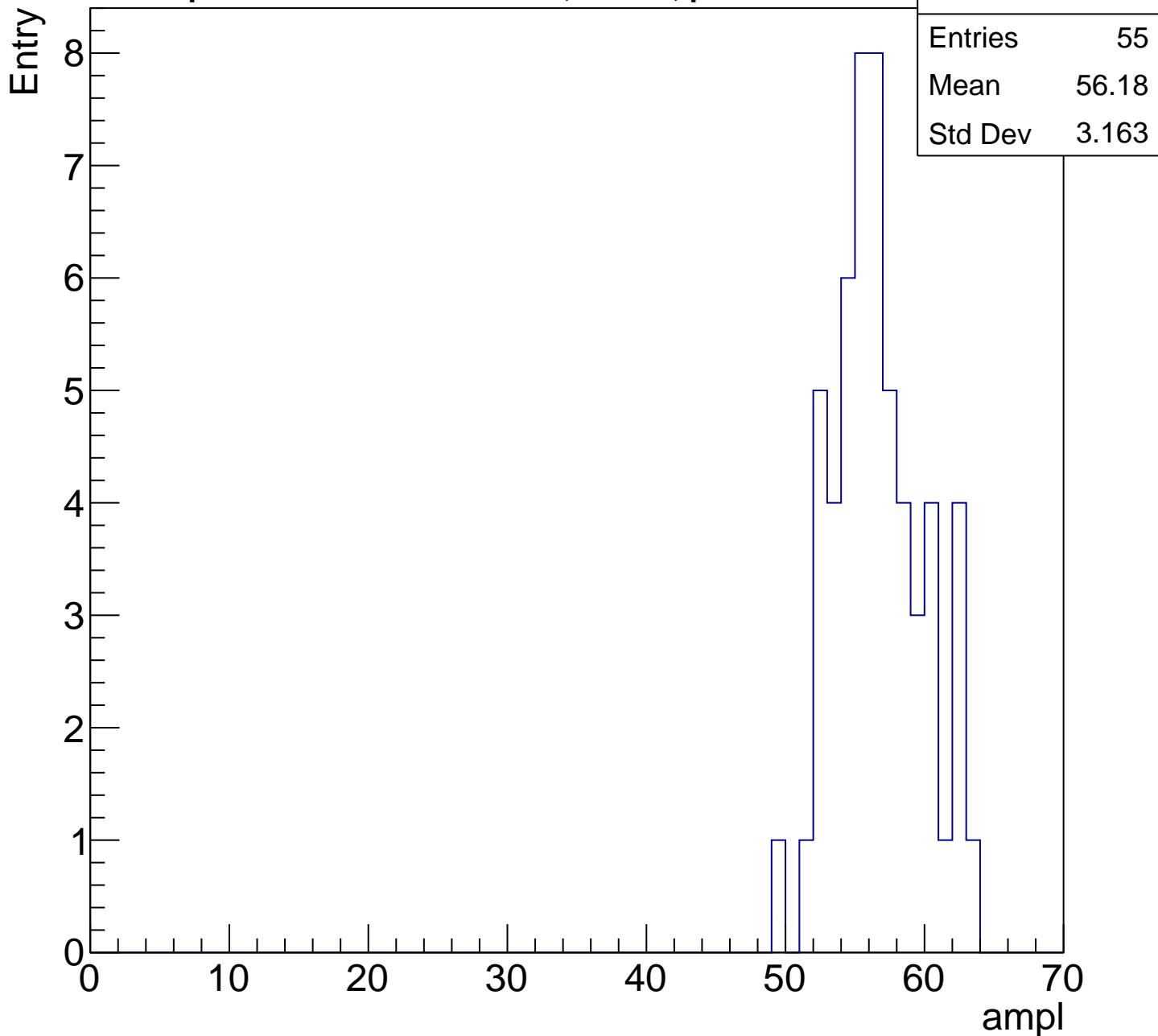
Entry

Entries	79
Mean	49.8
Std Dev	4.011



# B1L102S, U24-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

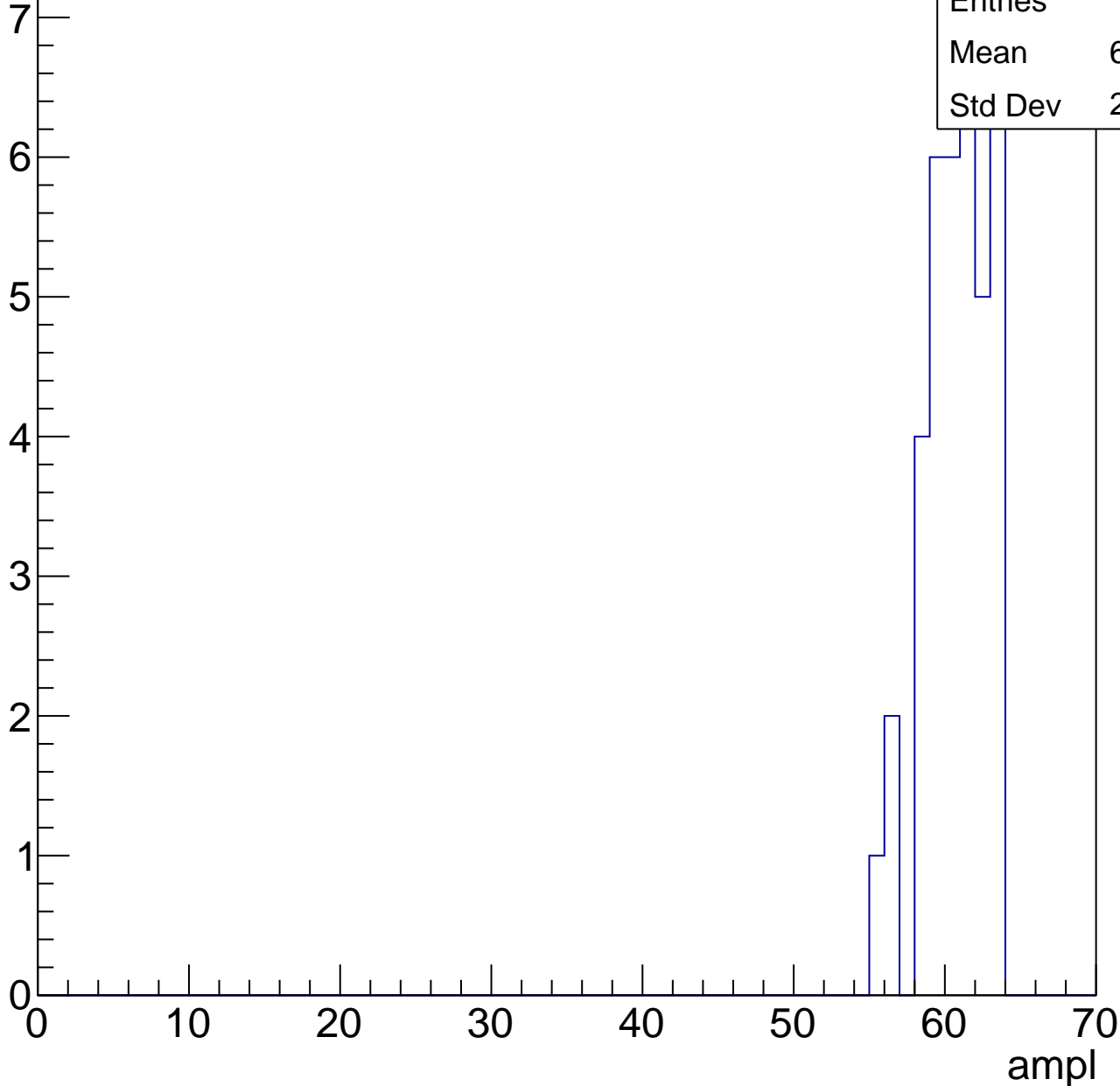


# B1L102S, U24-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	38
Mean	60.29
Std Dev	2.089



# B1L102S, U24-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

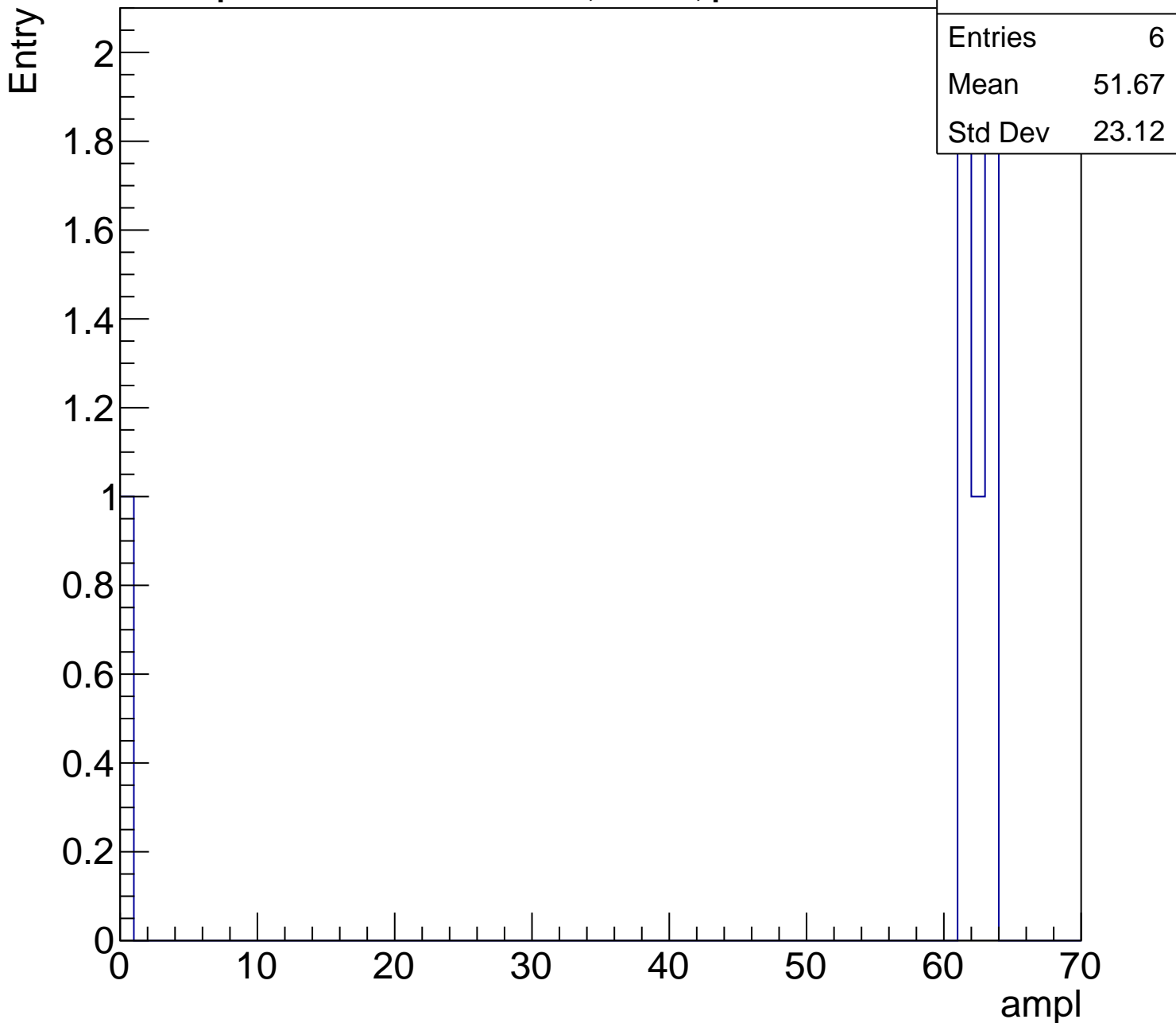
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.67
Std Dev	23.12

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch58, adc0

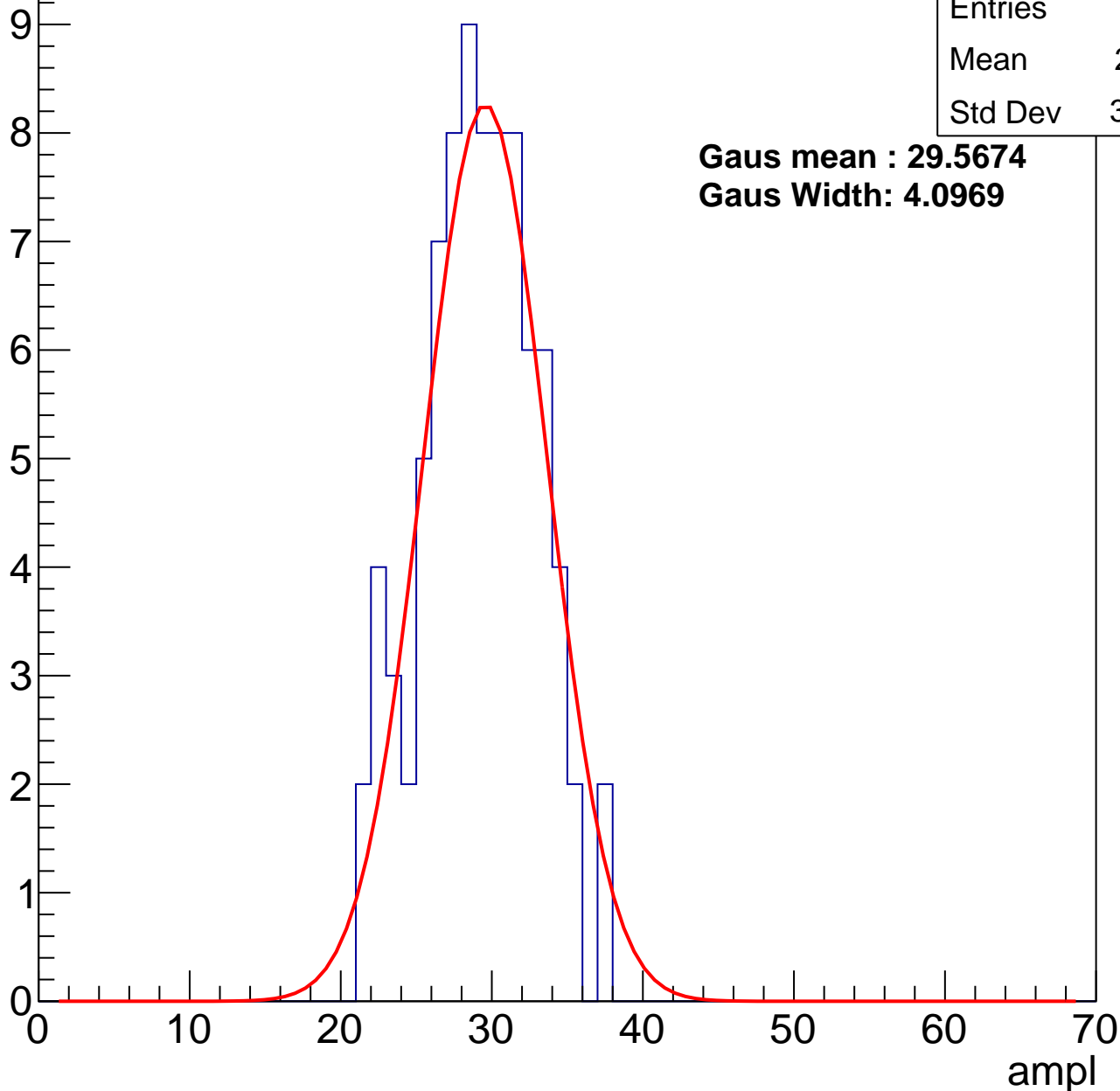
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.71
Std Dev	3.699

**Gaus mean : 29.5674**

**Gaus Width: 4.0969**



# B1L102S, U24-ch58, adc1

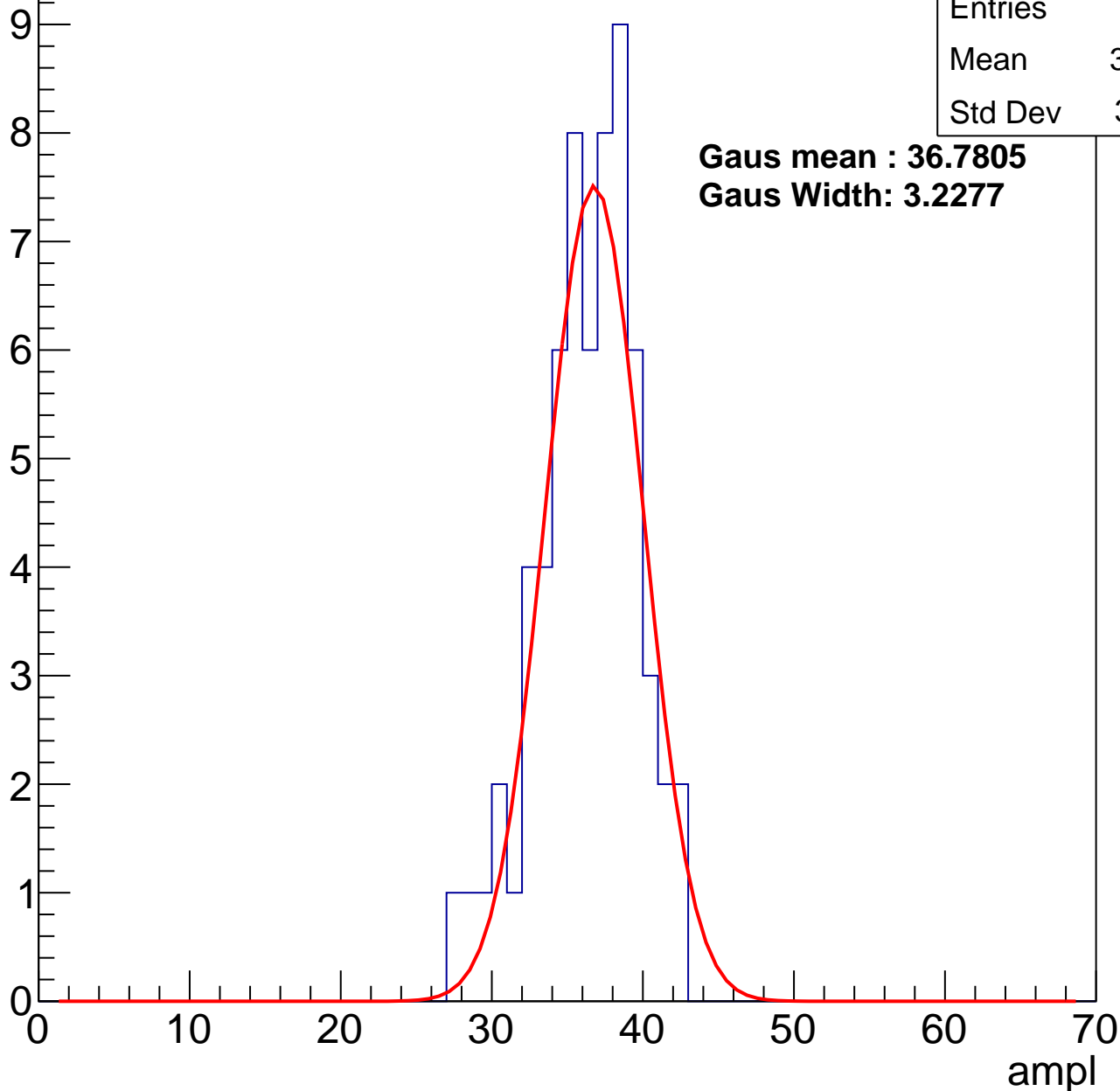
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.83
Std Dev	3.281

**Gaus mean : 36.7805**

**Gaus Width: 3.2277**



# B1L102S, U24-ch58, adc2

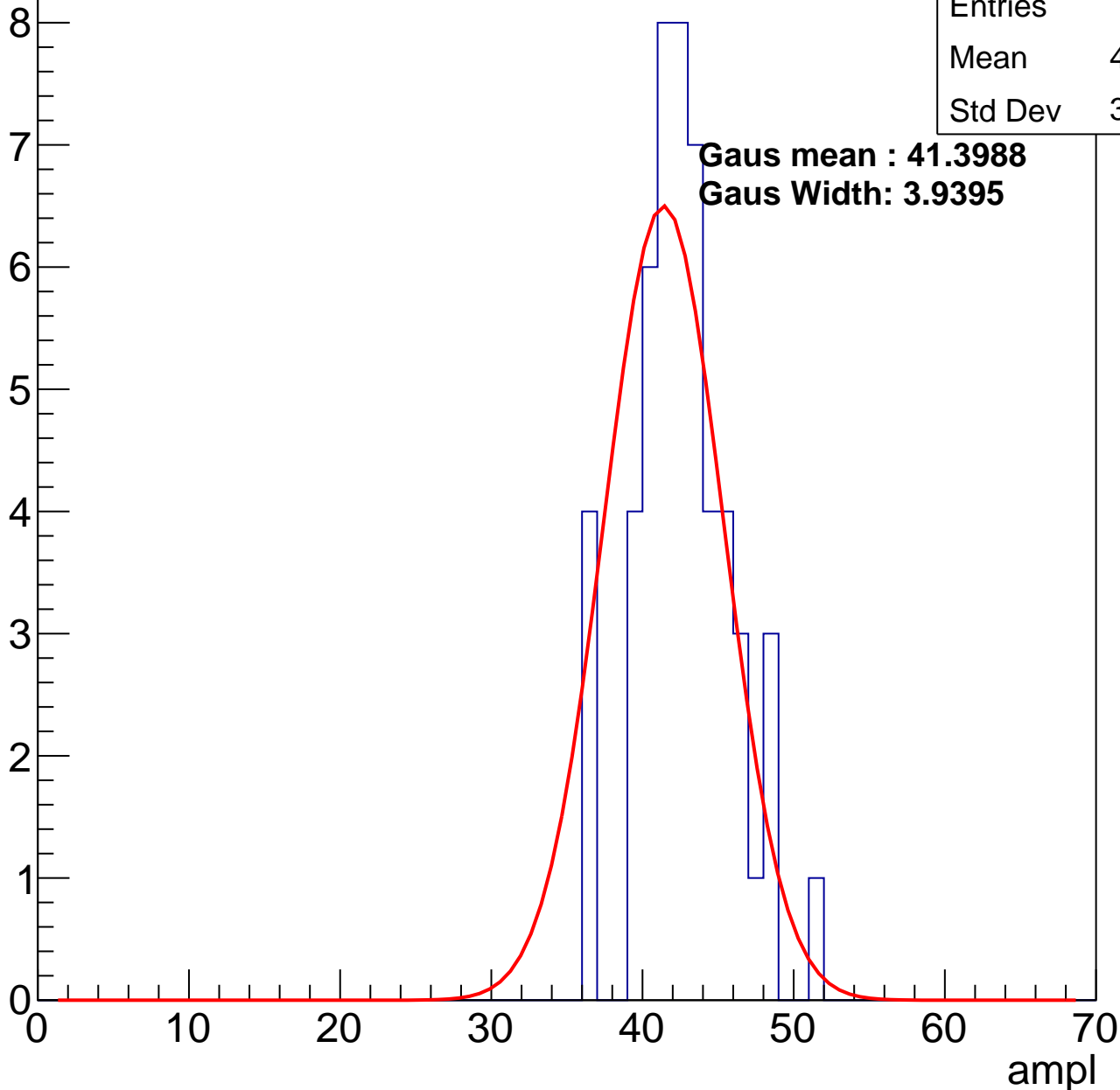
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.28
Std Dev	3.159

**Gaus mean : 41.3988**

**Gaus Width: 3.9395**

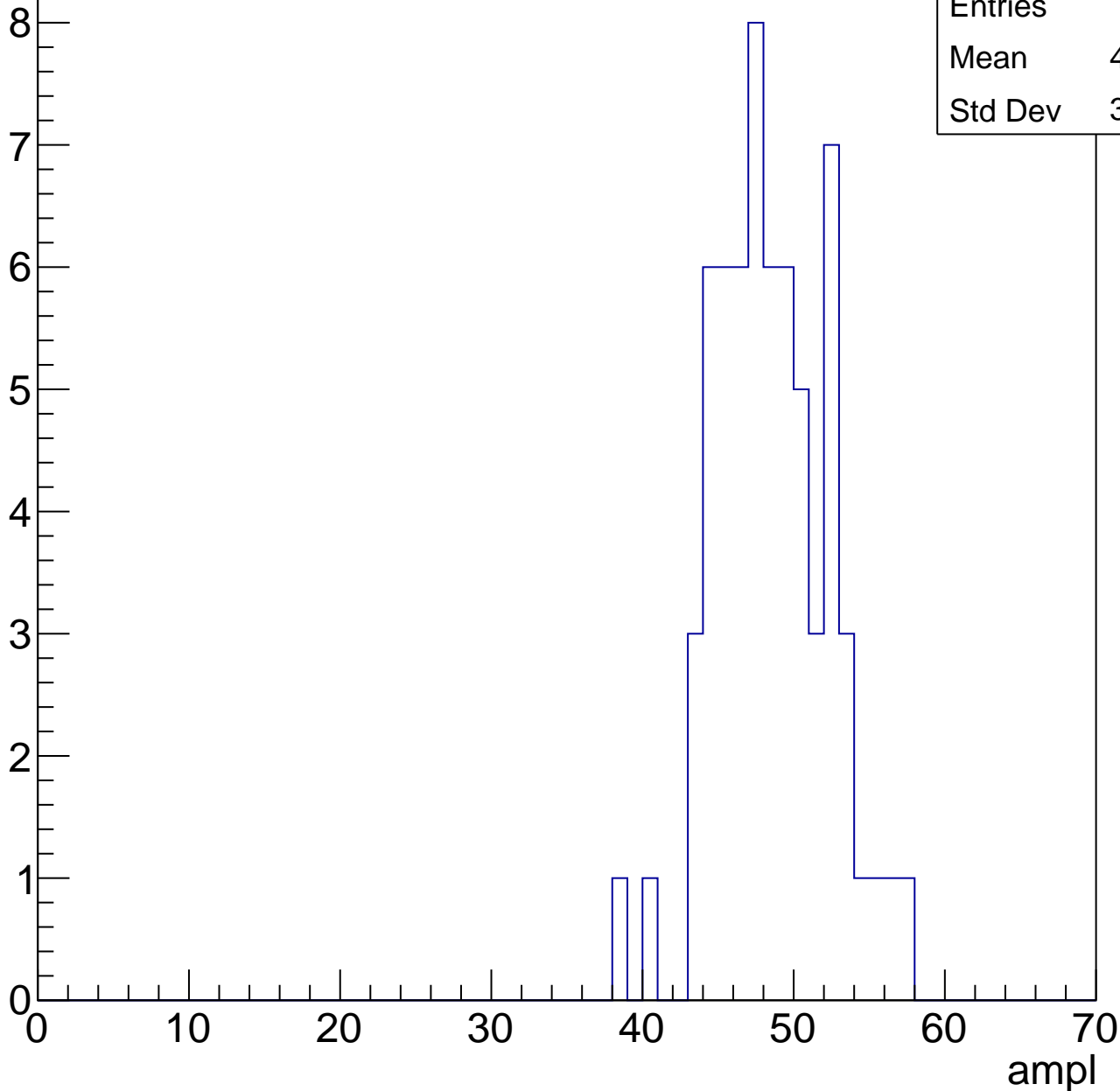


# B1L102S, U24-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.05
Std Dev	3.702

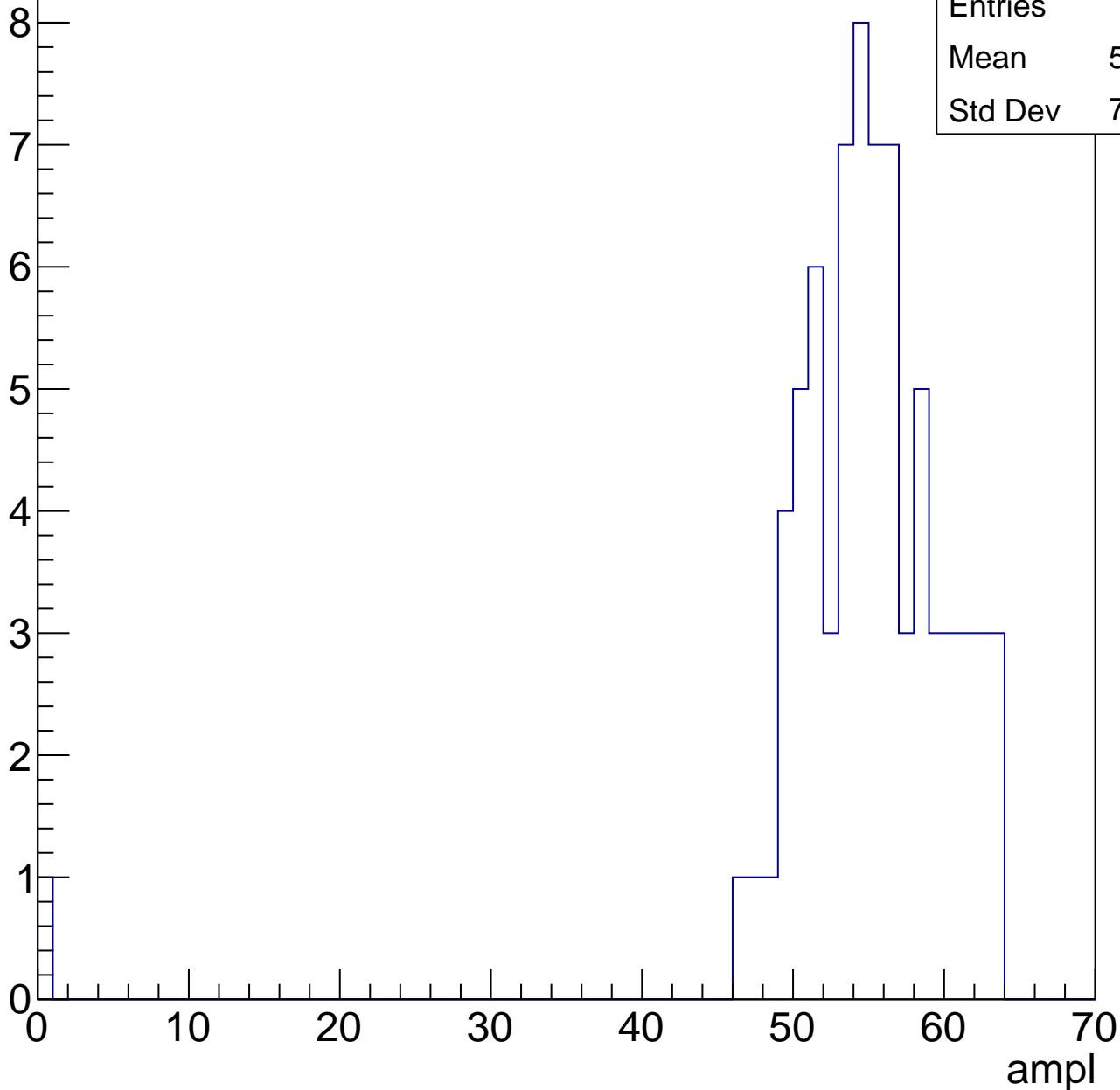


# B1L102S, U24-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

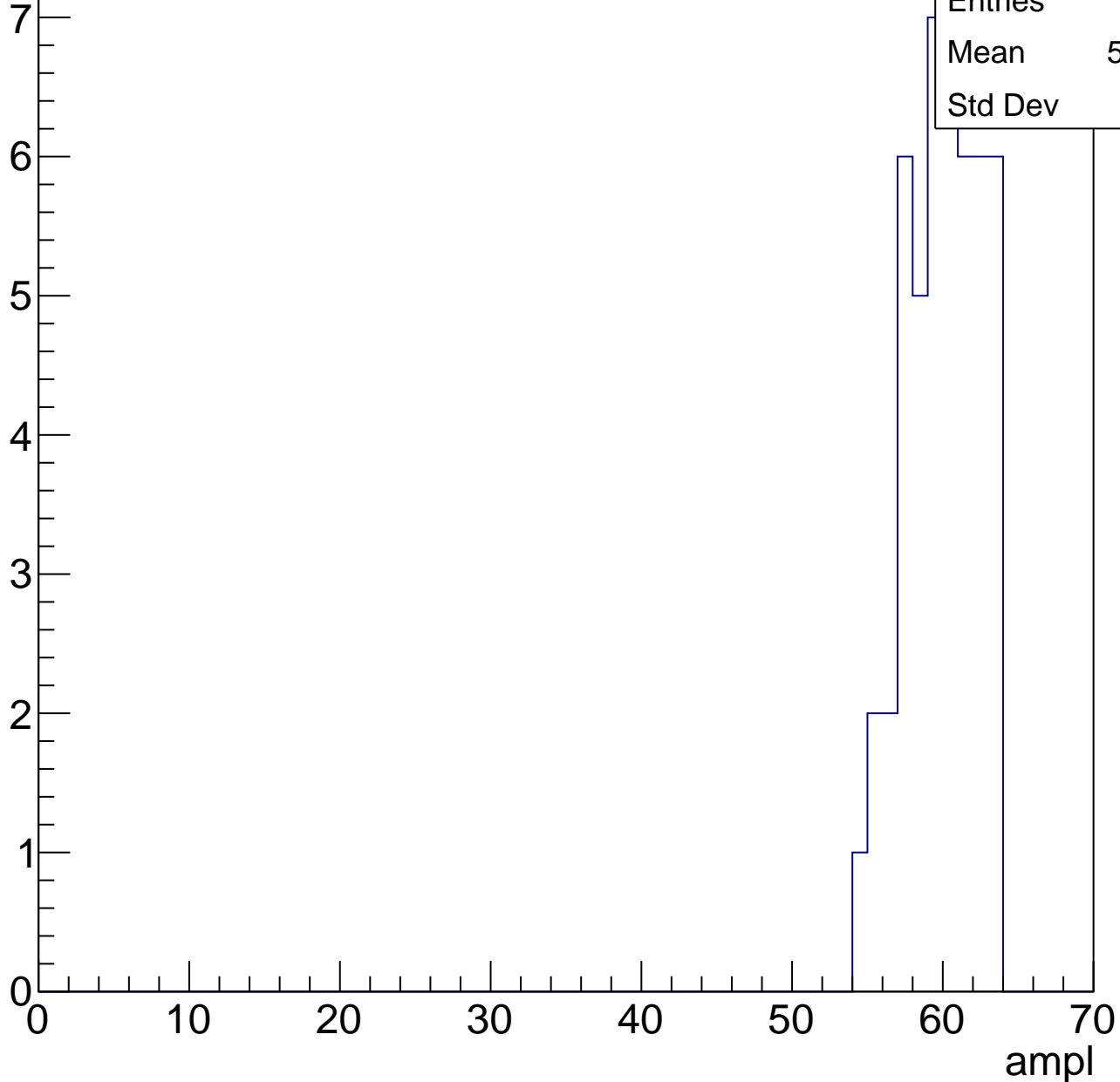
Entries	74
Mean	54.12
Std Dev	7.555



# B1L102S, U24-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	48
Mean	59.52
Std Dev	2.38

# B1L102S, U24-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U24-ch59, adc0

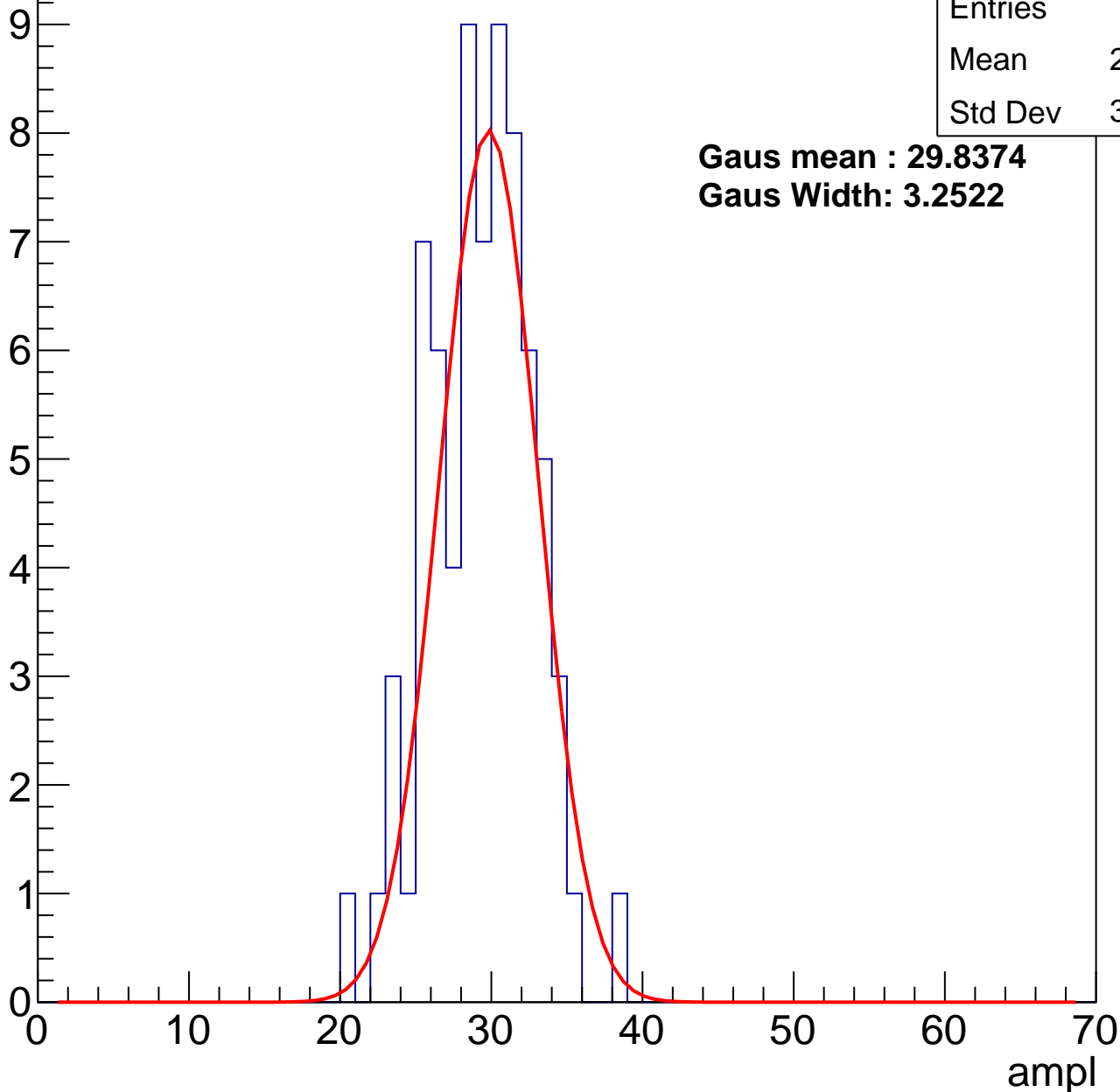
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.88
Std Dev	3.374

**Gaus mean : 29.8374**

**Gaus Width: 3.2522**



# B1L102S, U24-ch59, adc1

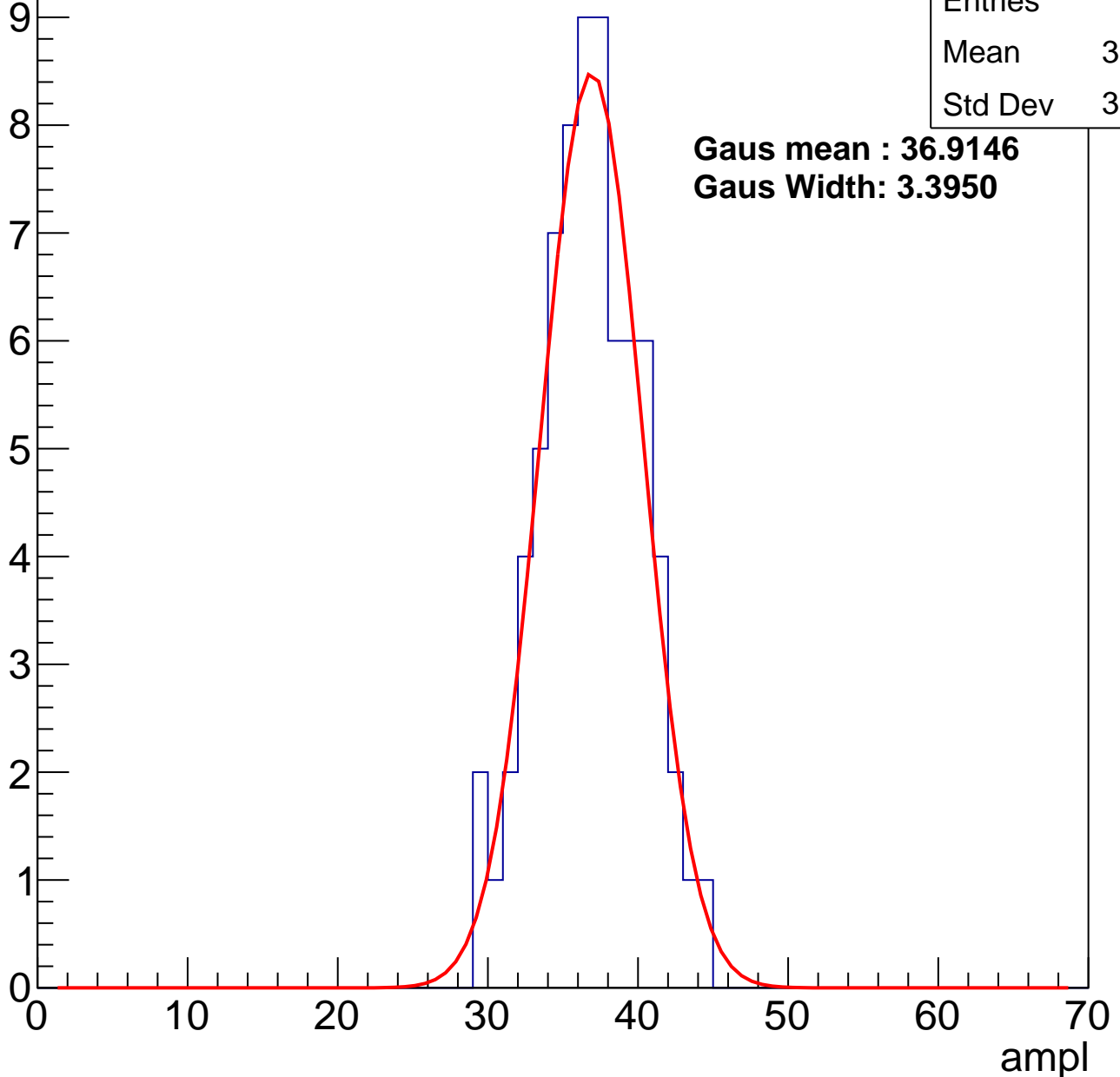
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.37
Std Dev	3.283

**Gaus mean : 36.9146**

**Gaus Width: 3.3950**



# B1L102S, U24-ch59, adc2

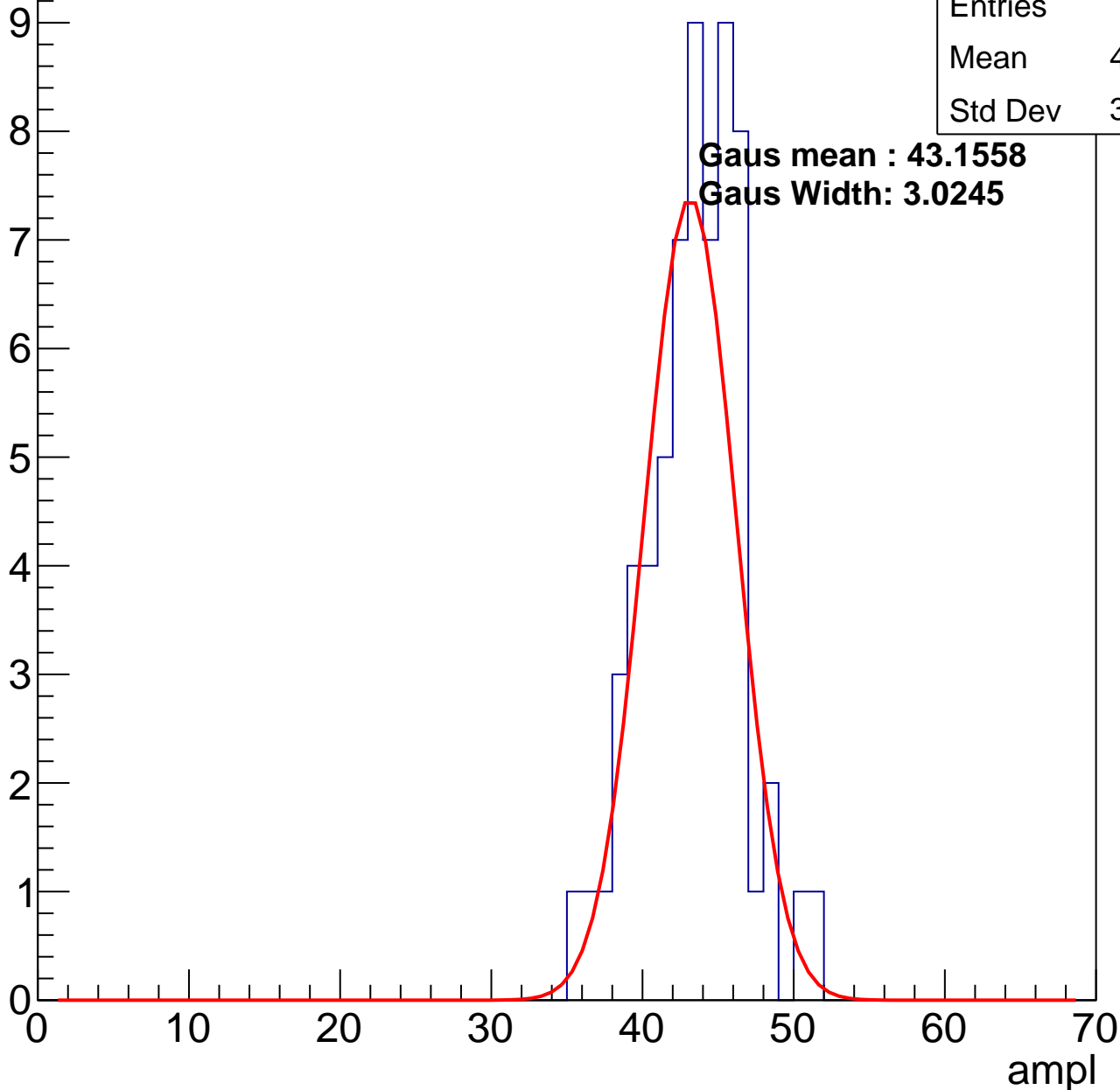
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.95
Std Dev	3.174

**Gaus mean : 43.1558**

**Gaus Width: 3.0245**

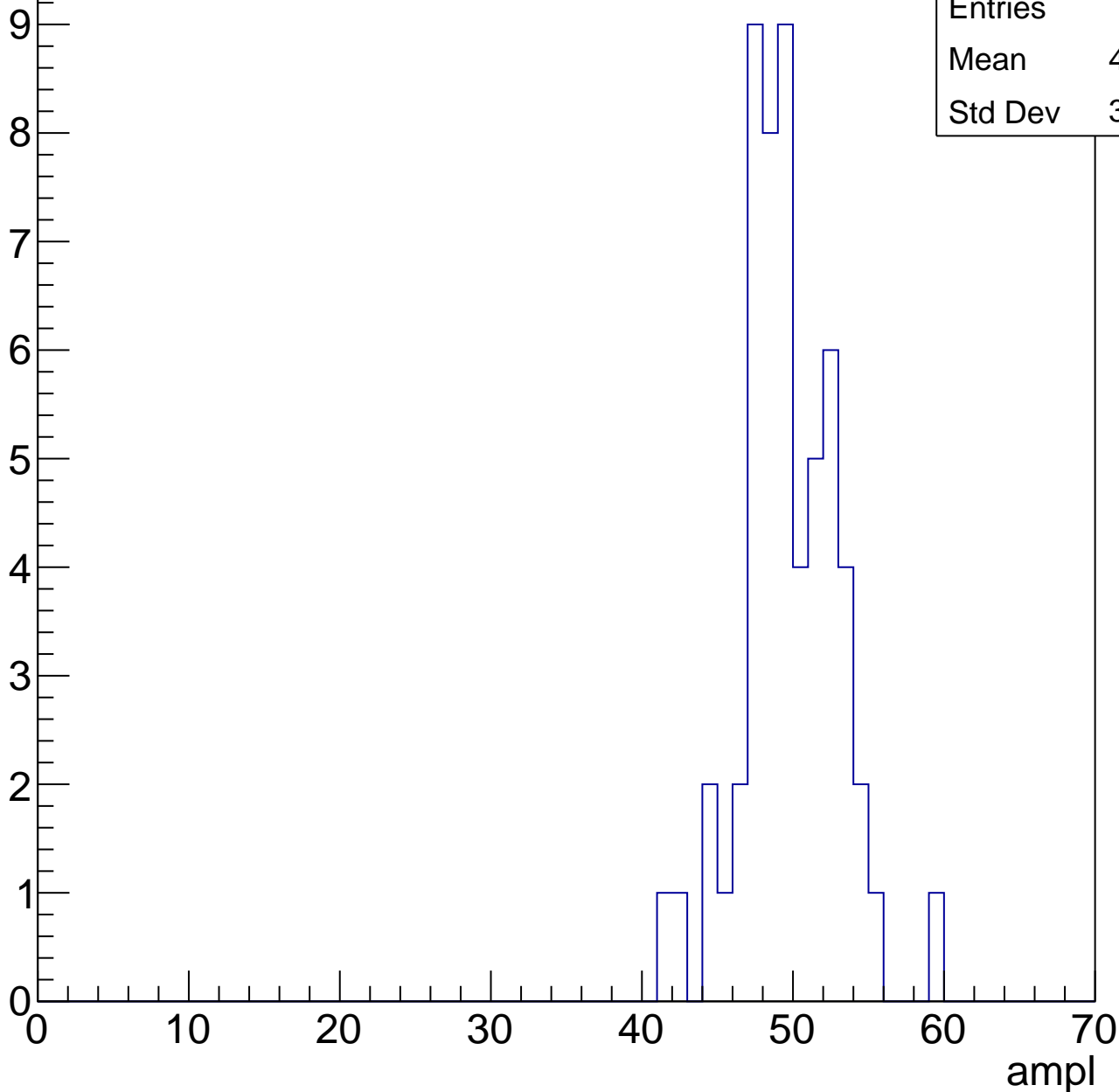


# B1L102S, U24-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.23
Std Dev	3.179

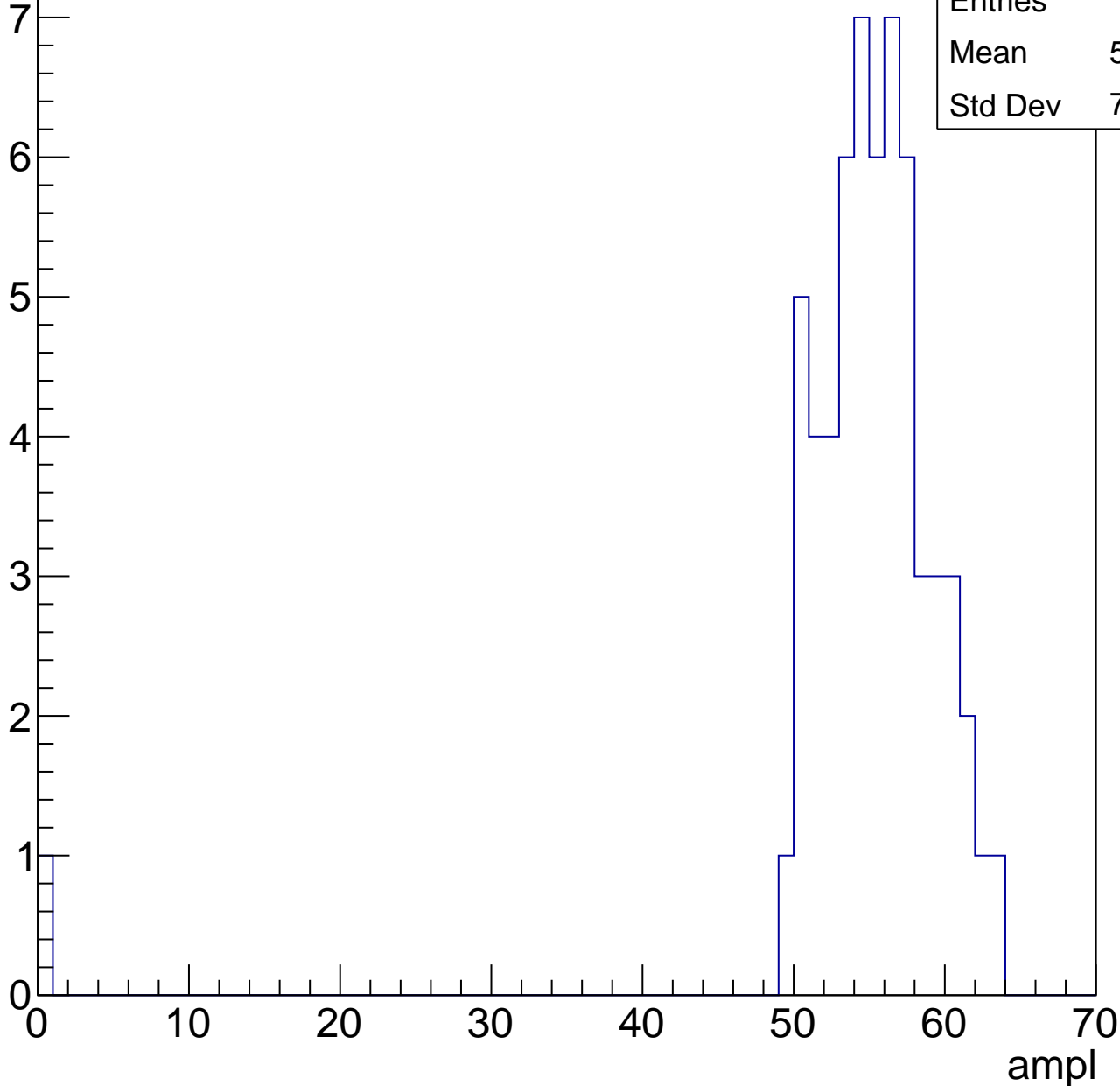


# B1L102S, U24-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.15
Std Dev	7.789



# B1L102S, U24-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	48
Mean	59.46
Std Dev	2.491

ampl

10

20

30

40

50

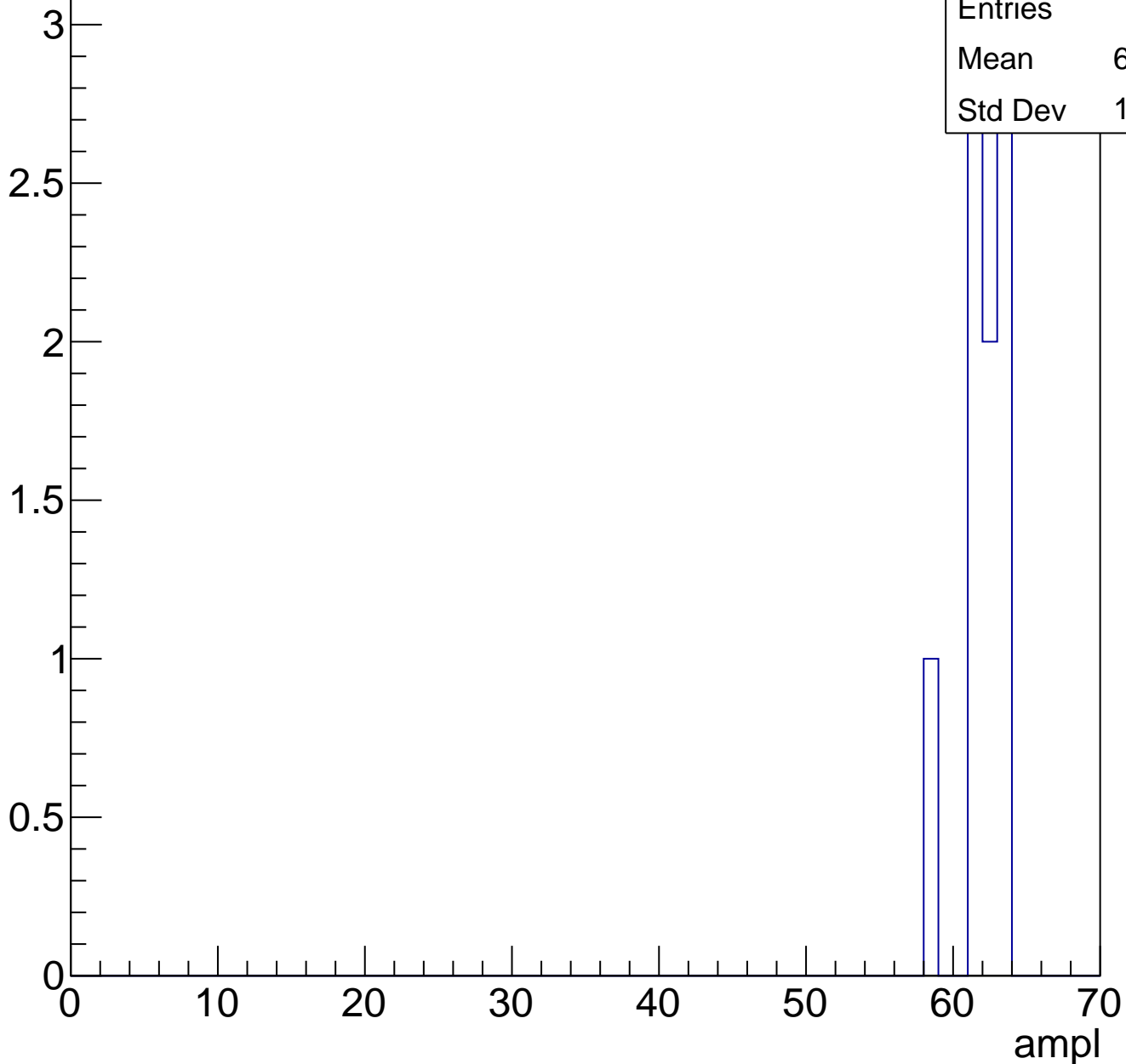
60

70

# B1L102S, U24-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch60, adc0

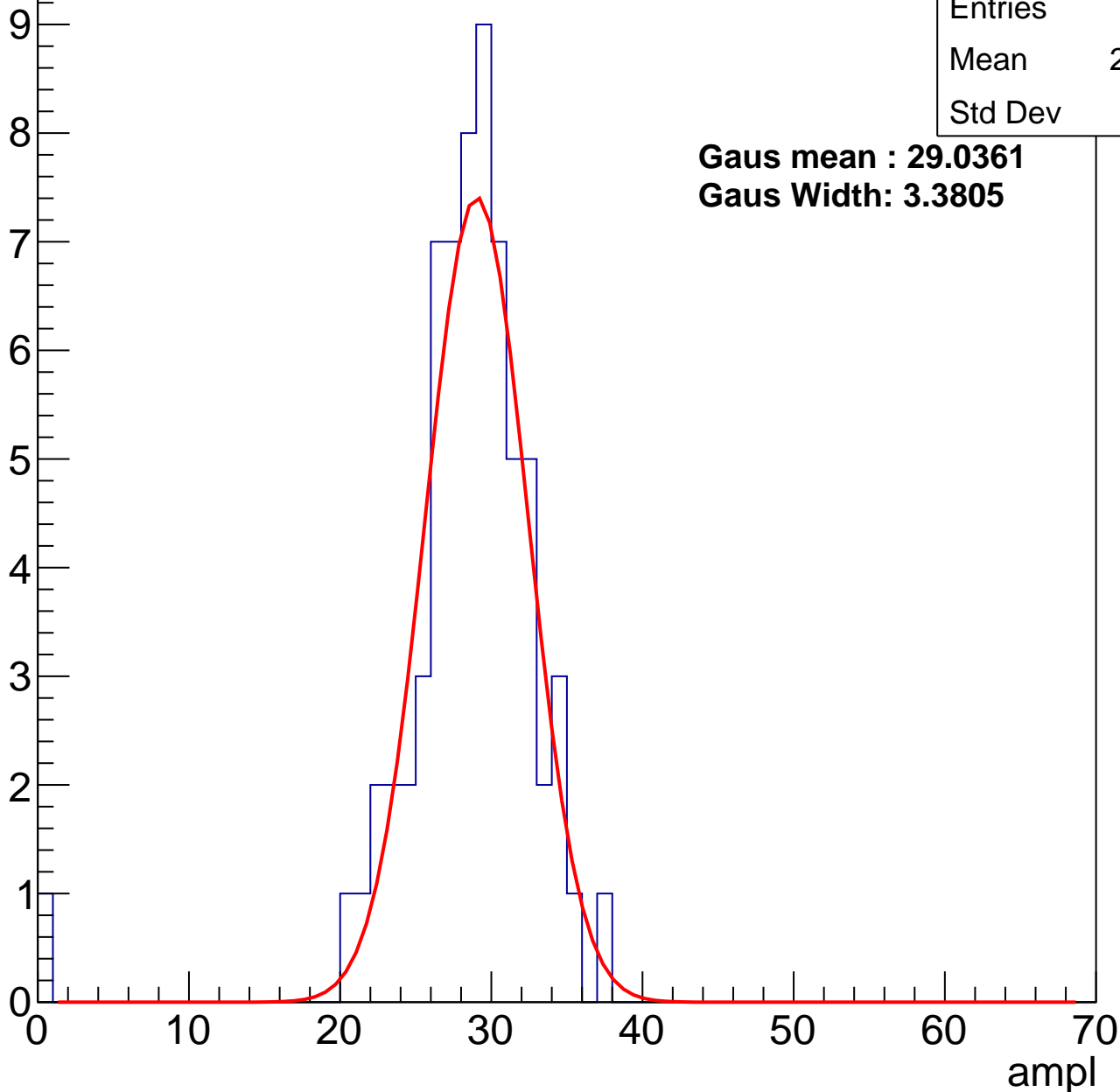
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.99
Std Dev	4.83

**Gaus mean : 29.0361**

**Gaus Width: 3.3805**



# B1L102S, U24-ch60, adc1

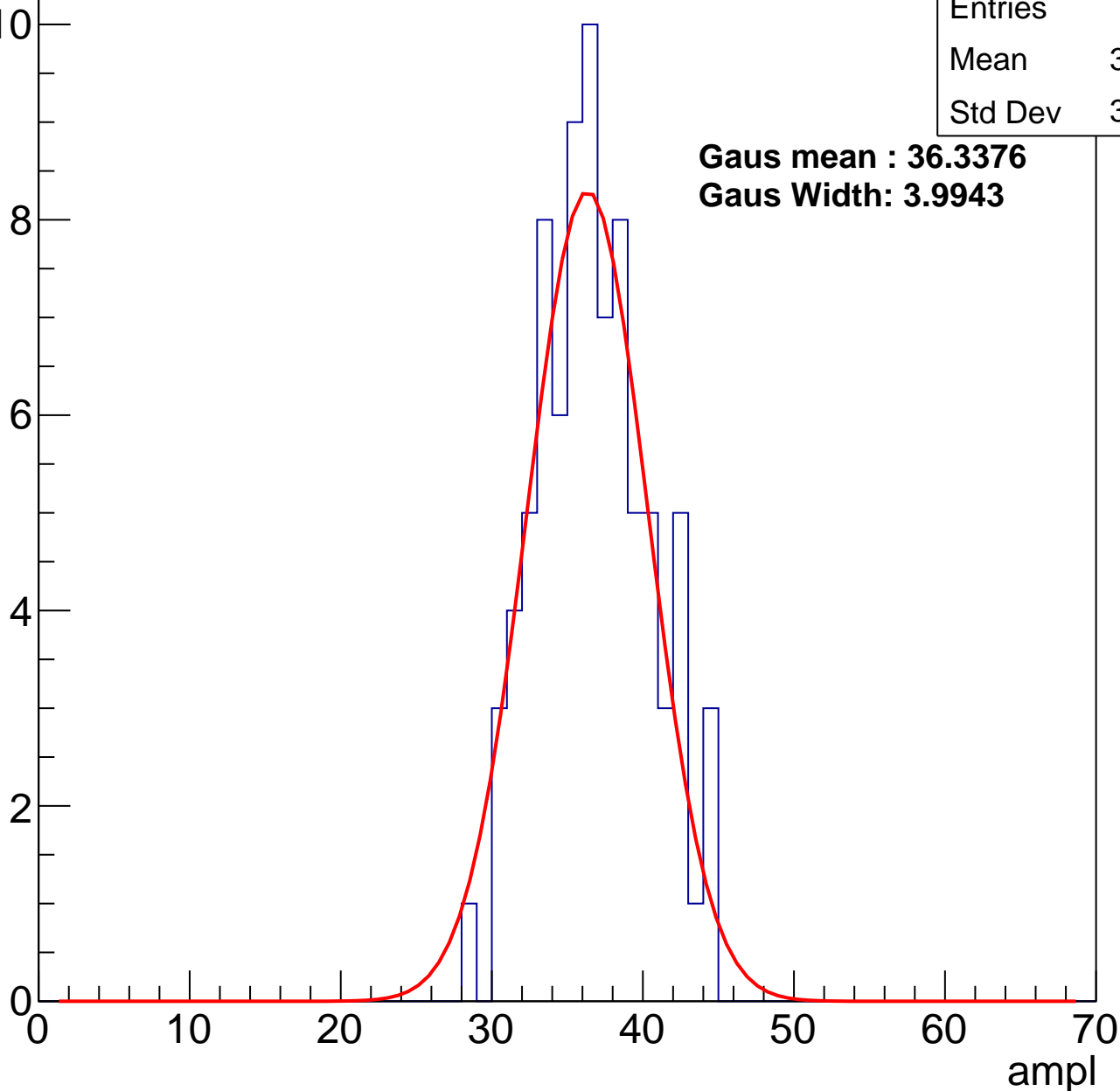
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.28
Std Dev	3.658

**Gaus mean : 36.3376**

**Gaus Width: 3.9943**



# B1L102S, U24-ch60, adc2

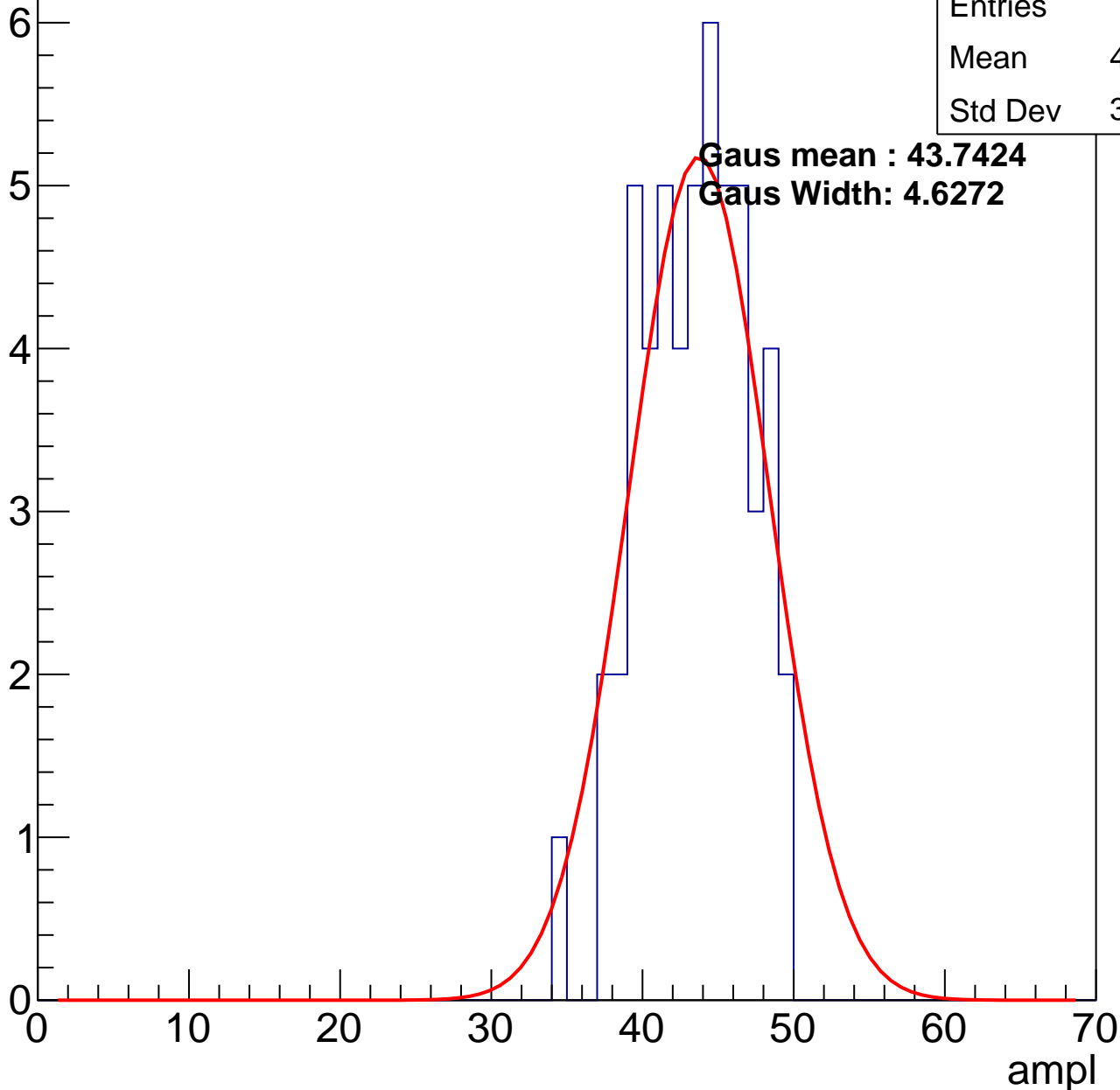
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.96
Std Dev	3.458

**Gaus mean : 43.7424**

**Gaus Width: 4.6272**

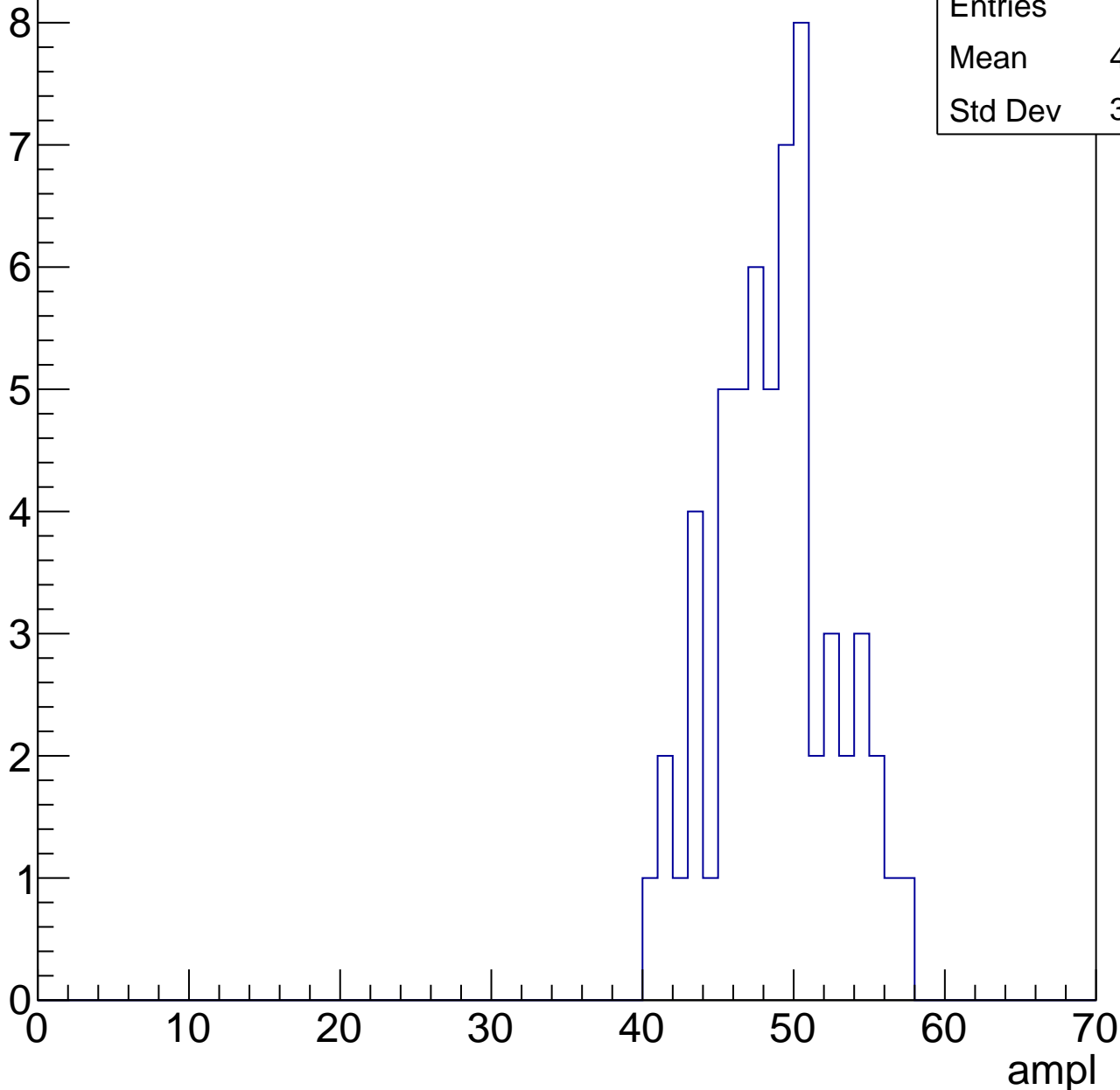


# B1L102S, U24-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.29
Std Dev	3.875

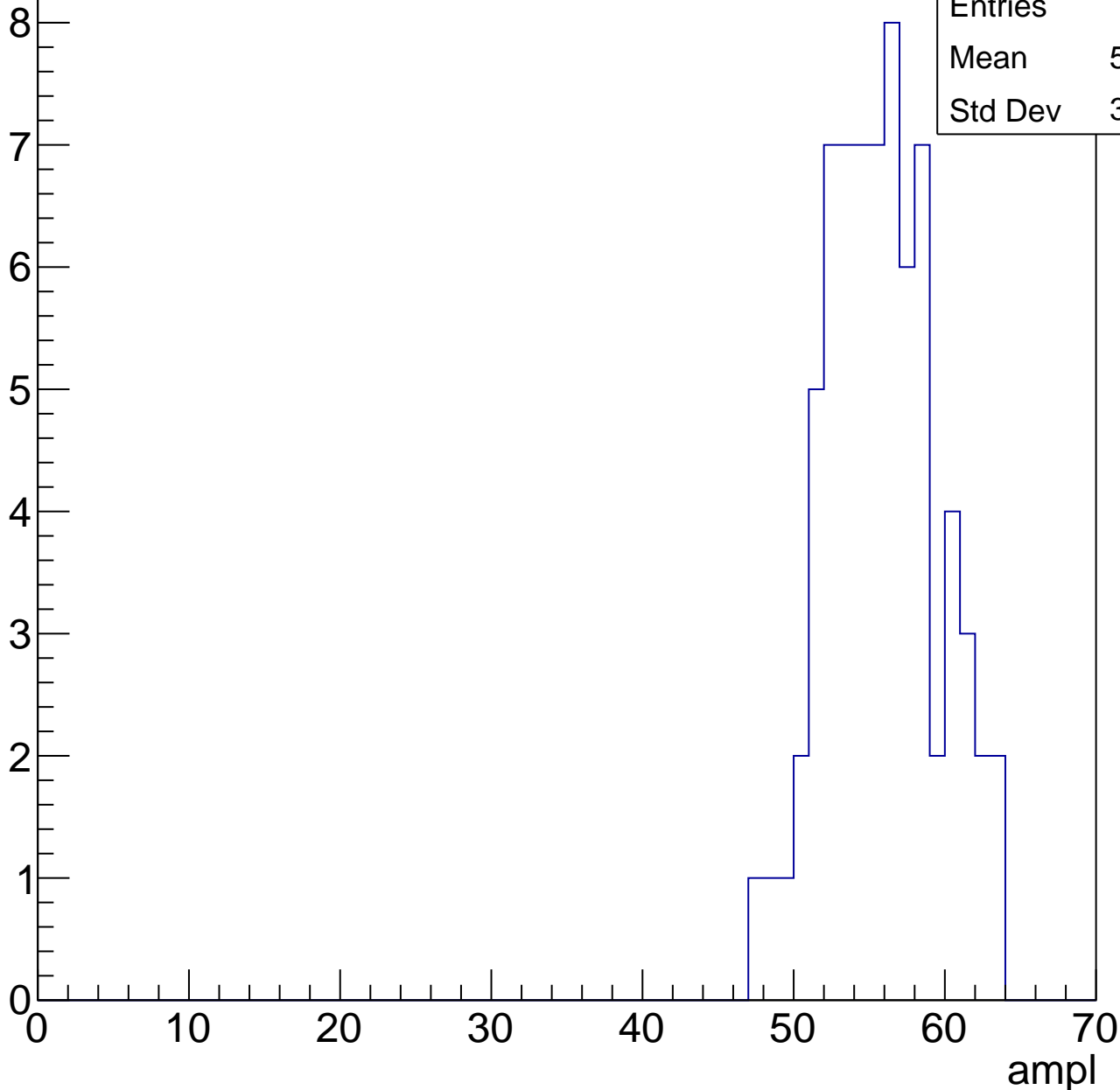


# B1L102S, U24-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

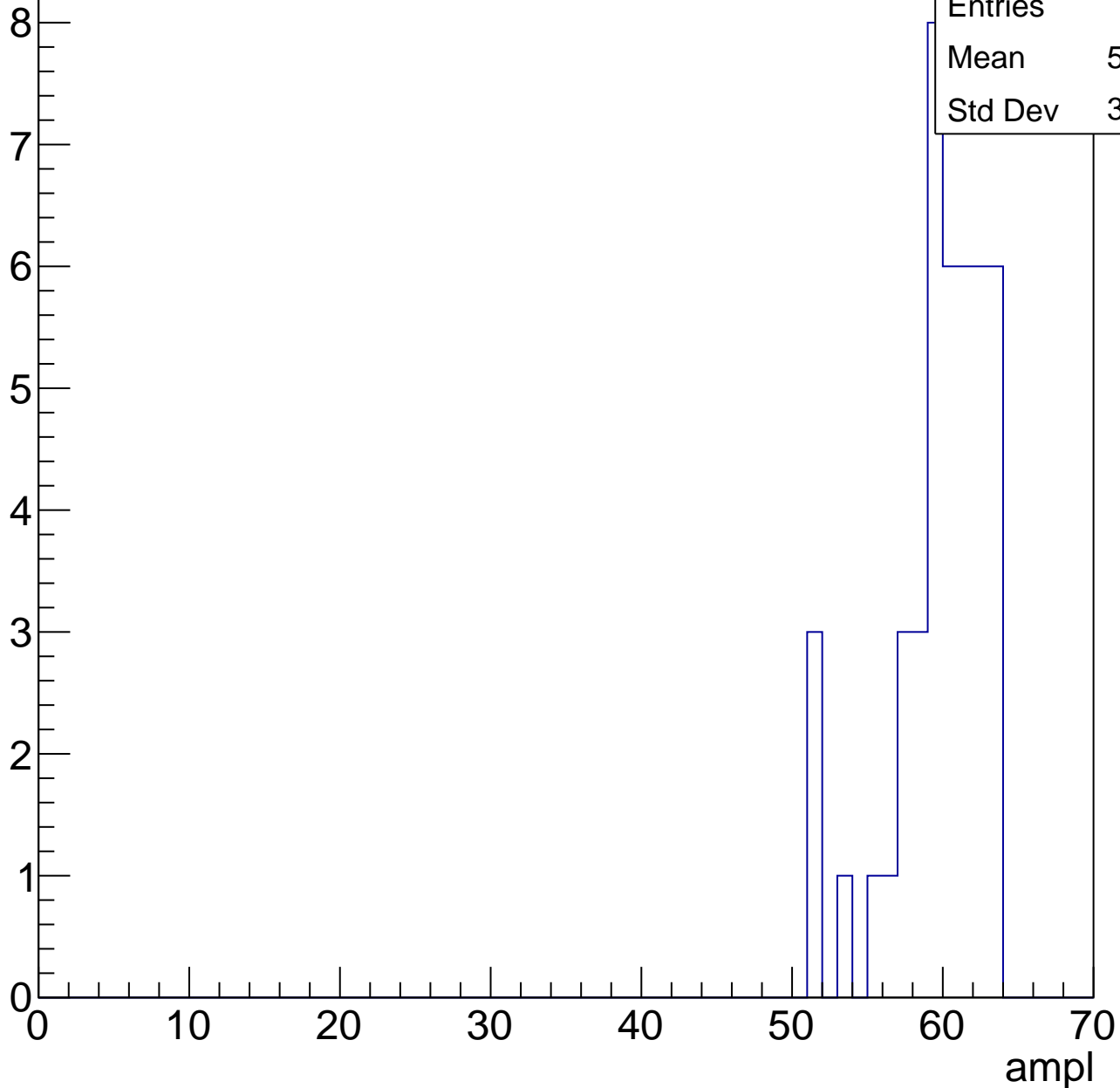
Entries	72
Mean	55.33
Std Dev	3.594



# B1L102S, U24-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

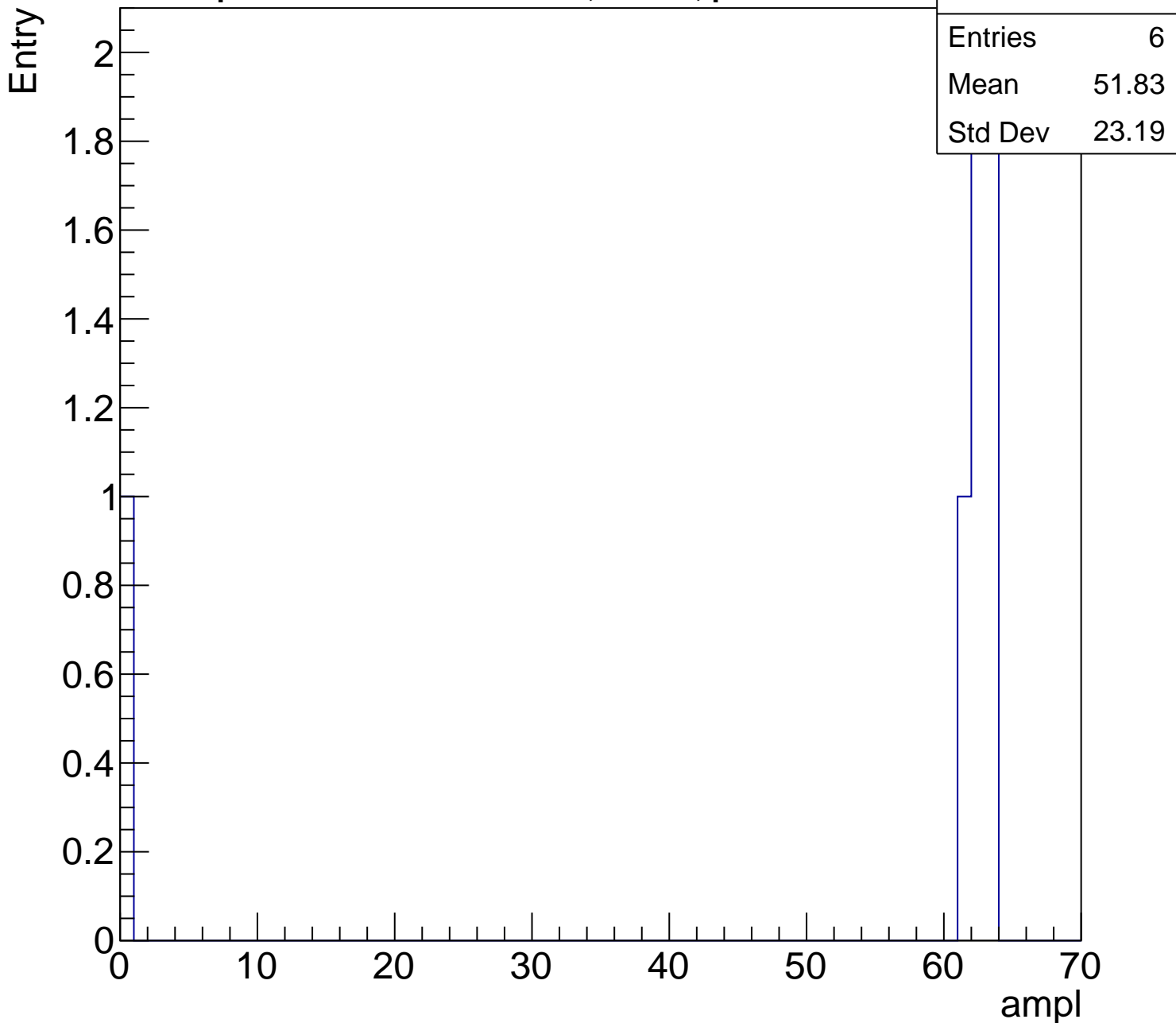
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

ampl

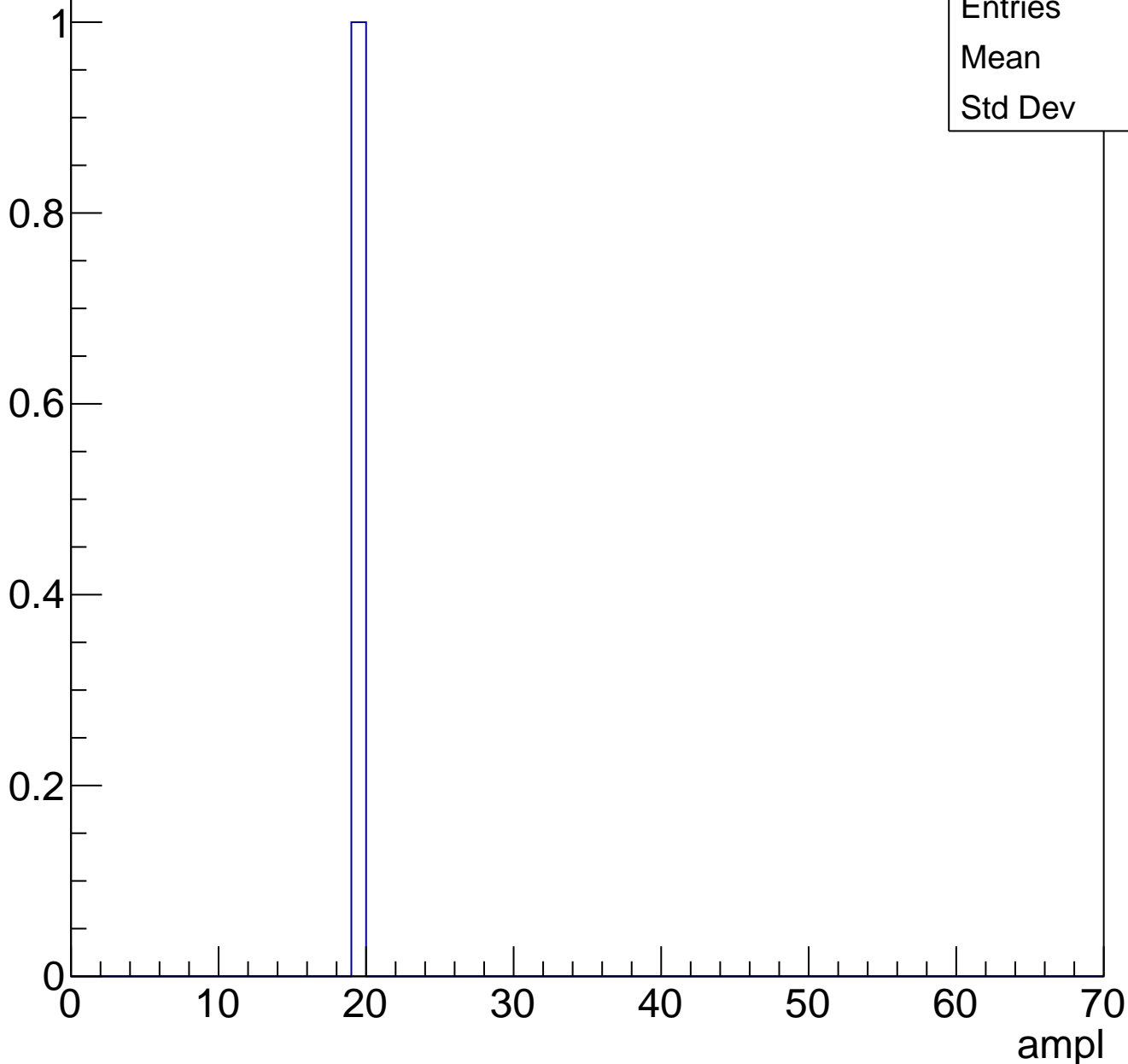




# B1L102S, U24-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch61, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	69
Mean	28.9
Std Dev	3.431

**Gaus mean : 28.9057**

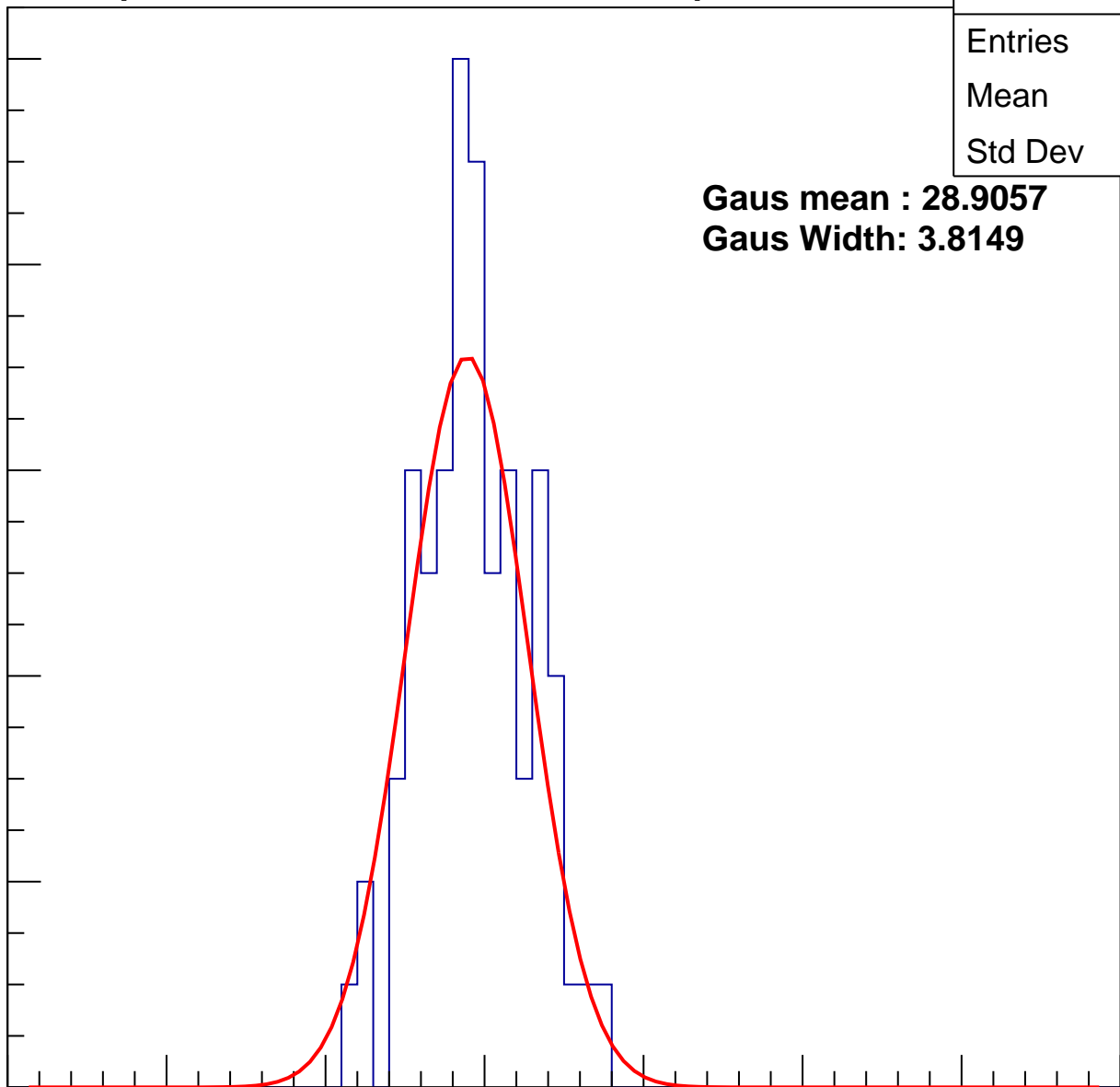
**Gaus Width: 3.8149**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch61, adc1

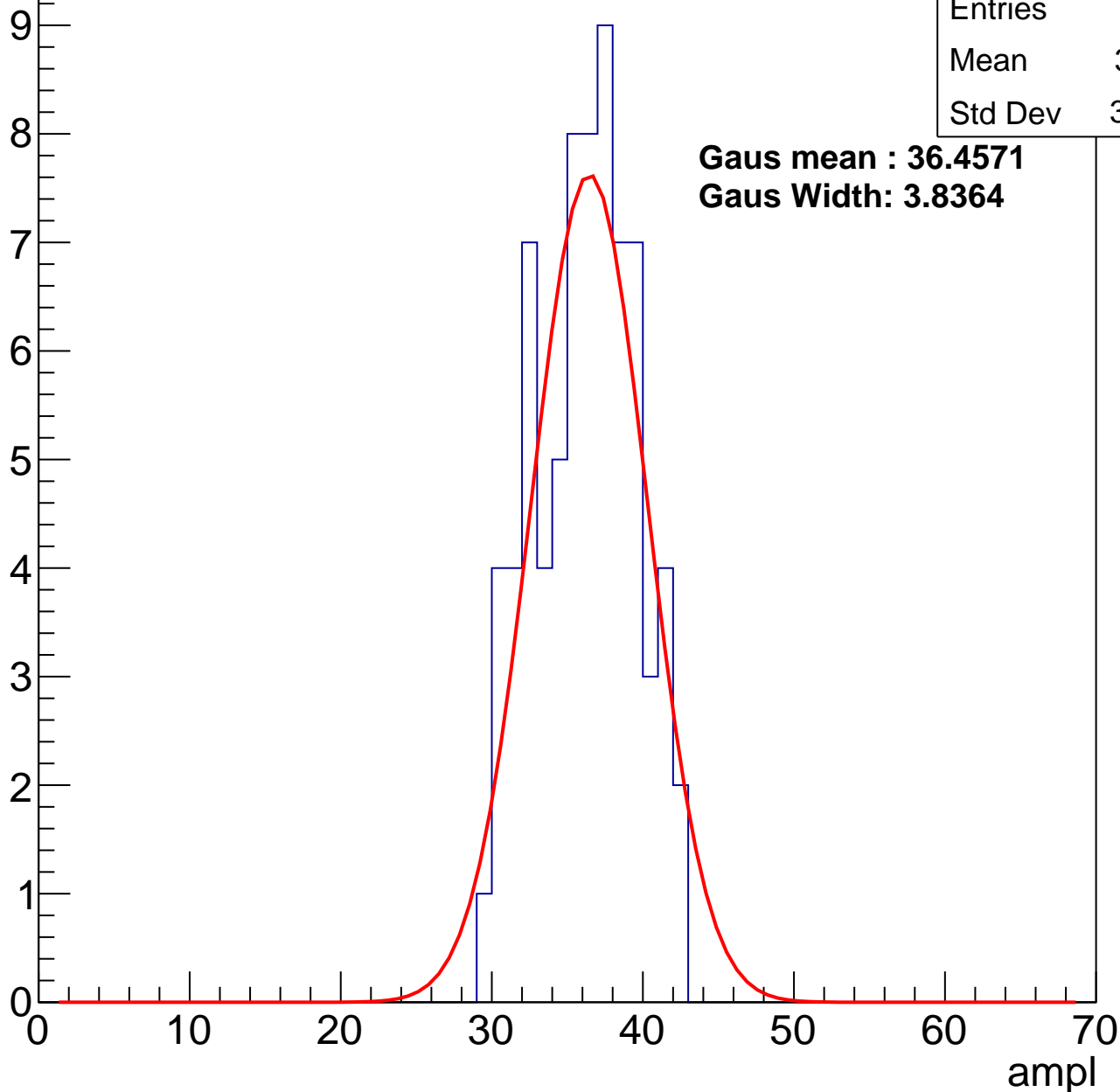
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.71
Std Dev	3.275

**Gaus mean : 36.4571**

**Gaus Width: 3.8364**



# B1L102S, U24-ch61, adc2

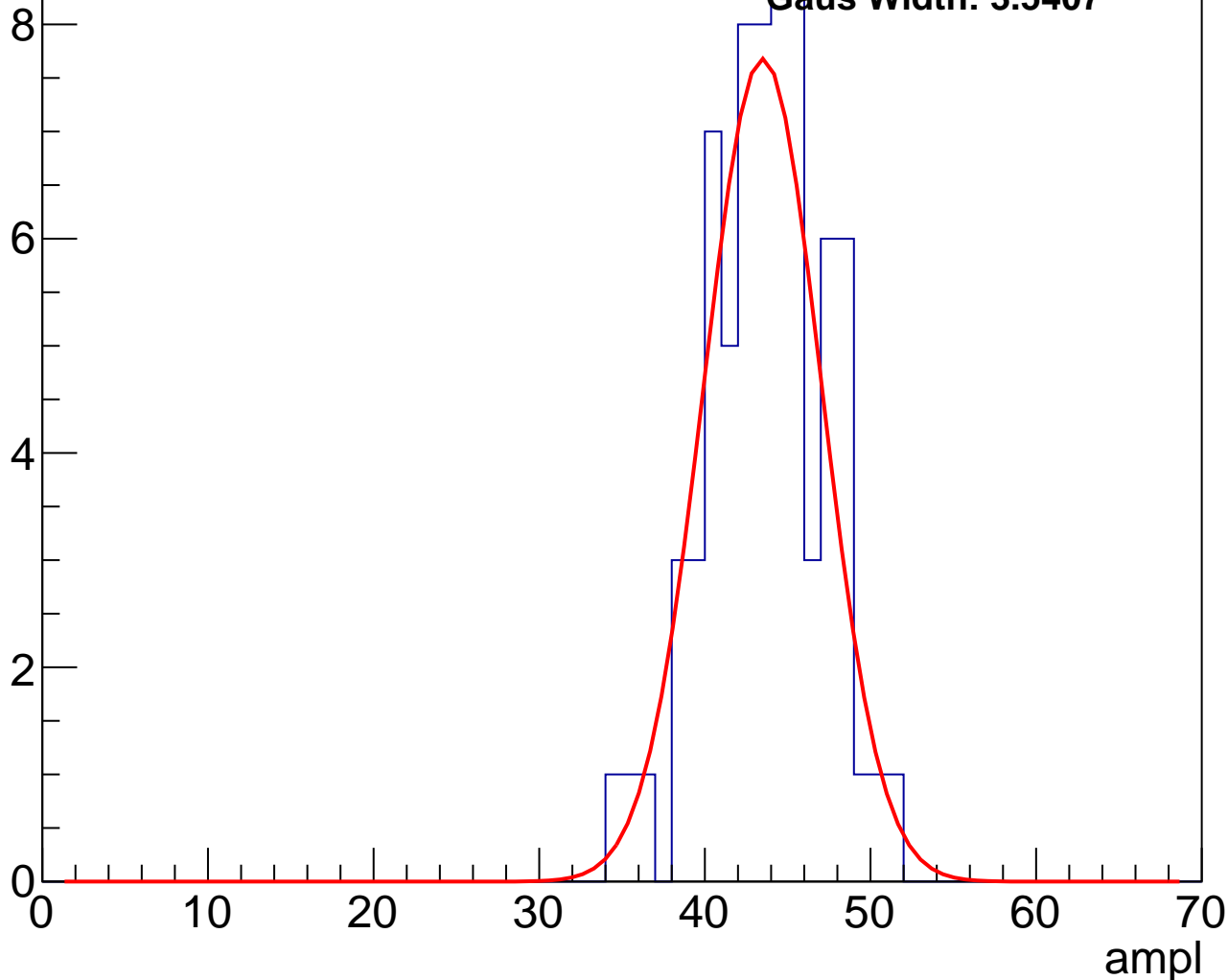
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.31
Std Dev	3.432

**Gaus mean : 43.4926**

**Gaus Width: 3.5407**

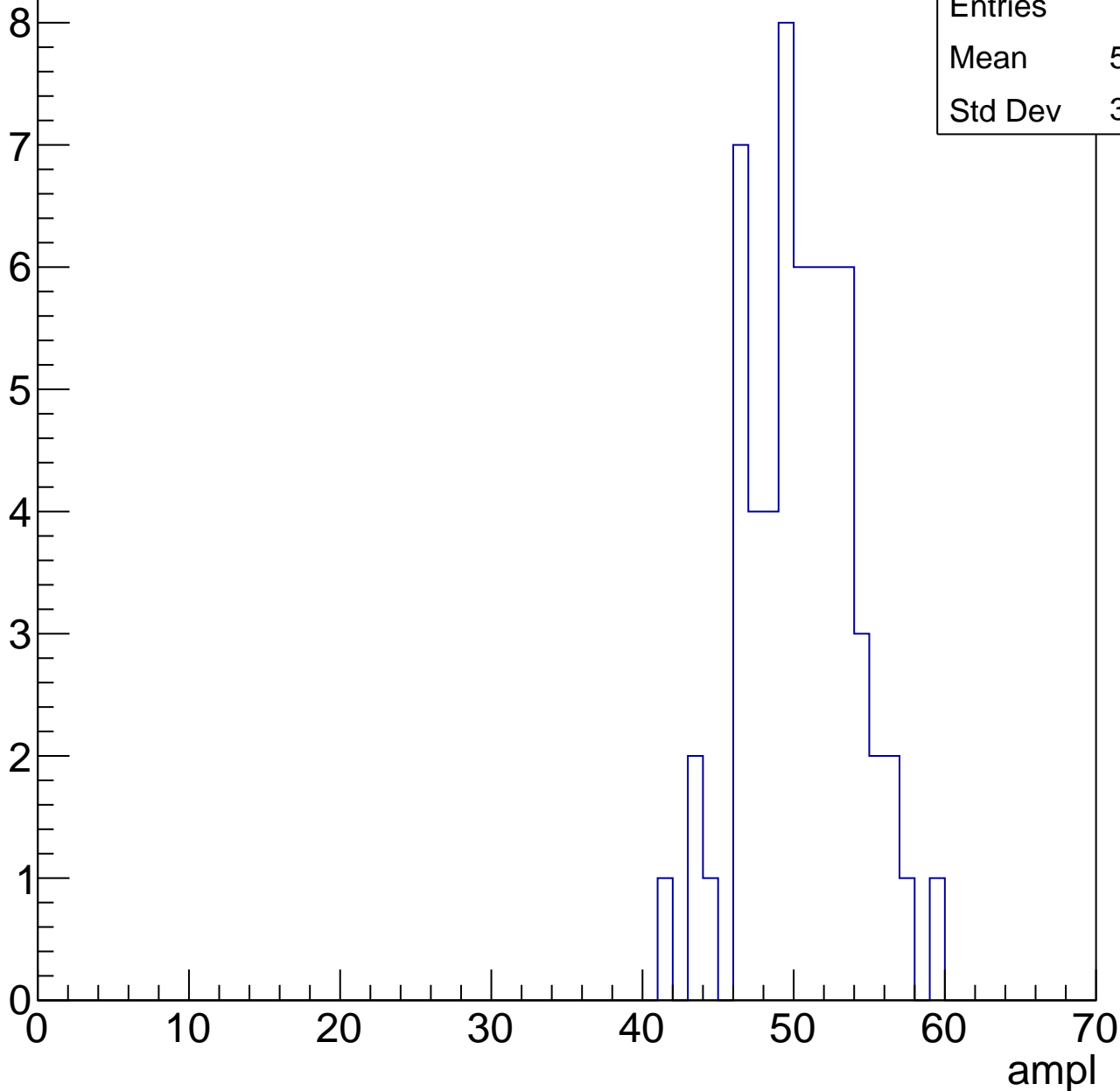


# B1L102S, U24-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

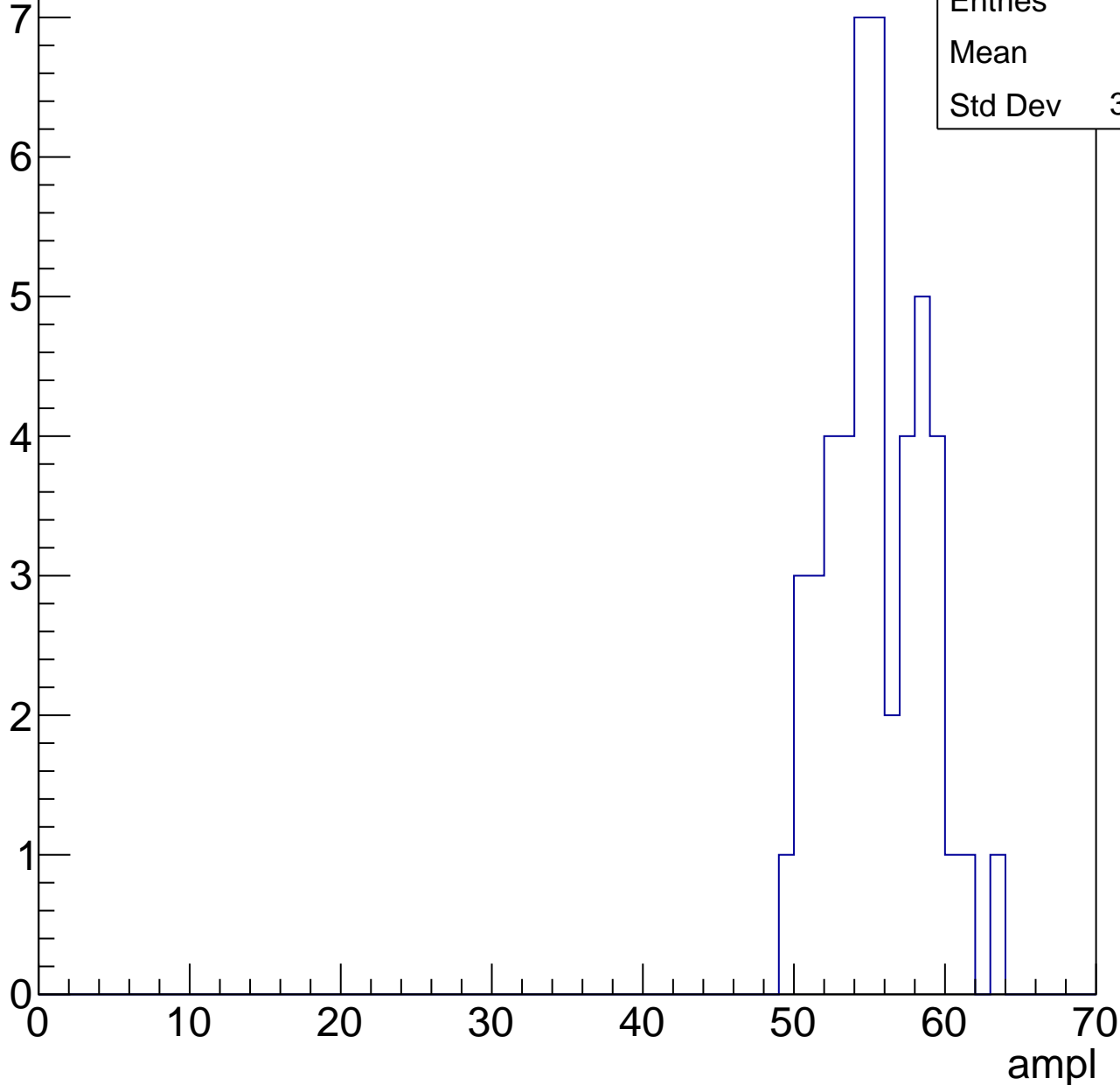
Entries	60
Mean	50.02
Std Dev	3.585



# B1L102S, U24-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



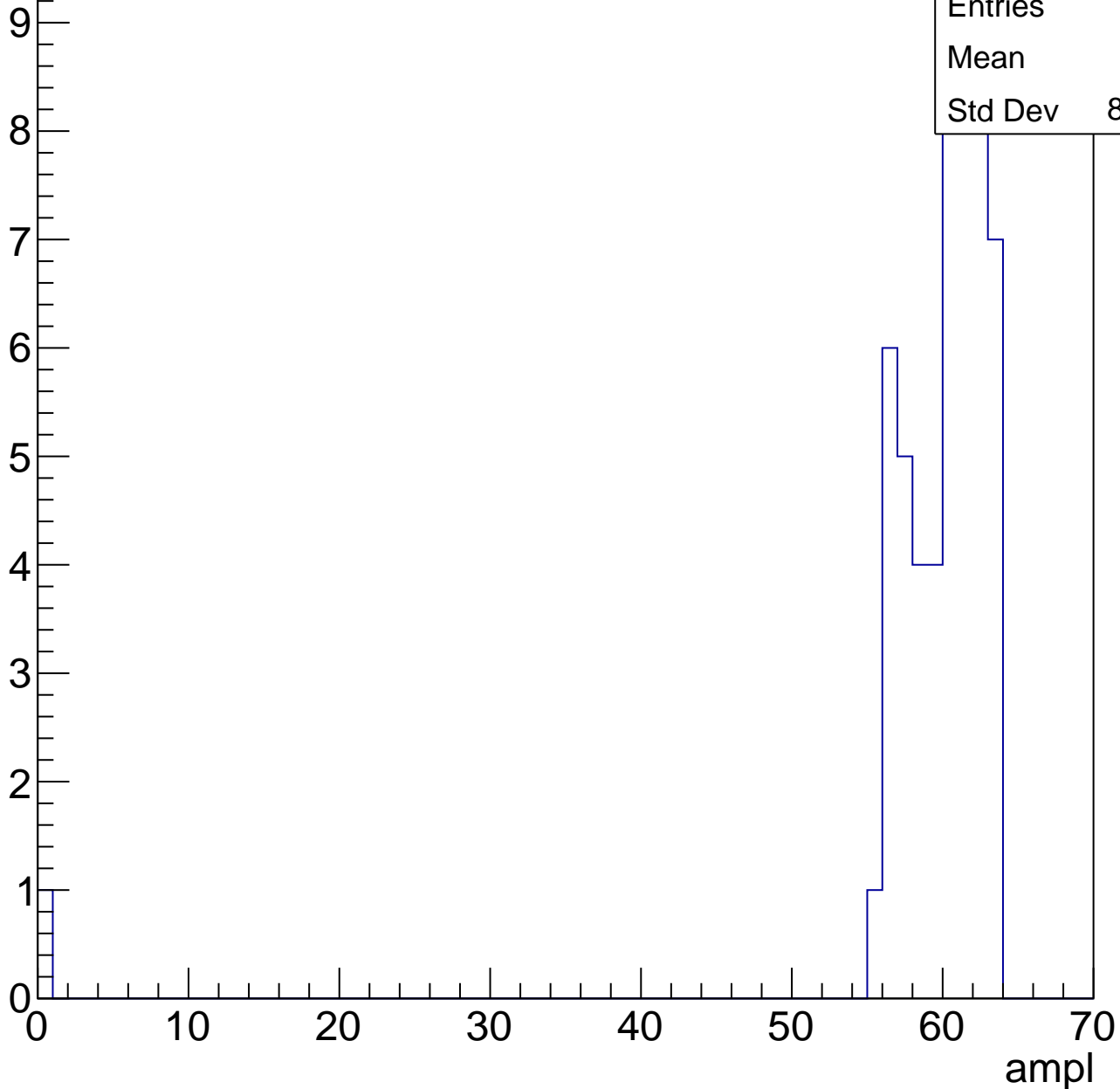
Entries	47
Mean	55
Std Dev	3.162

# B1L102S, U24-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

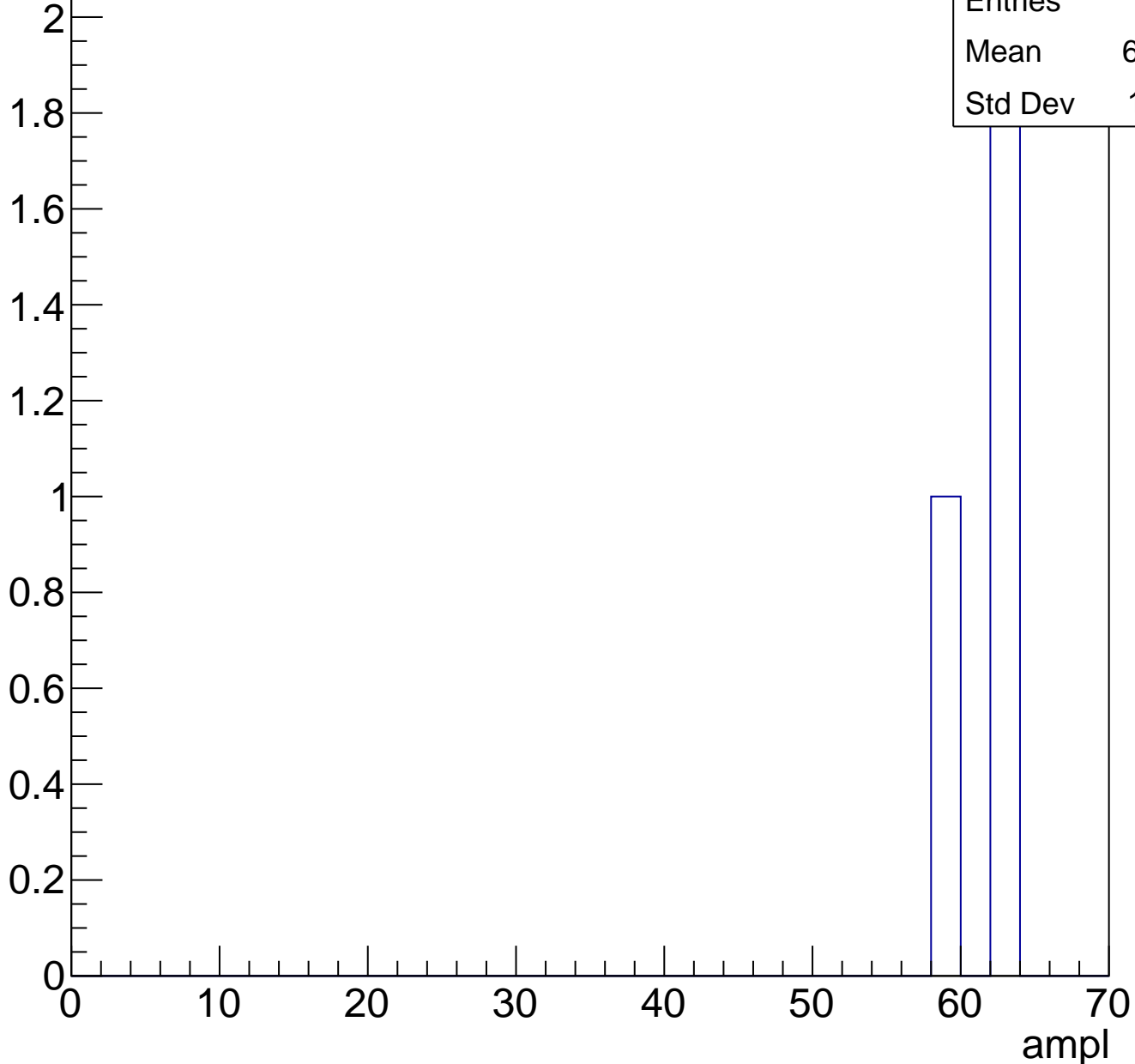
Entries	54
Mean	58.7
Std Dev	8.388



# B1L102S, U24-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

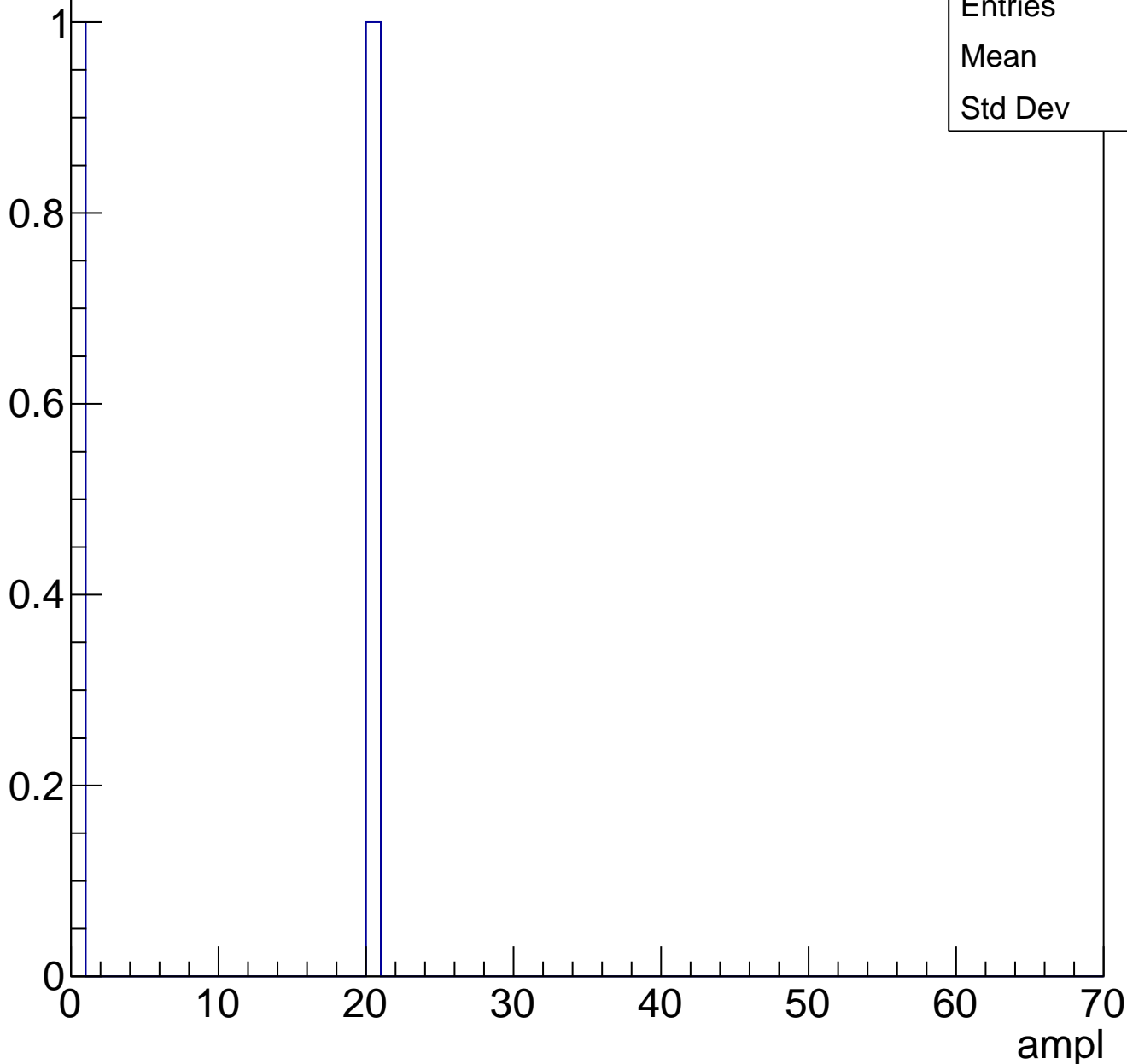




# B1L102S, U24-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch62, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	28.96
Std Dev	4.669

**Gaus mean : 30.0487**

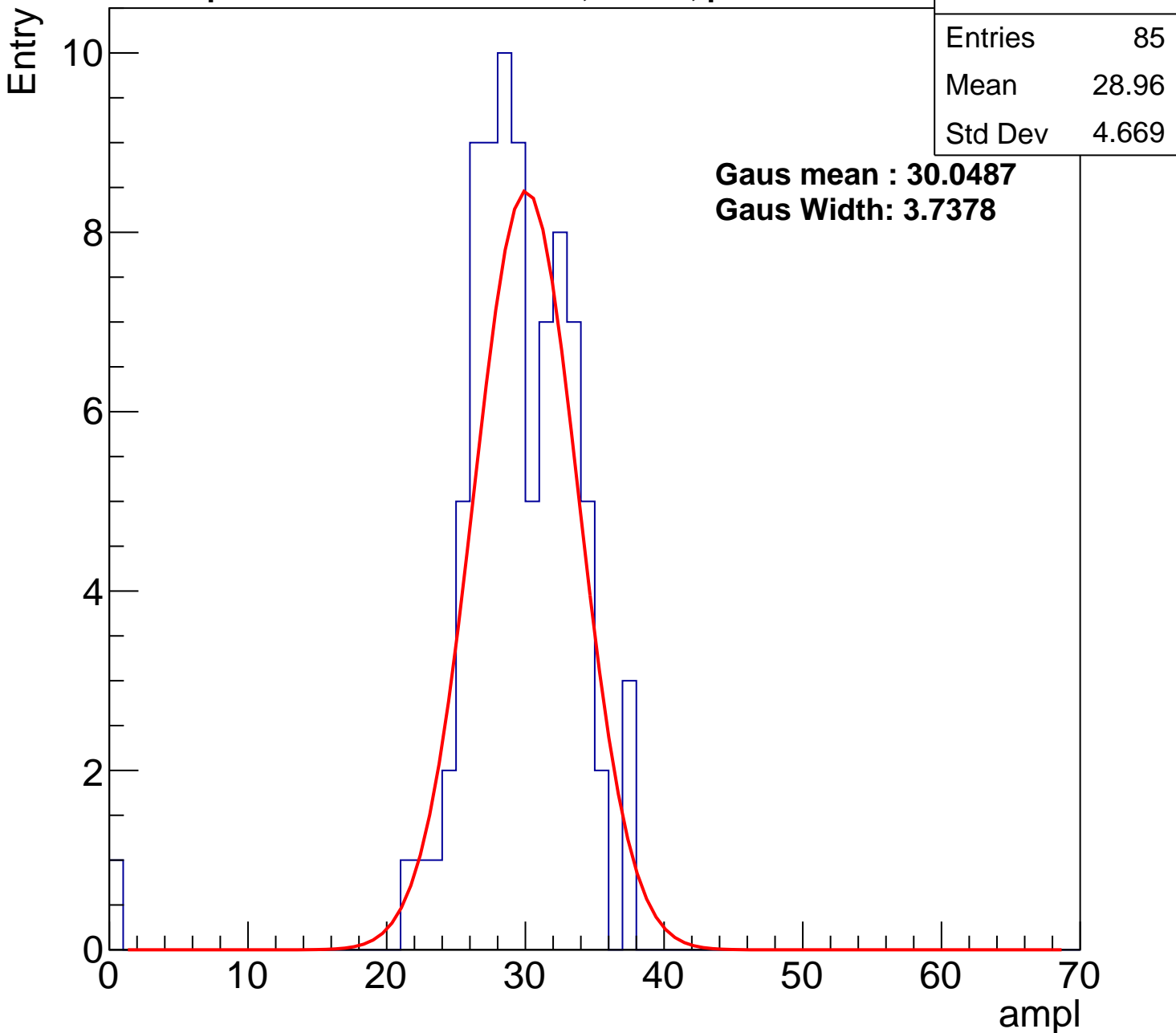
**Gaus Width: 3.7378**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch62, adc1

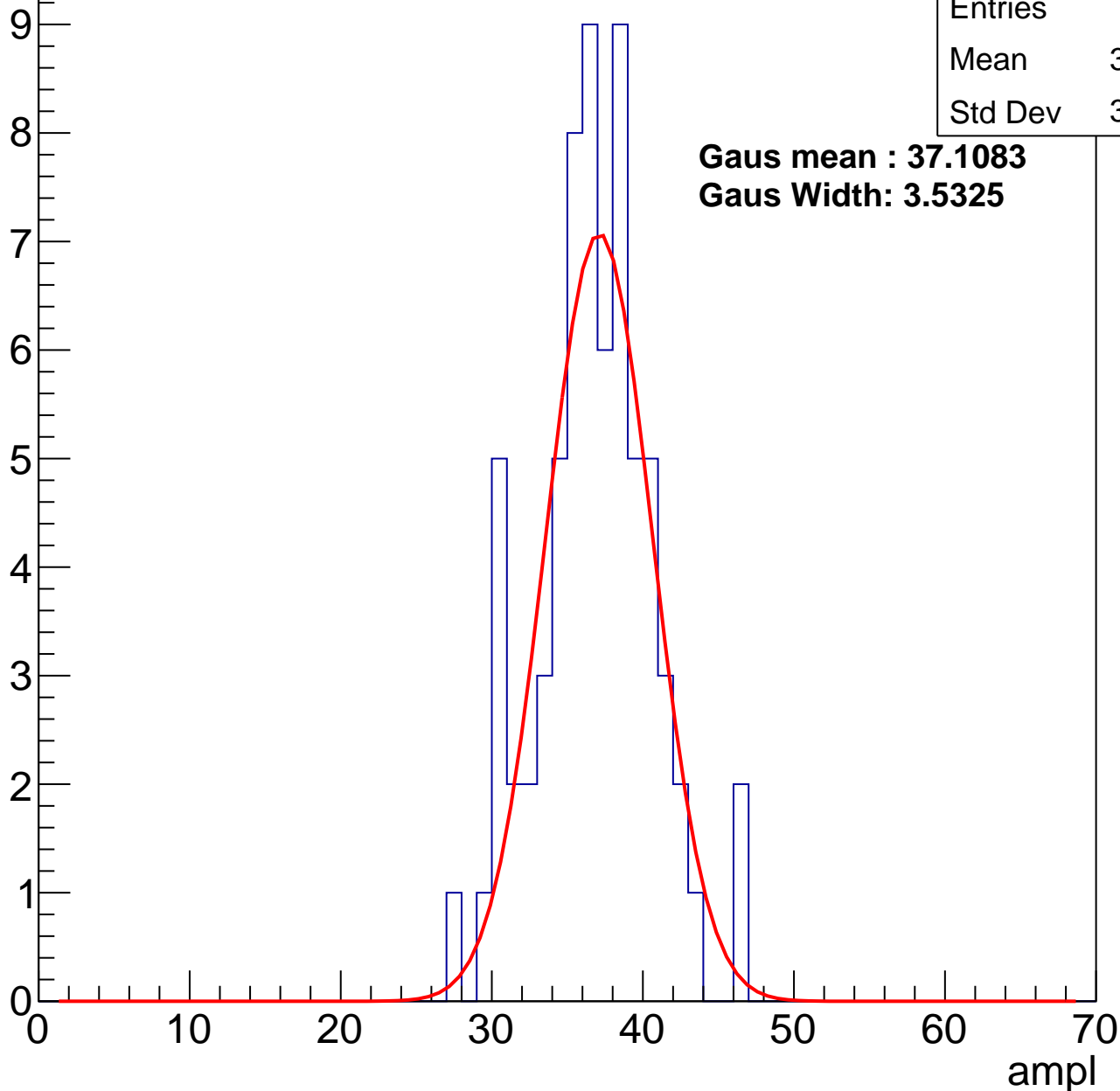
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.32
Std Dev	3.813

**Gaus mean : 37.1083**

**Gaus Width: 3.5325**



# B1L102S, U24-ch62, adc2

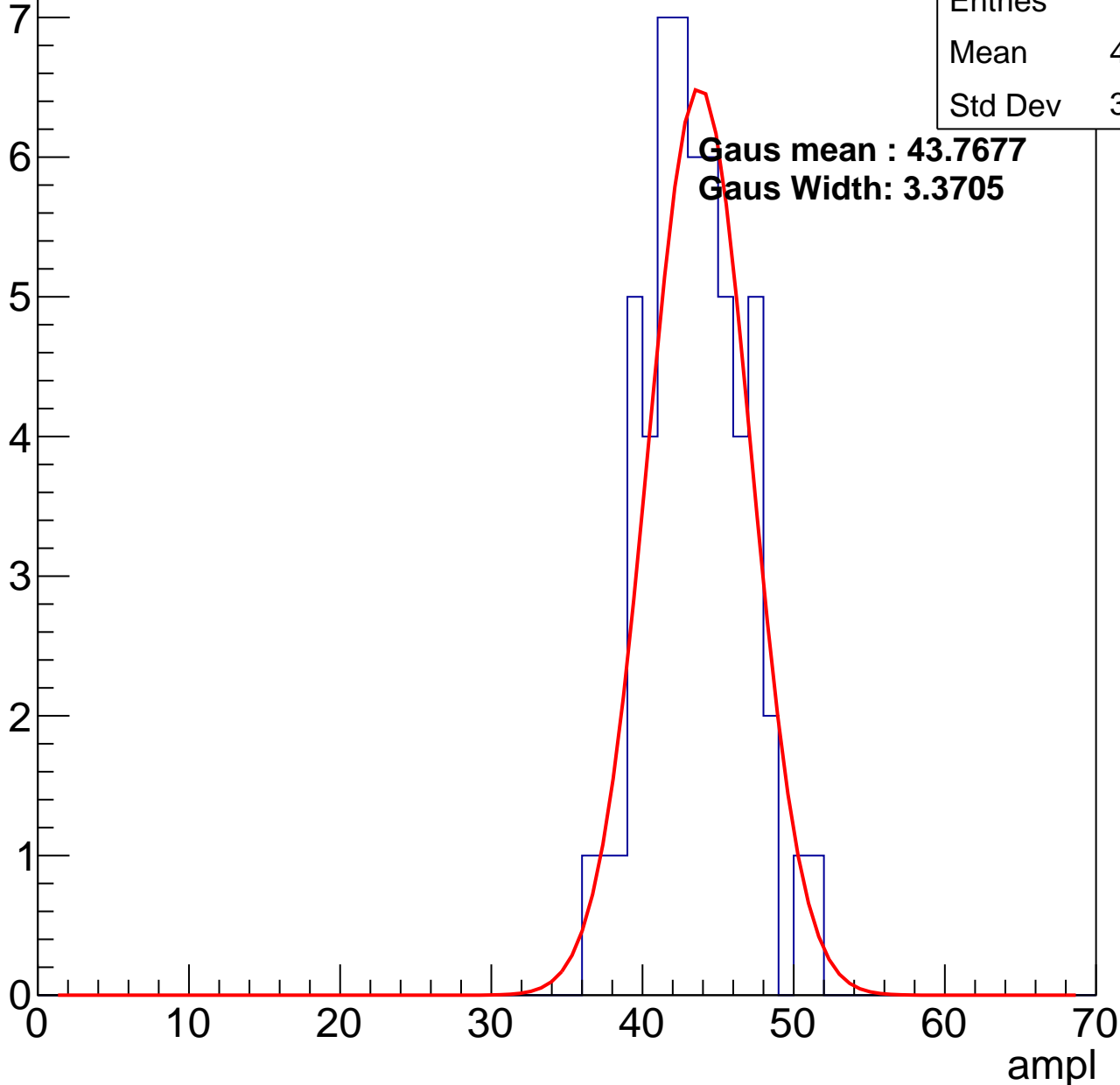
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.04
Std Dev	3.179

**Gaus mean : 43.7677**

**Gaus Width: 3.3705**

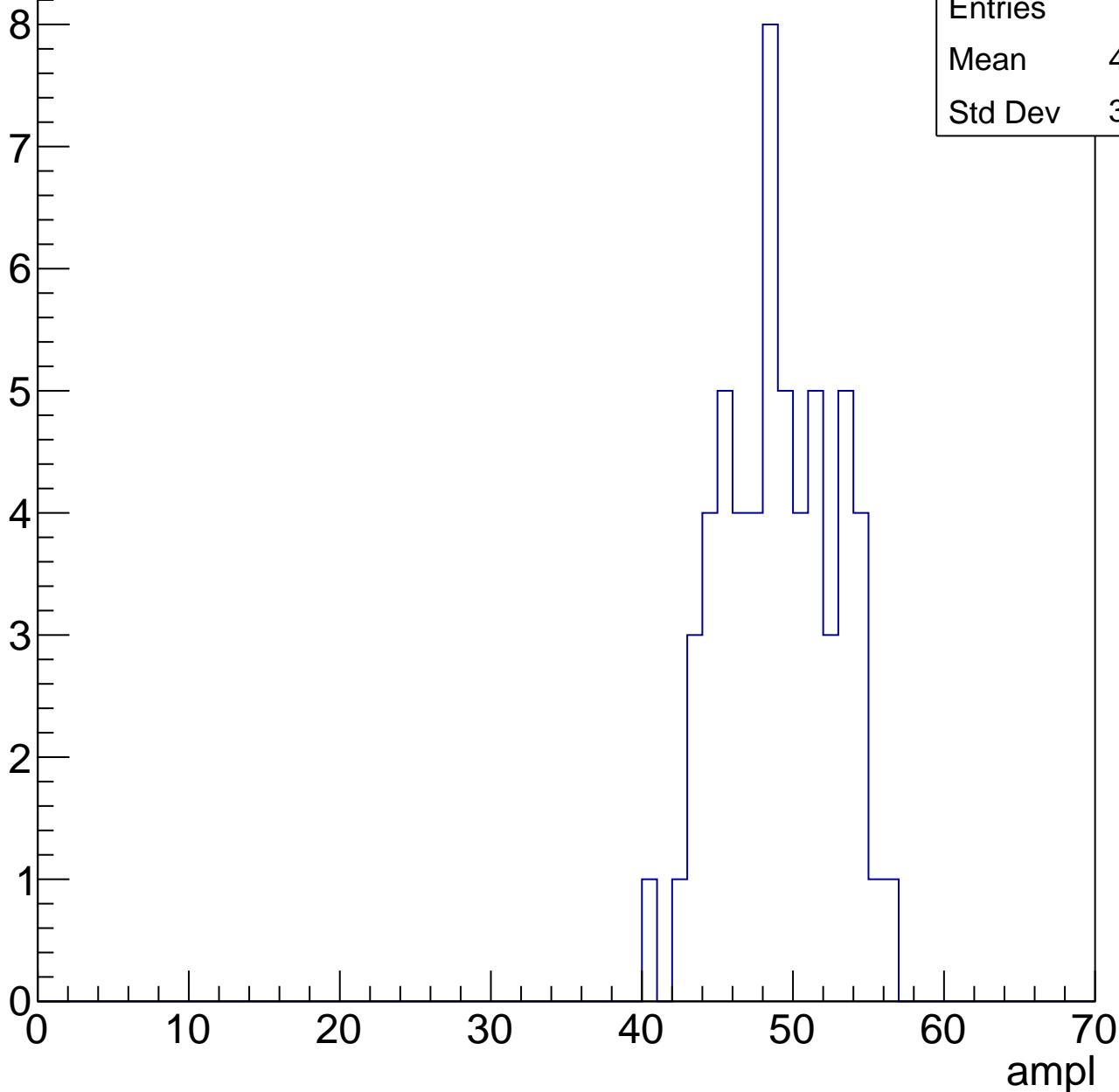


# B1L102S, U24-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.55
Std Dev	3.673

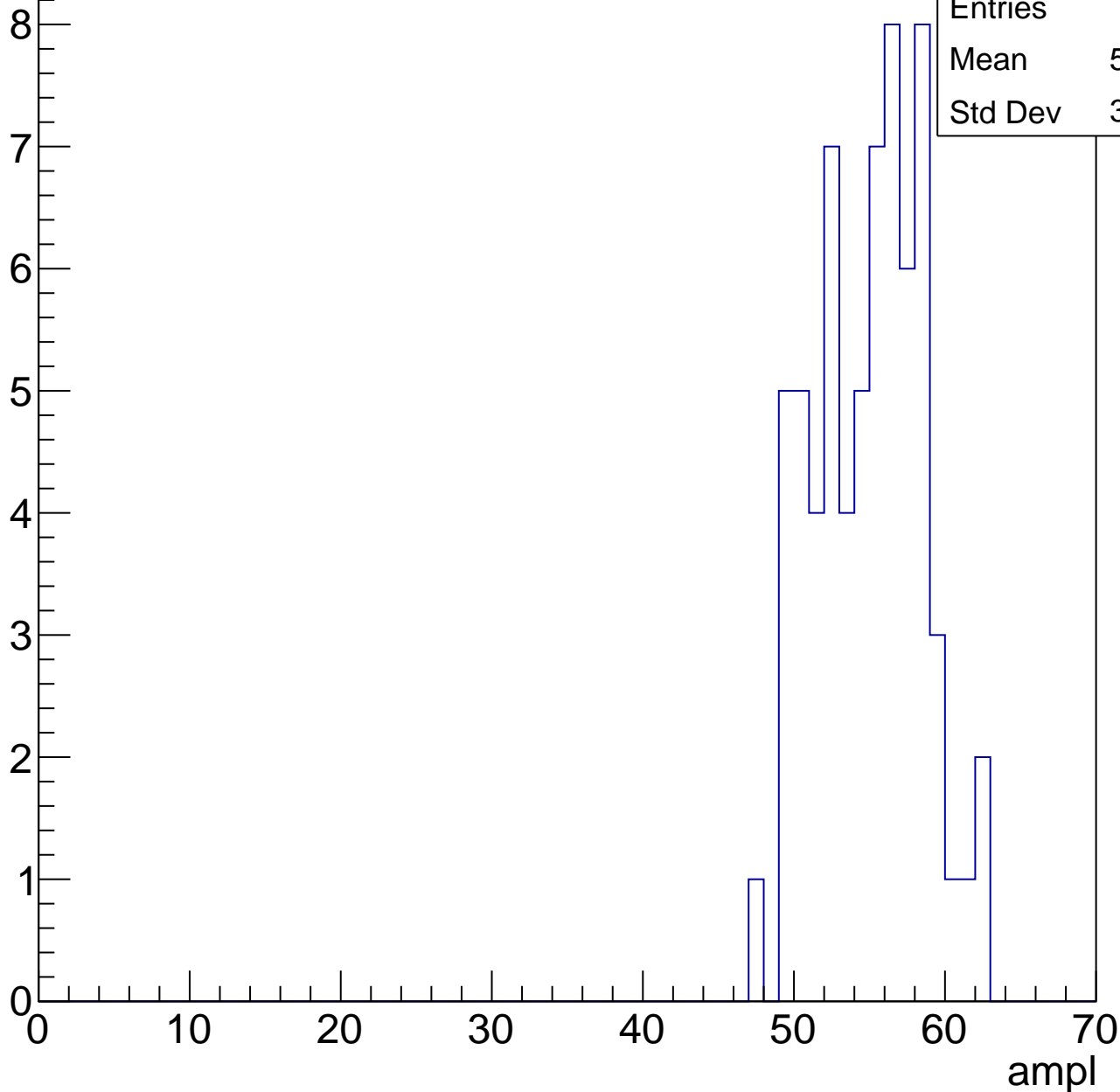


# B1L102S, U24-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.52
Std Dev	3.483

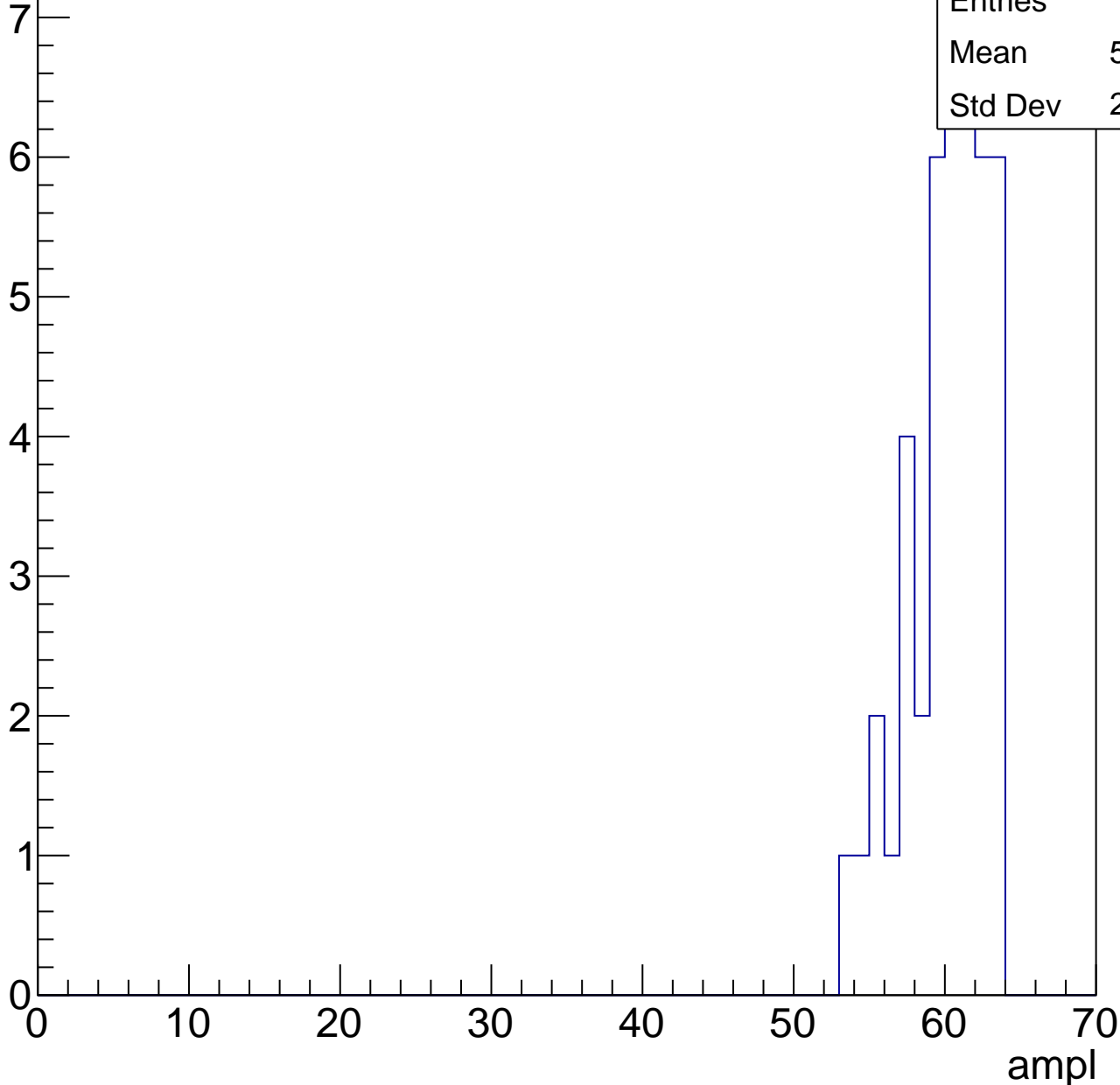


# B1L102S, U24-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

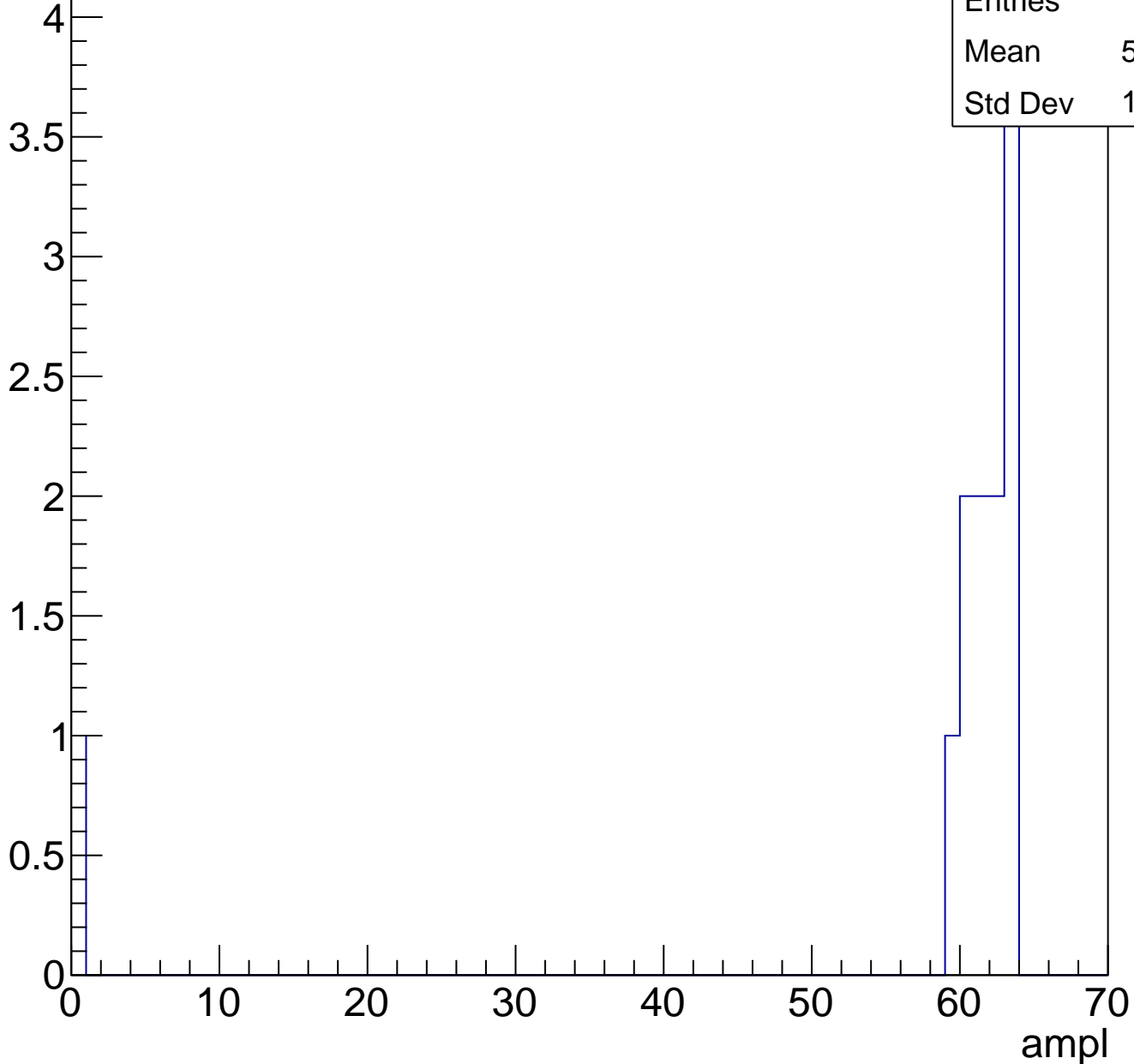
Entries	43
Mean	59.72
Std Dev	2.564



# B1L102S, U24-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch63, adc0

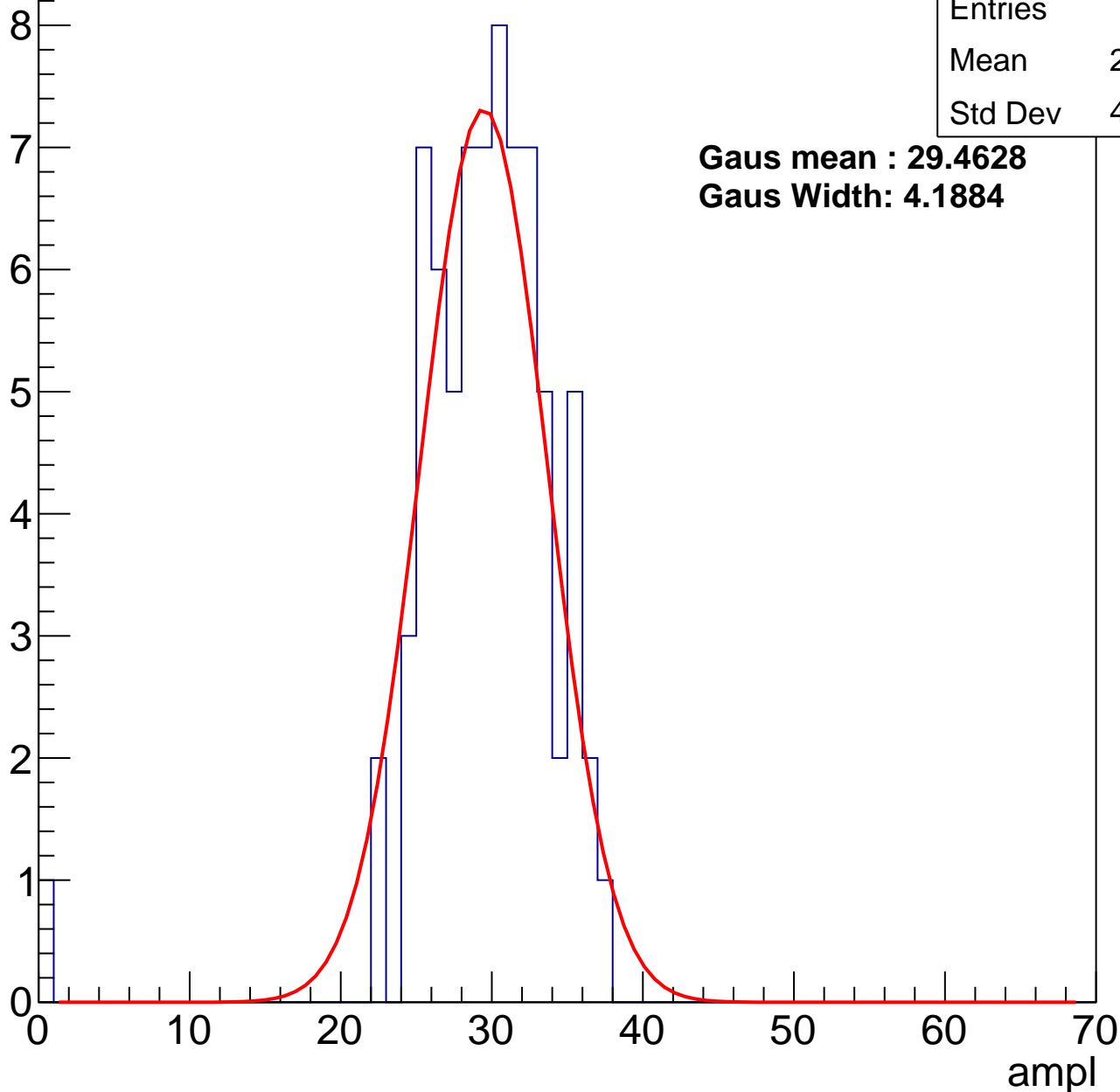
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.05
Std Dev	4.869

**Gaus mean : 29.4628**

**Gaus Width: 4.1884**



# B1L102S, U24-ch63, adc1

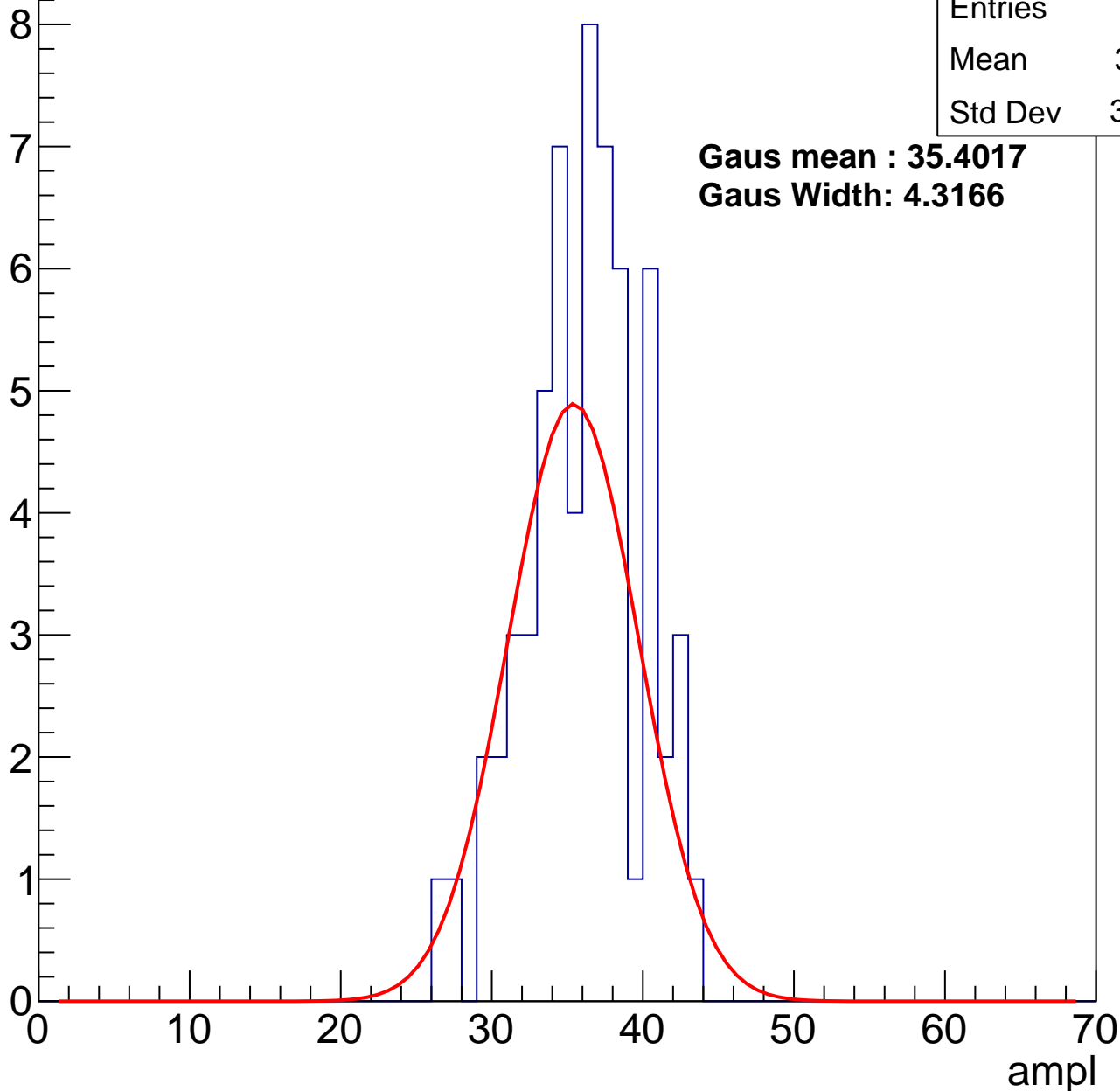
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.61
Std Dev	3.778

**Gaus mean : 35.4017**

**Gaus Width: 4.3166**



# B1L102S, U24-ch63, adc2

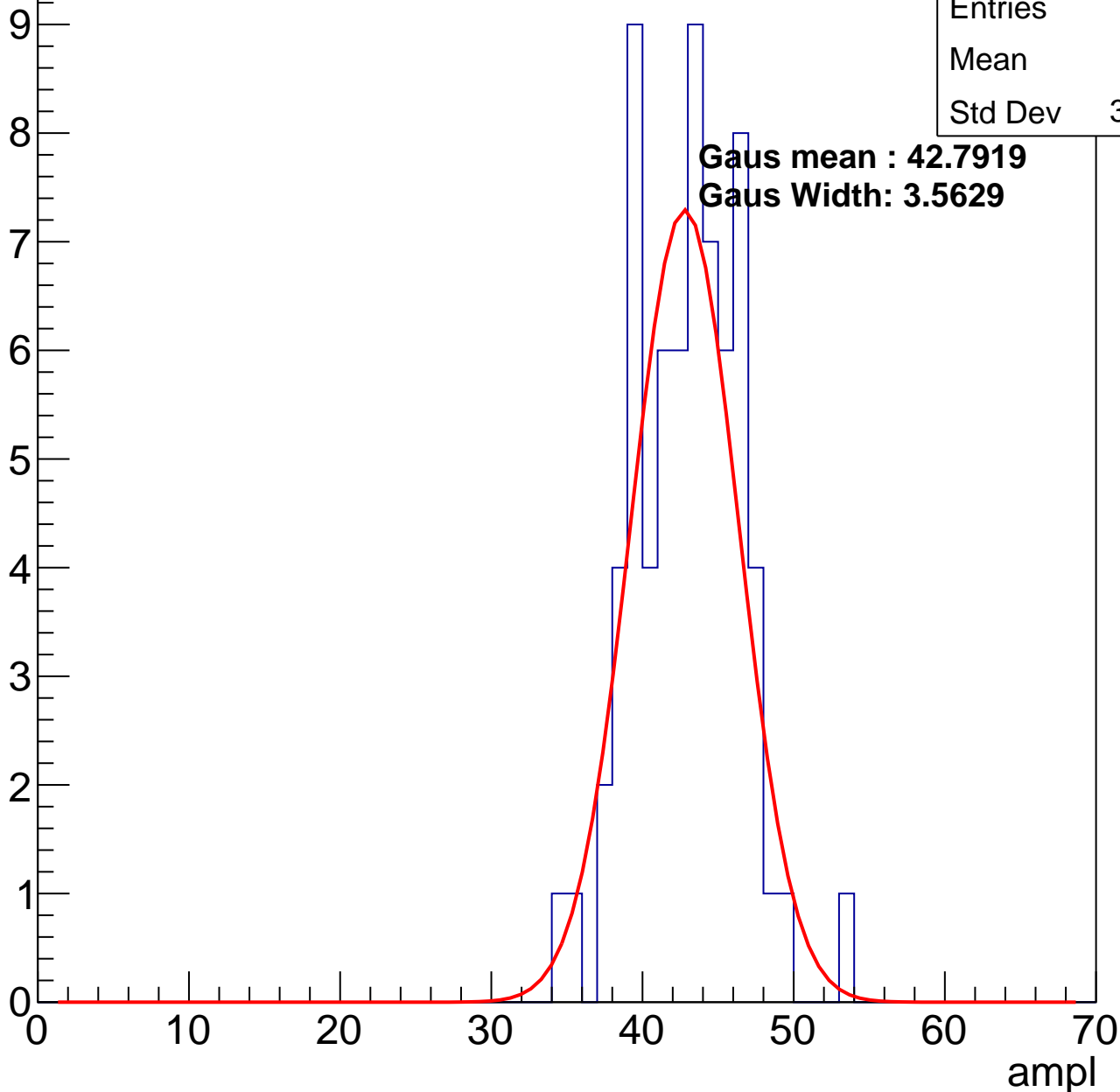
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.5
Std Dev	3.459

**Gaus mean : 42.7919**

**Gaus Width: 3.5629**

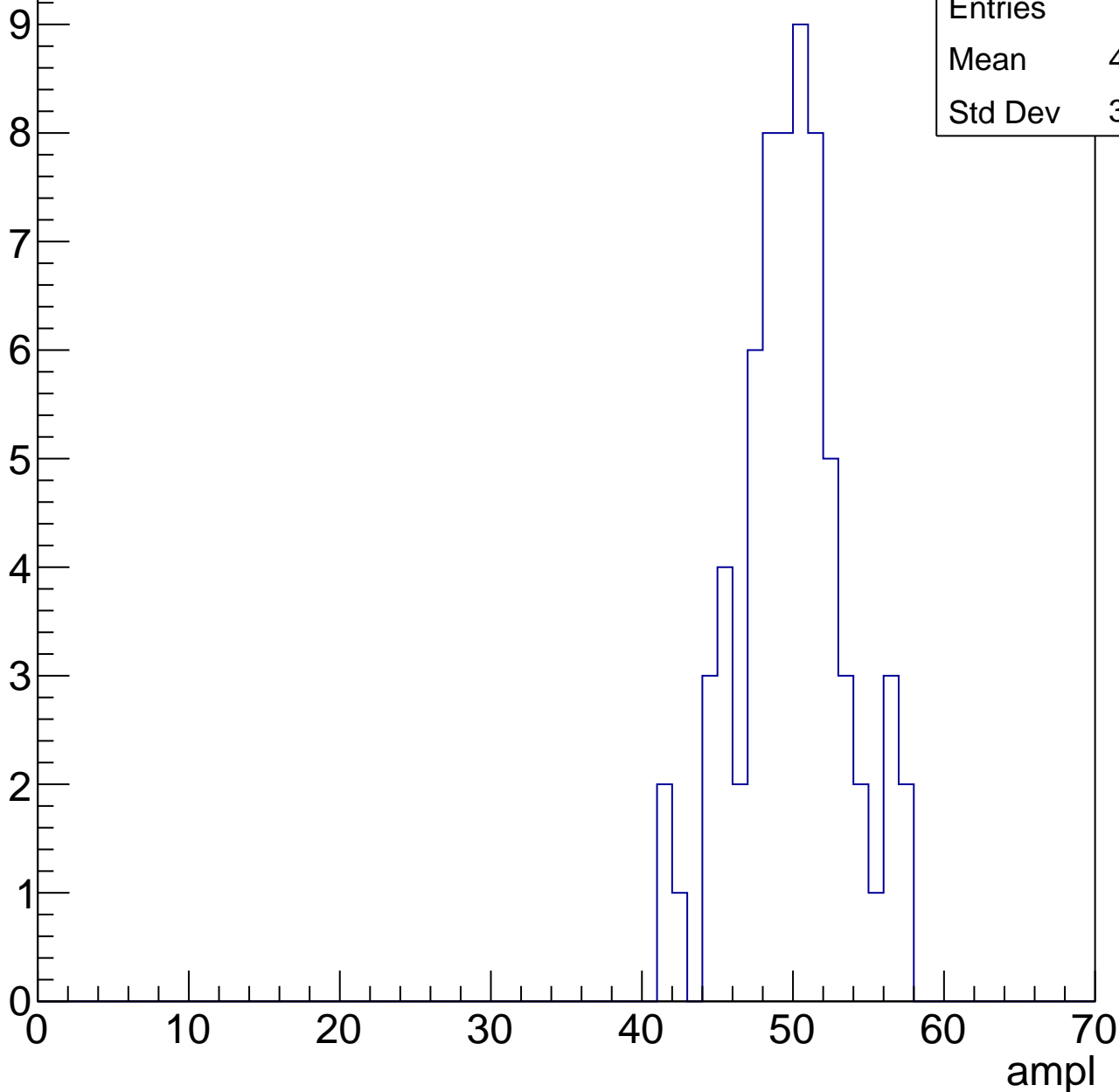


# B1L102S, U24-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.37
Std Dev	3.565

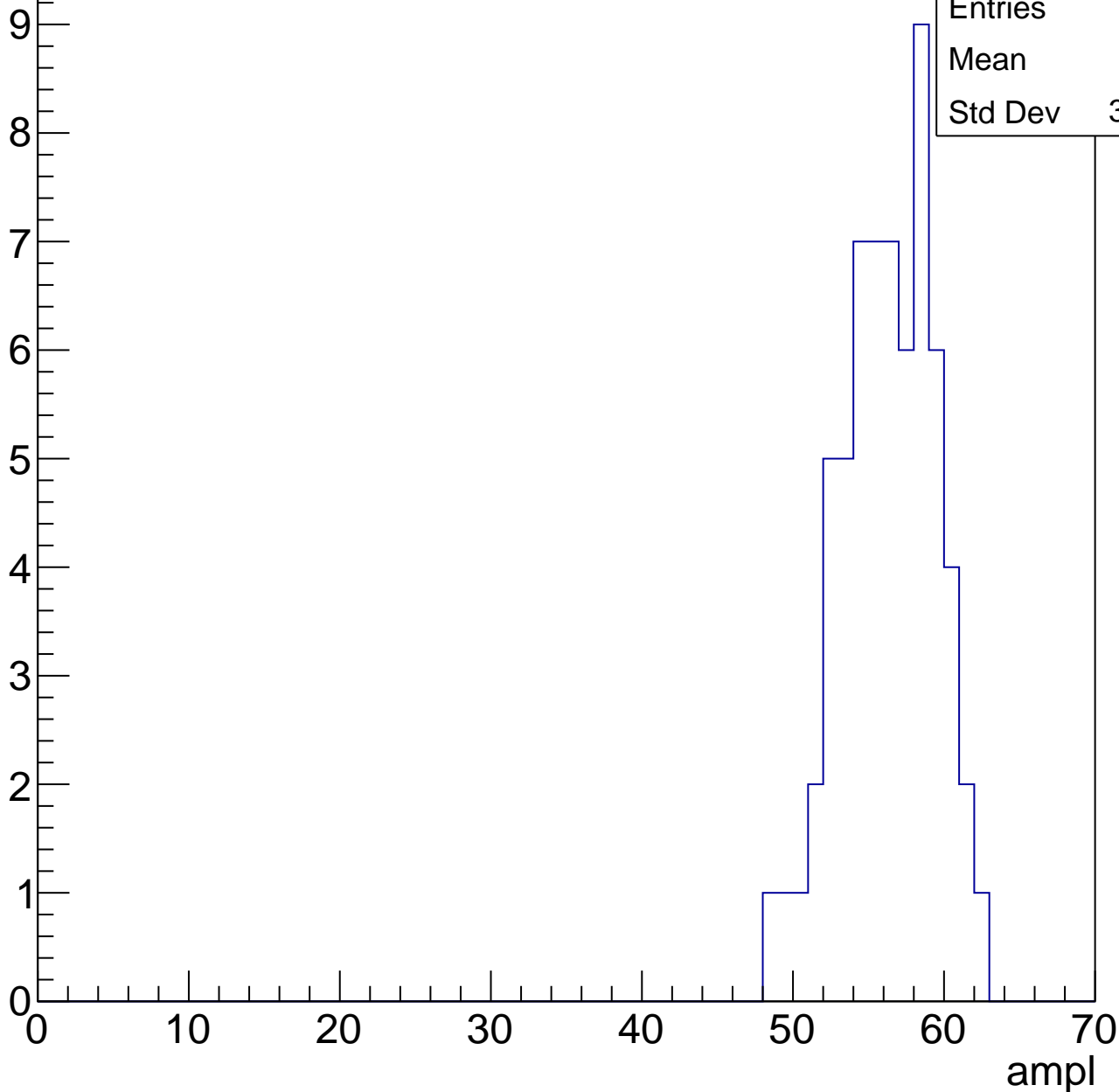


# B1L102S, U24-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.8
Std Dev	3.068

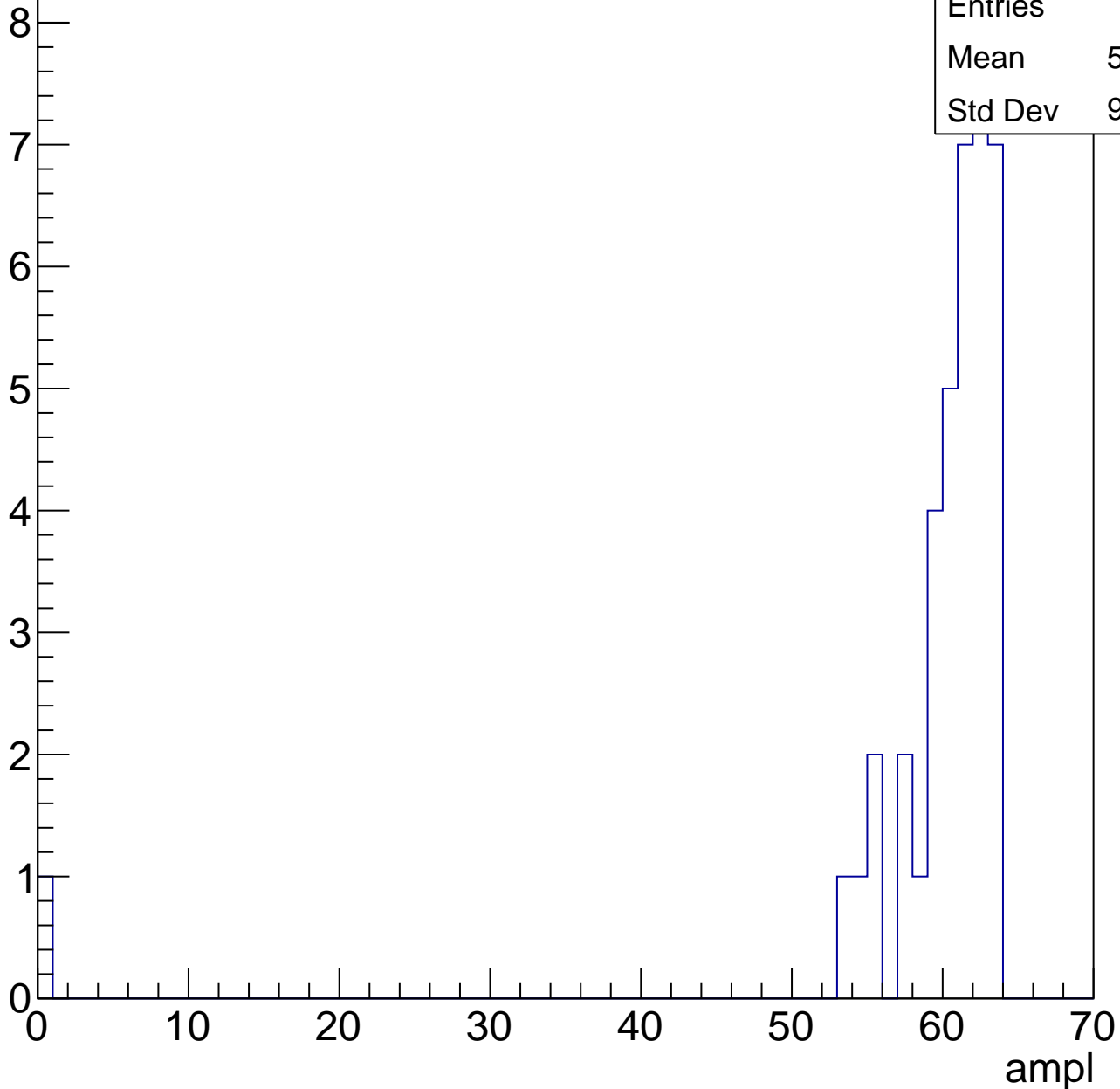


# B1L102S, U24-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.69
Std Dev	9.866



# B1L102S, U24-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch64, adc0

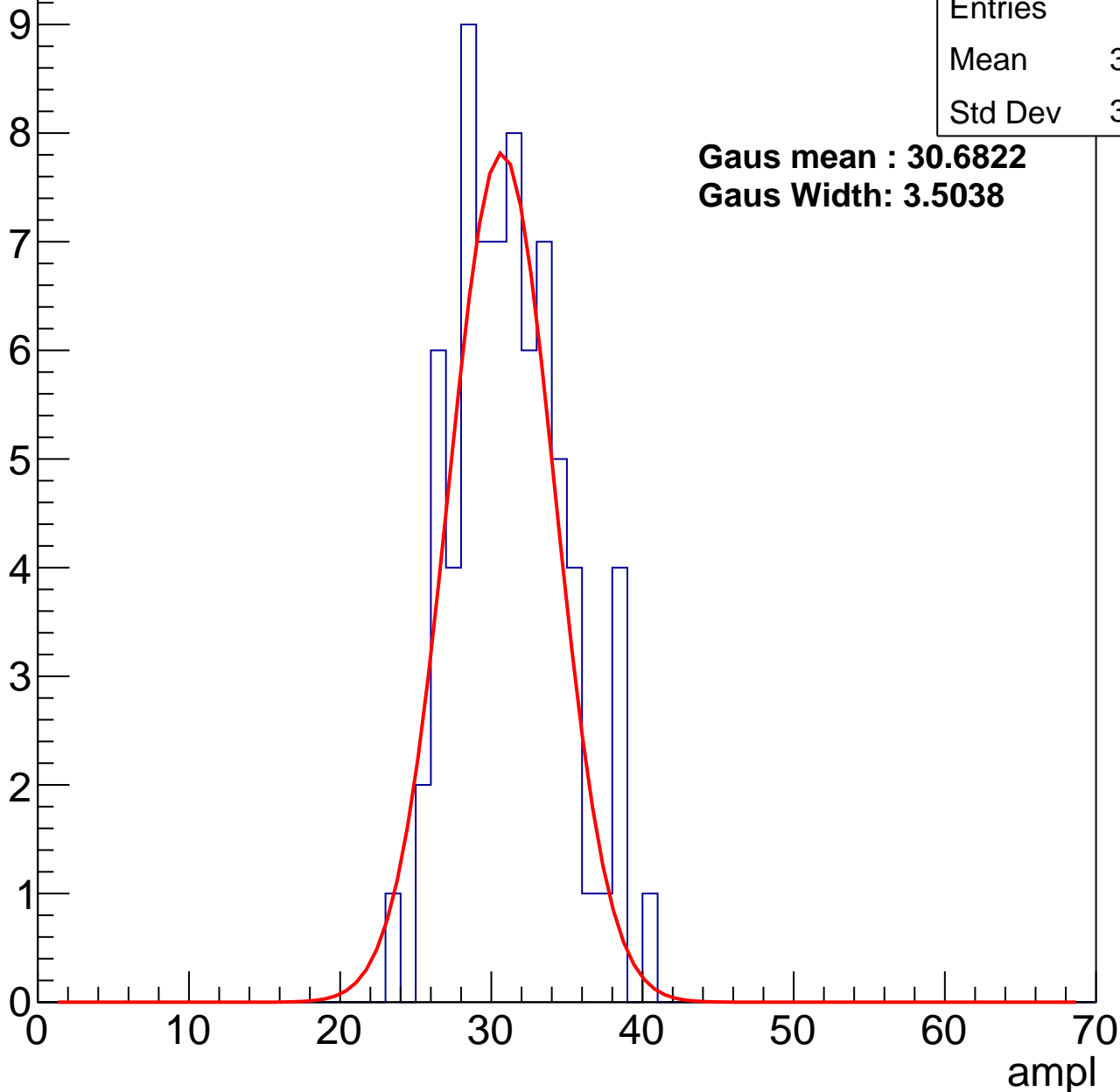
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.79
Std Dev	3.596

**Gaus mean : 30.6822**

**Gaus Width: 3.5038**



# B1L102S, U24-ch64, adc1

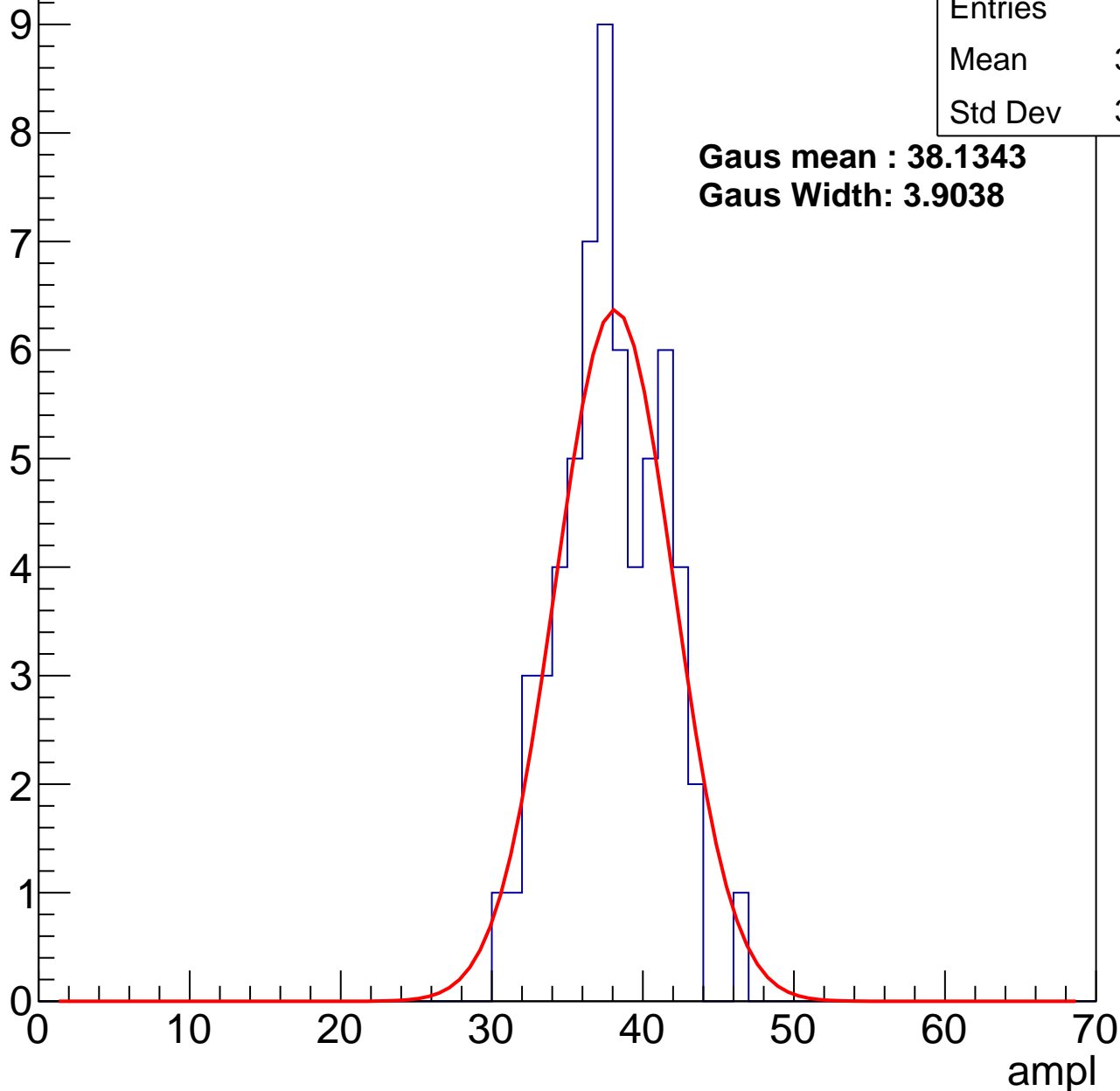
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	37.41
Std Dev	3.331

**Gaus mean : 38.1343**

**Gaus Width: 3.9038**



# B1L102S, U24-ch64, adc2

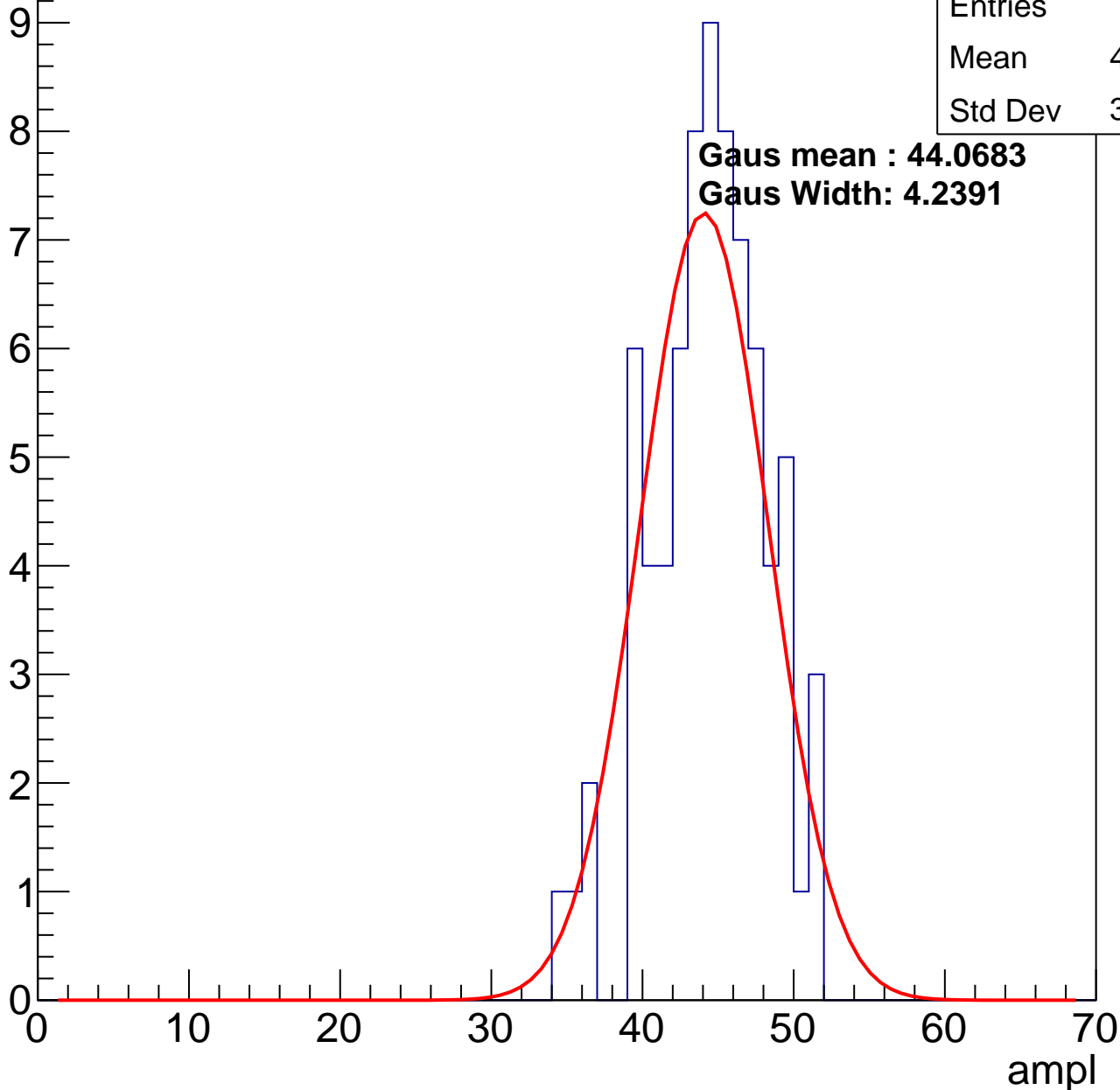
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.93
Std Dev	3.746

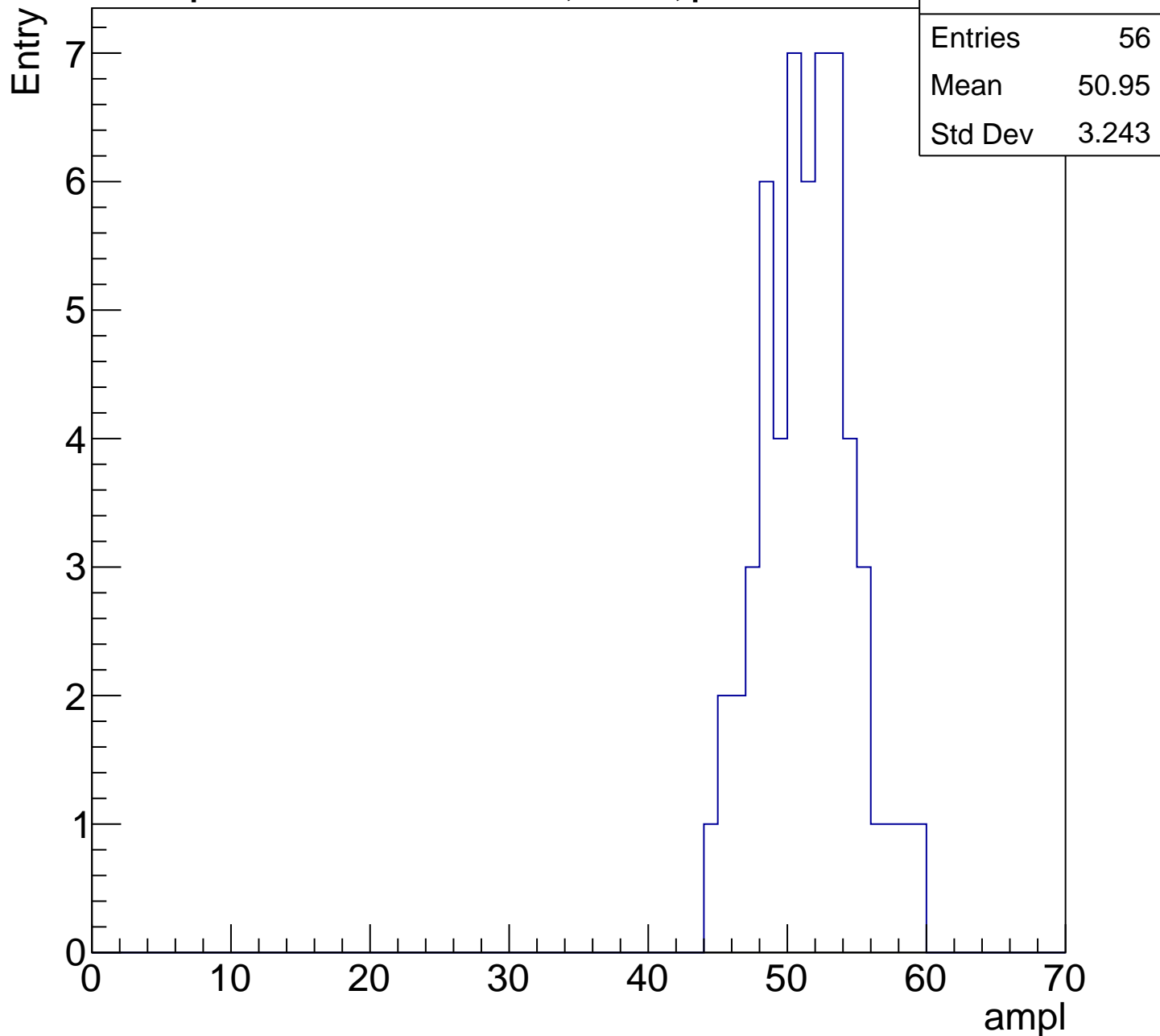
**Gaus mean : 44.0683**

**Gaus Width: 4.2391**



# B1L102S, U24-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

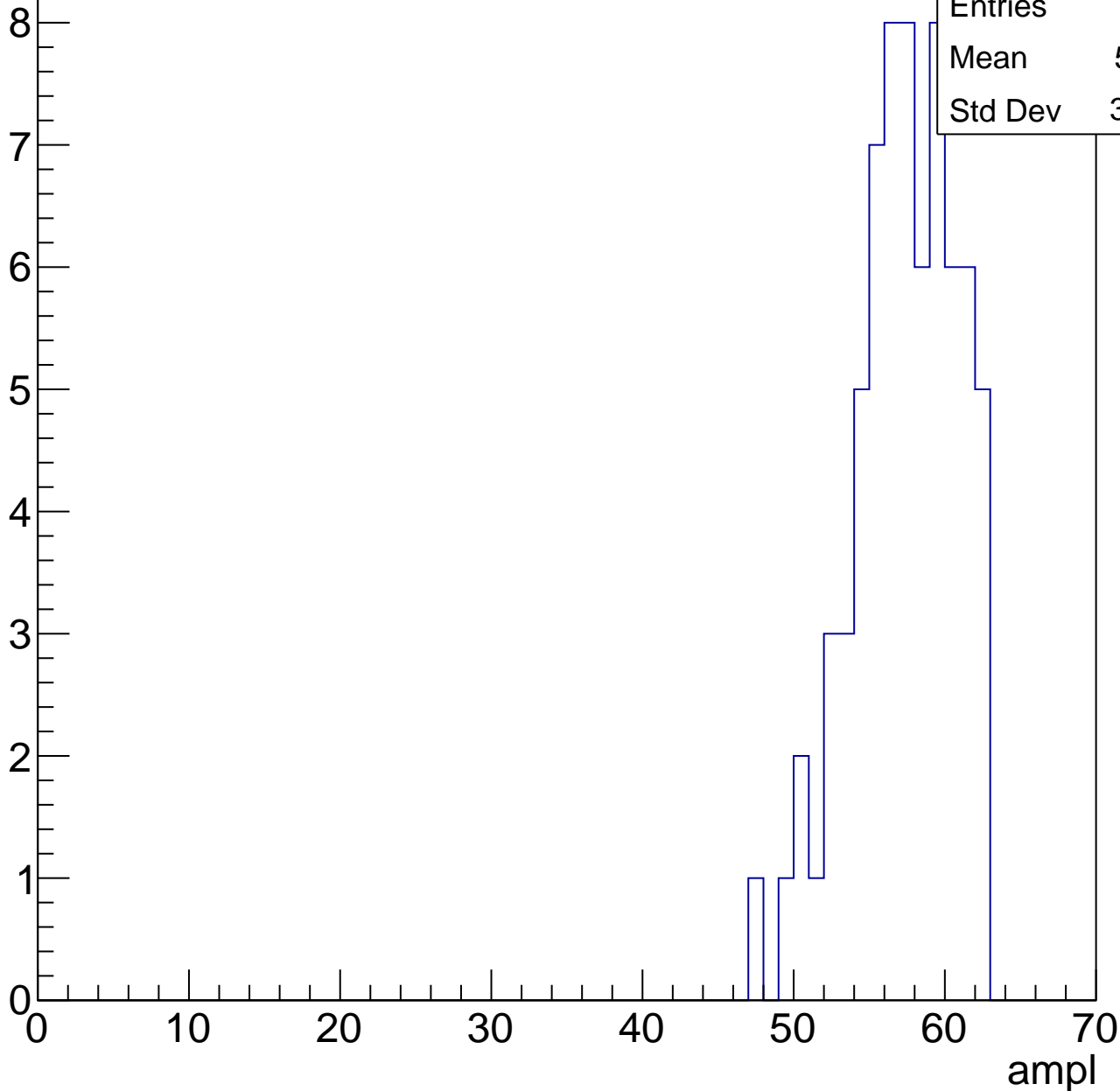


# B1L102S, U24-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	56.81
Std Dev	3.407

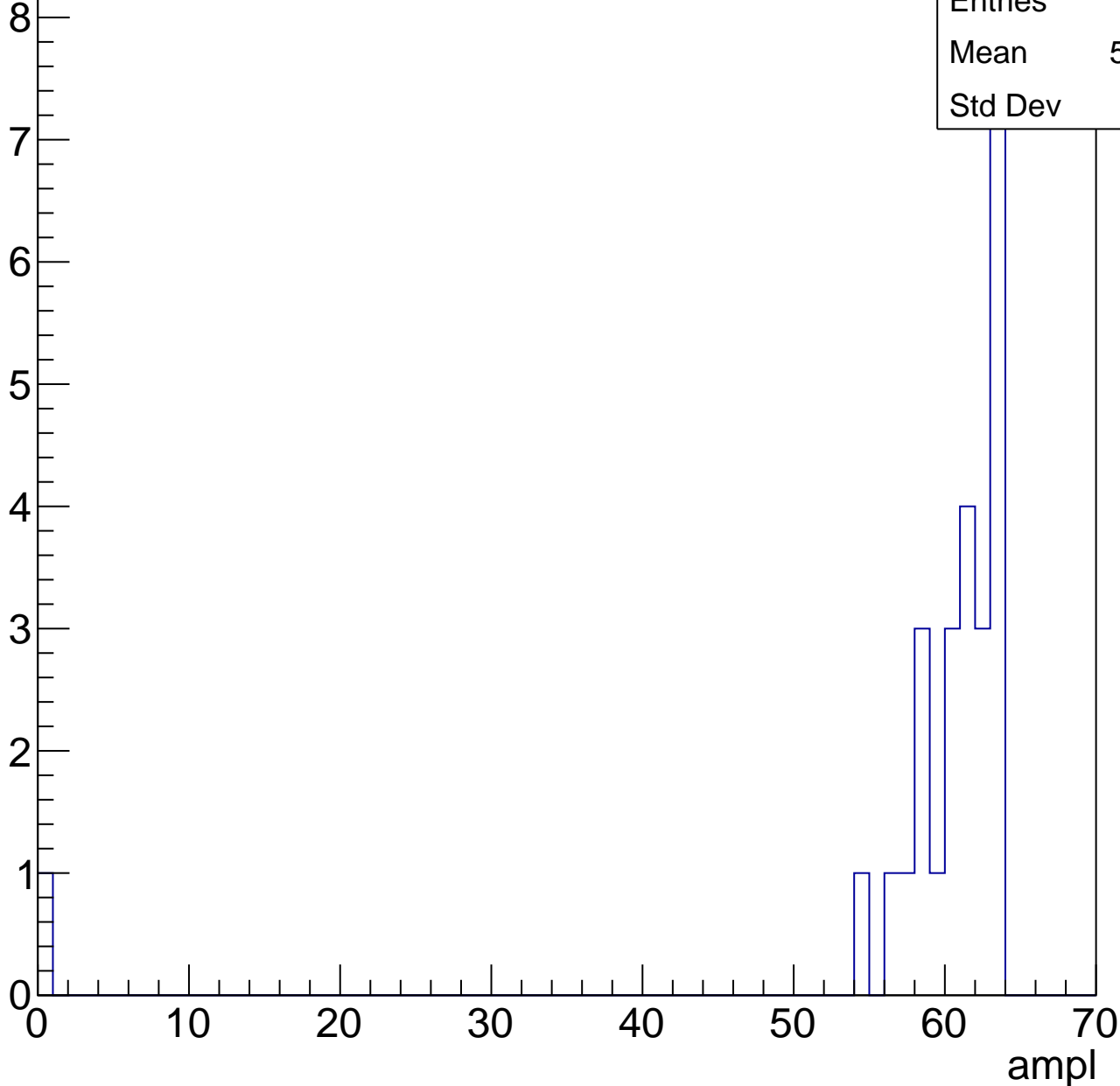


# B1L102S, U24-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	26
Mean	58.23
Std Dev	11.9



# B1L102S, U24-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch65, adc0

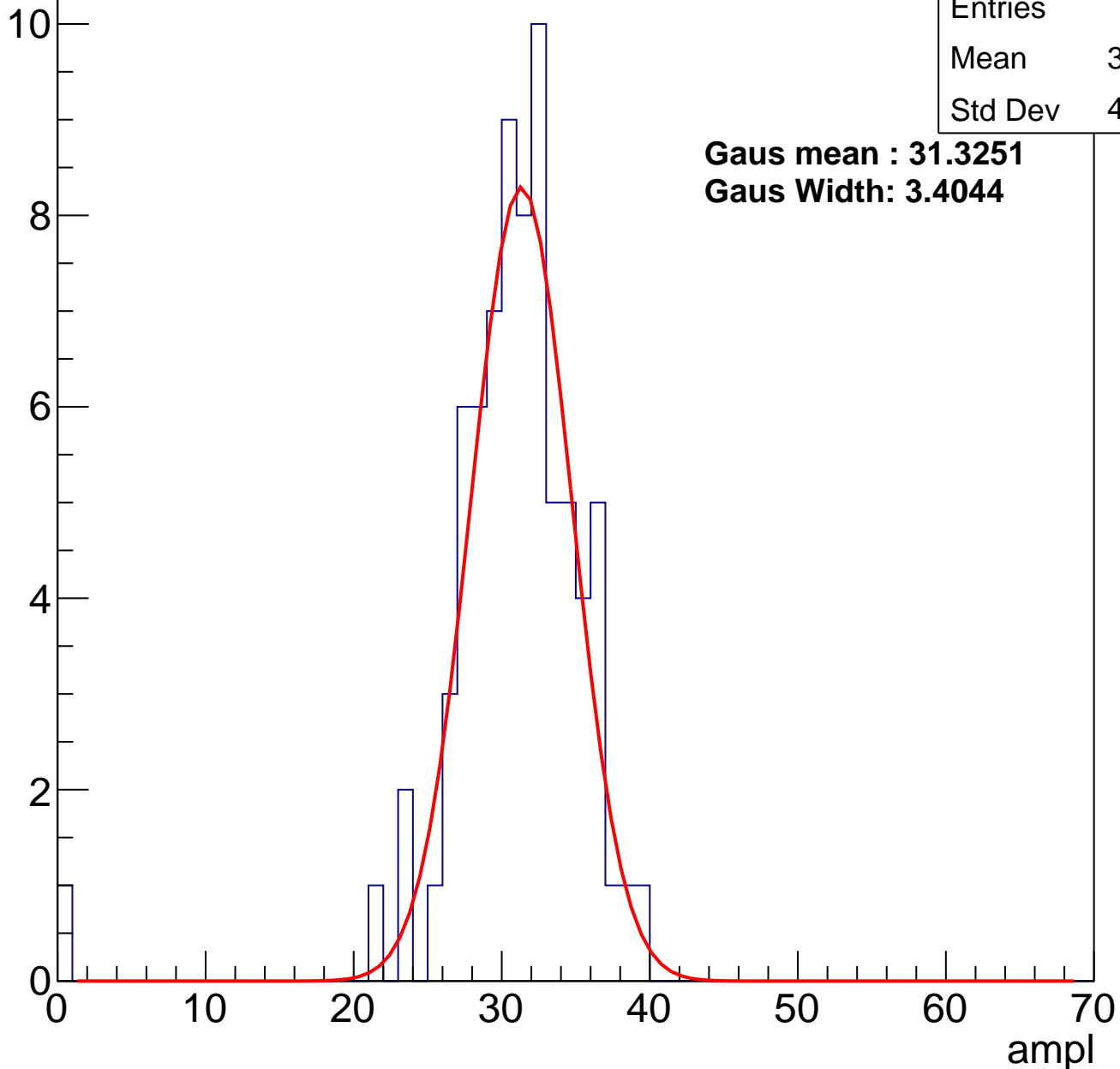
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	76
Mean	30.39
Std Dev	4.963

**Gaus mean : 31.3251**

**Gaus Width: 3.4044**

Entry



# B1L102S, U24-ch65, adc1

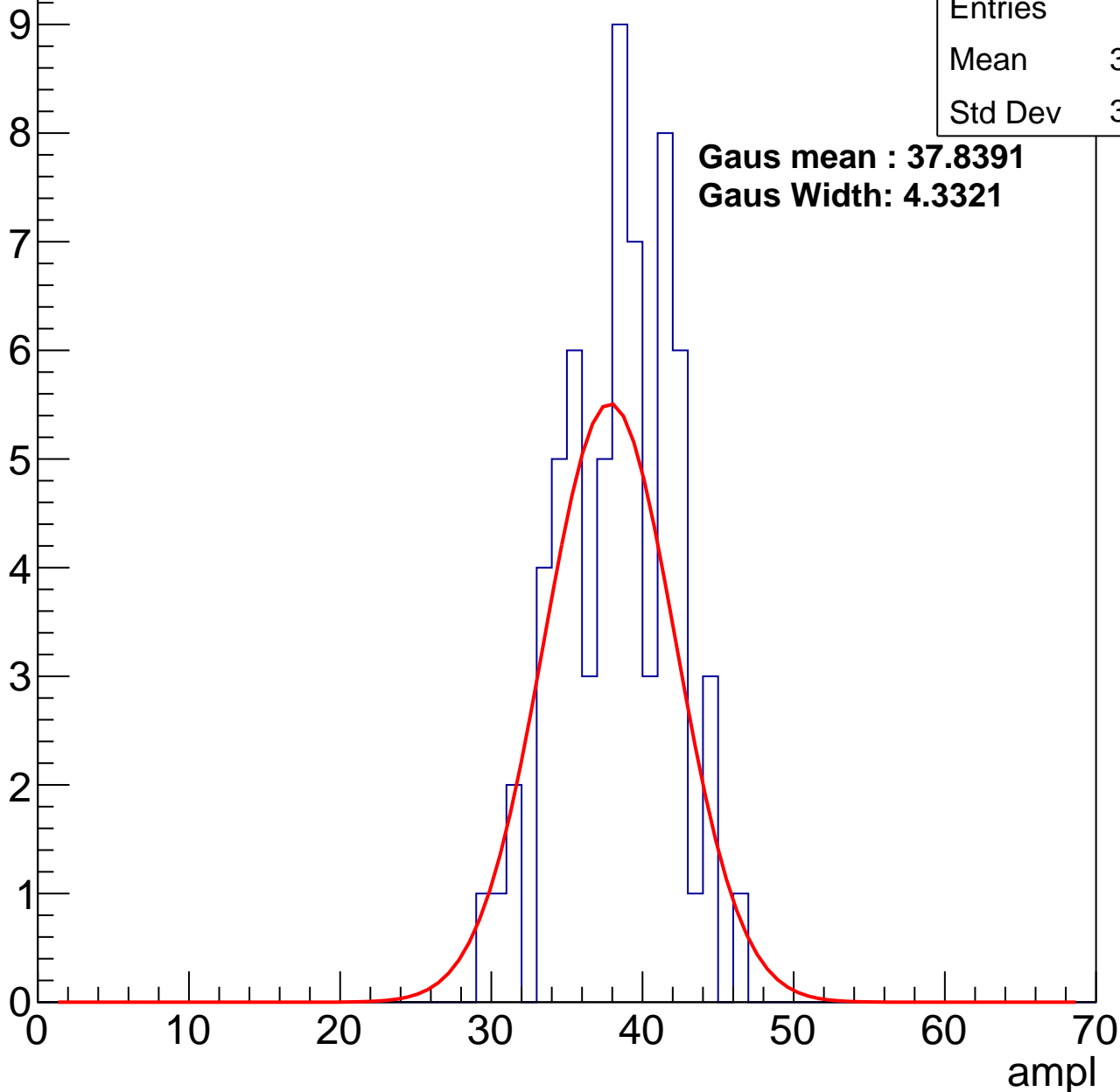
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.88
Std Dev	3.673

**Gaus mean : 37.8391**

**Gaus Width: 4.3321**



# B1L102S, U24-ch65, adc2

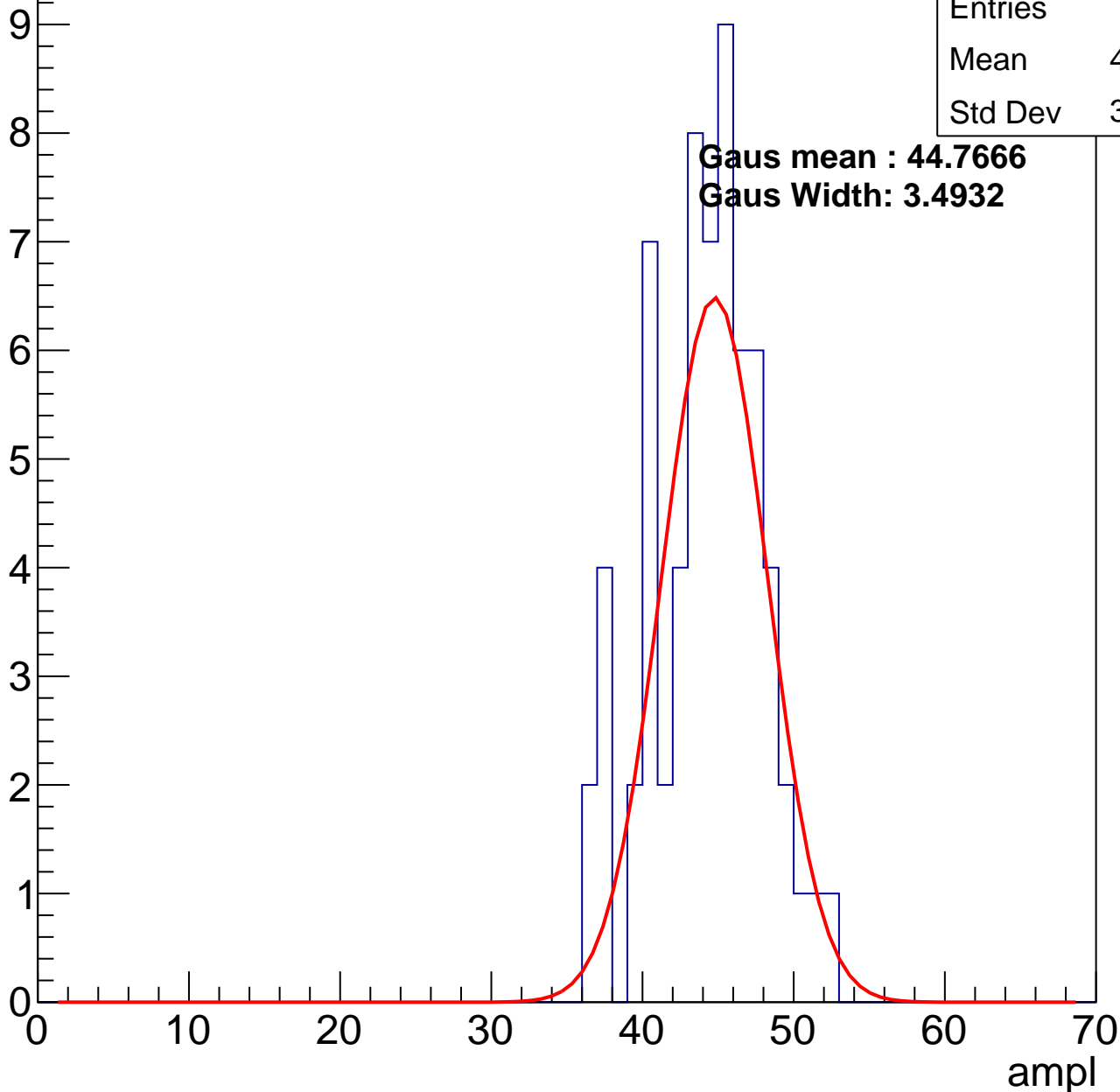
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.73
Std Dev	3.637

**Gaus mean : 44.7666**

**Gaus Width: 3.4932**

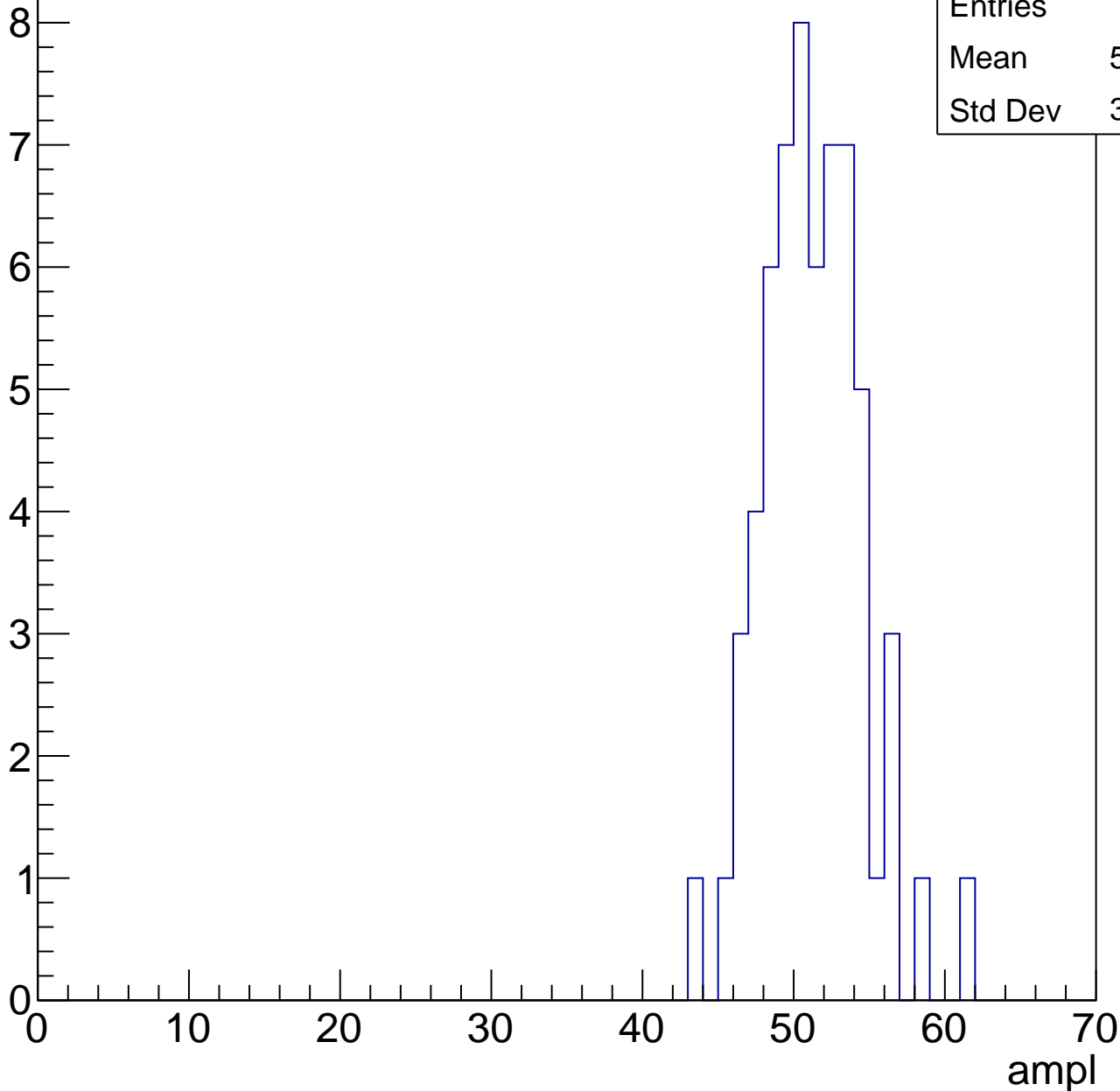


# B1L102S, U24-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

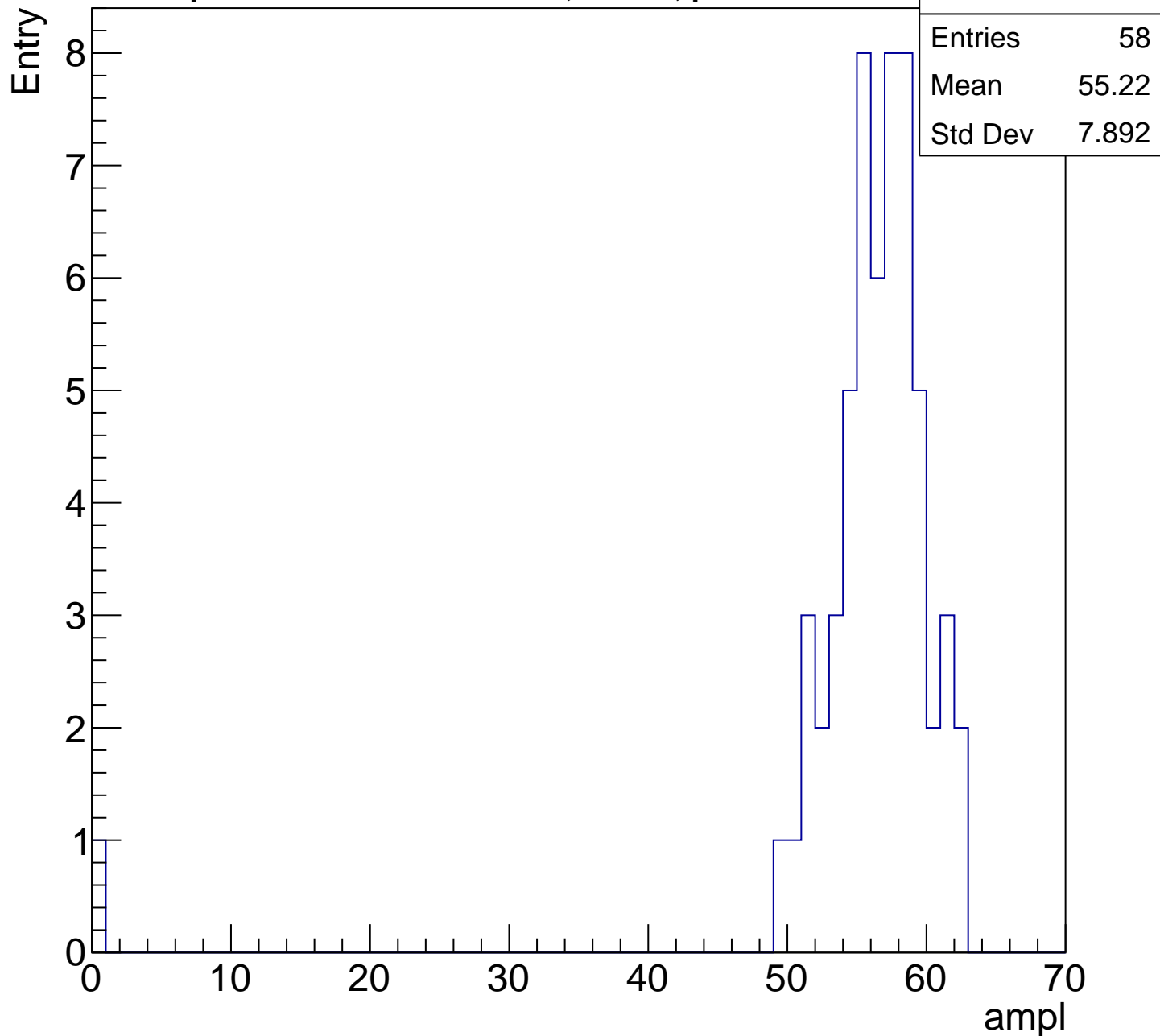
Entry

Entries	61
Mean	50.79
Std Dev	3.255



# B1L102S, U24-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2



# B1L102S, U24-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	39
Mean	60.69
Std Dev	2.102

Entry

10

8

6

4

2

0

0

10

20

30

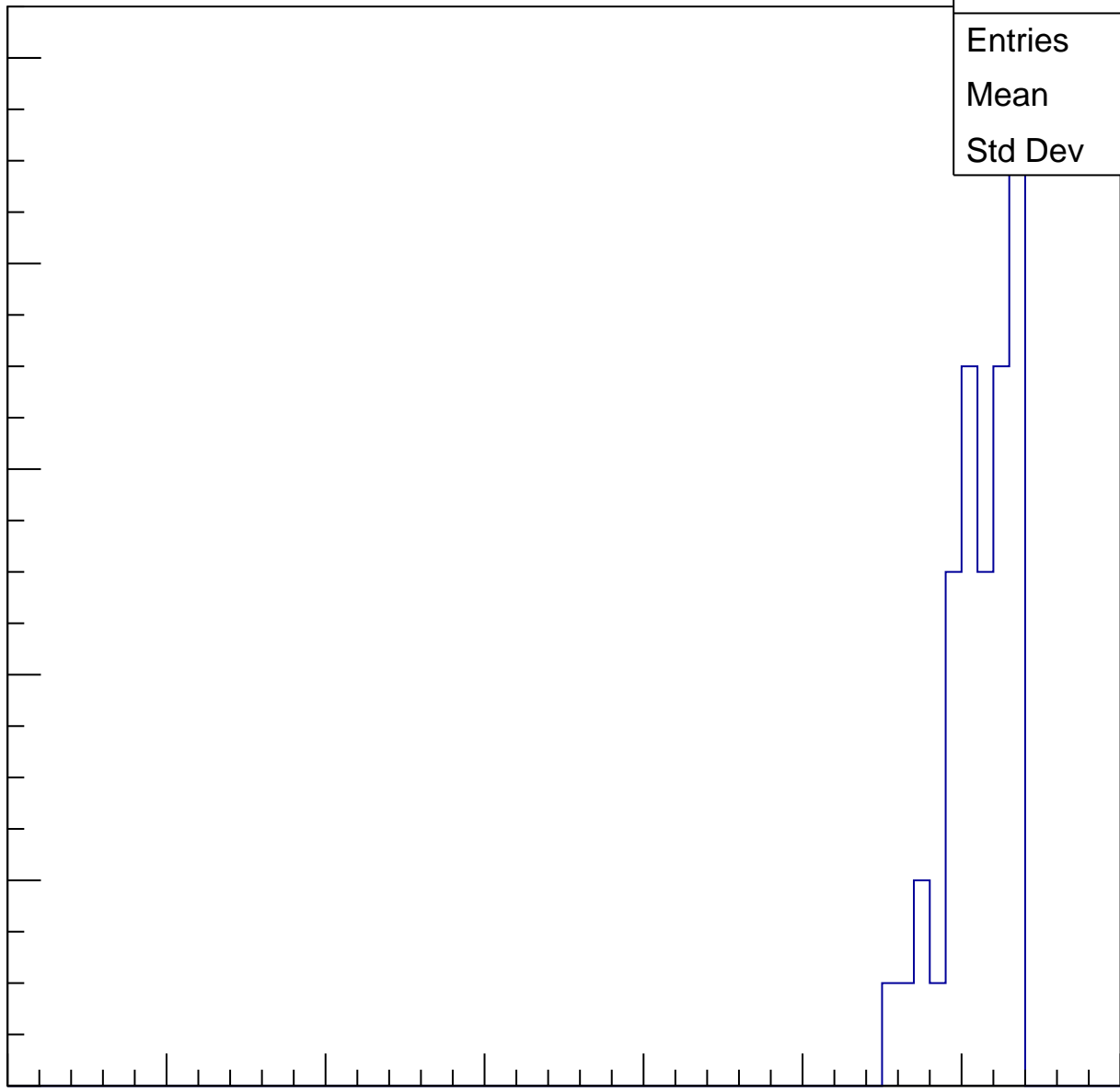
40

50

60

70

ampl



# B1L102S, U24-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch66, adc0

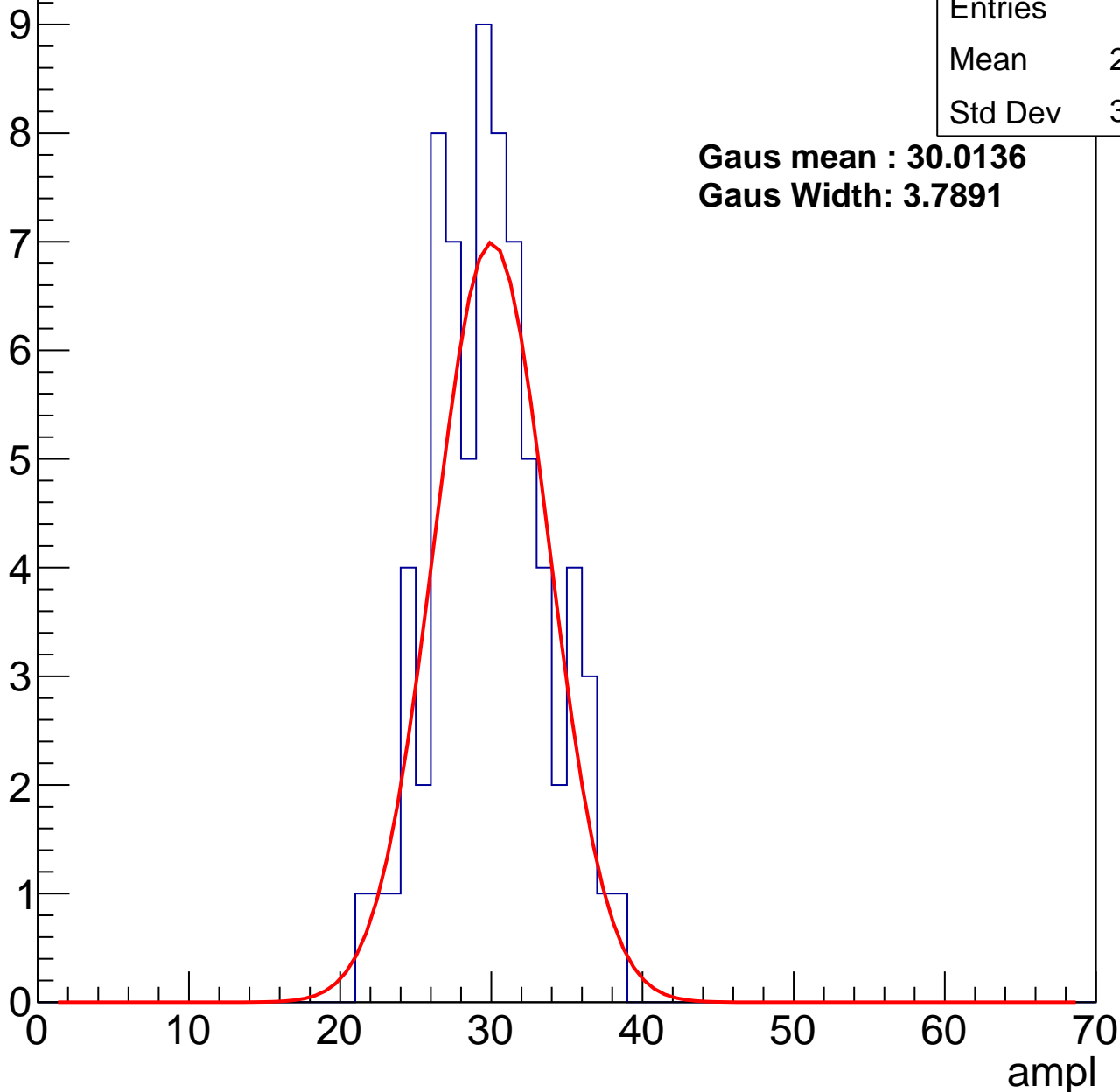
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.45
Std Dev	3.705

**Gaus mean : 30.0136**

**Gaus Width: 3.7891**



# B1L102S, U24-ch66, adc1

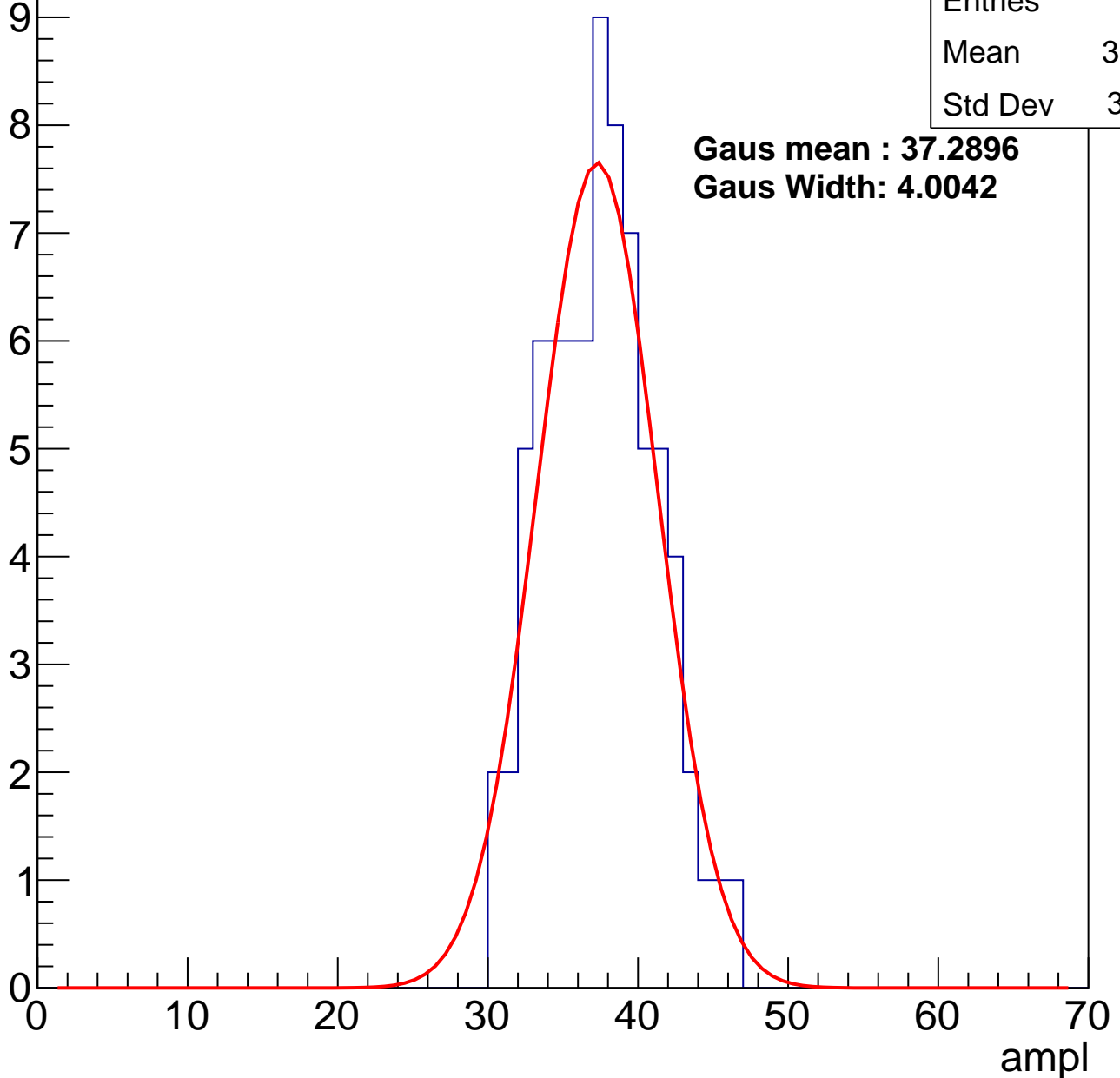
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.03
Std Dev	3.631

**Gaus mean : 37.2896**

**Gaus Width: 4.0042**



# B1L102S, U24-ch66, adc2

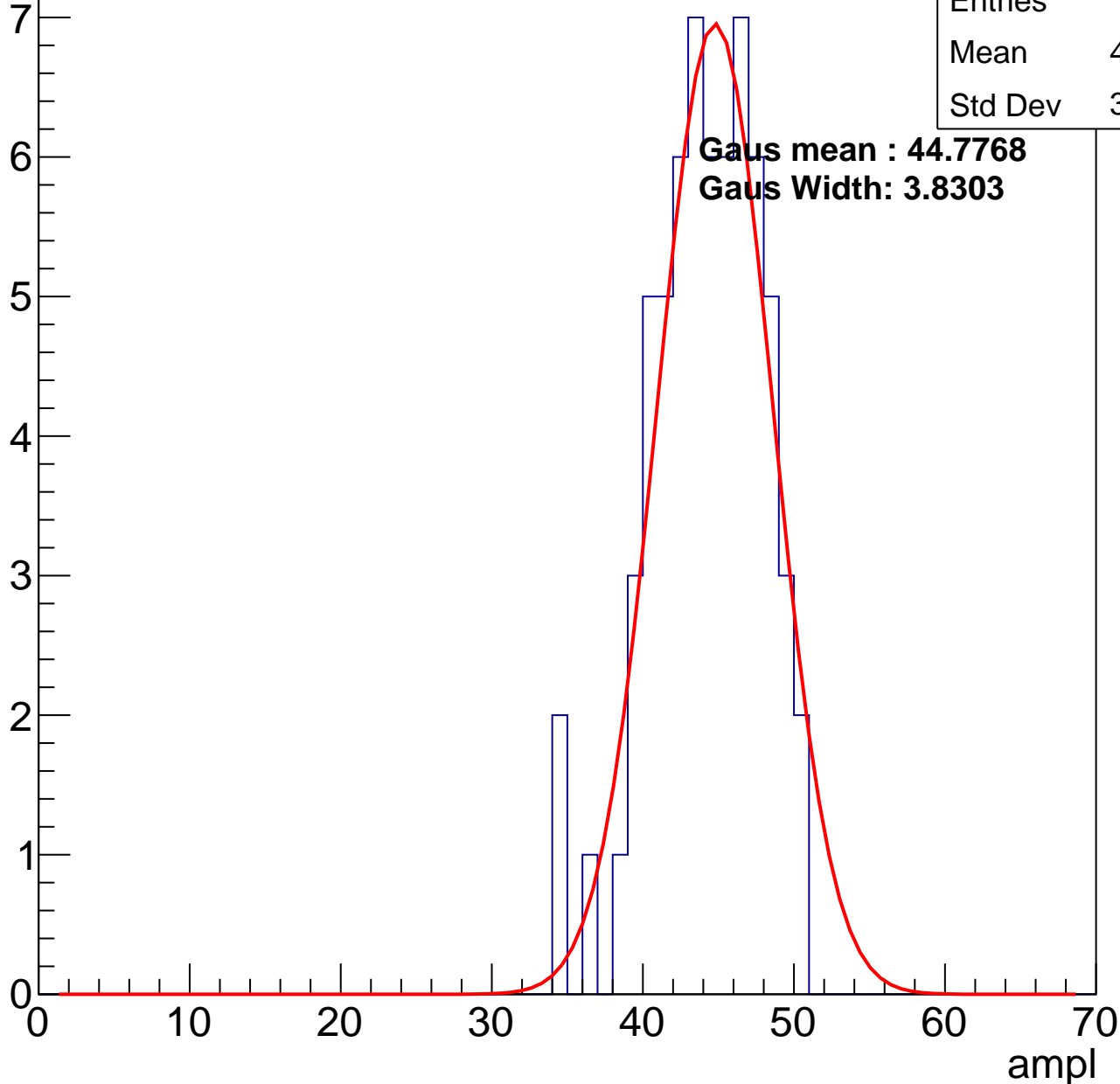
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.72
Std Dev	3.597

**Gaus mean : 44.7768**

**Gaus Width: 3.8303**

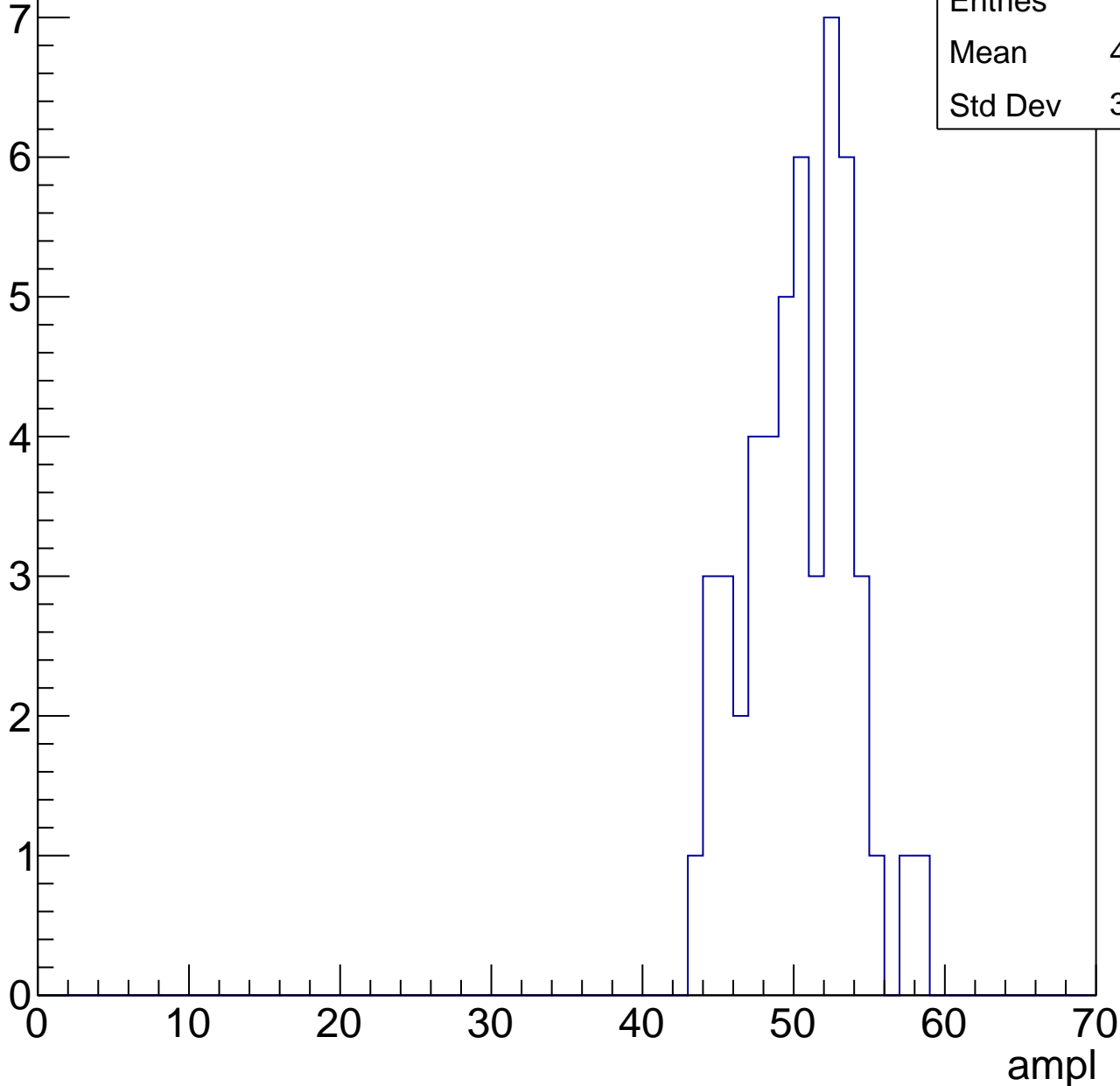


# B1L102S, U24-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

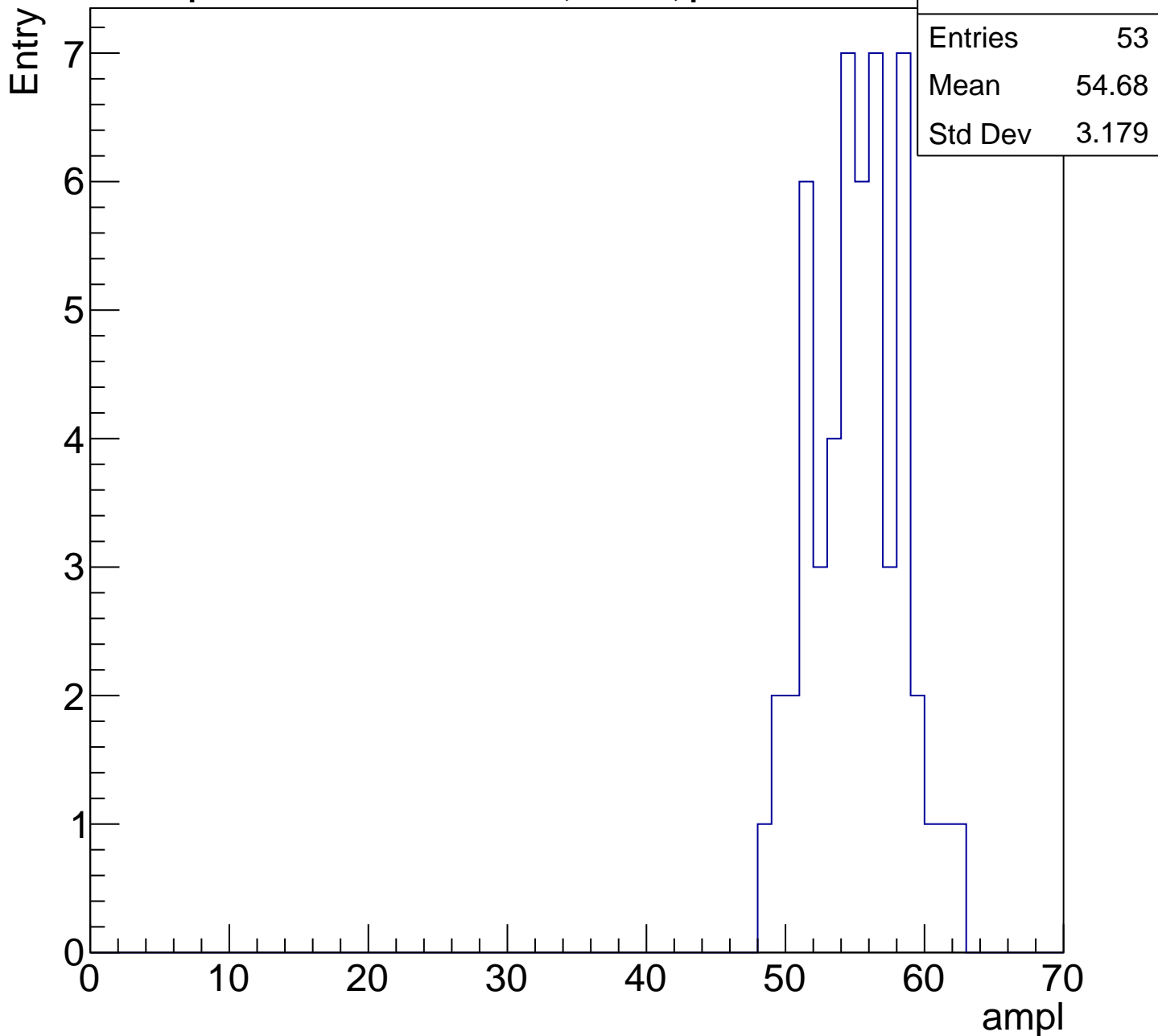
Entry

Entries	50
Mean	49.88
Std Dev	3.439



# B1L102S, U24-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

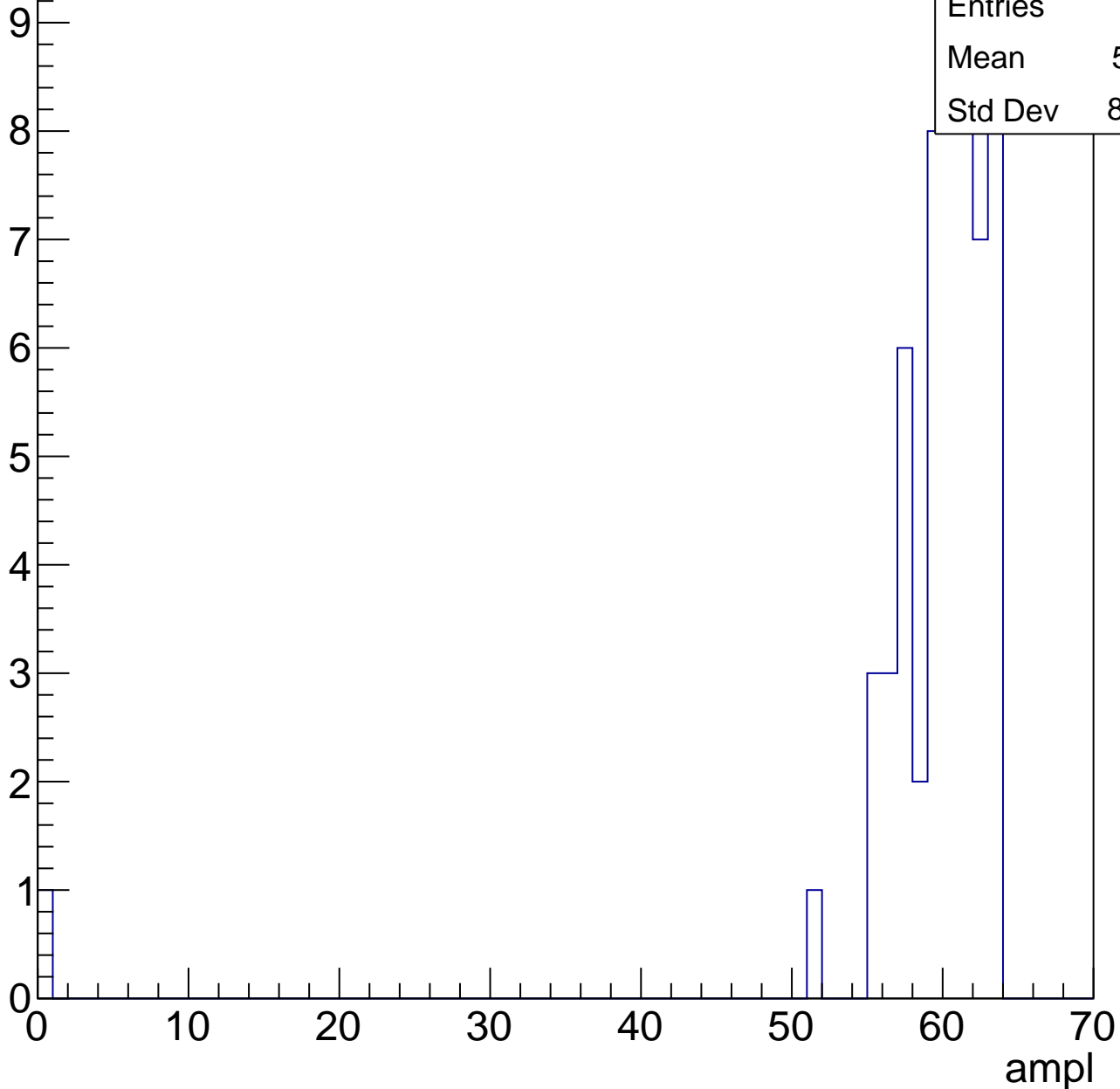


# B1L102S, U24-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.61
Std Dev	8.244



# B1L102S, U24-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch67, adc0

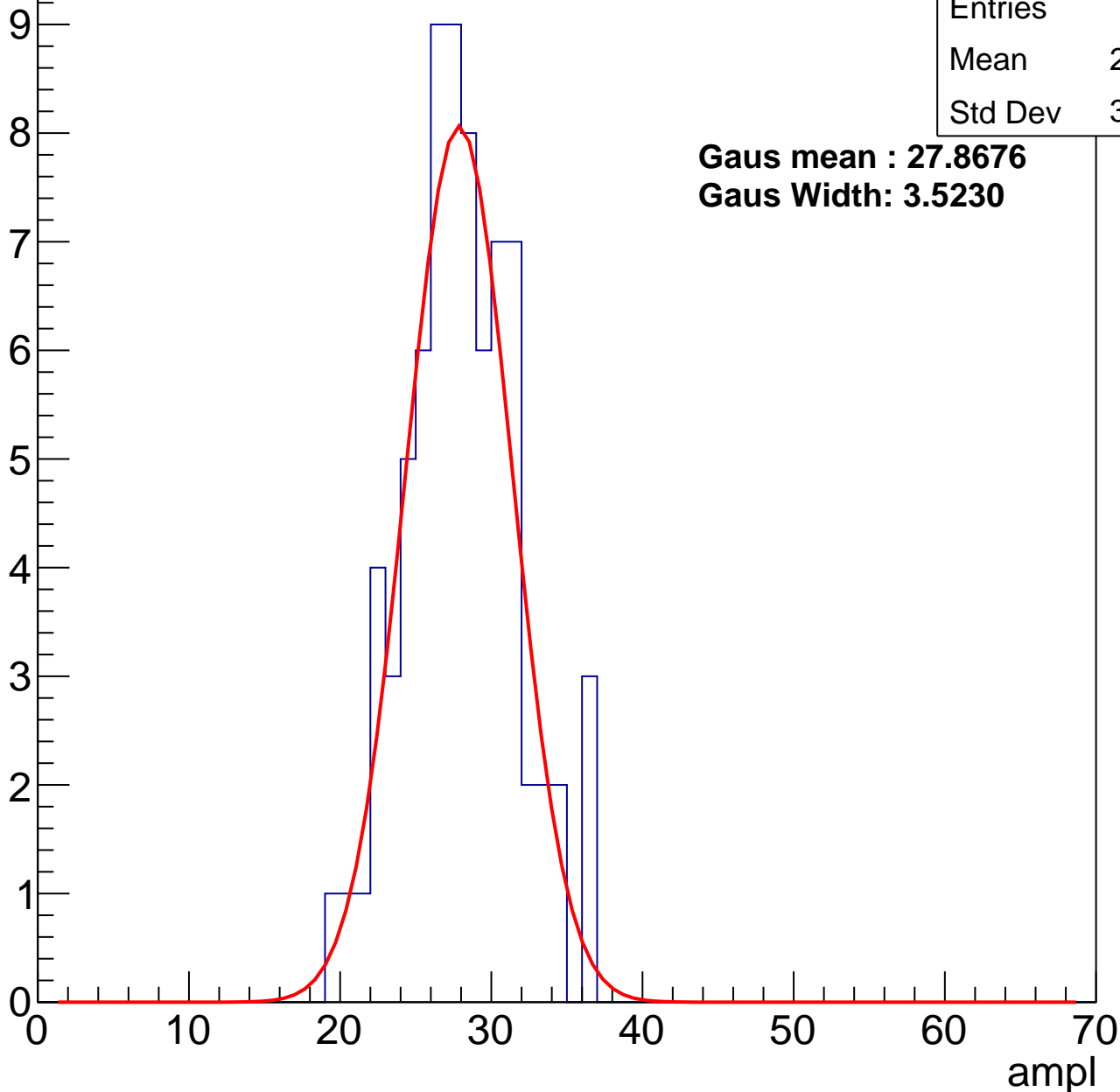
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.57
Std Dev	3.665

**Gaus mean : 27.8676**

**Gaus Width: 3.5230**



# B1L102S, U24-ch67, adc1

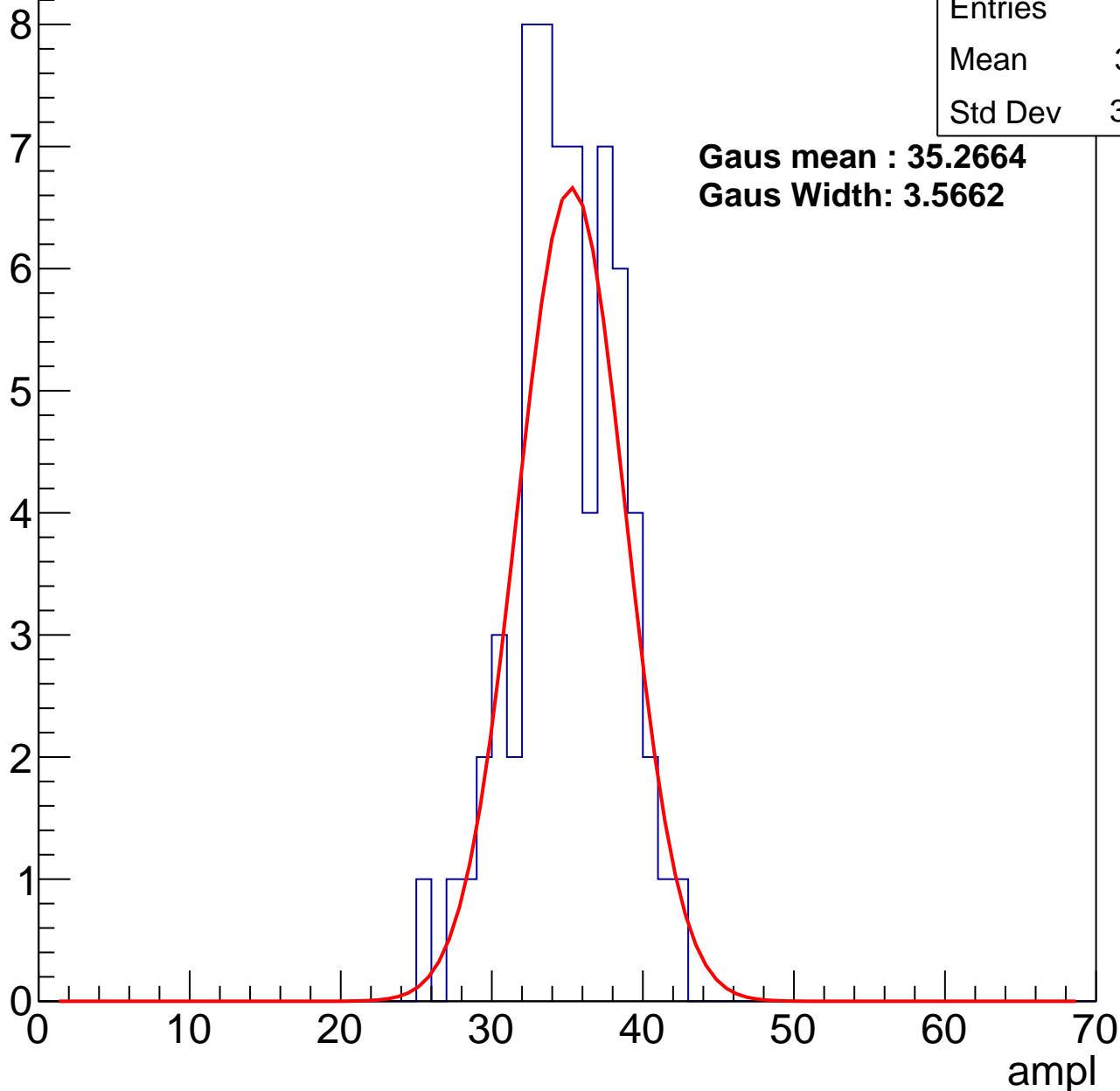
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.51
Std Dev	3.447

**Gaus mean : 35.2664**

**Gaus Width: 3.5662**



# B1L102S, U24-ch67, adc2

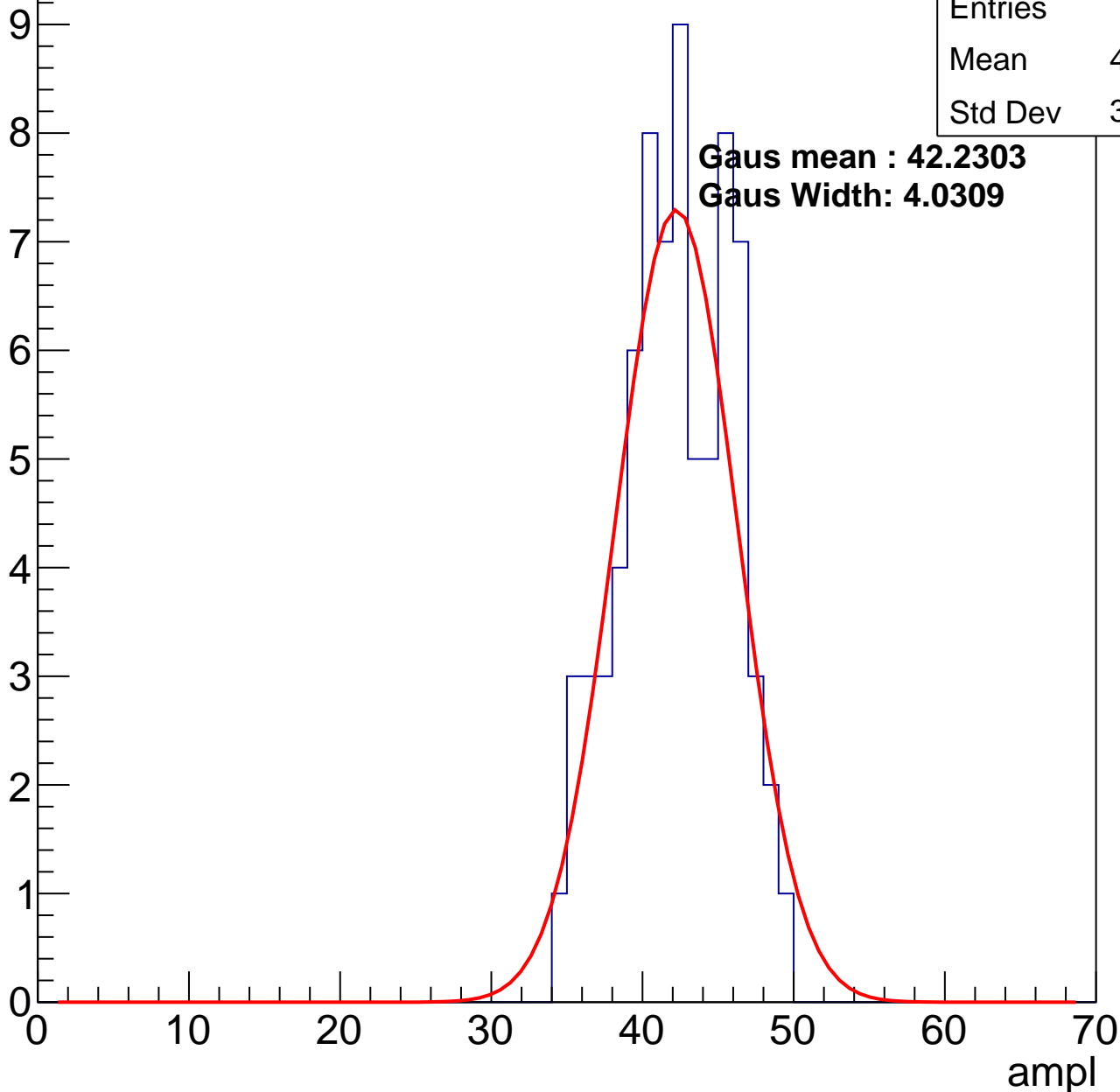
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.76
Std Dev	3.562

**Gaus mean : 42.2303**

**Gaus Width: 4.0309**

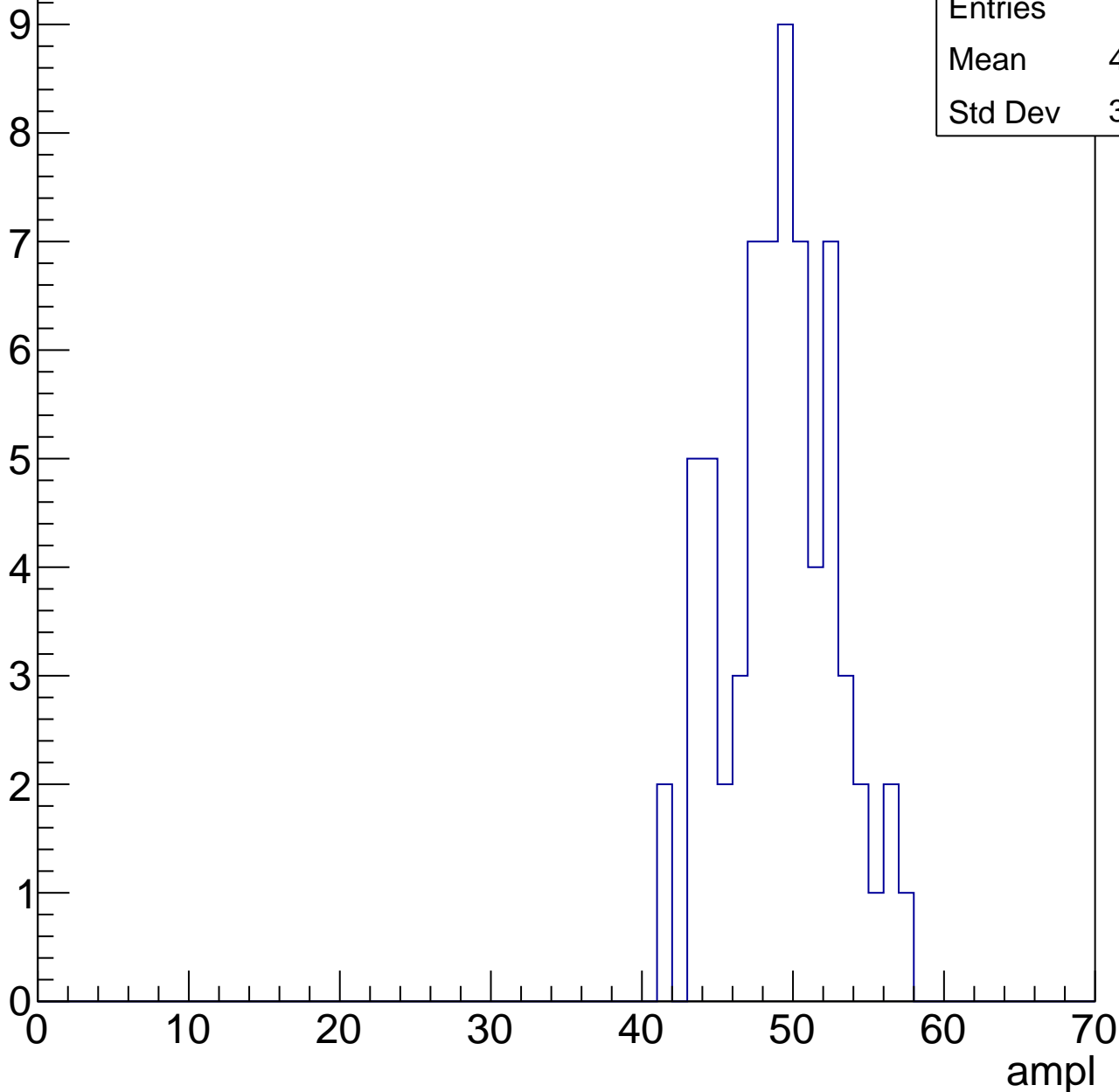


# B1L102S, U24-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.66
Std Dev	3.667

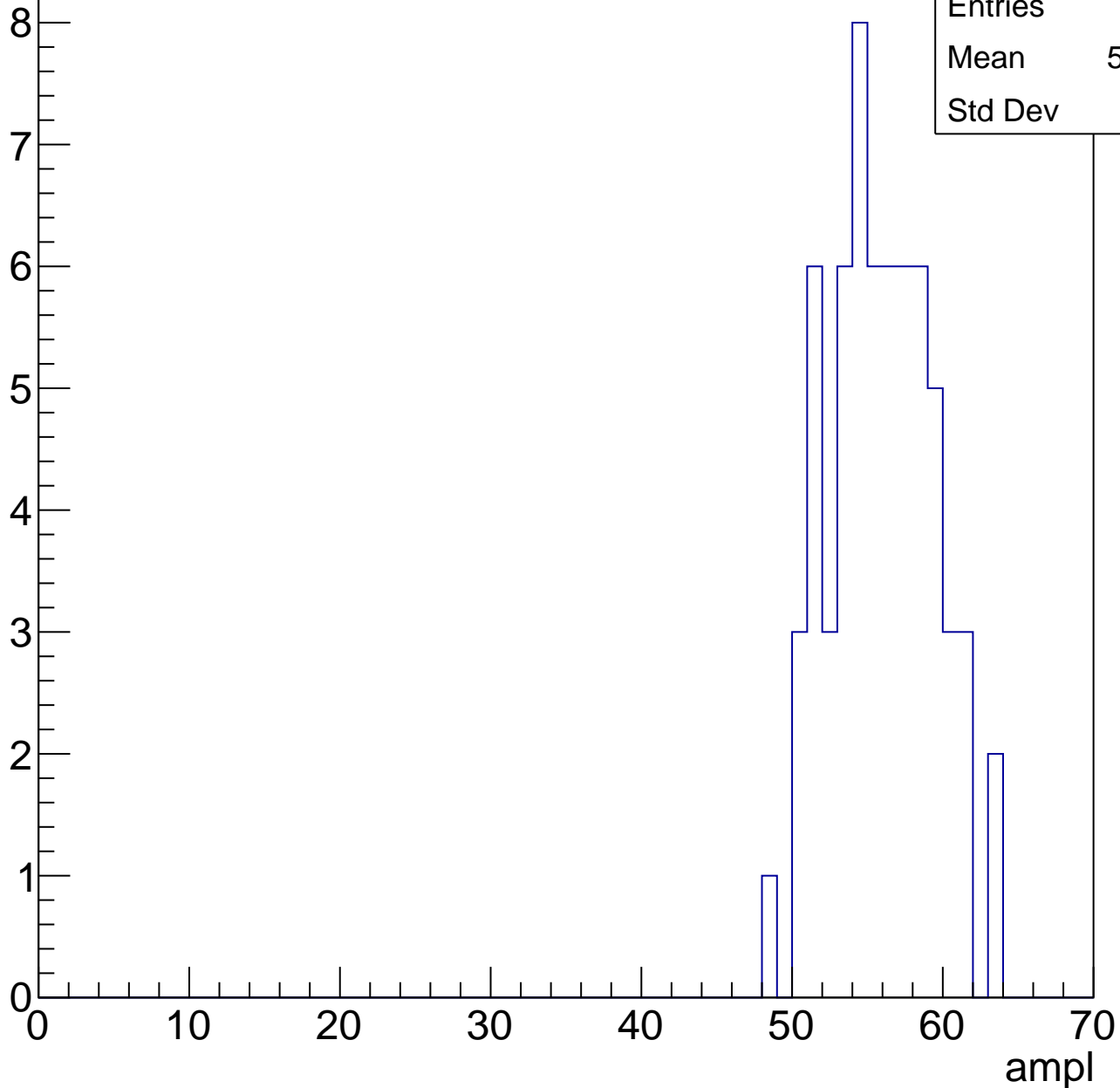


# B1L102S, U24-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.47
Std Dev	3.4

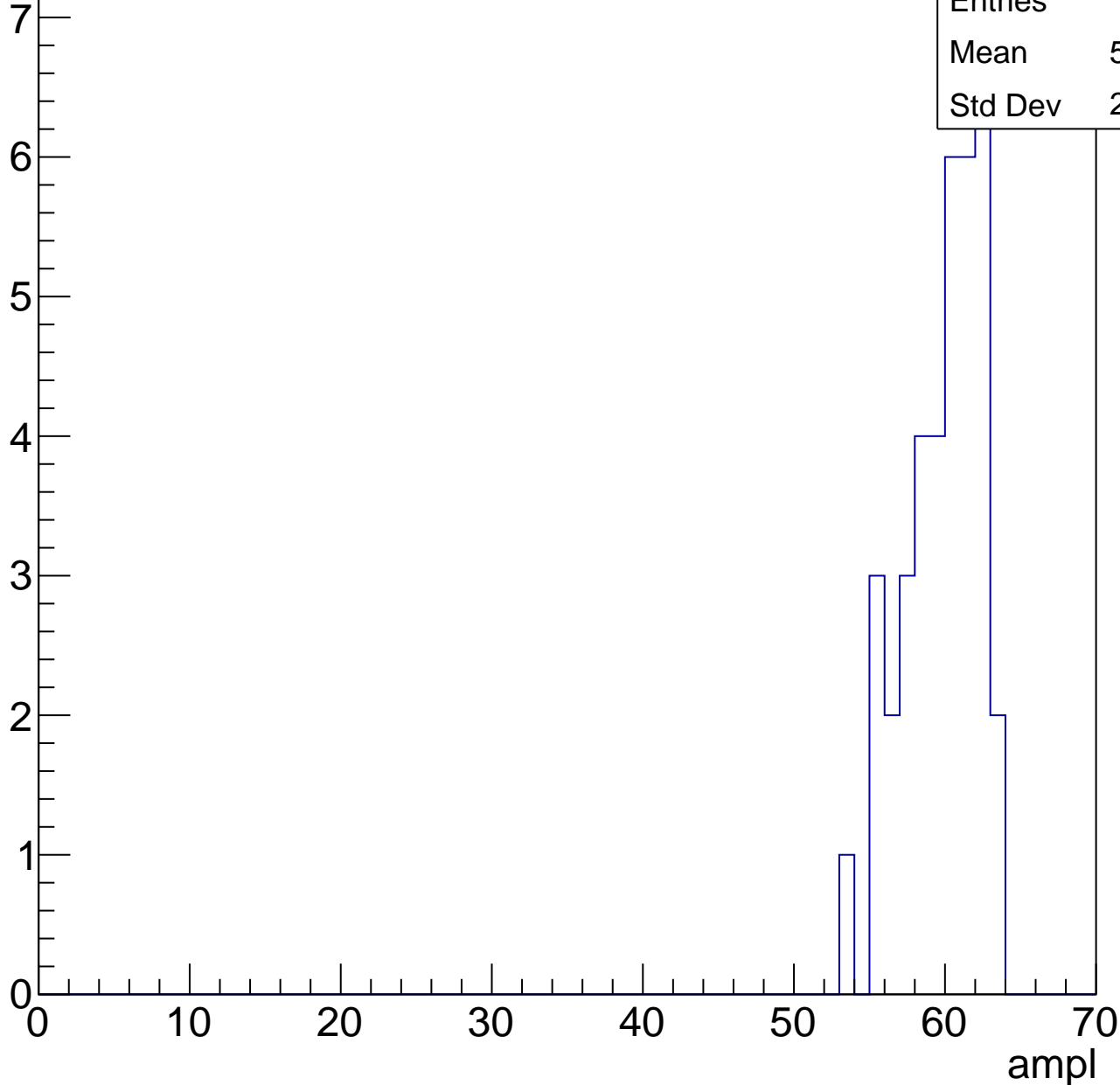


# B1L102S, U24-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.34
Std Dev	2.506

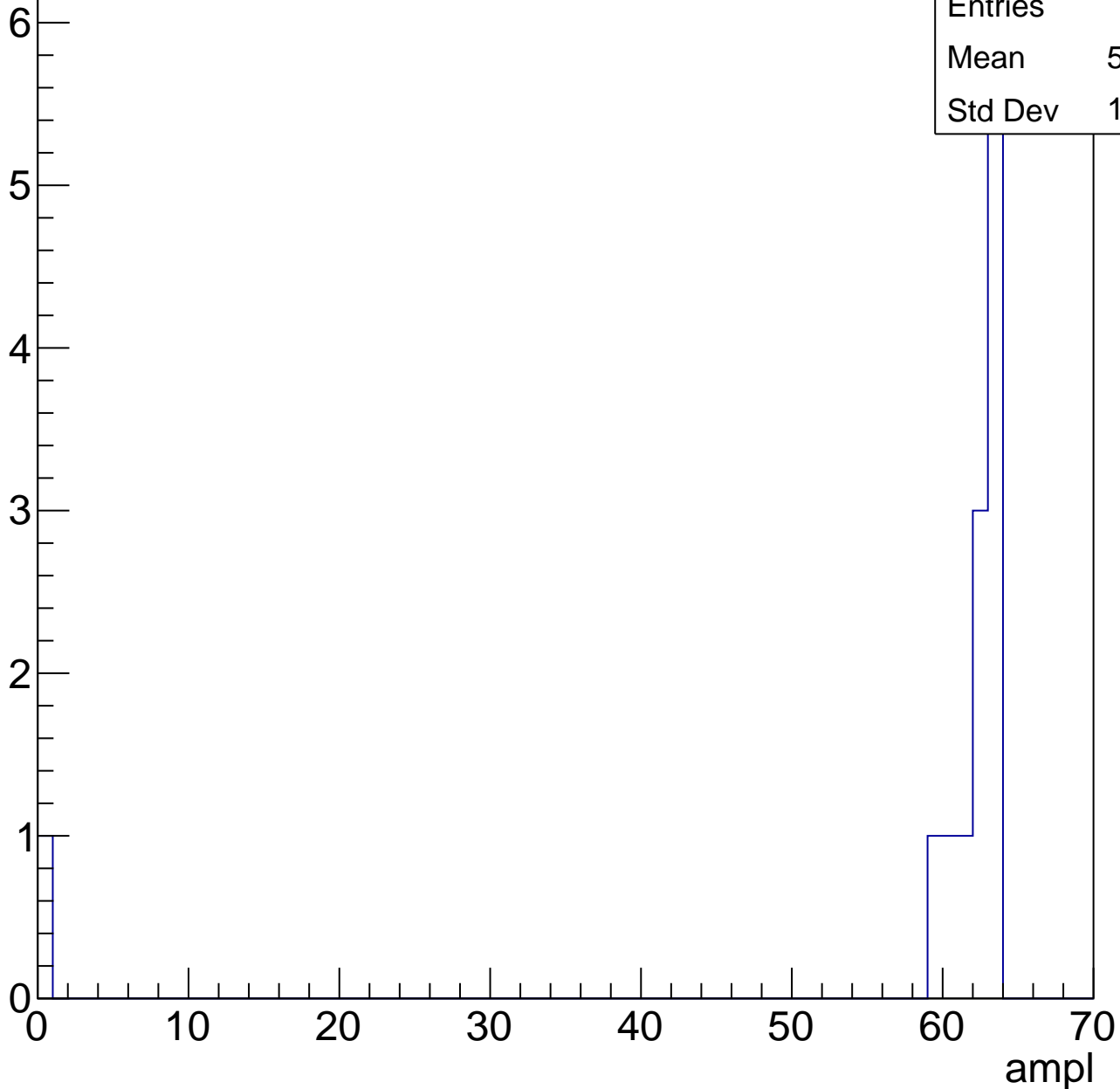


# B1L102S, U24-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	57.23
Std Dev	16.57





# B1L102S, U24-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch68, adc0

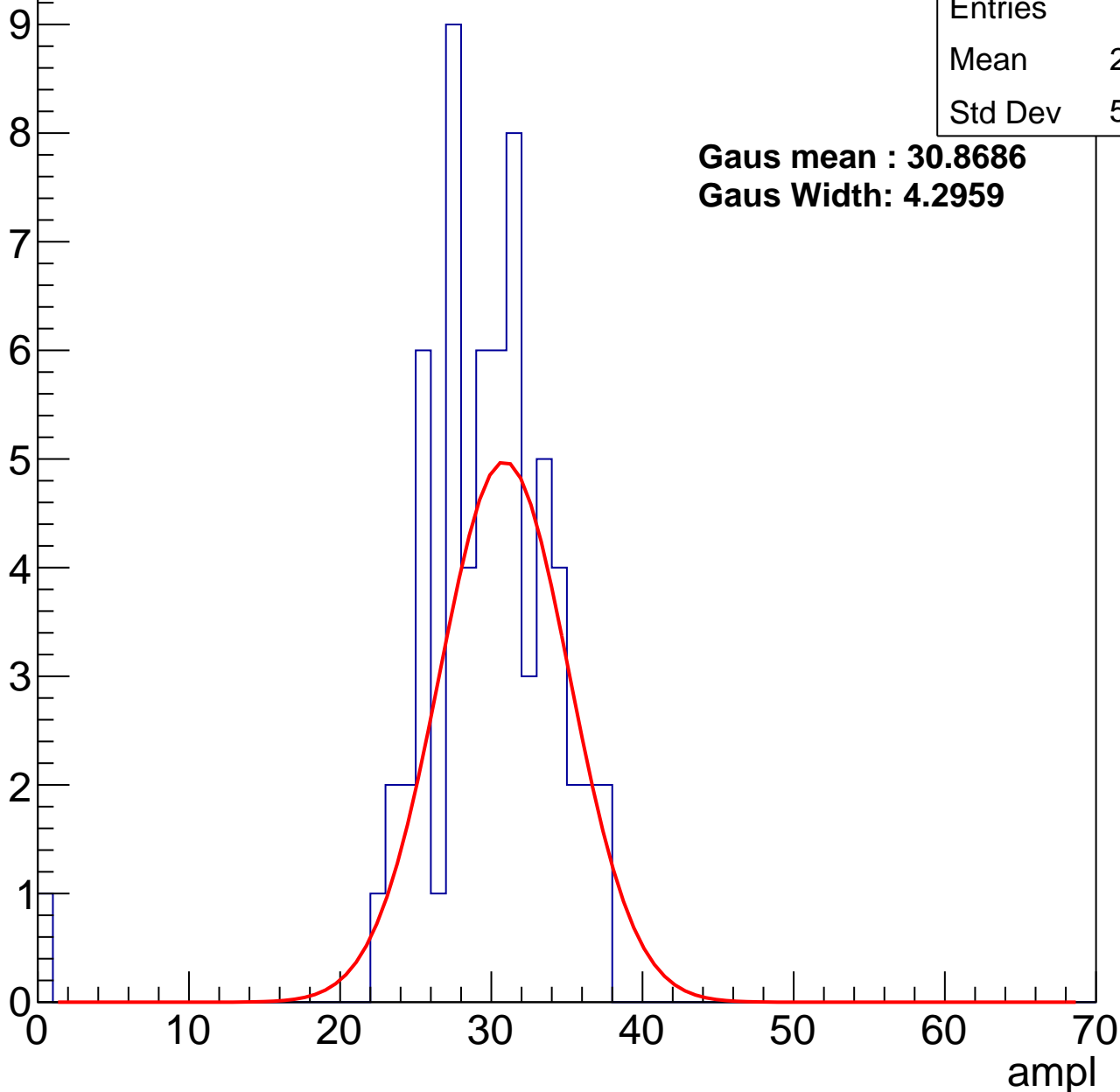
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	29.09
Std Dev	5.153

**Gaus mean : 30.8686**

**Gaus Width: 4.2959**



# B1L102S, U24-ch68, adc1

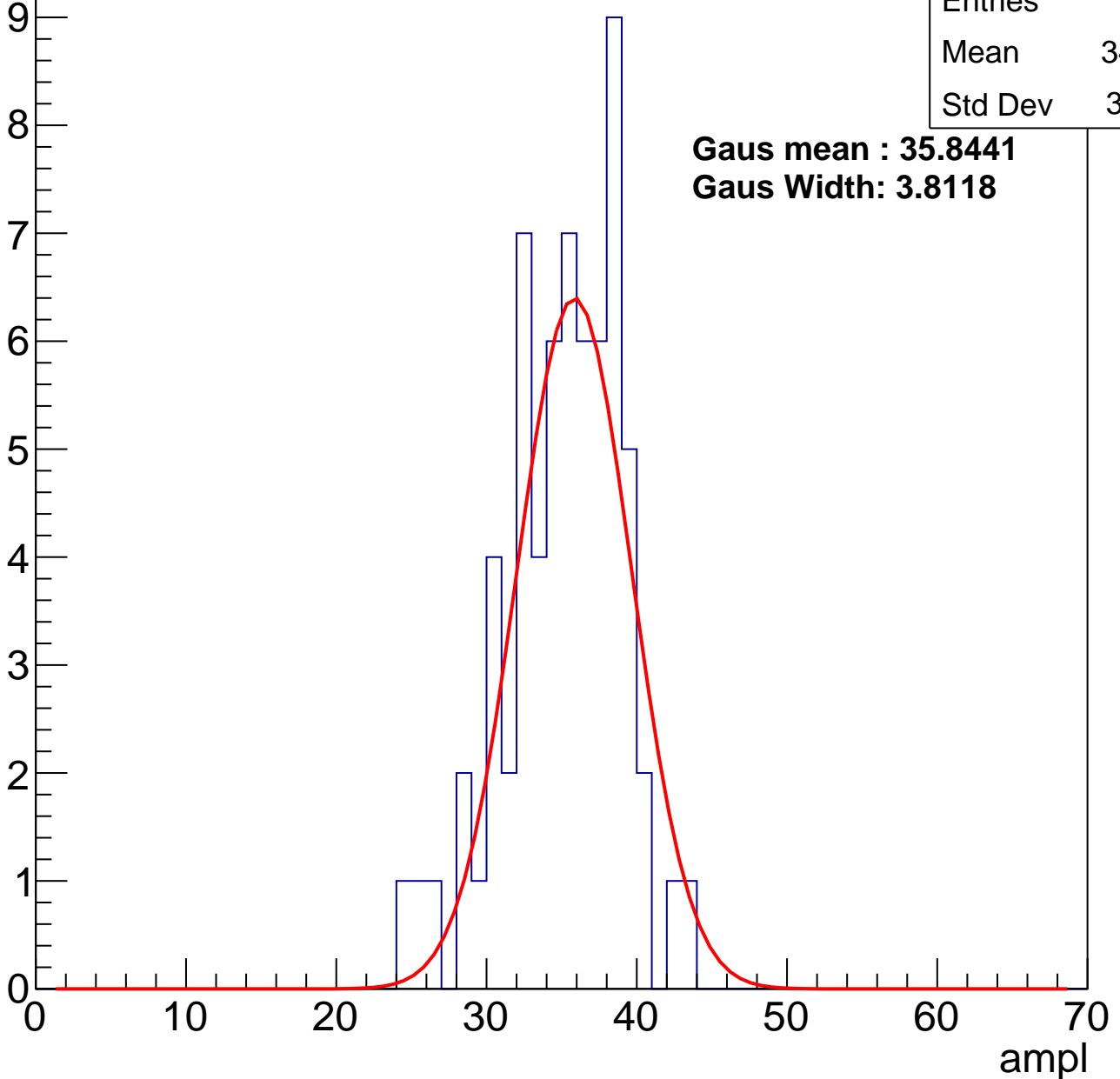
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.65
Std Dev	3.891

**Gaus mean : 35.8441**

**Gaus Width: 3.8118**



# B1L102S, U24-ch68, adc2

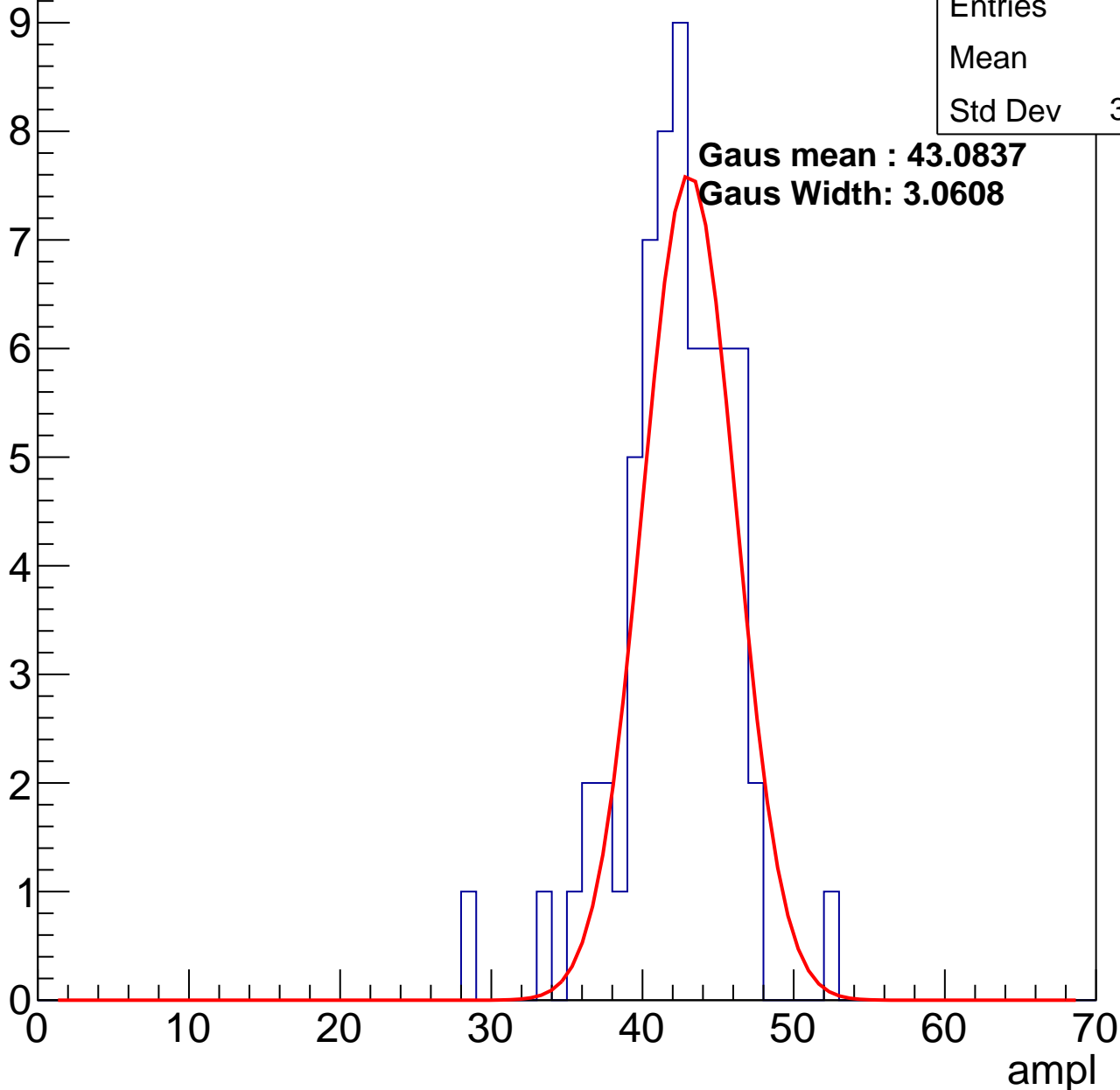
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.8
Std Dev	3.692

**Gaus mean : 43.0837**

**Gaus Width: 3.0608**



# B1L102S, U24-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

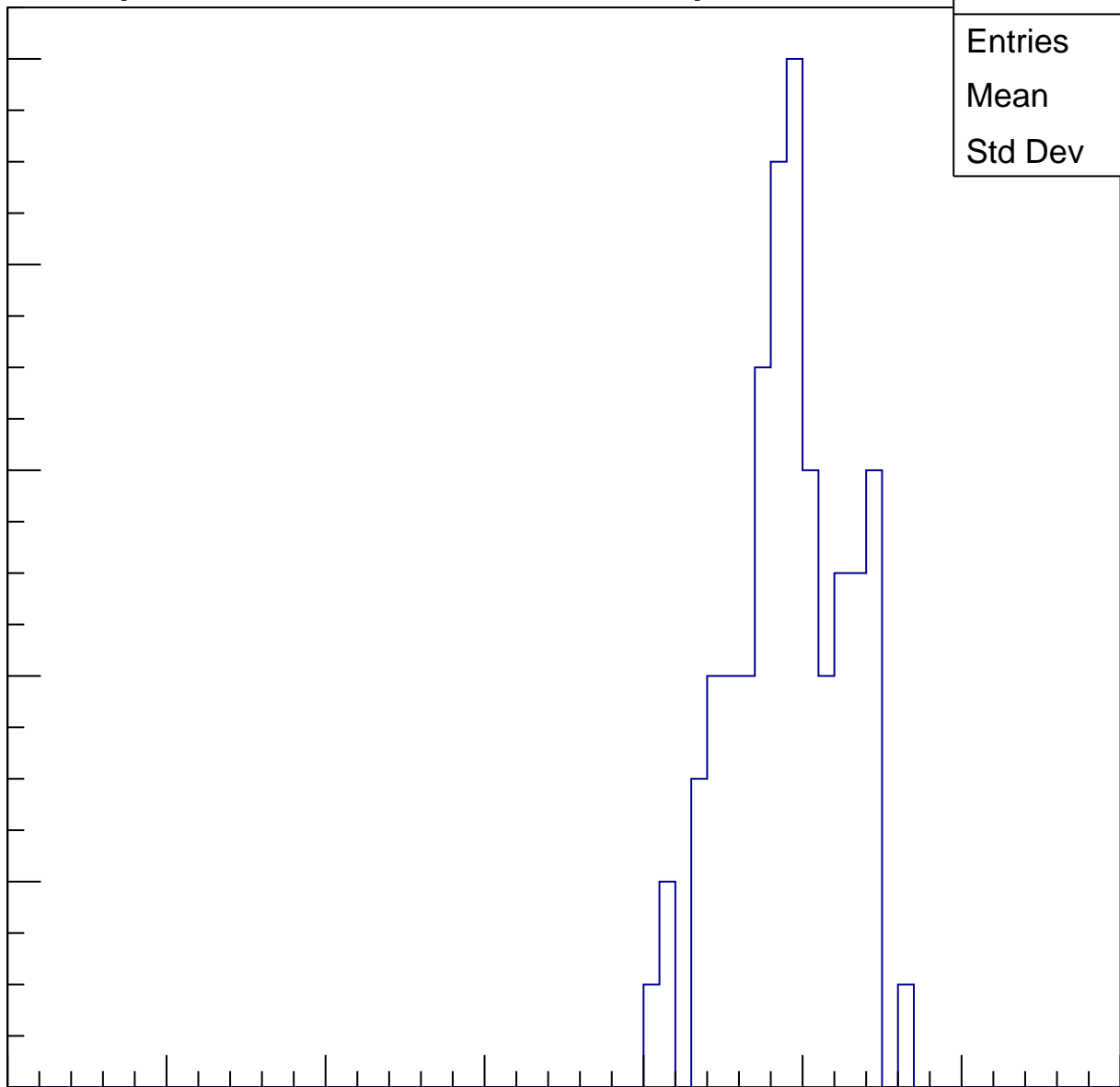
Entries	71
Mean	48.61
Std Dev	3.538

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

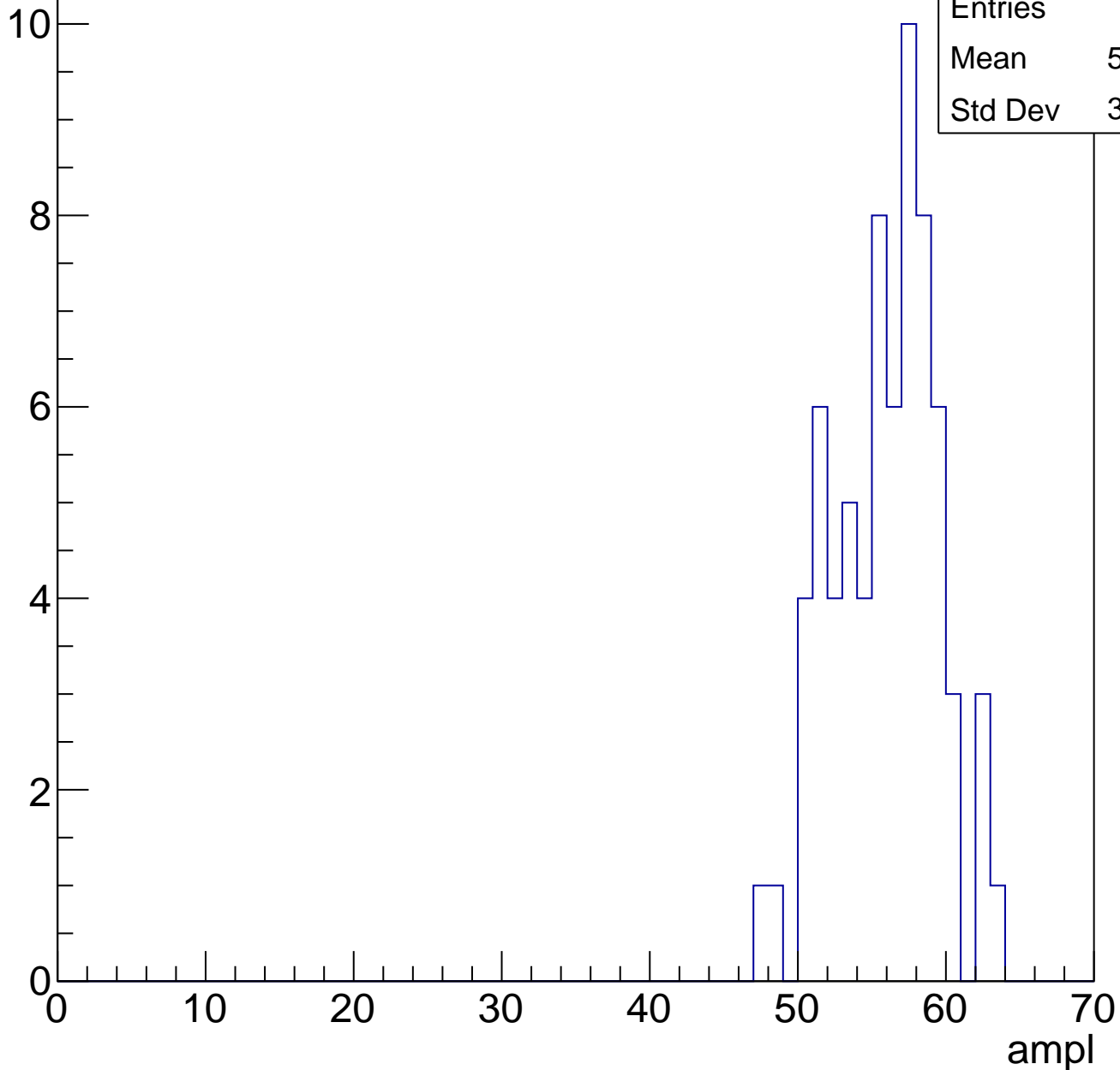


# B1L102S, U24-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	70
Mean	55.47
Std Dev	3.492

Entry

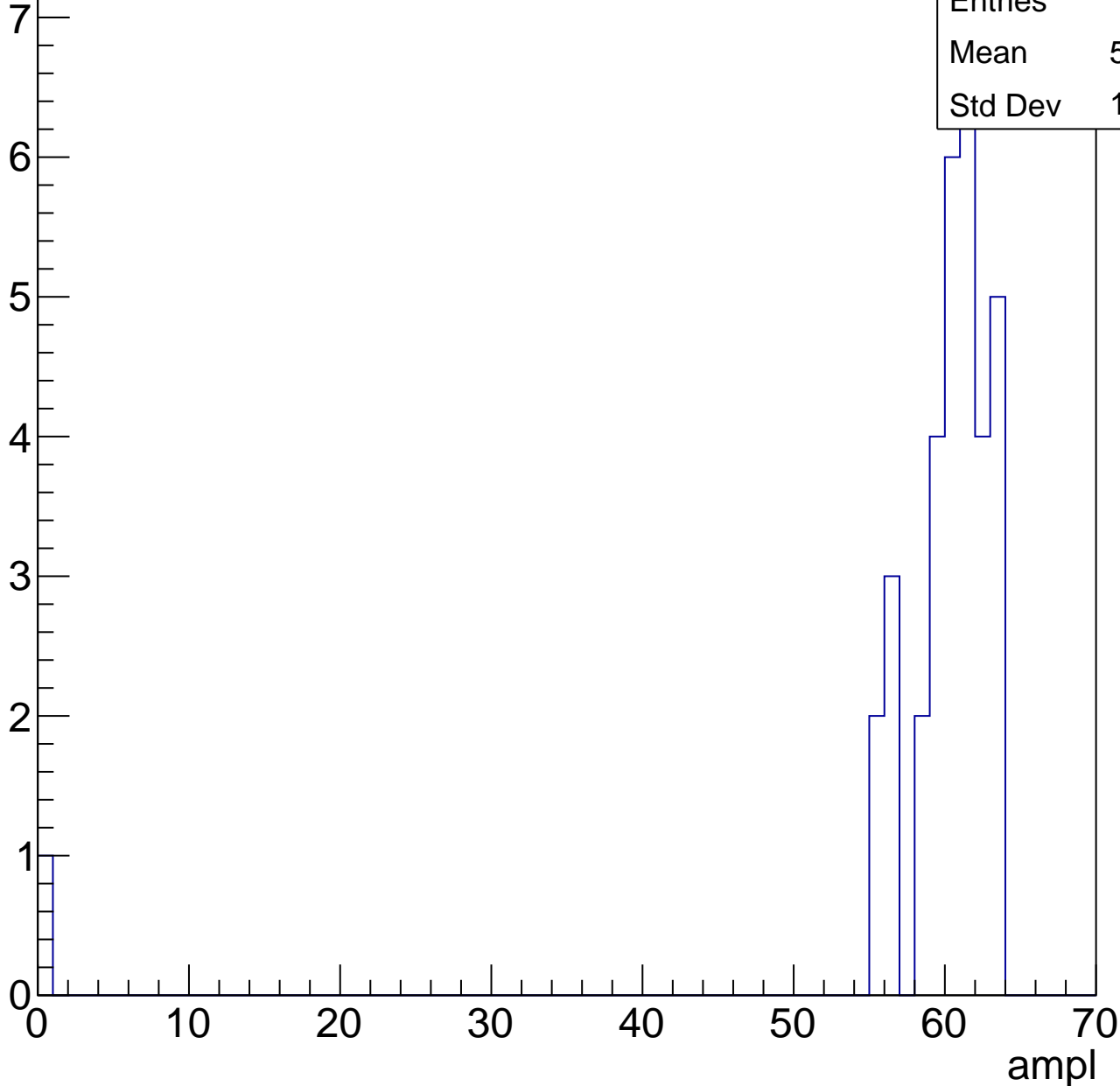


# B1L102S, U24-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

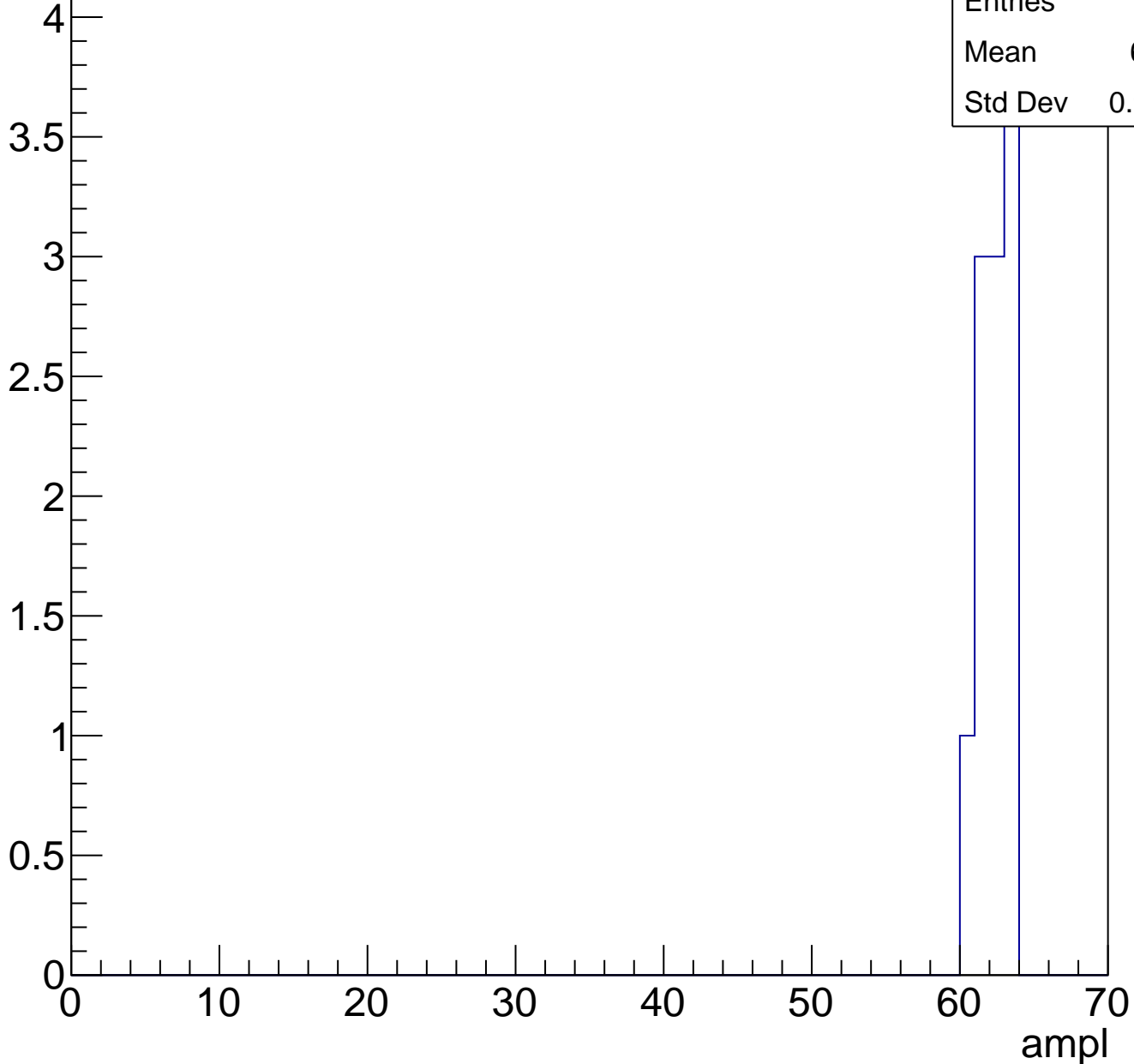
Entries	34
Mean	58.24
Std Dev	10.39



# B1L102S, U24-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch69, adc0

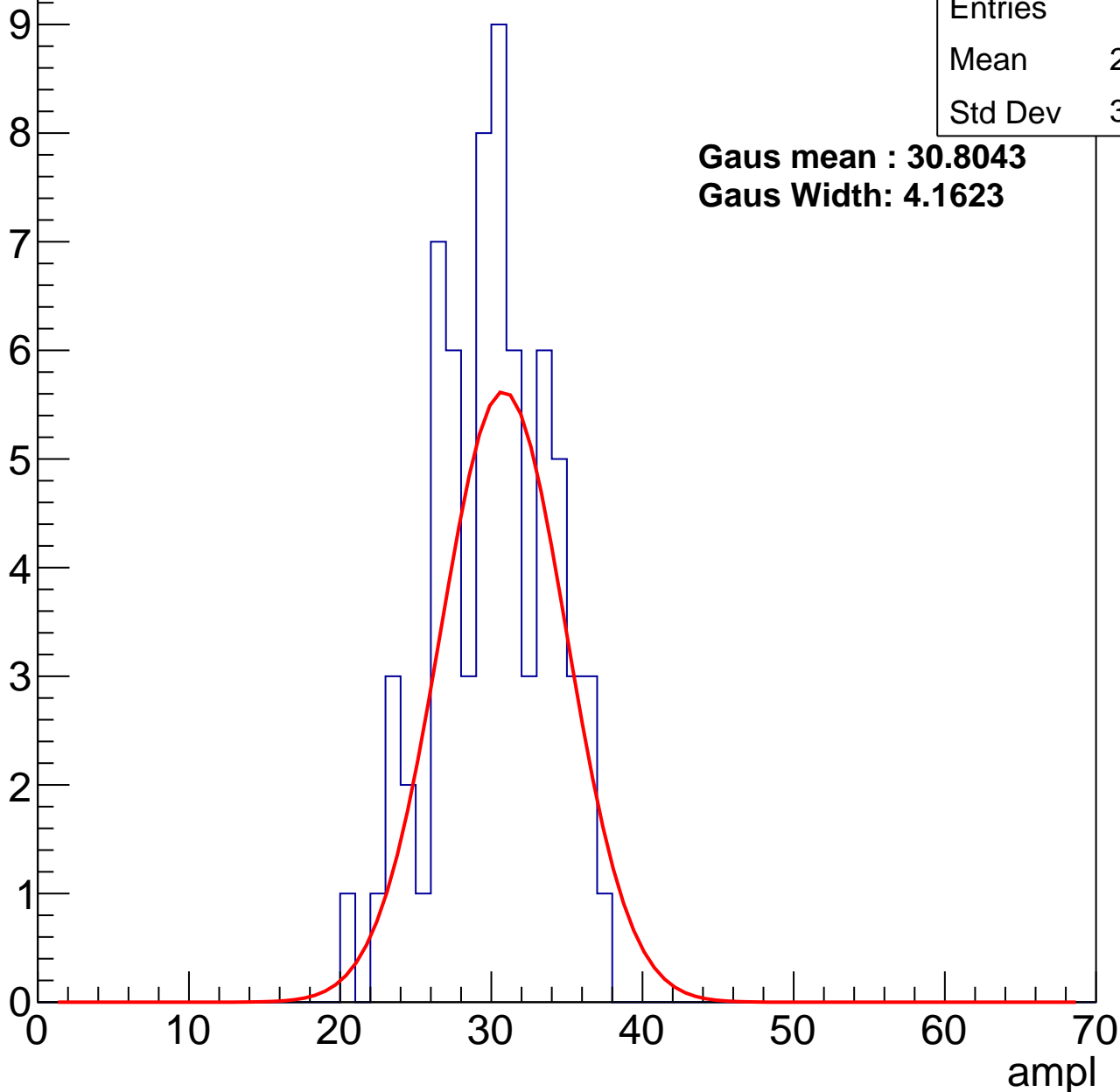
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.62
Std Dev	3.773

**Gaus mean : 30.8043**

**Gaus Width: 4.1623**



# B1L102S, U24-ch69, adc1

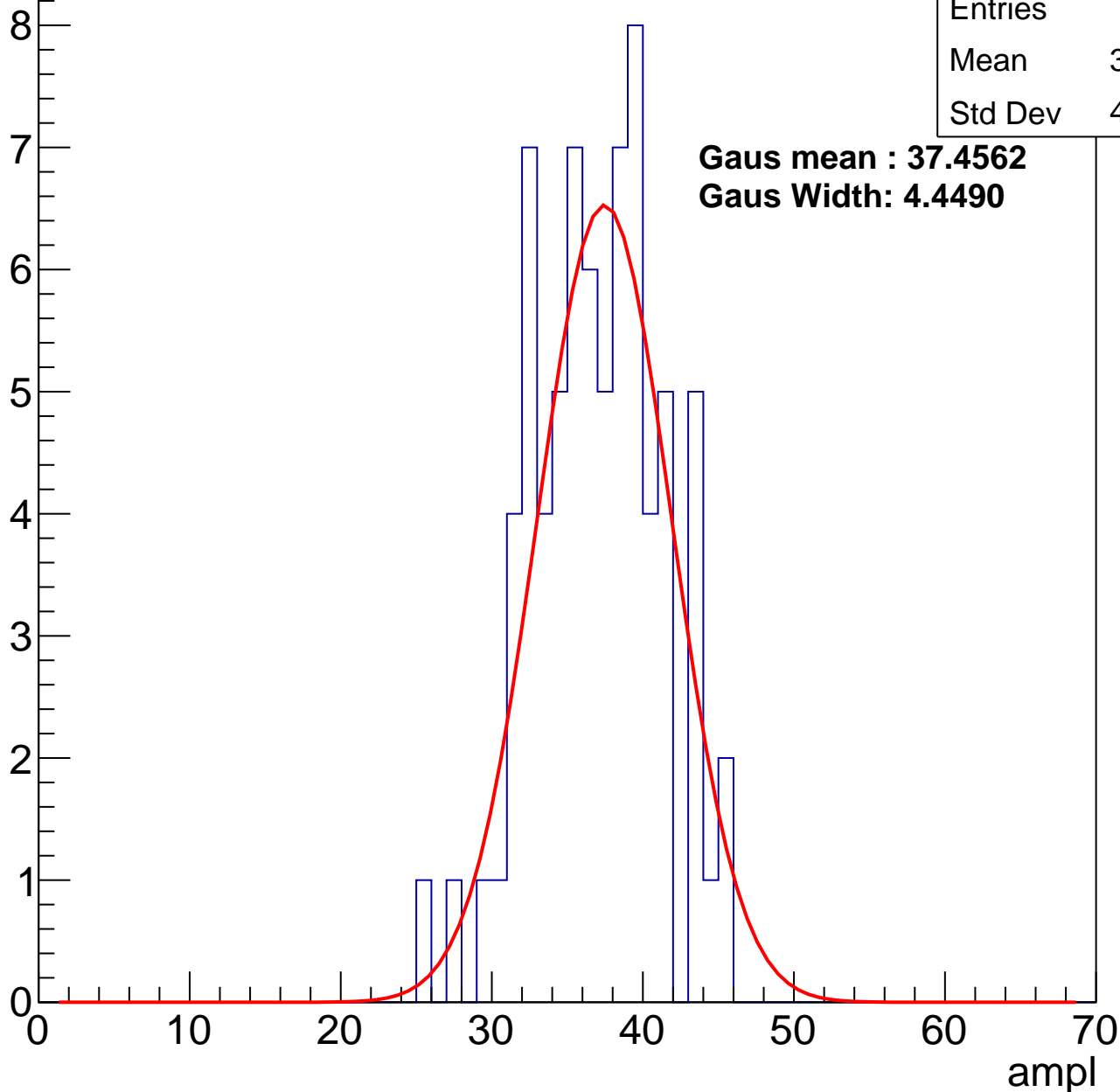
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.47
Std Dev	4.208

**Gaus mean : 37.4562**

**Gaus Width: 4.4490**



# B1L102S, U24-ch69, adc2

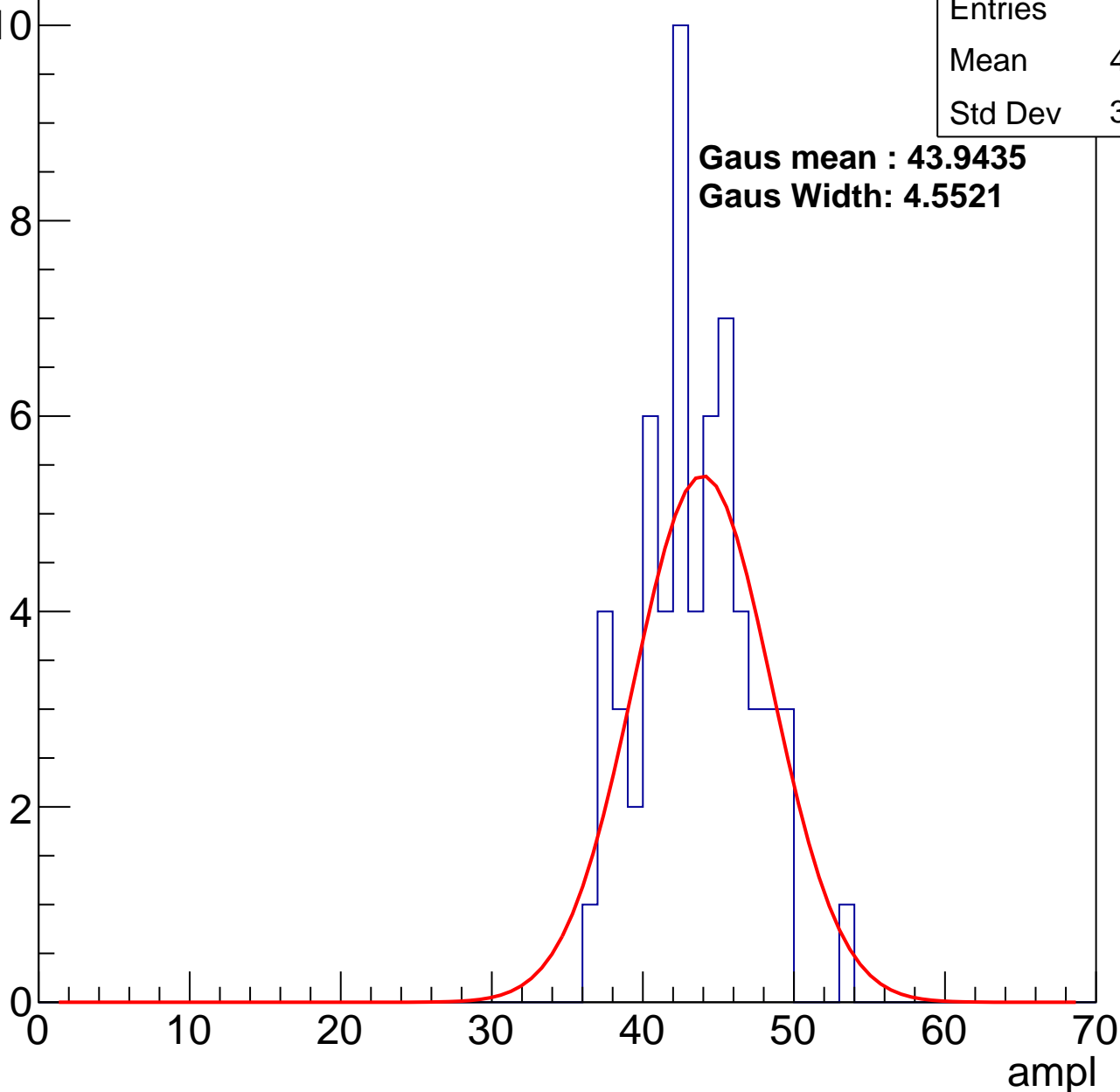
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.95
Std Dev	3.578

**Gaus mean : 43.9435**

**Gaus Width: 4.5521**

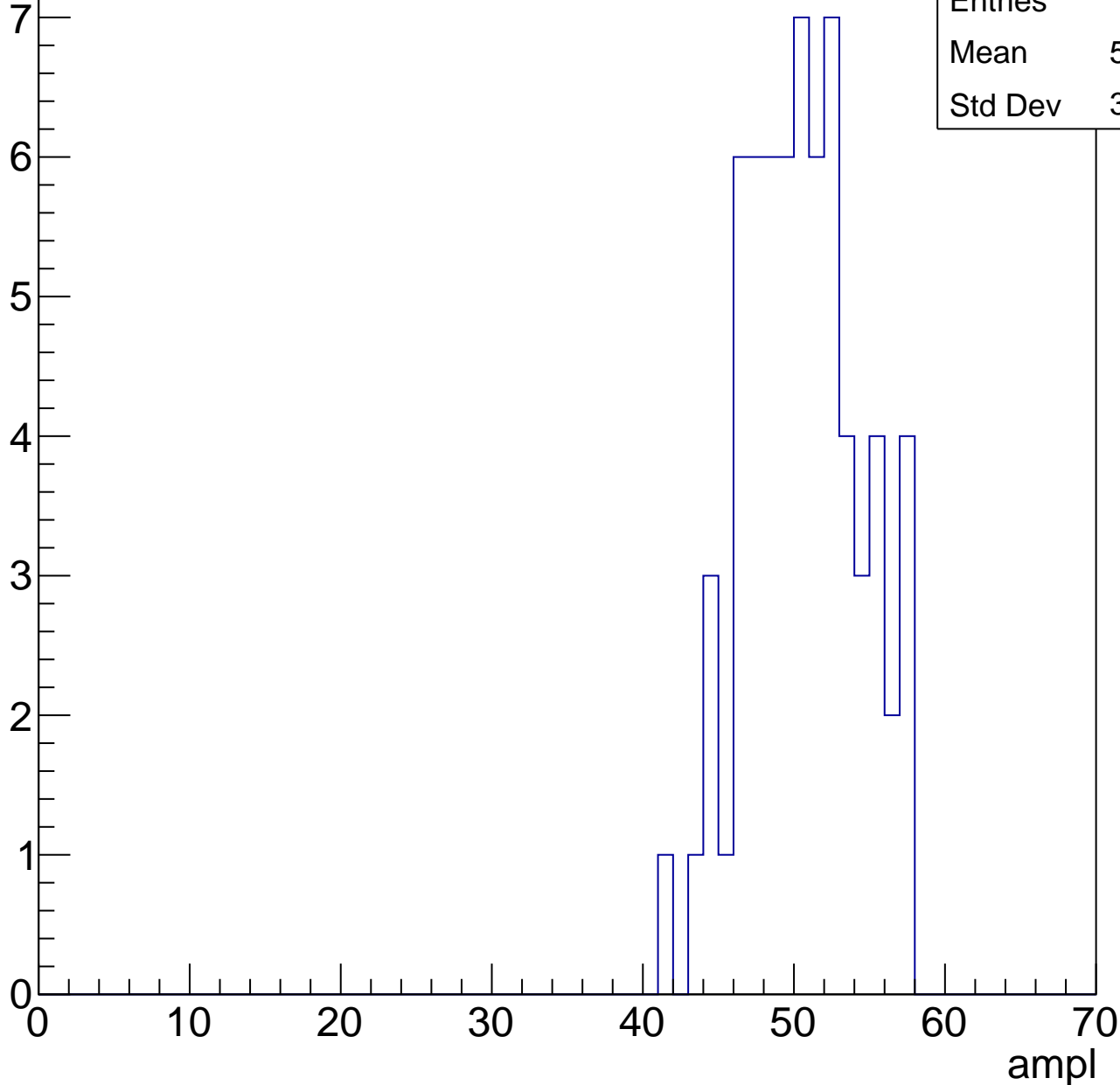


# B1L102S, U24-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.07
Std Dev	3.723

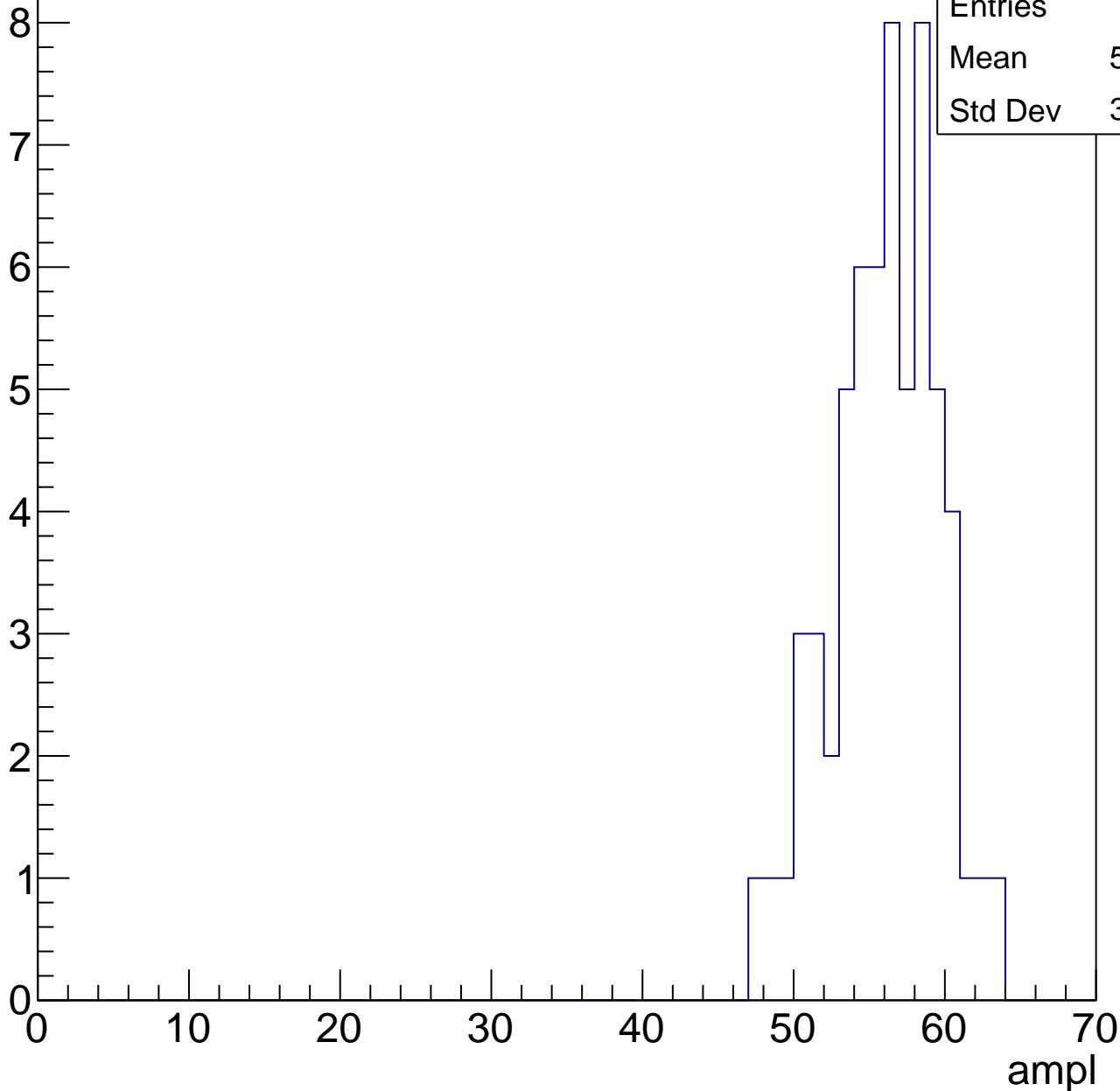


# B1L102S, U24-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.54
Std Dev	3.453

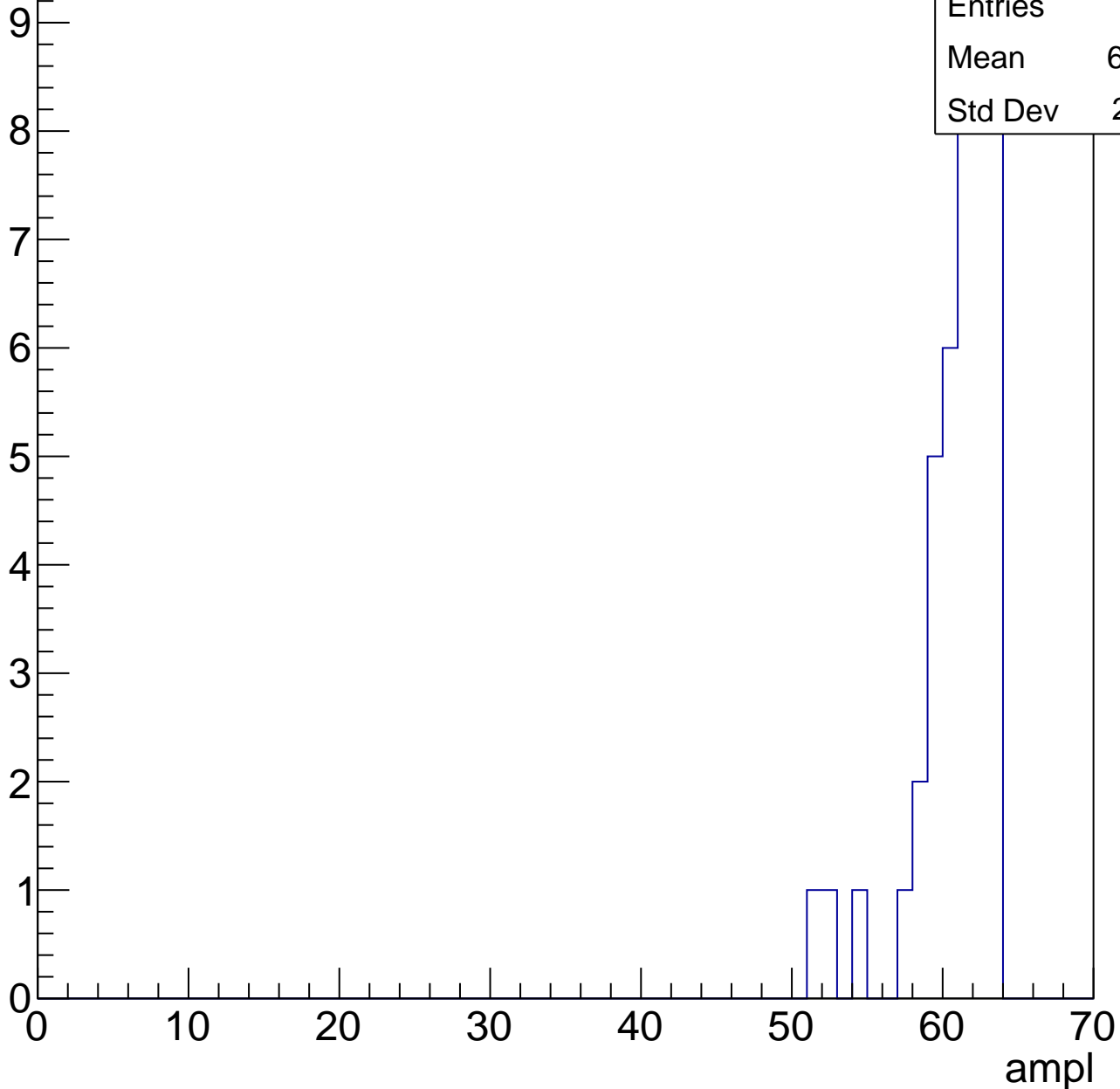


# B1L102S, U24-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

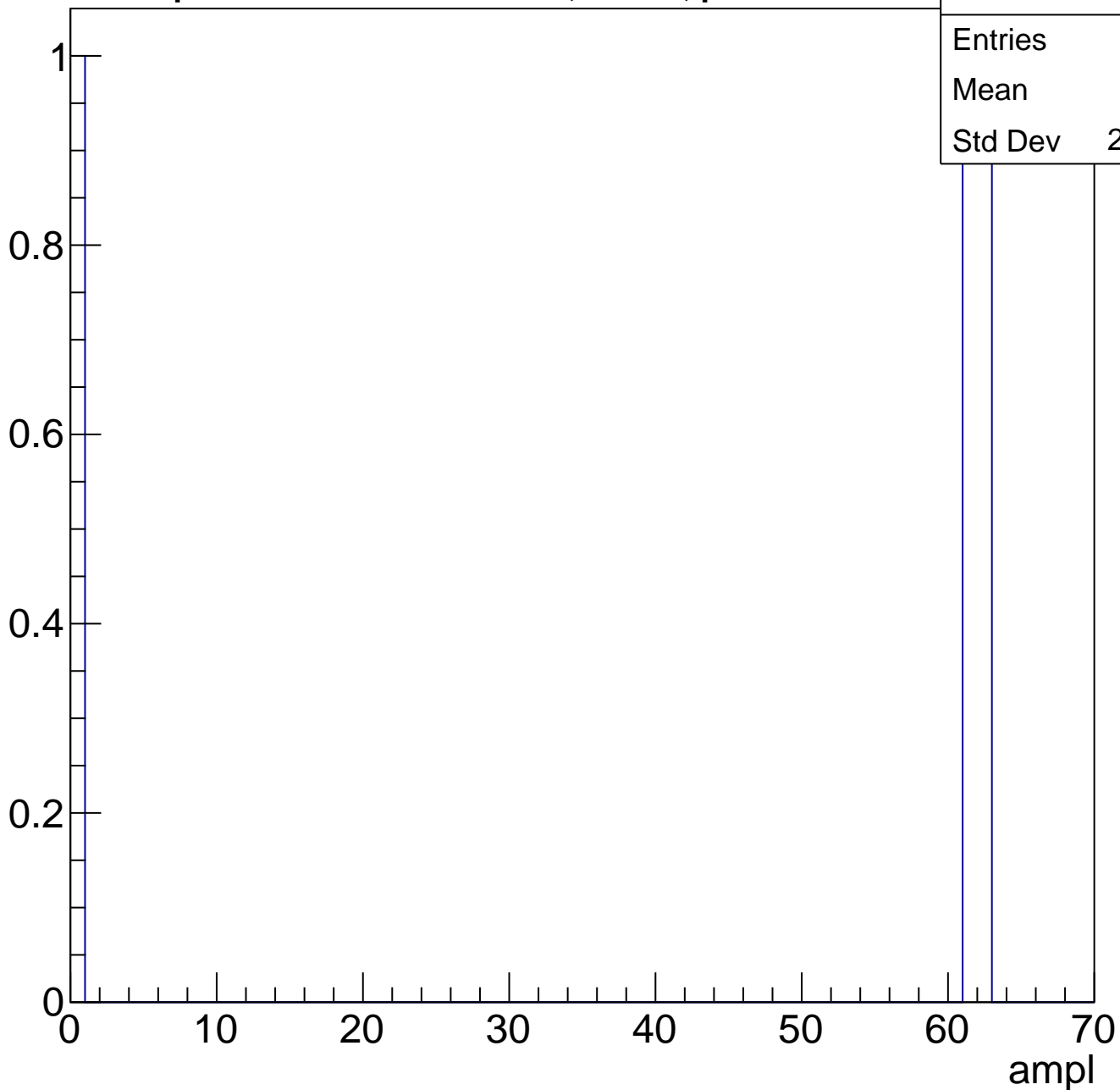
Entries	42
Mean	60.38
Std Dev	2.751



# B1L102S, U24-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch70, adc0

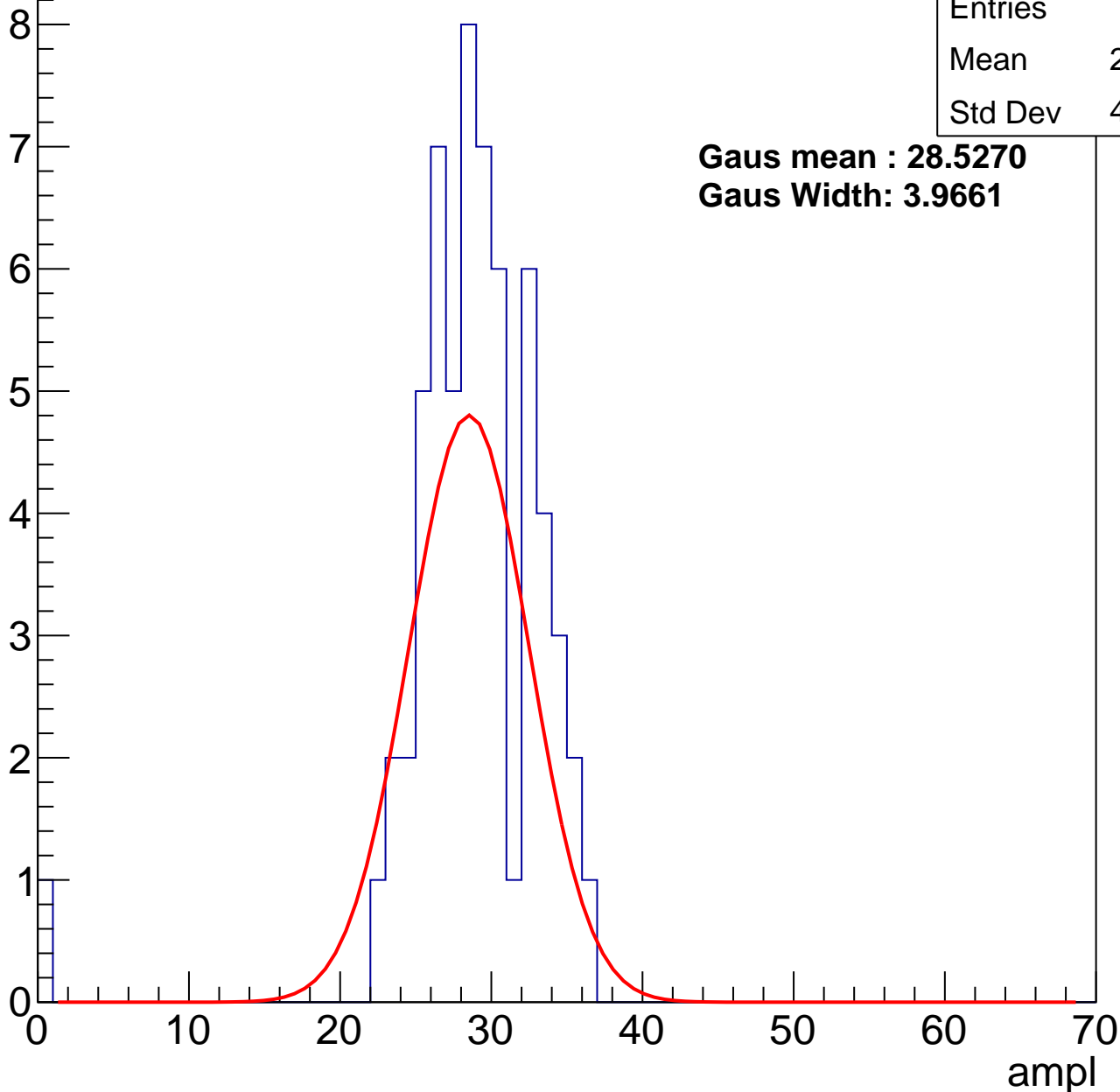
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.33
Std Dev	4.925

**Gaus mean : 28.5270**

**Gaus Width: 3.9661**



# B1L102S, U24-ch70, adc1

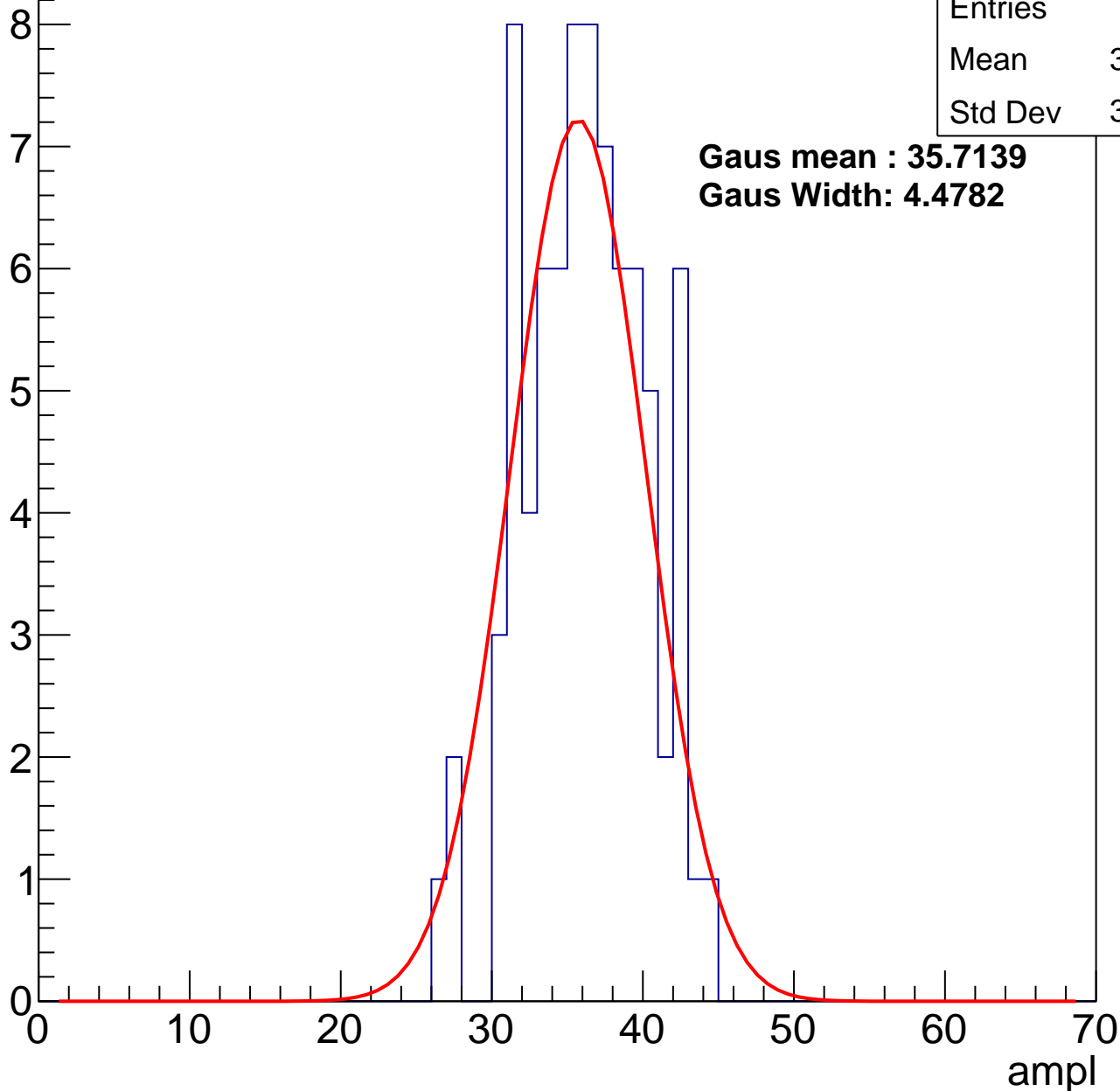
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.73
Std Dev	3.965

**Gaus mean : 35.7139**

**Gaus Width: 4.4782**



# B1L102S, U24-ch70, adc2

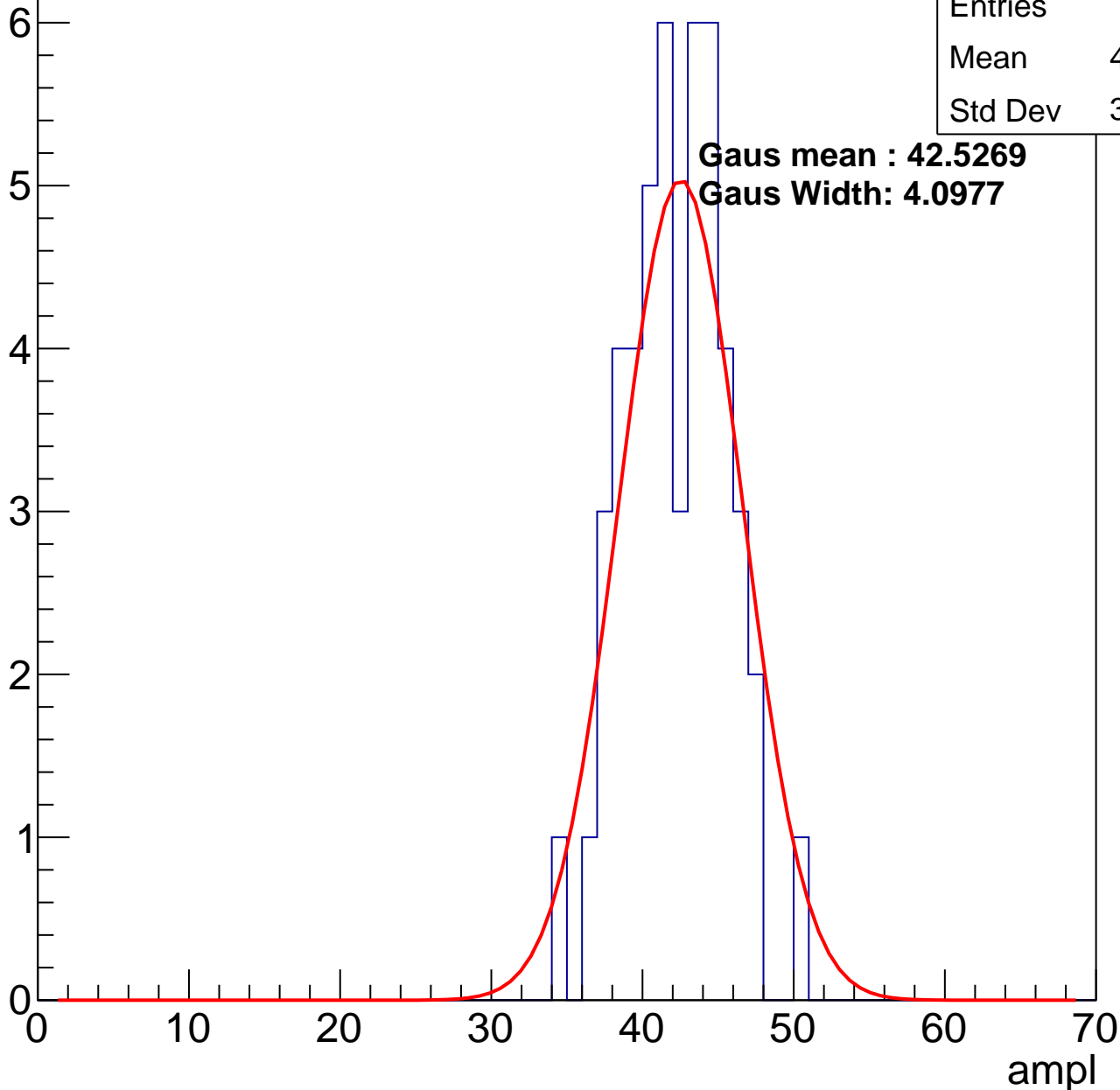
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.73
Std Dev	3.275

**Gaus mean : 42.5269**

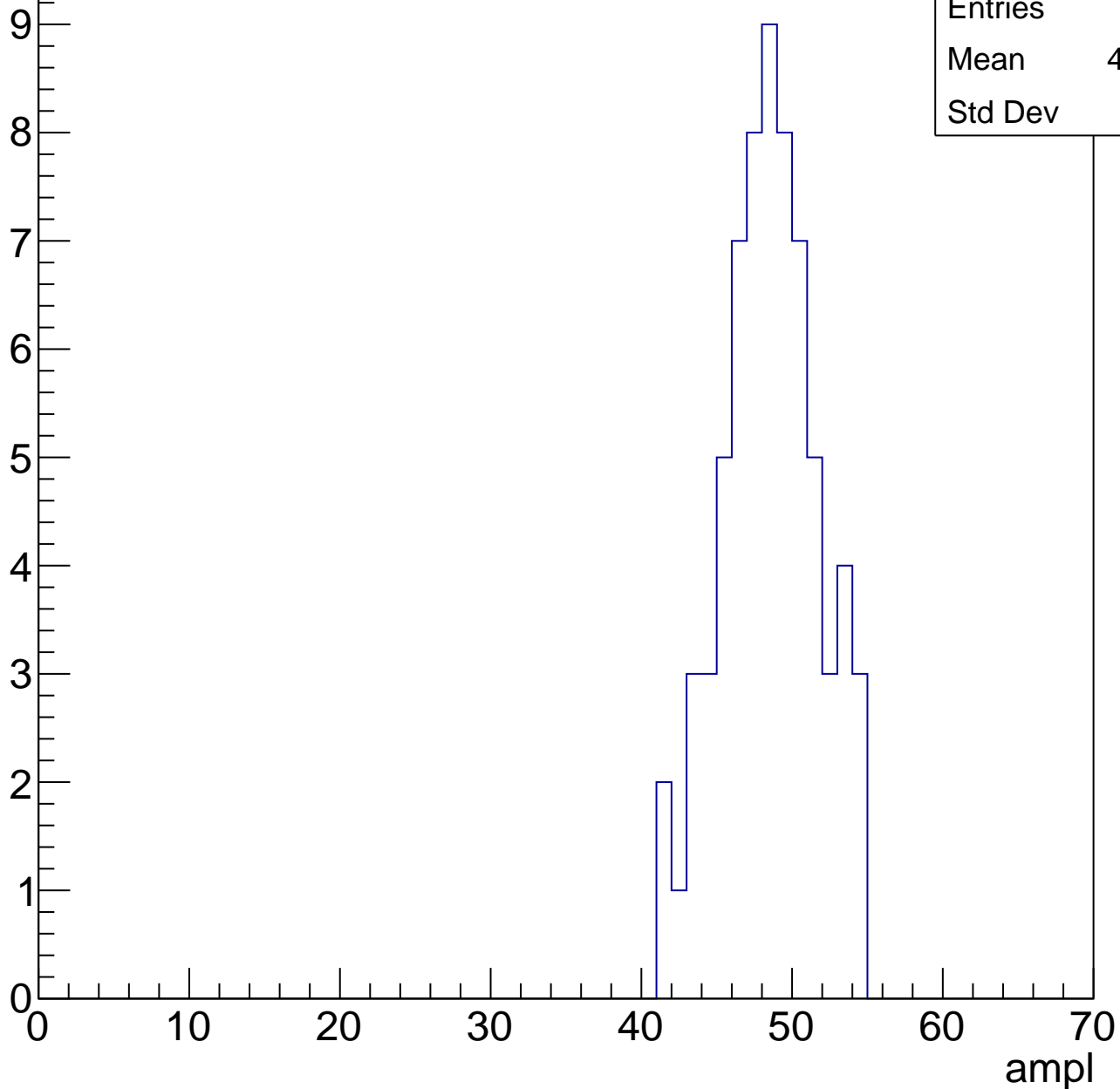
**Gaus Width: 4.0977**



# B1L102S, U24-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



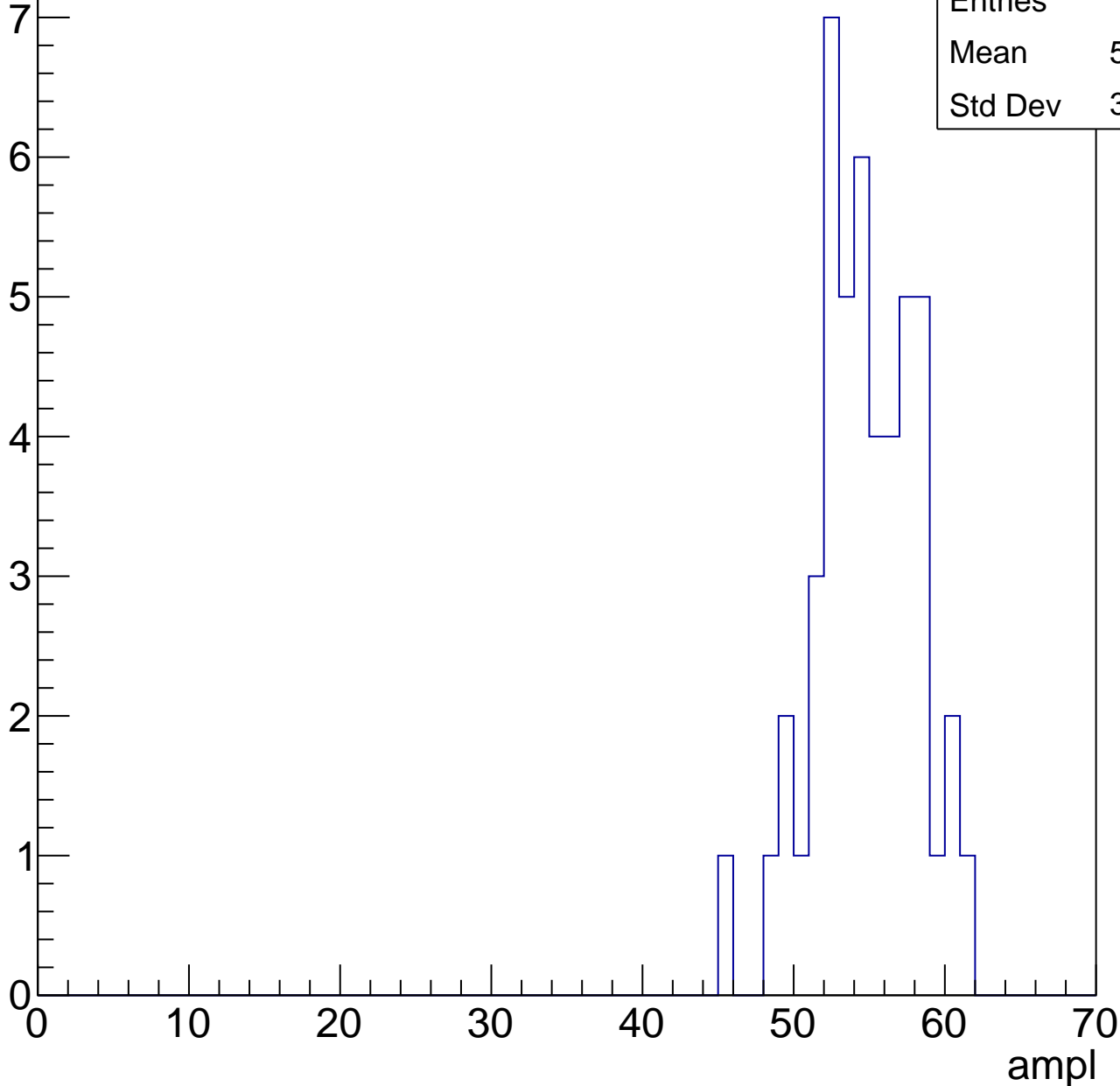
Entries	68
Mean	48.04
Std Dev	3.15

# B1L102S, U24-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.29
Std Dev	3.323

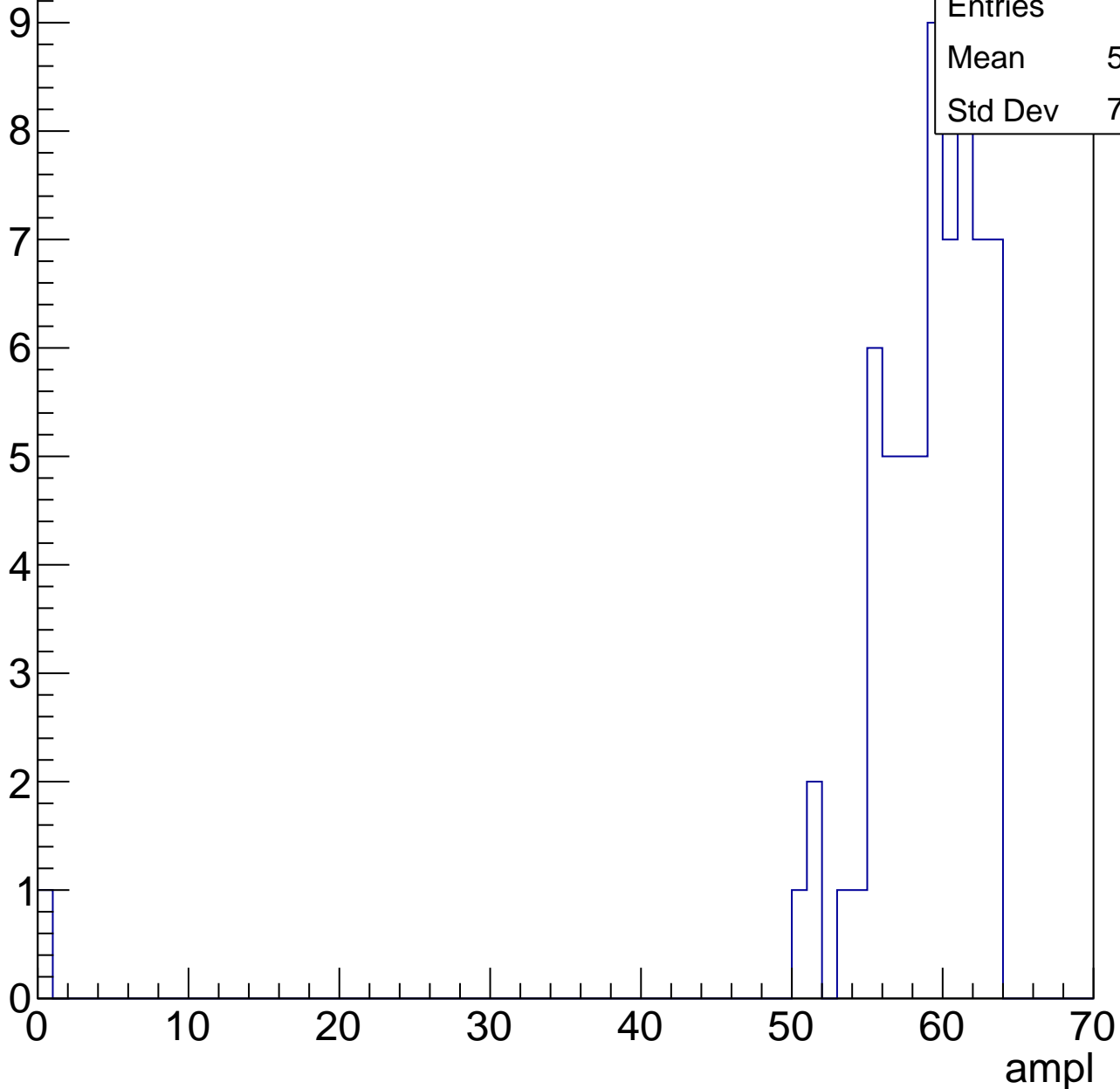


# B1L102S, U24-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	57.82
Std Dev	7.884



# B1L102S, U24-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

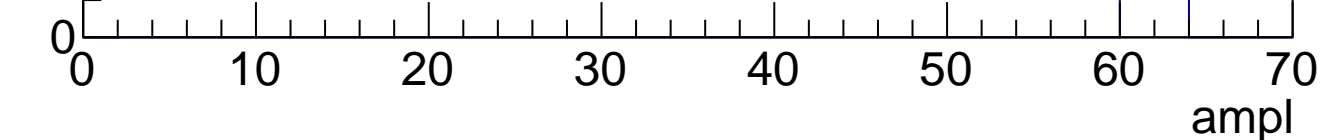
7

Mean

62

Std Dev

1.069

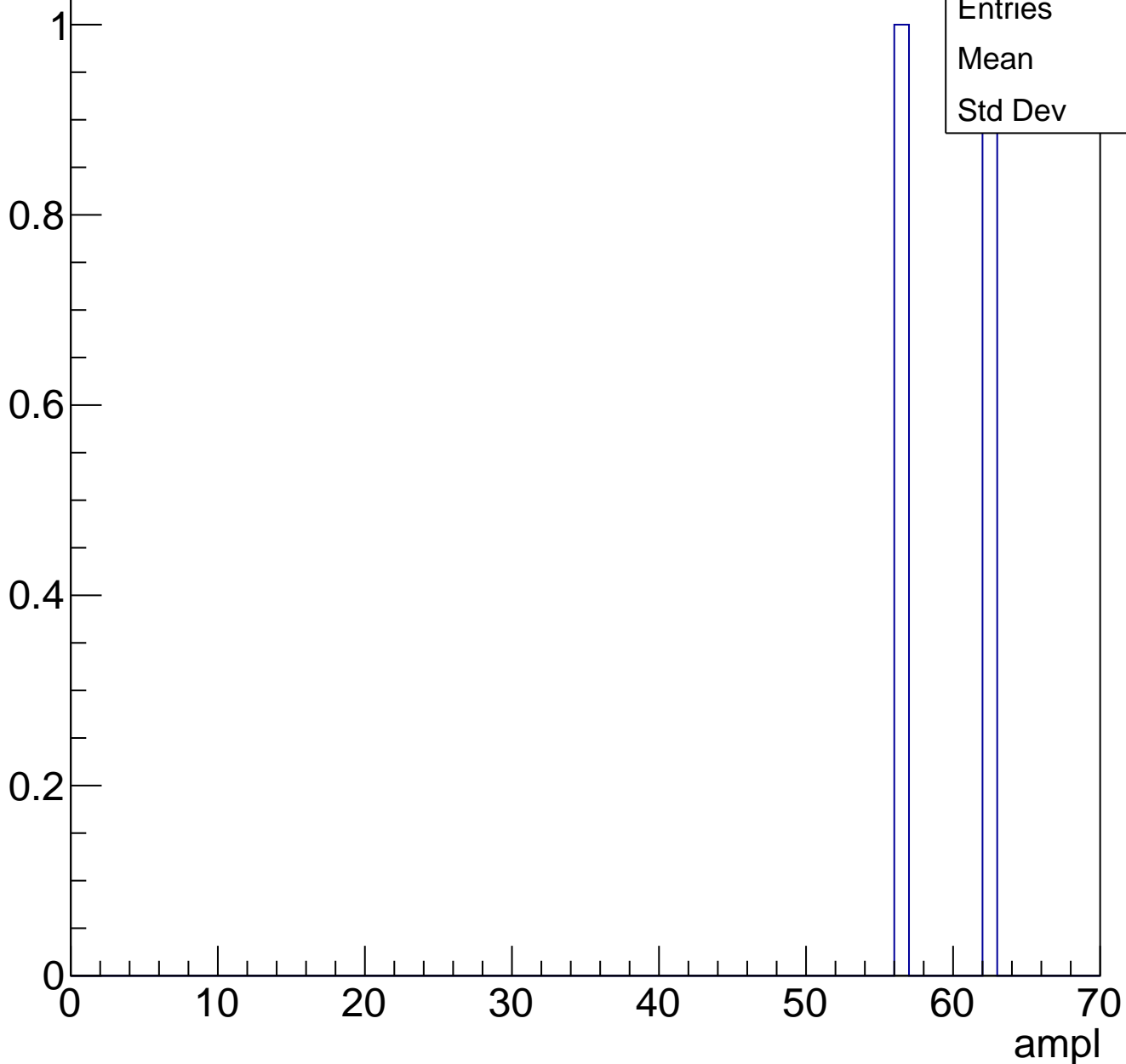




# B1L102S, U24-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch71, adc0

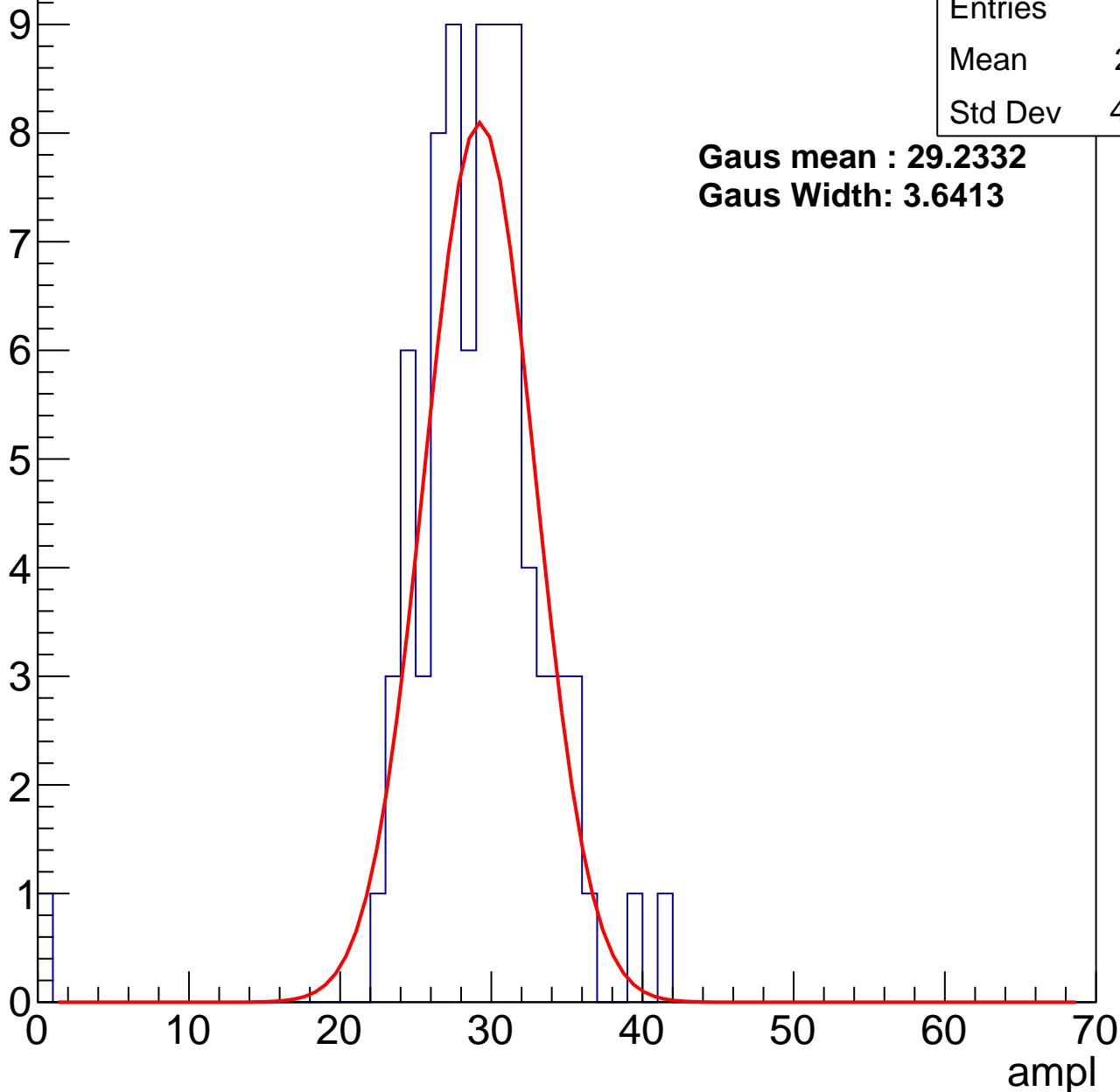
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.61
Std Dev	4.877

**Gaus mean : 29.2332**

**Gaus Width: 3.6413**



# B1L102S, U24-ch71, adc1

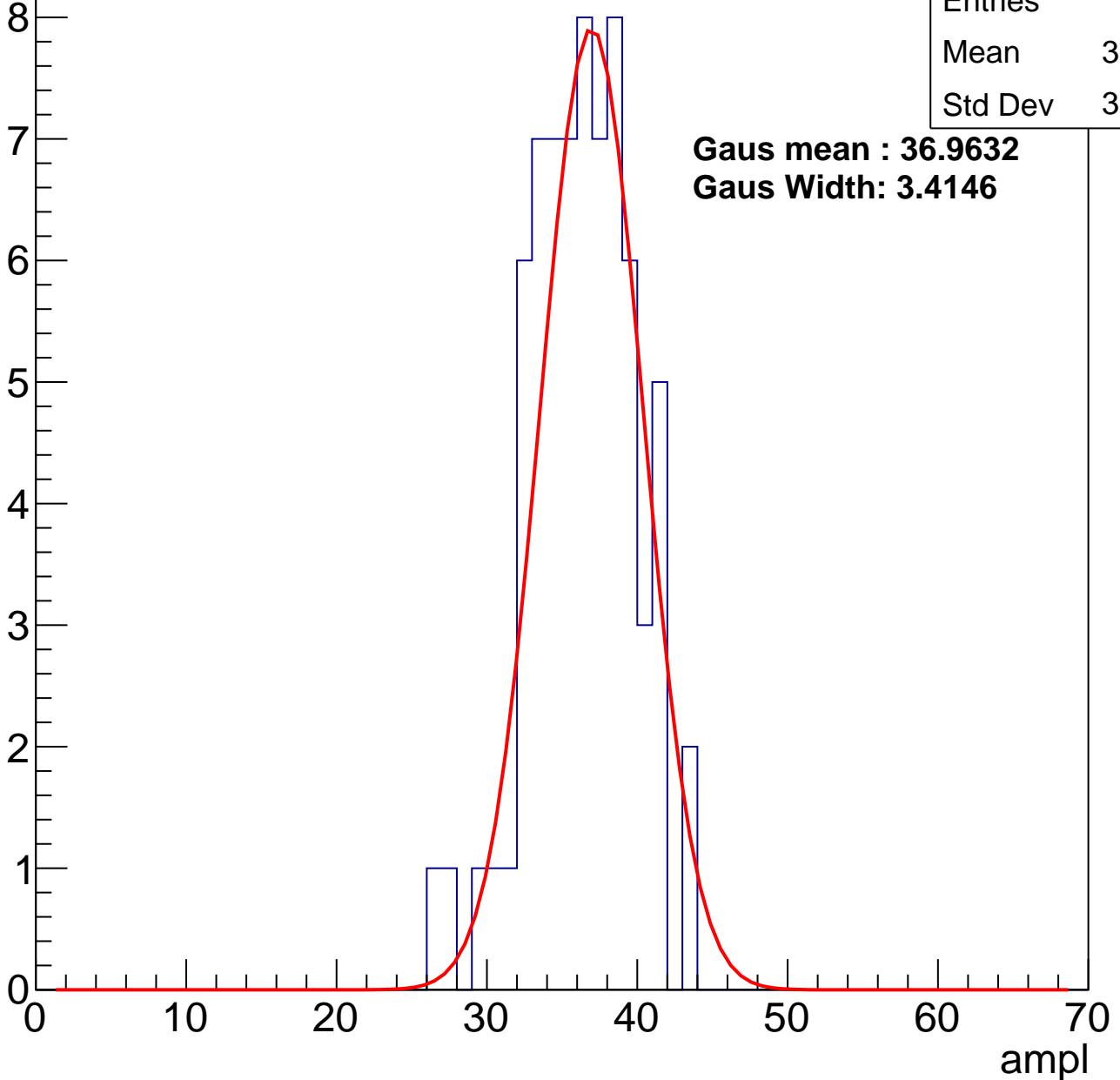
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.85
Std Dev	3.455

**Gaus mean : 36.9632**

**Gaus Width: 3.4146**



# B1L102S, U24-ch71, adc2

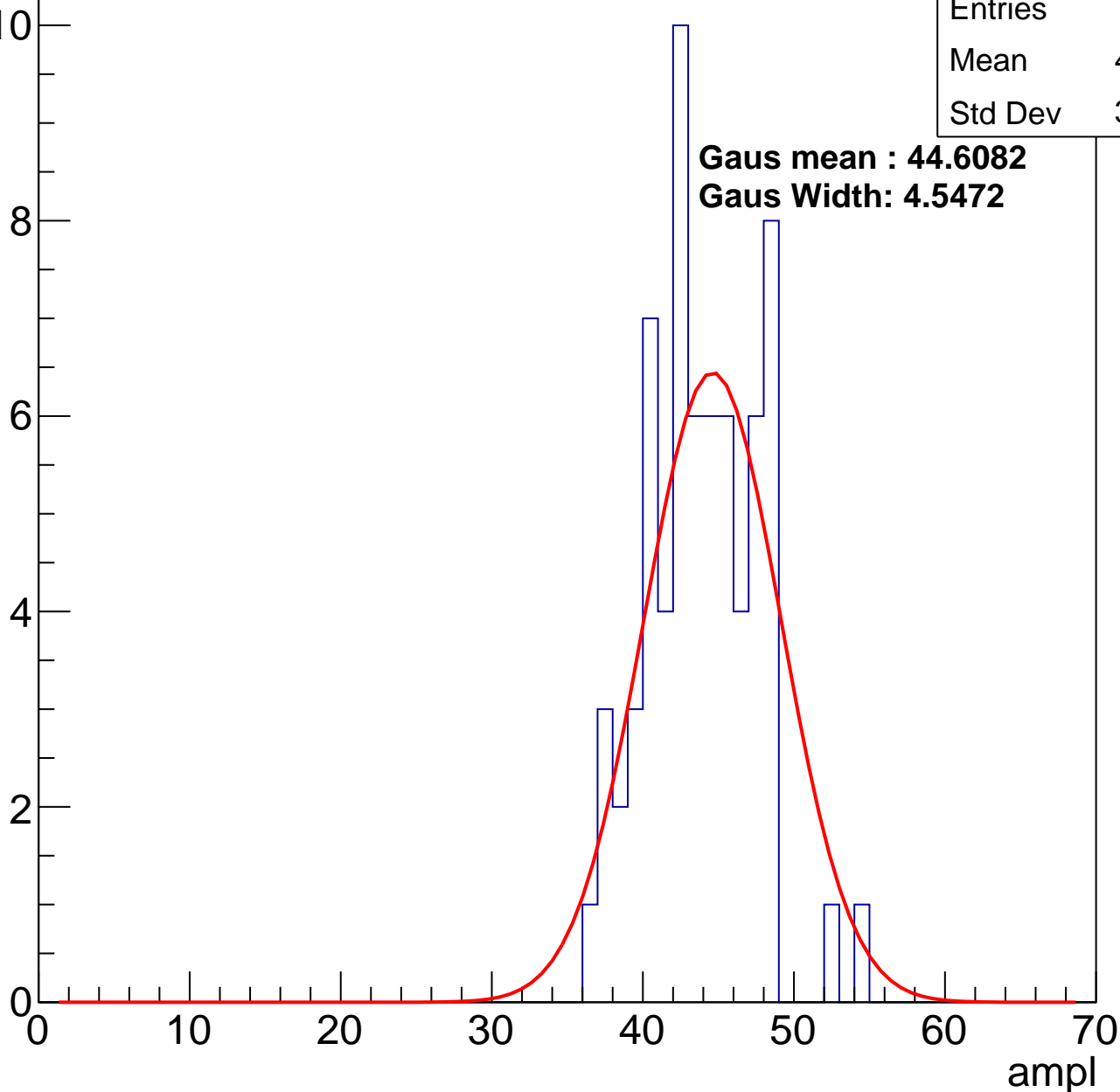
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.41
Std Dev	3.631

**Gaus mean : 44.6082**

**Gaus Width: 4.5472**



# B1L102S, U24-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

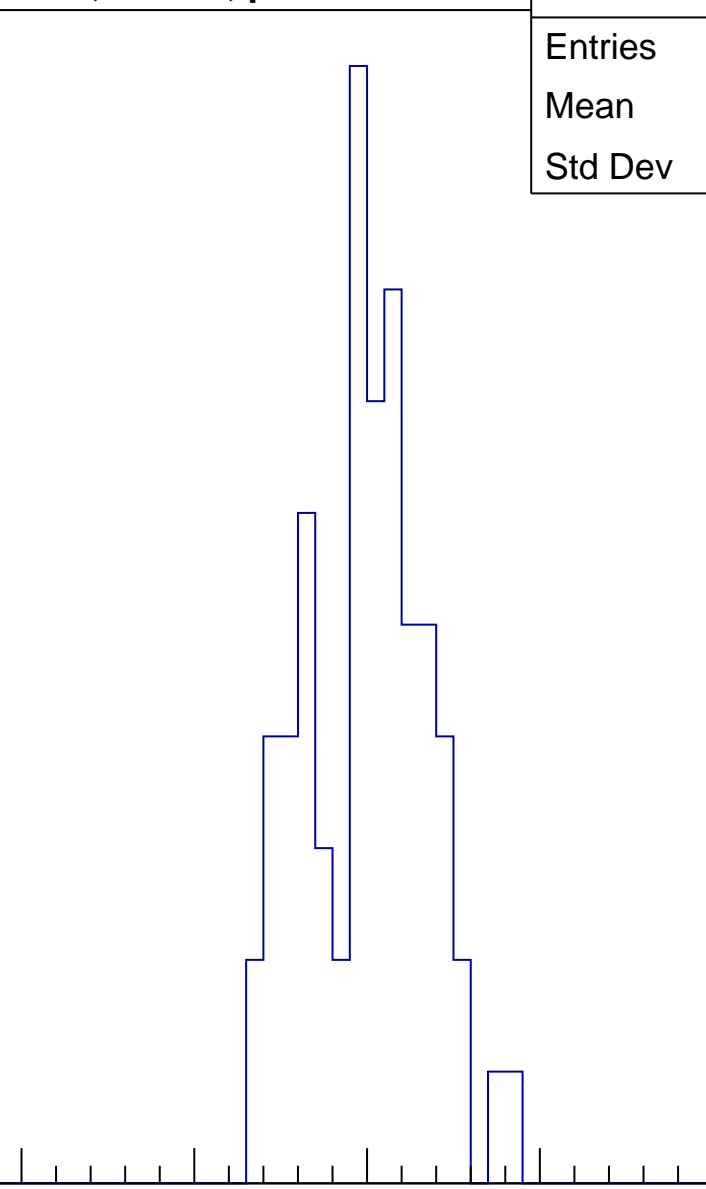
Entries	64
Mean	49.52
Std Dev	3.464

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

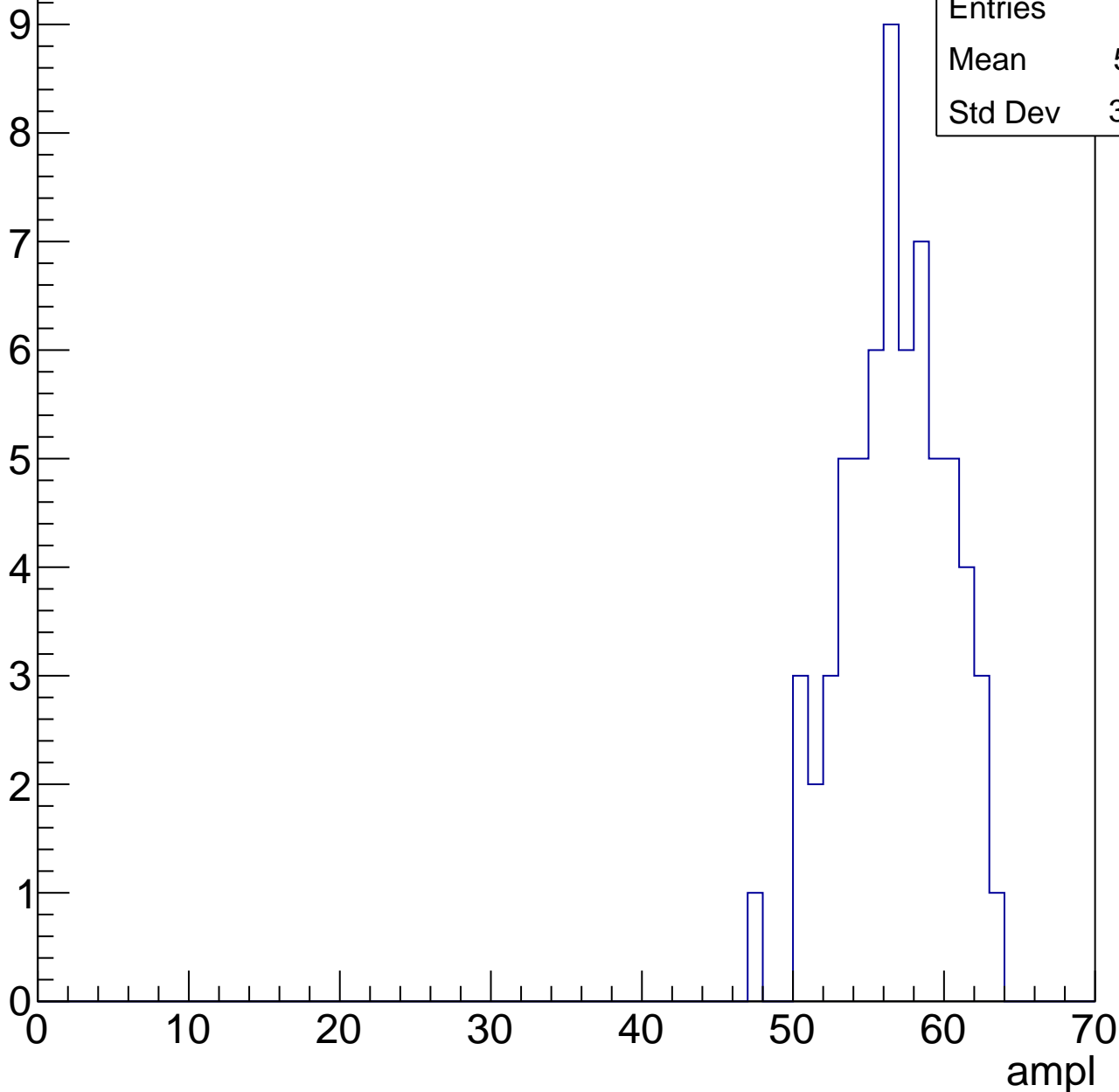


# B1L102S, U24-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	56.31
Std Dev	3.437

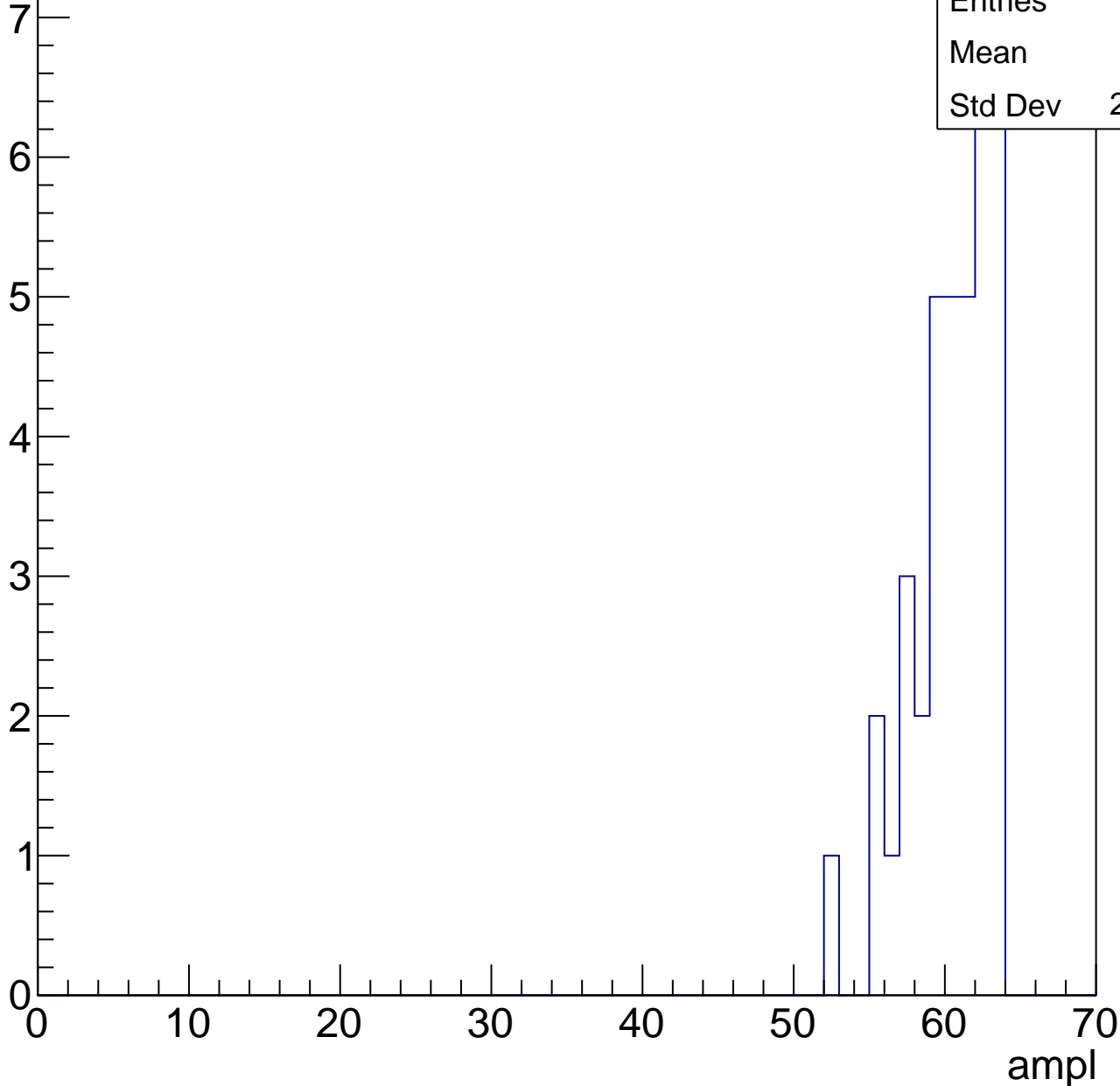


# B1L102S, U24-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	38
Mean	60
Std Dev	2.646



# B1L102S, U24-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

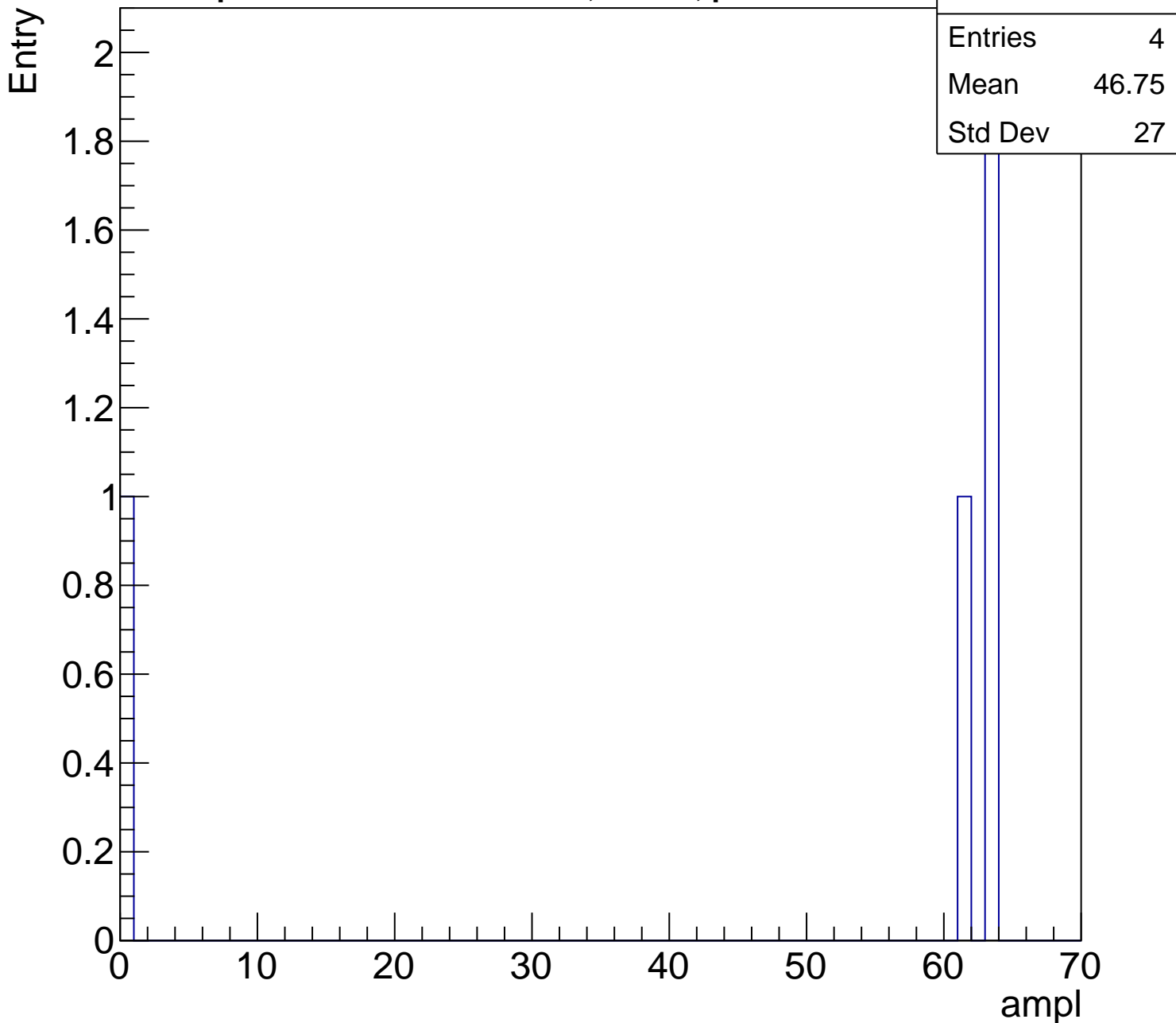
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L102S, U24-ch72, adc0

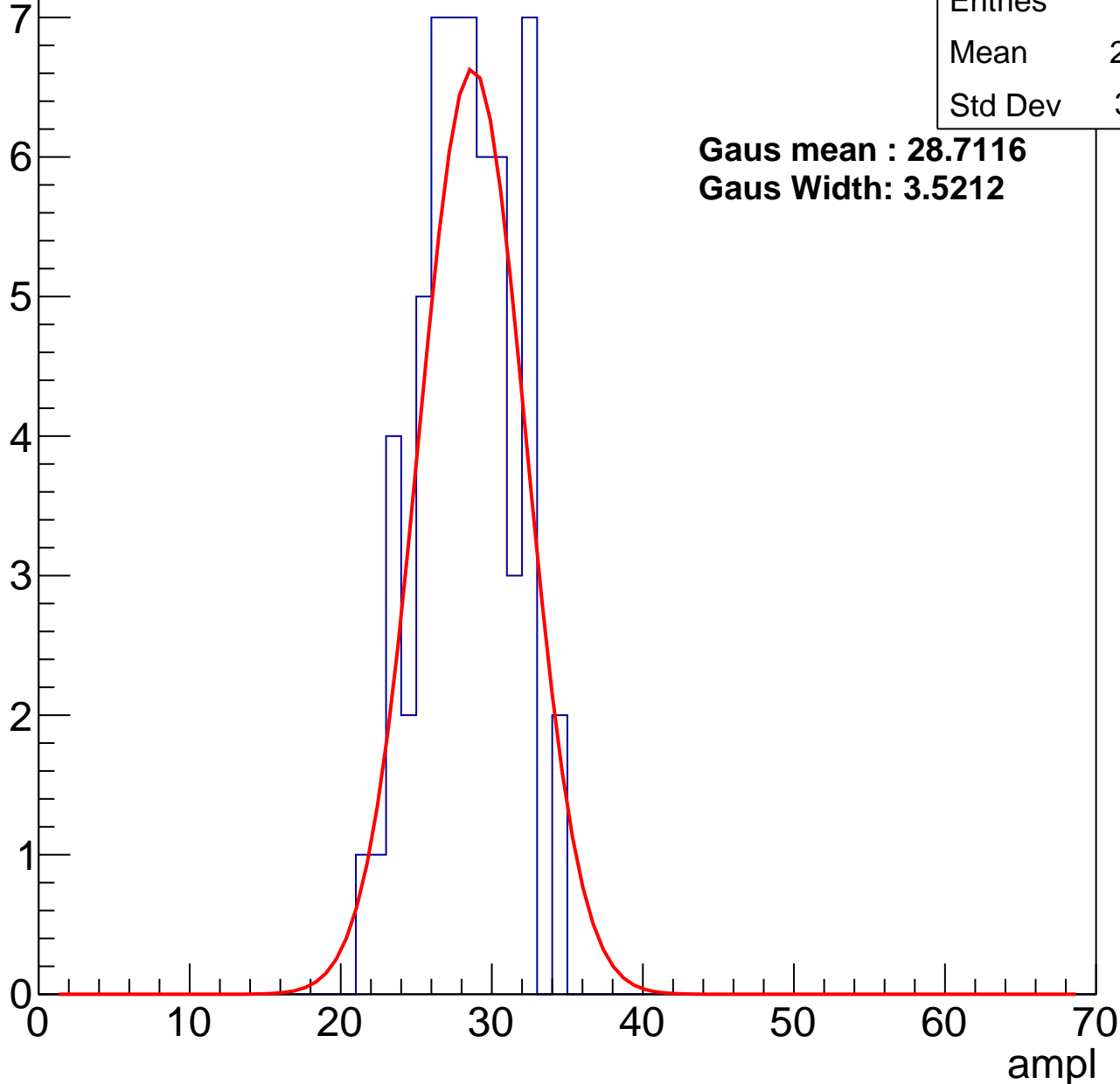
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.83
Std Dev	3.041

**Gaus mean : 28.7116**

**Gaus Width: 3.5212**



# B1L102S, U24-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	78
Mean	34.36
Std Dev	3.843

**Gaus mean : 35.4627**

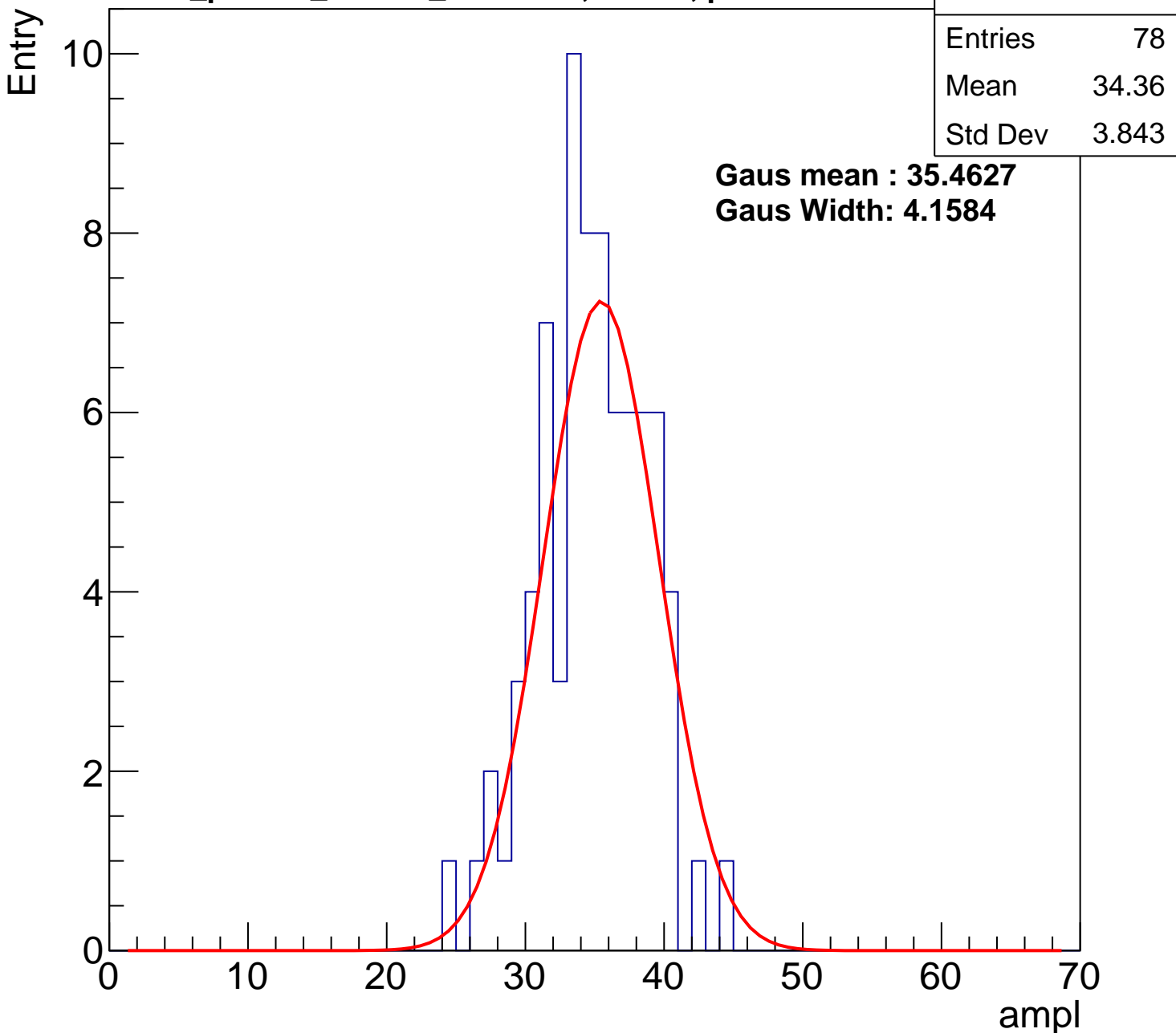
**Gaus Width: 4.1584**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch72, adc2

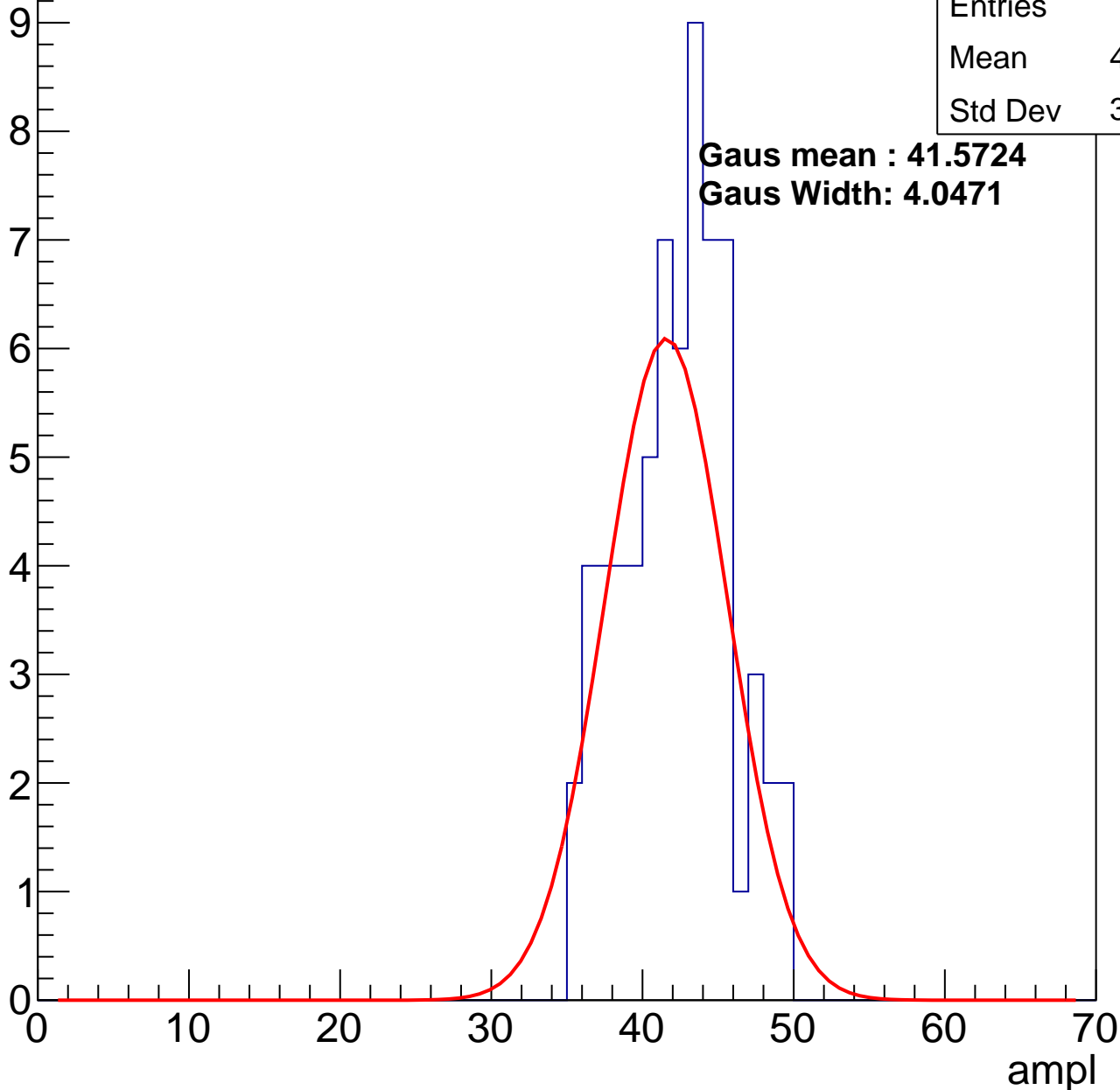
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.79
Std Dev	3.513

**Gaus mean : 41.5724**

**Gaus Width: 4.0471**

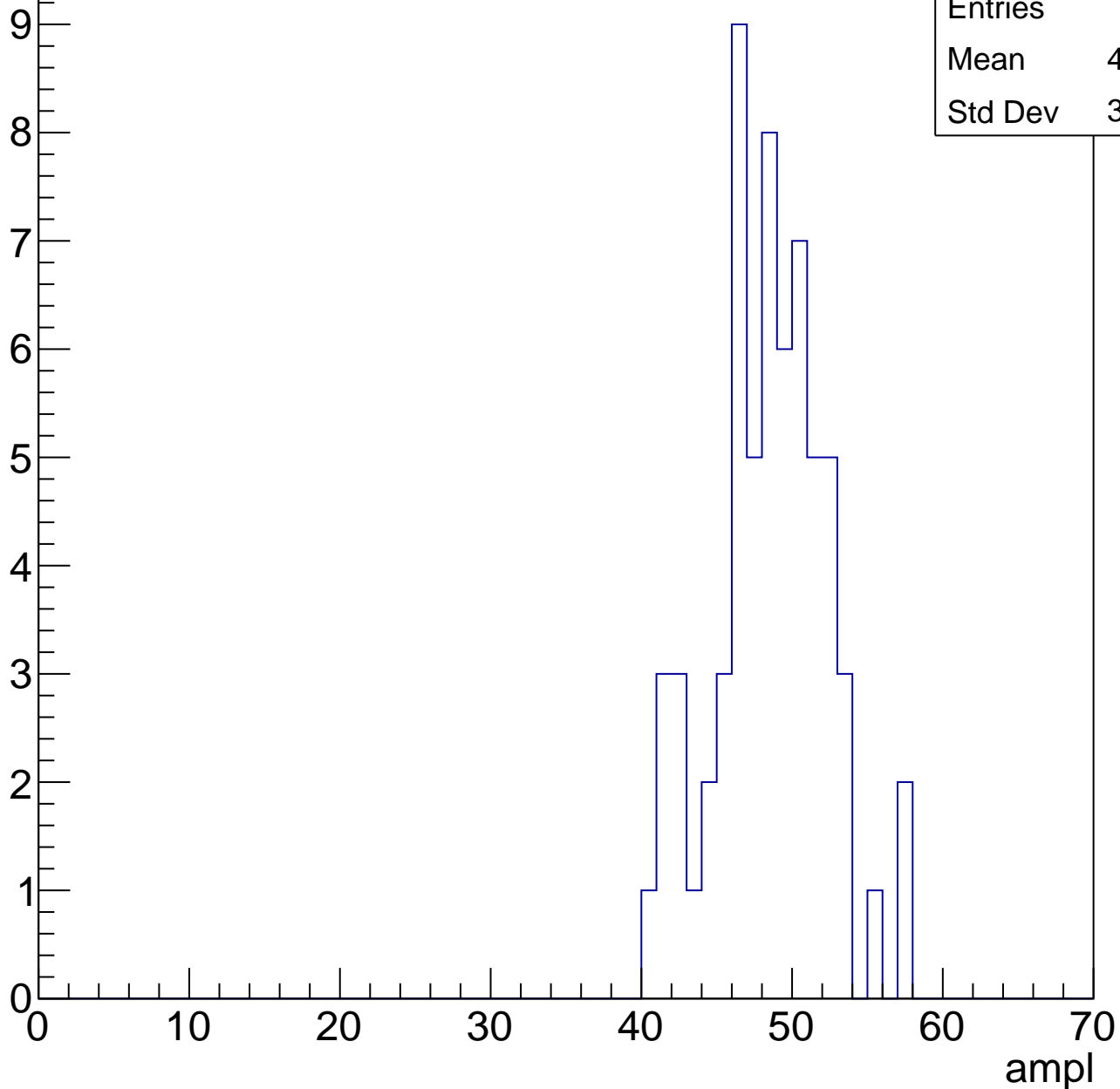


# B1L102S, U24-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.05
Std Dev	3.727

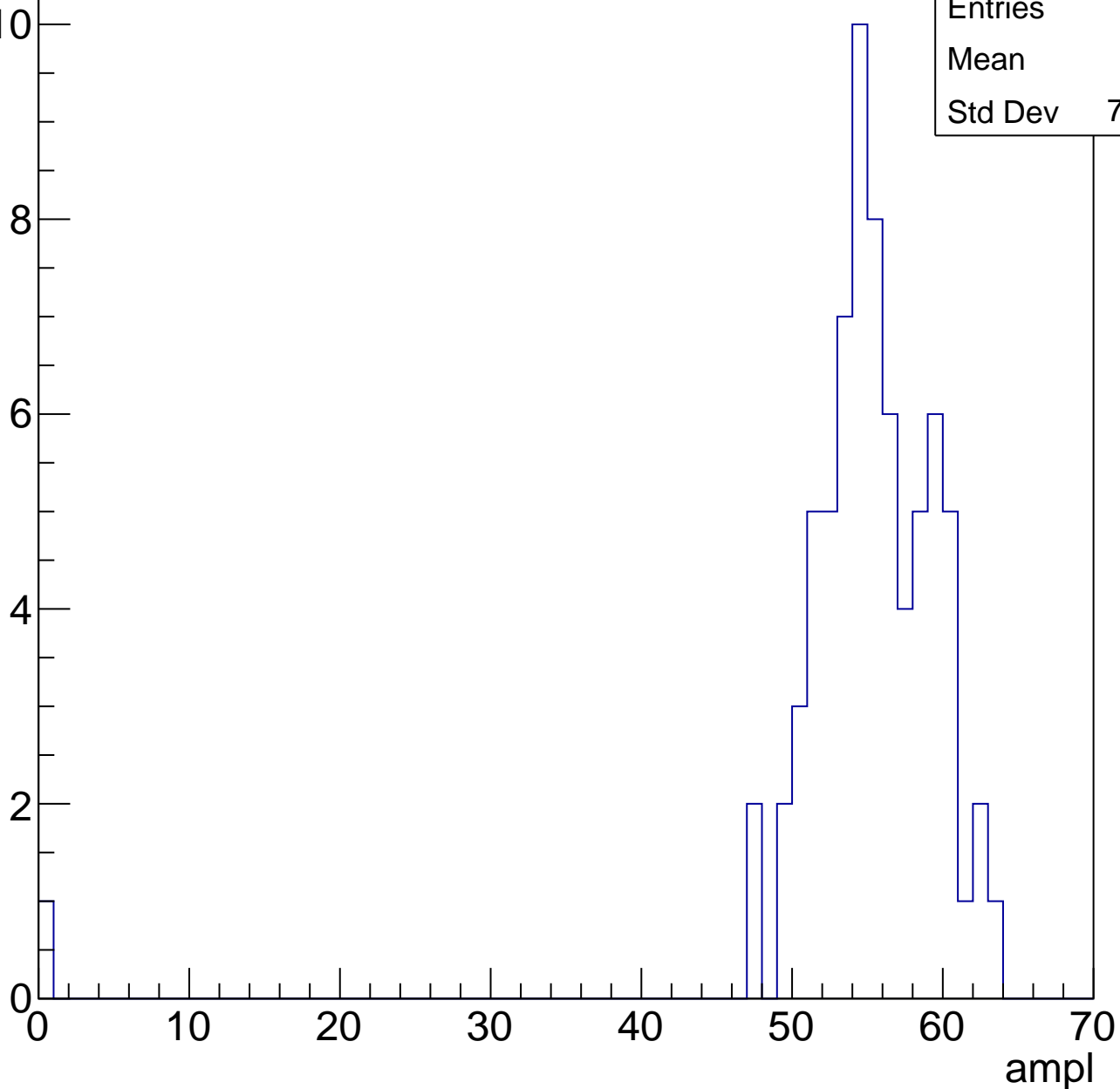


# B1L102S, U24-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	54.3
Std Dev	7.322

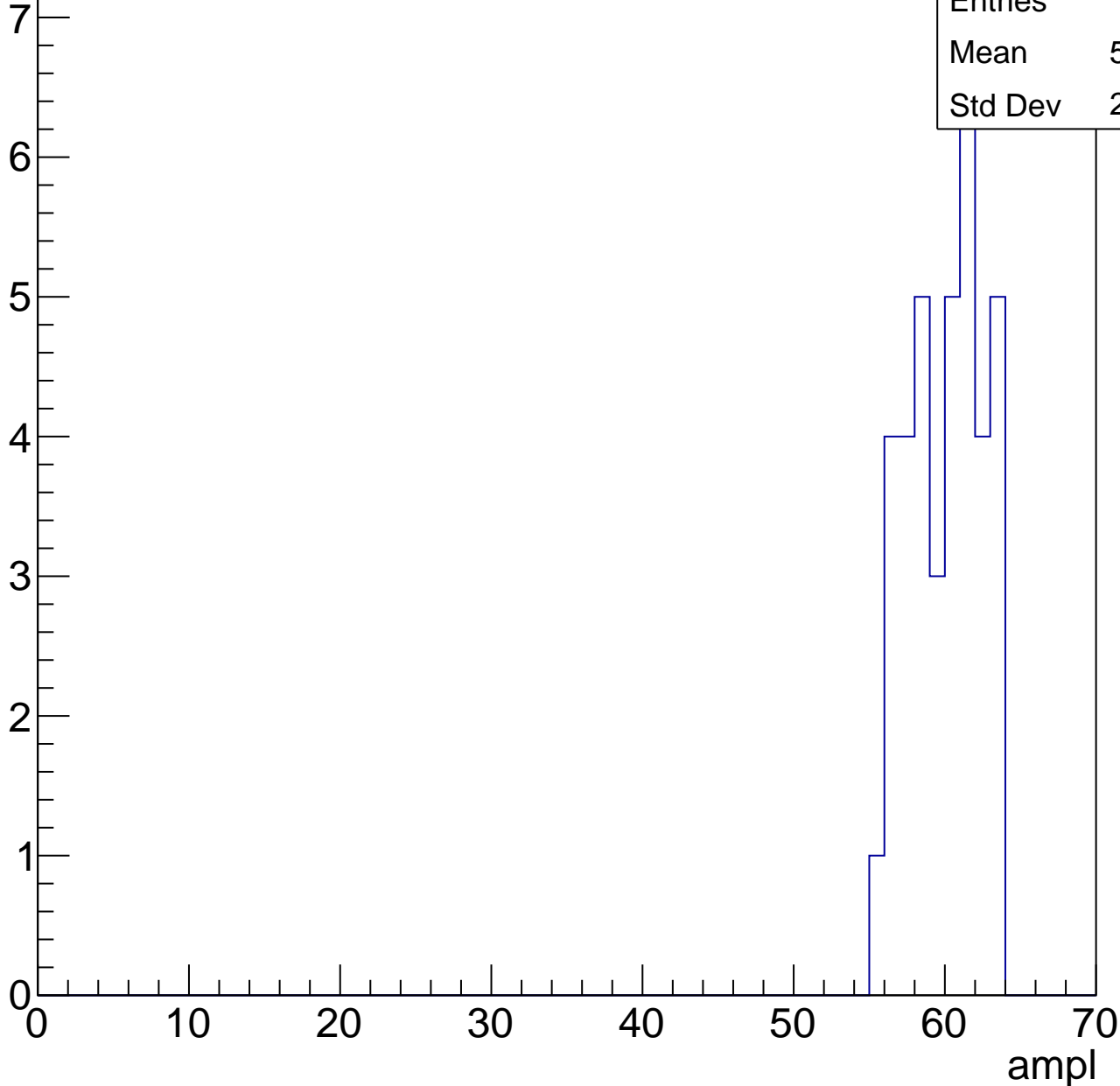


# B1L102S, U24-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

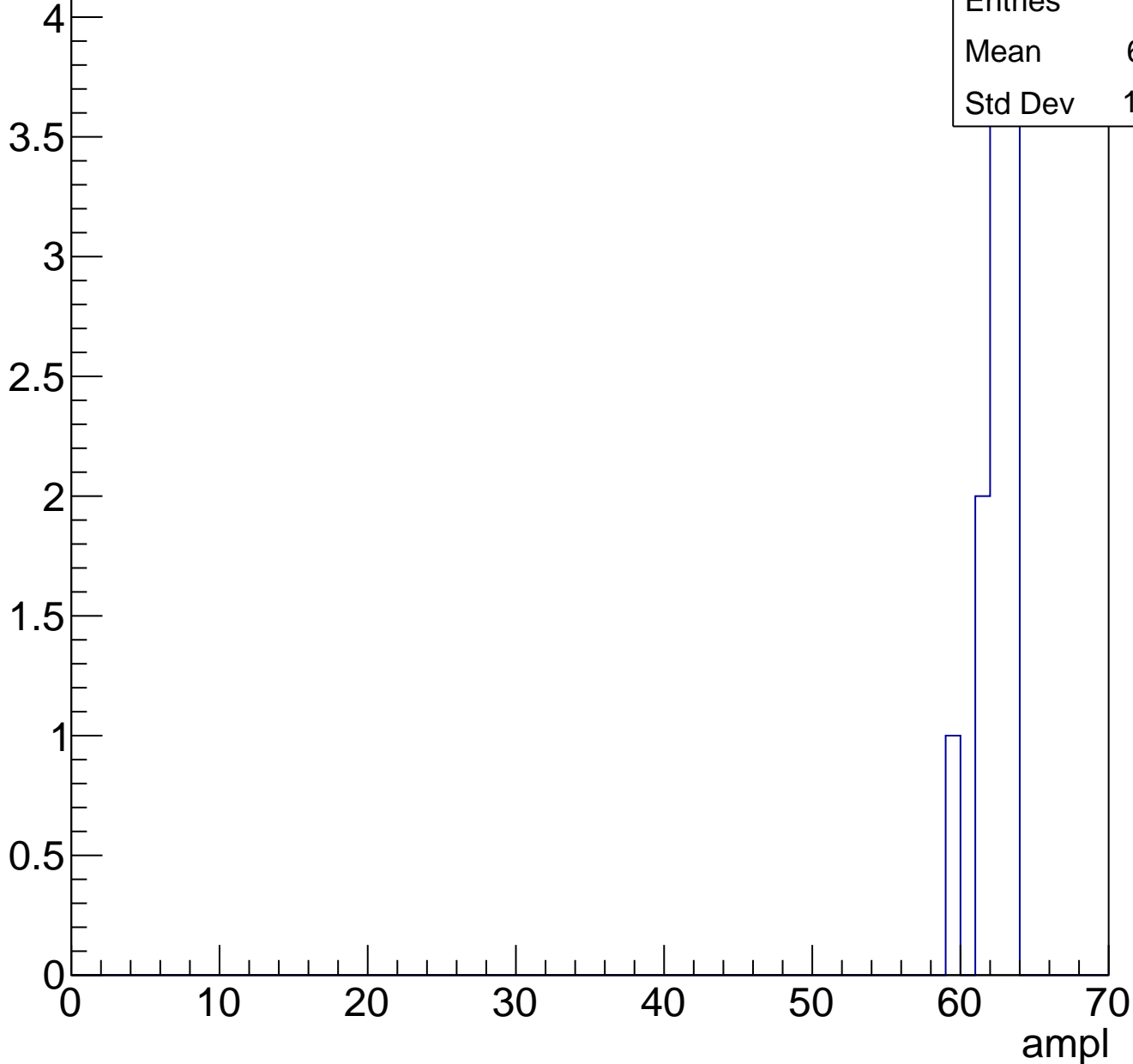
Entries	38
Mean	59.58
Std Dev	2.347



# B1L102S, U24-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch73, adc0

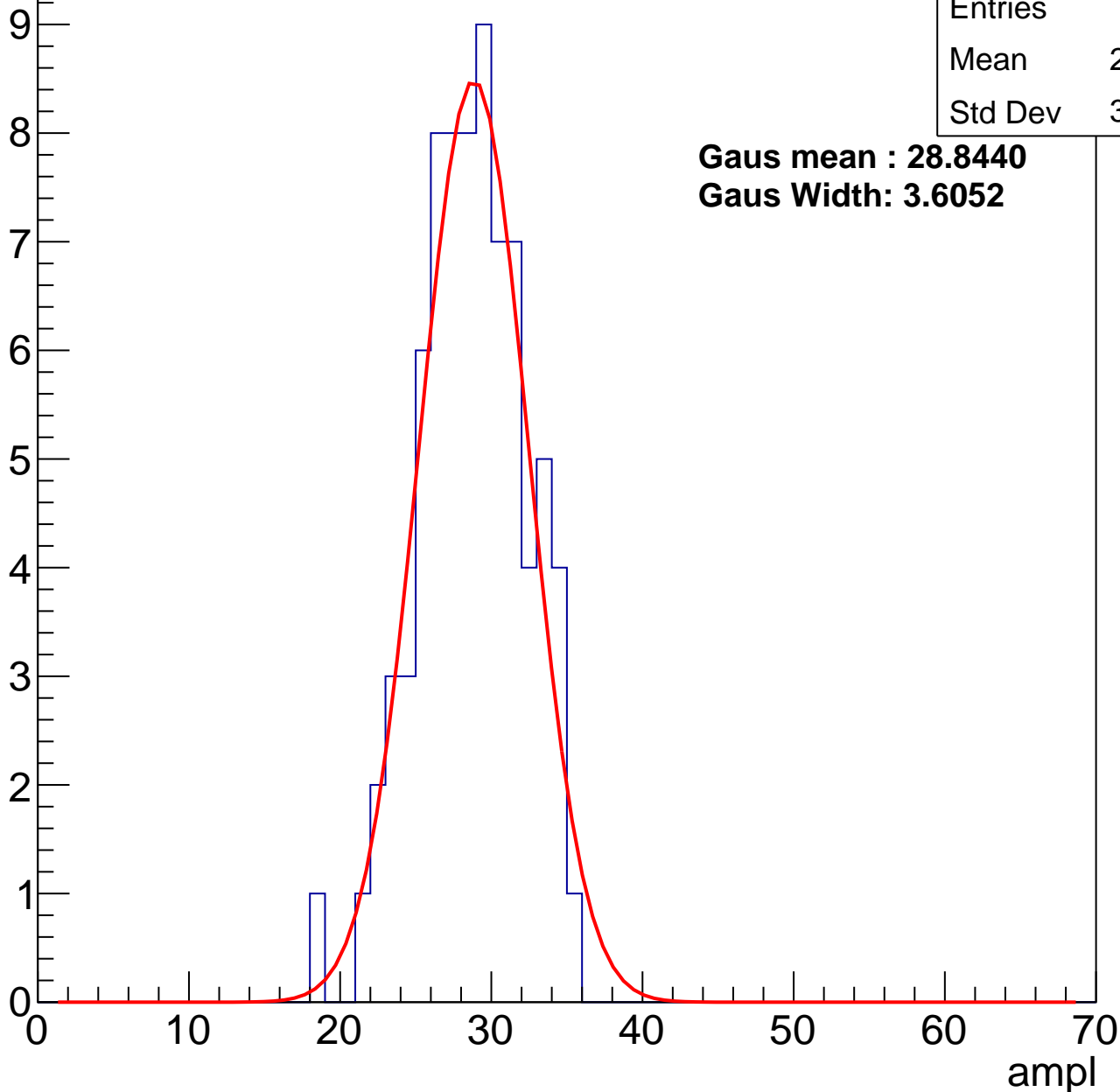
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.23
Std Dev	3.449

**Gaus mean : 28.8440**

**Gaus Width: 3.6052**



# B1L102S, U24-ch73, adc1

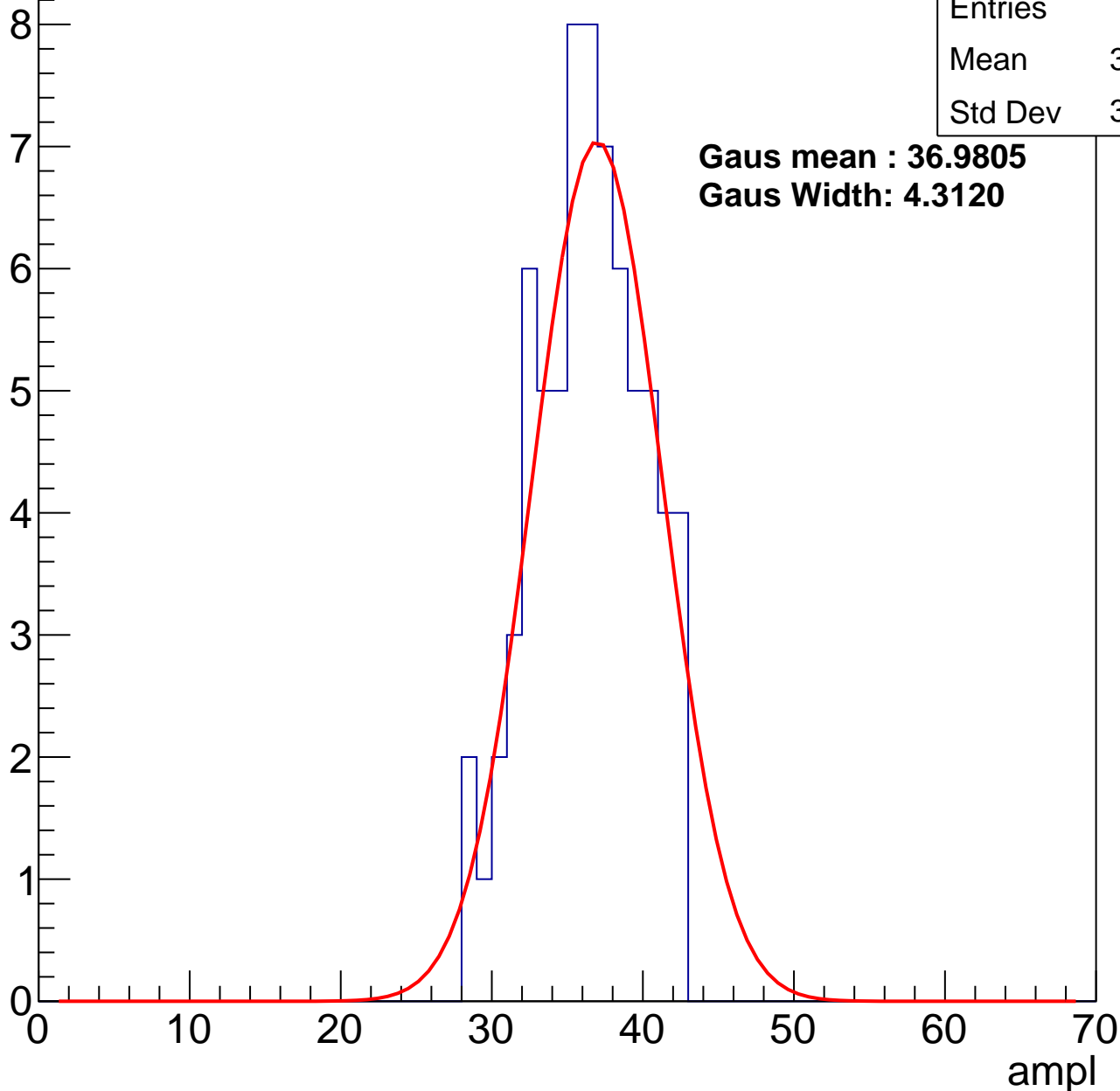
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.87
Std Dev	3.544

**Gaus mean : 36.9805**

**Gaus Width: 4.3120**



# B1L102S, U24-ch73, adc2

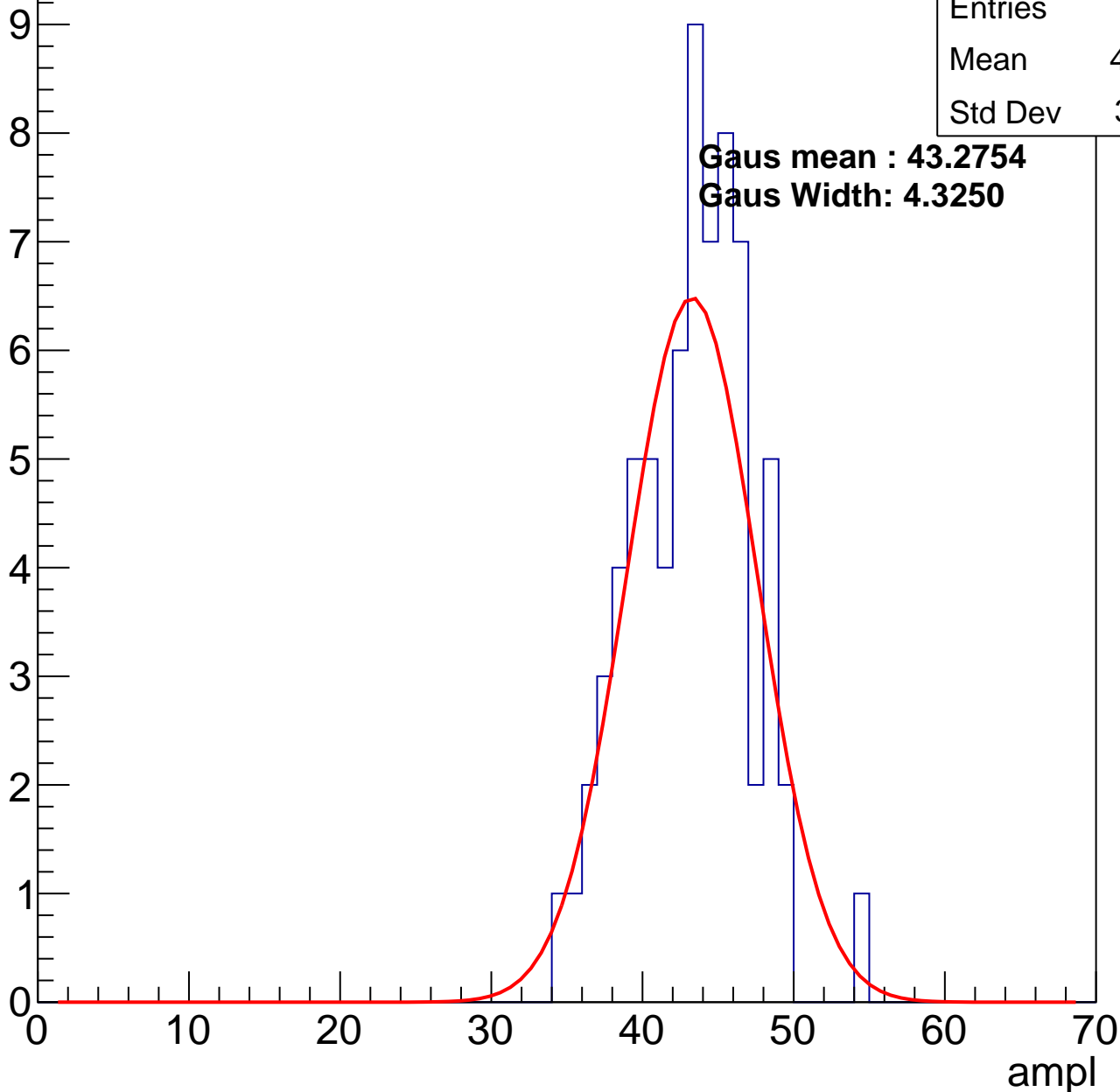
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.75
Std Dev	3.811

**Gaus mean : 43.2754**

**Gaus Width: 4.3250**

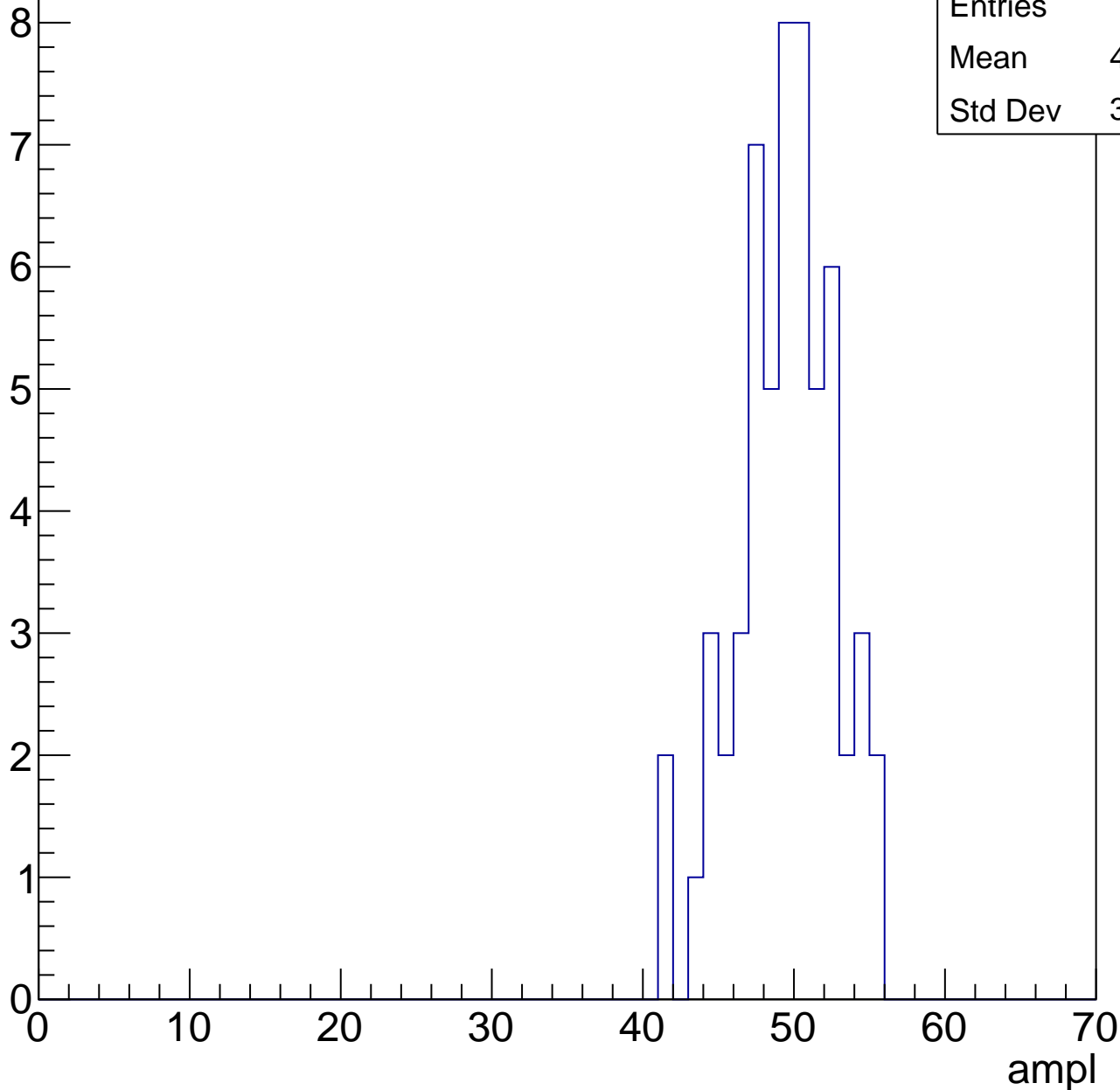


# B1L102S, U24-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.96
Std Dev	3.223

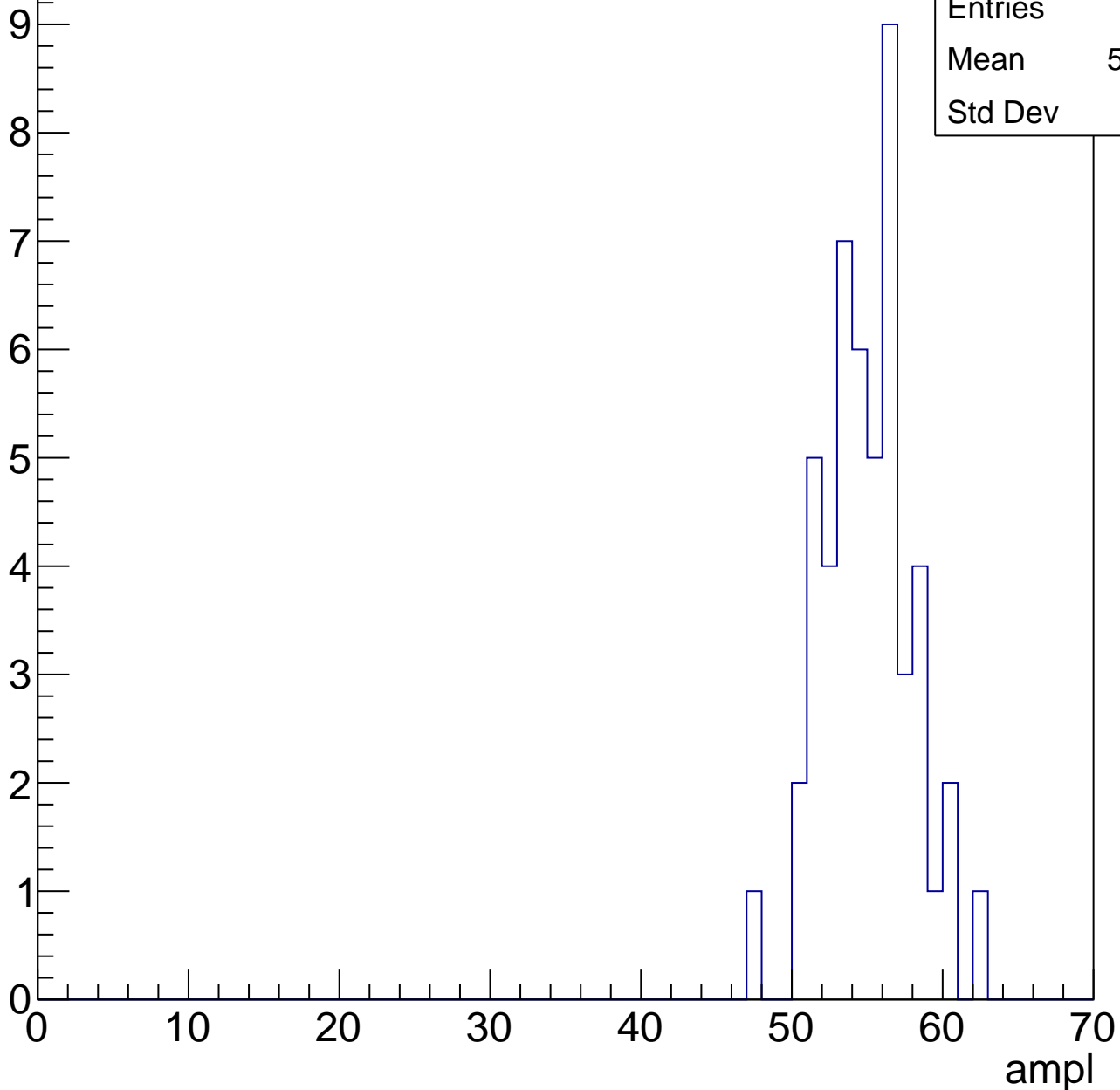


# B1L102S, U24-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.56
Std Dev	2.92

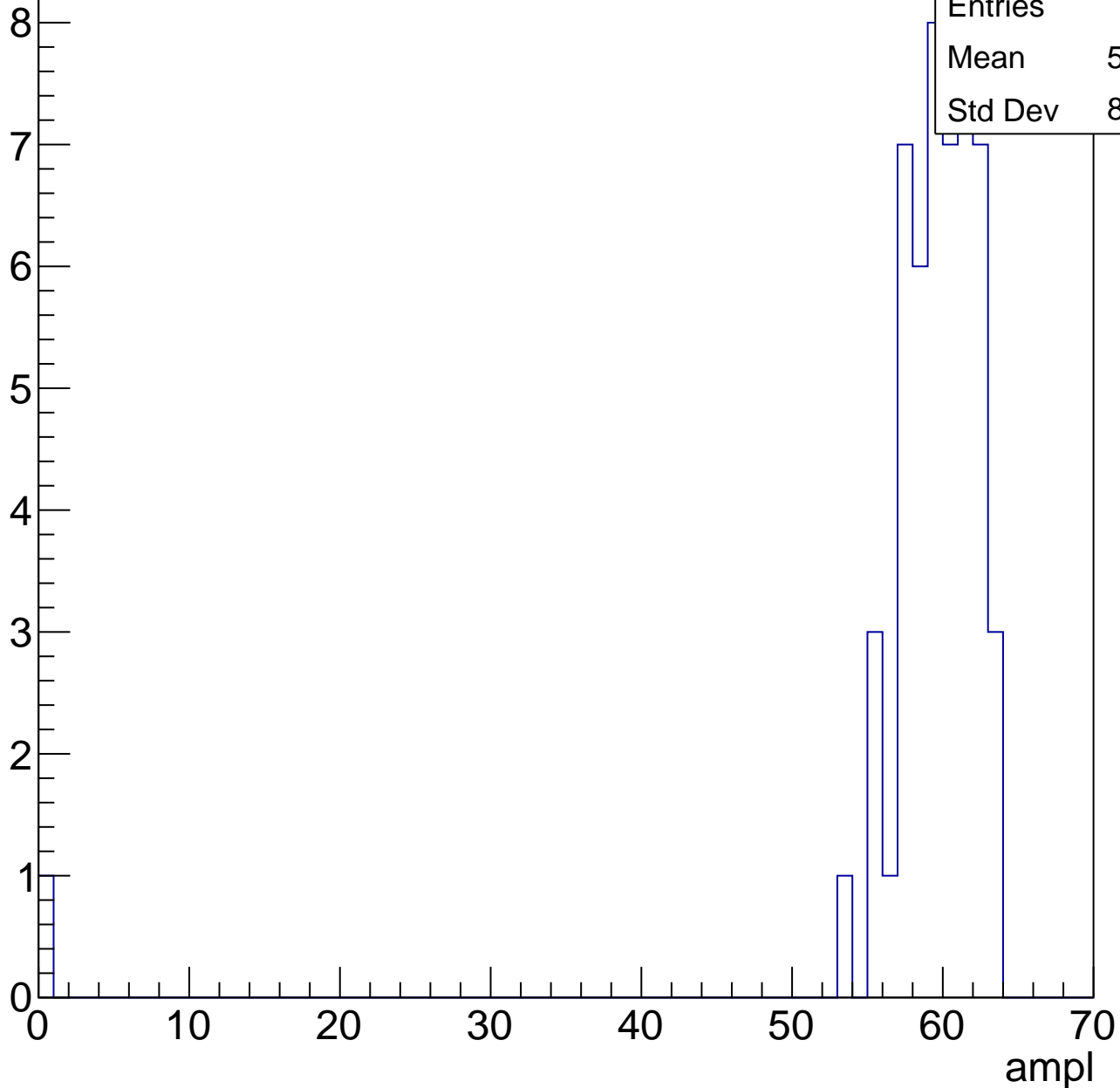


# B1L102S, U24-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.15
Std Dev	8.459

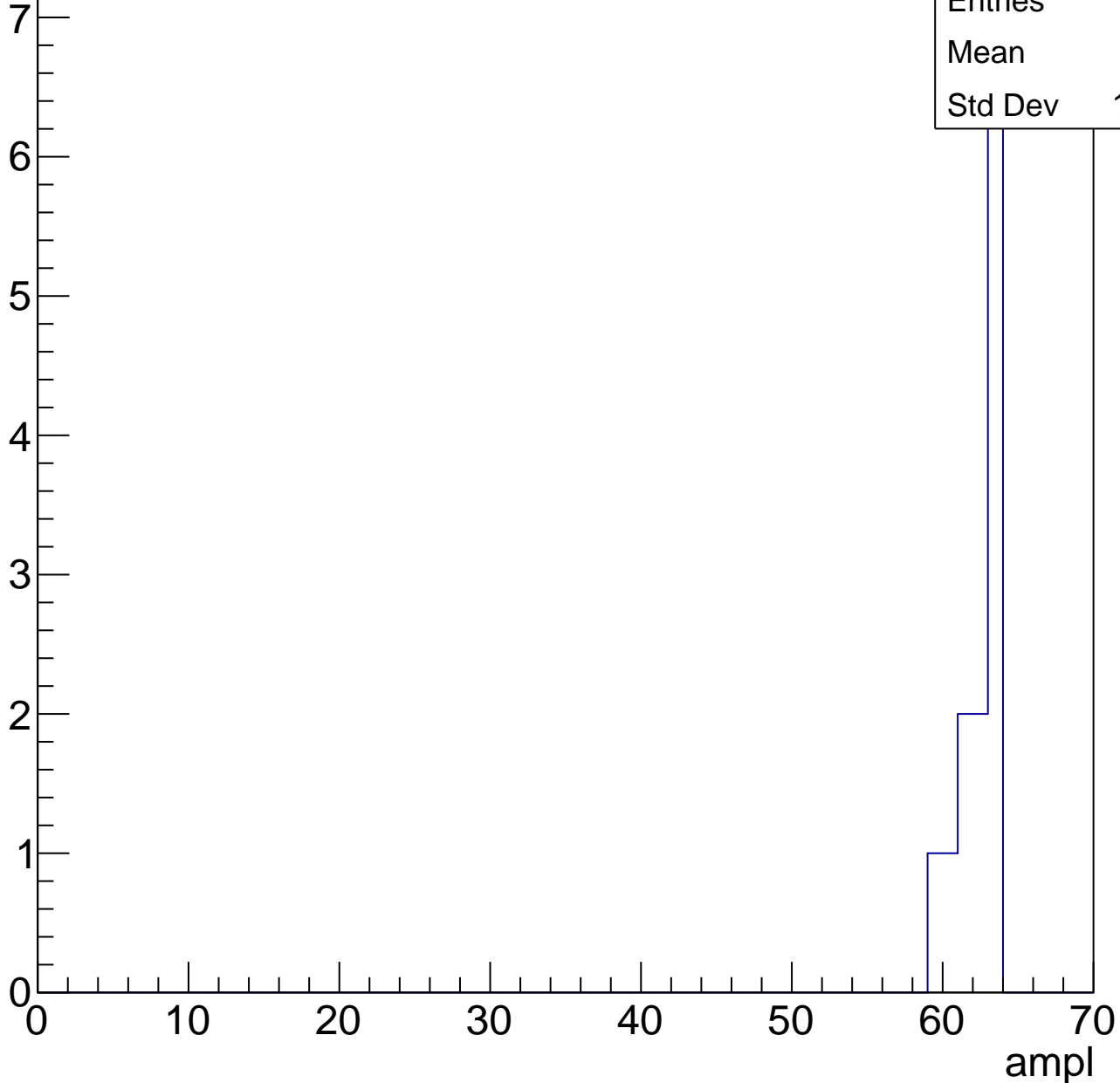


# B1L102S, U24-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	62
Std Dev	1.301





# B1L102S, U24-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch74, adc0

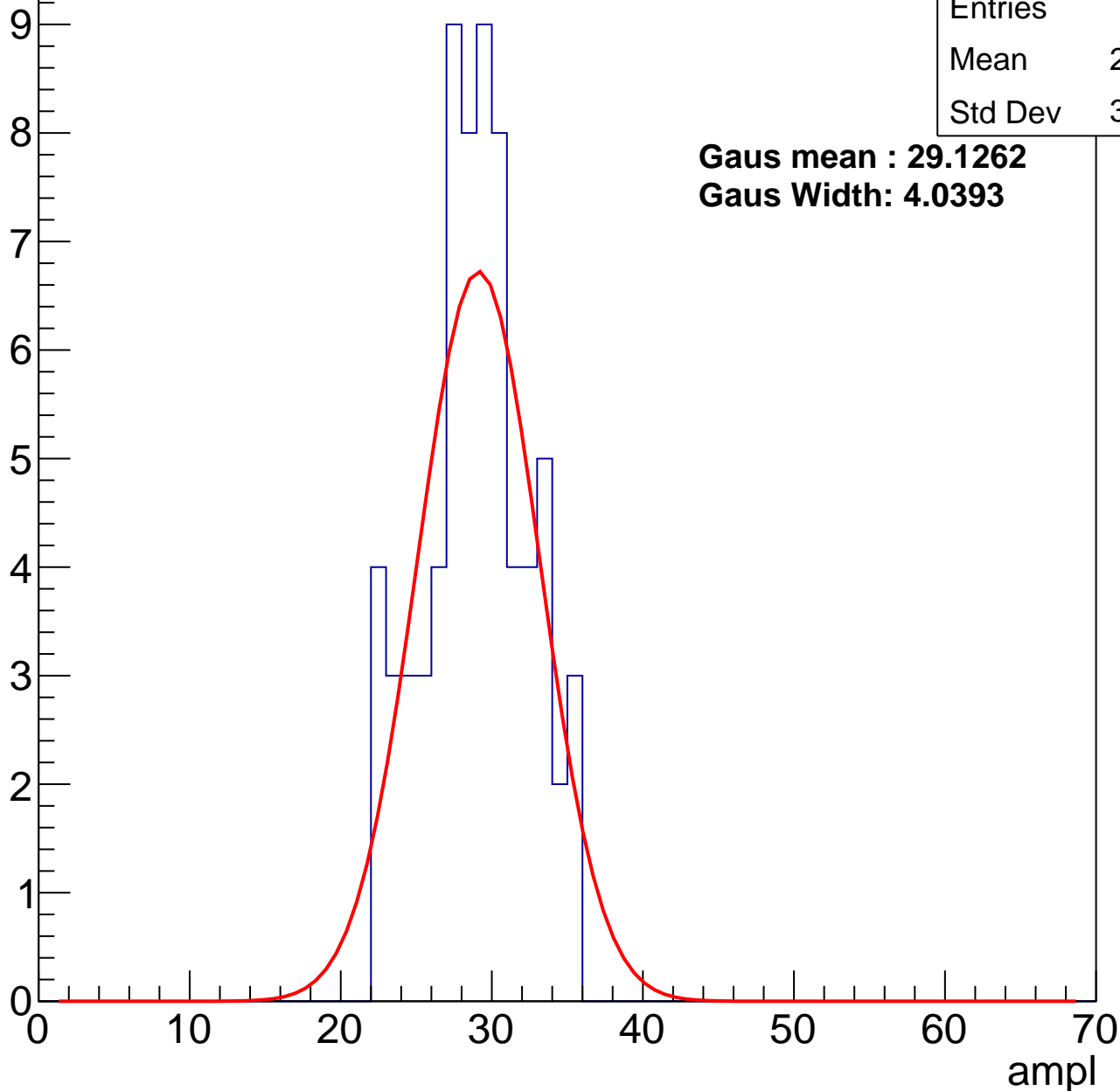
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.49
Std Dev	3.378

**Gaus mean : 29.1262**

**Gaus Width: 4.0393**



# B1L102S, U24-ch74, adc1

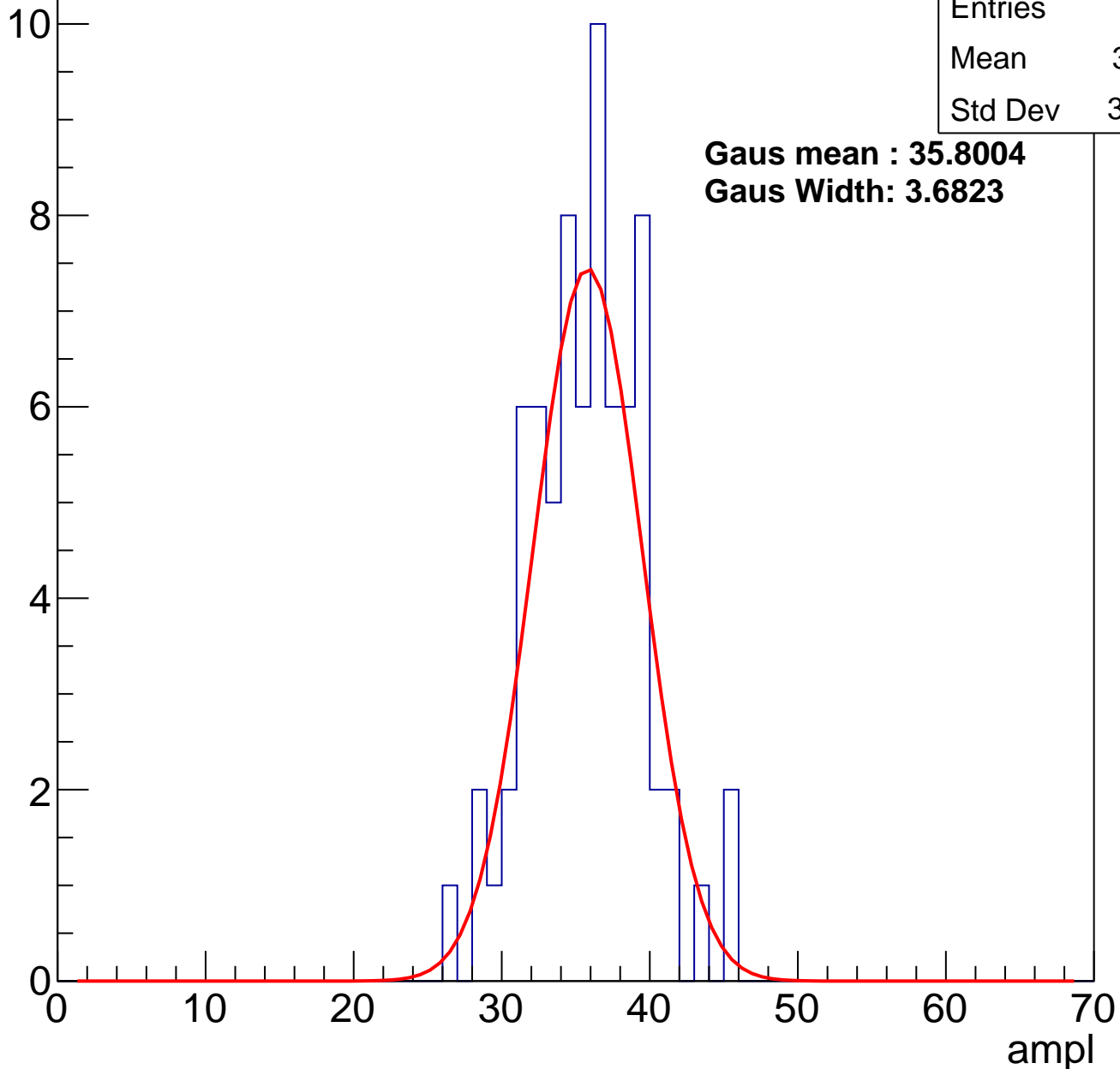
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	74
Mean	35.31
Std Dev	3.745

**Gaus mean : 35.8004**

**Gaus Width: 3.6823**

Entry



# B1L102S, U24-ch74, adc2

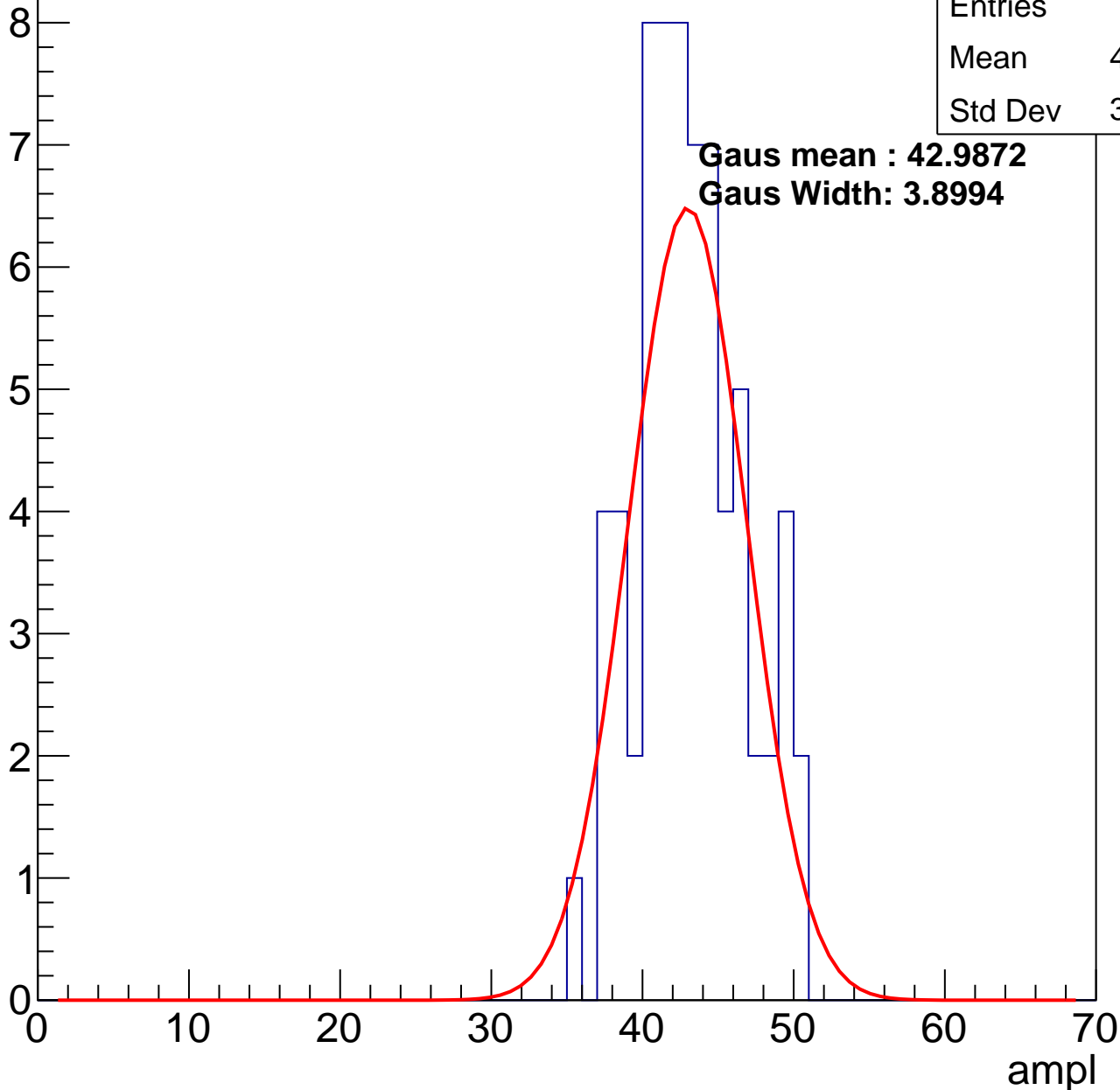
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.68
Std Dev	3.508

**Gaus mean : 42.9872**

**Gaus Width: 3.8994**

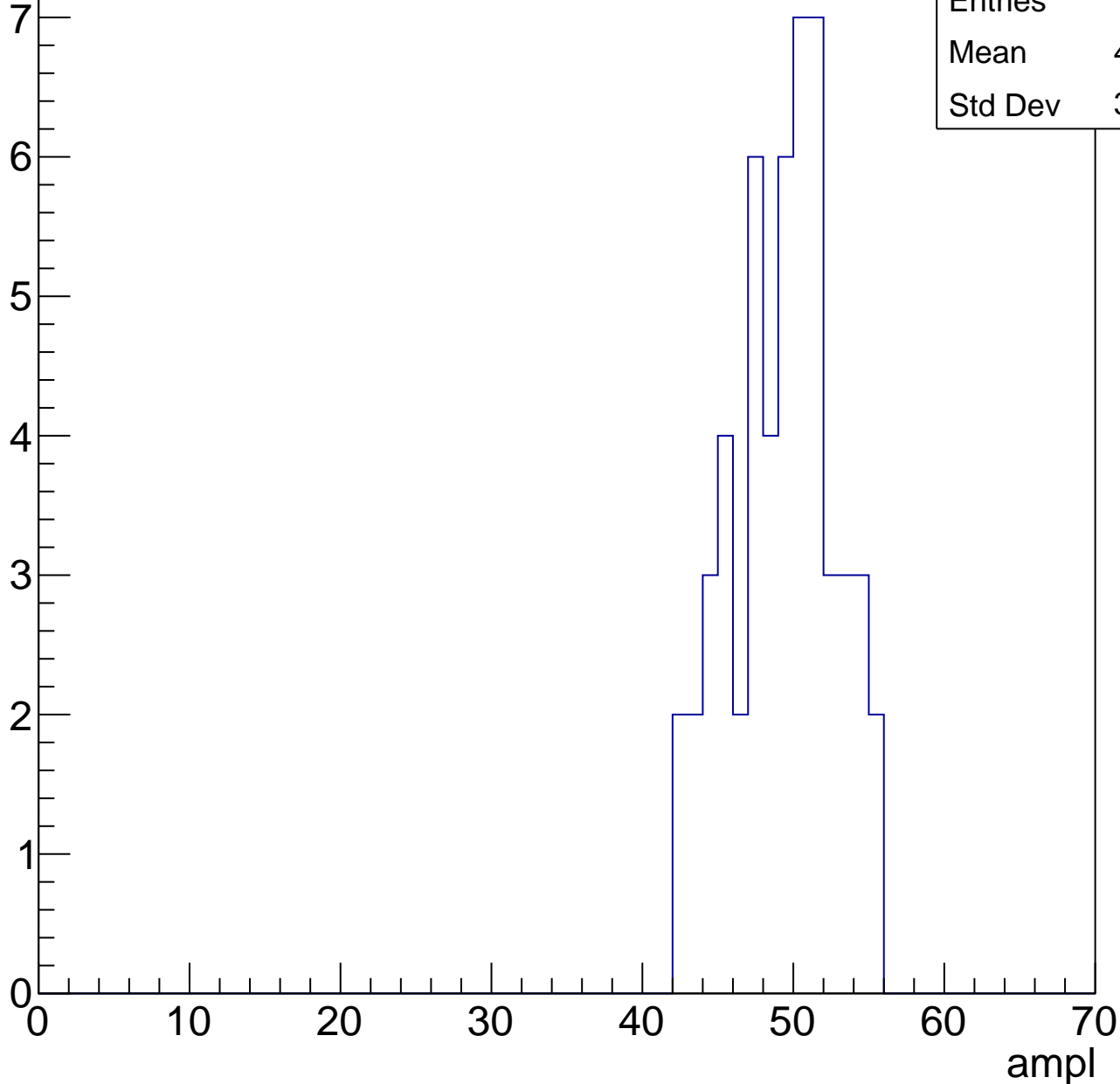


# B1L102S, U24-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.81
Std Dev	3.361

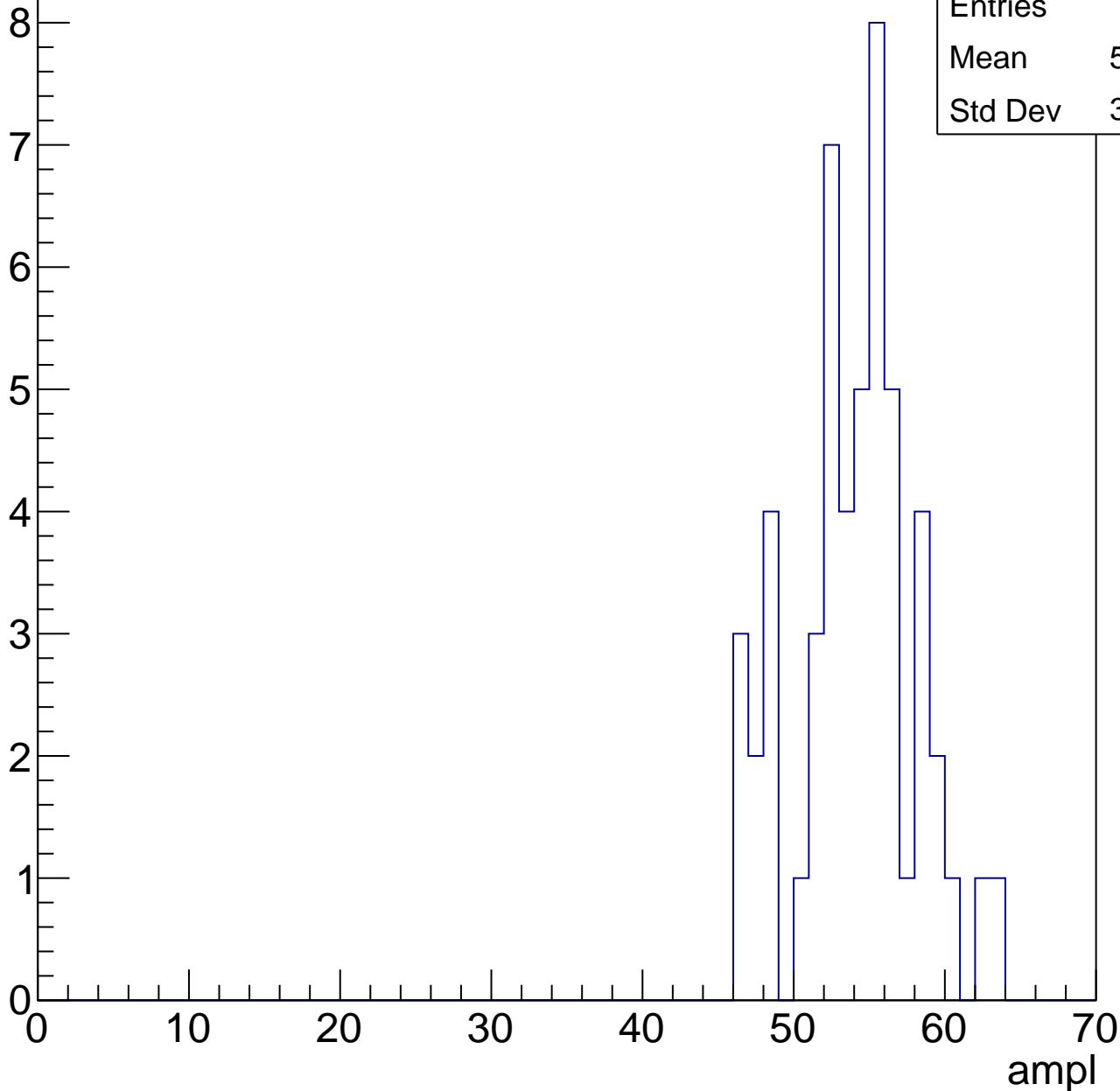


# B1L102S, U24-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

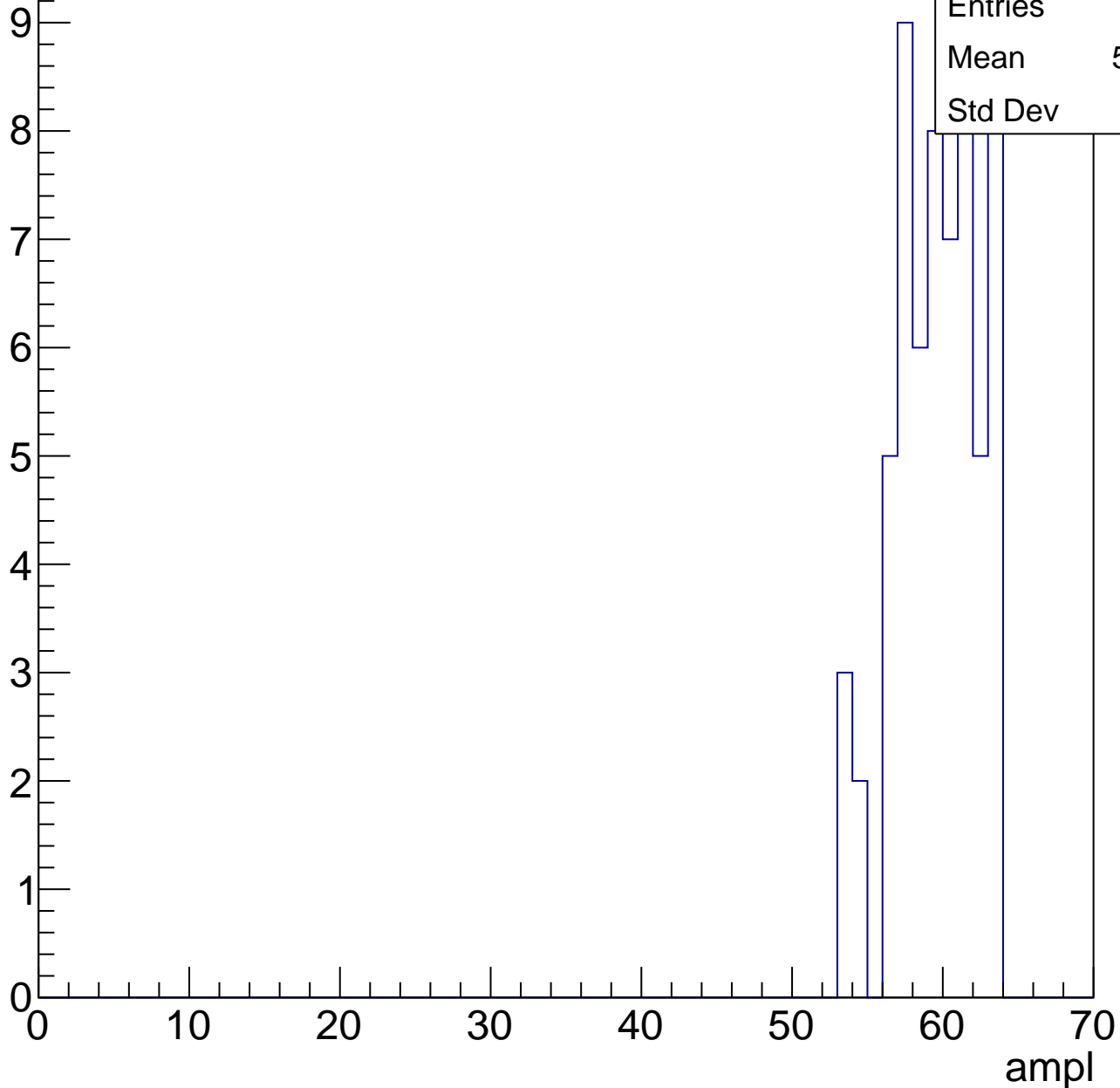
Entries	52
Mean	53.56
Std Dev	3.997



# B1L102S, U24-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

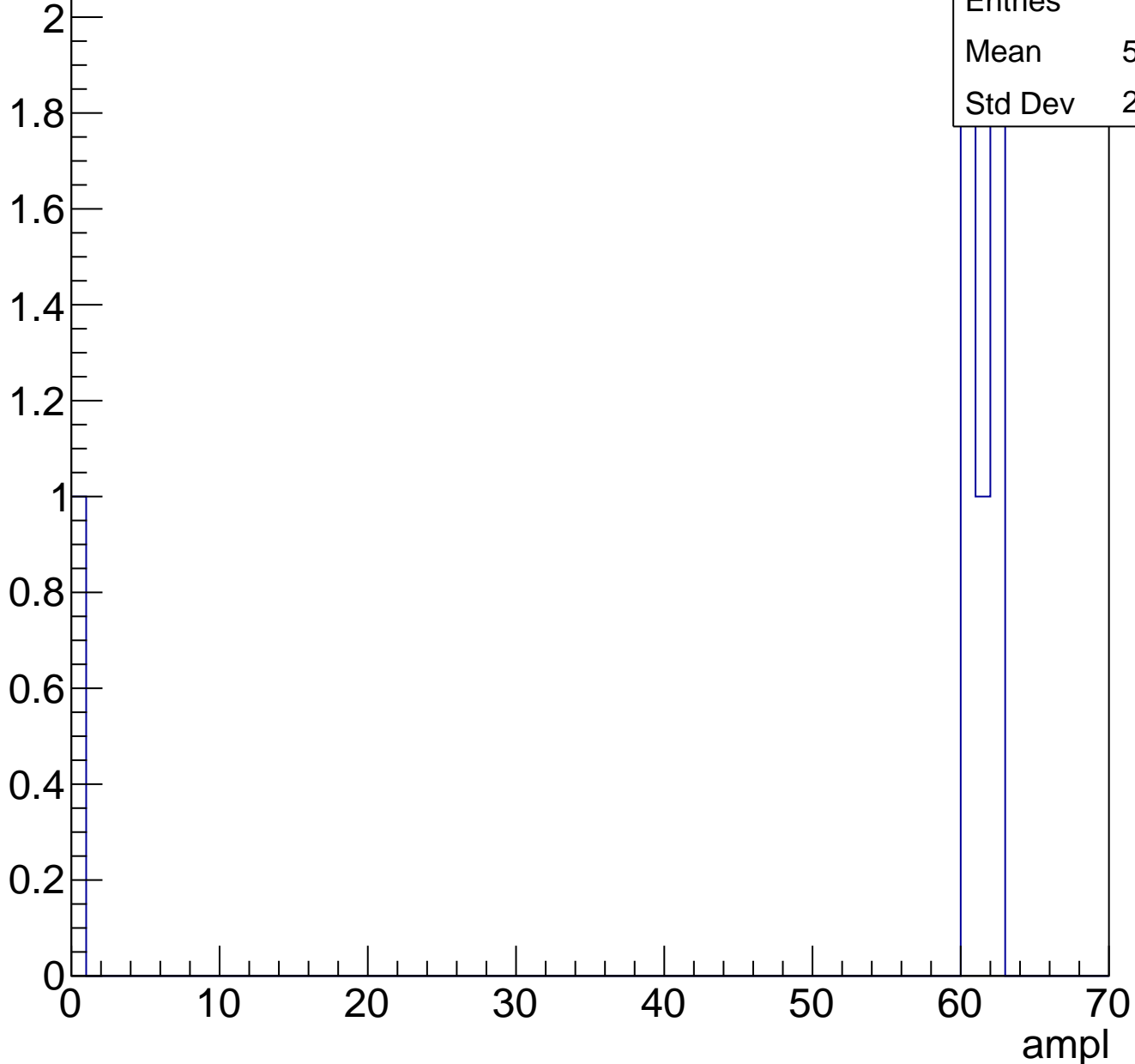


Entries	62
Mean	59.11
Std Dev	2.76

# B1L102S, U24-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



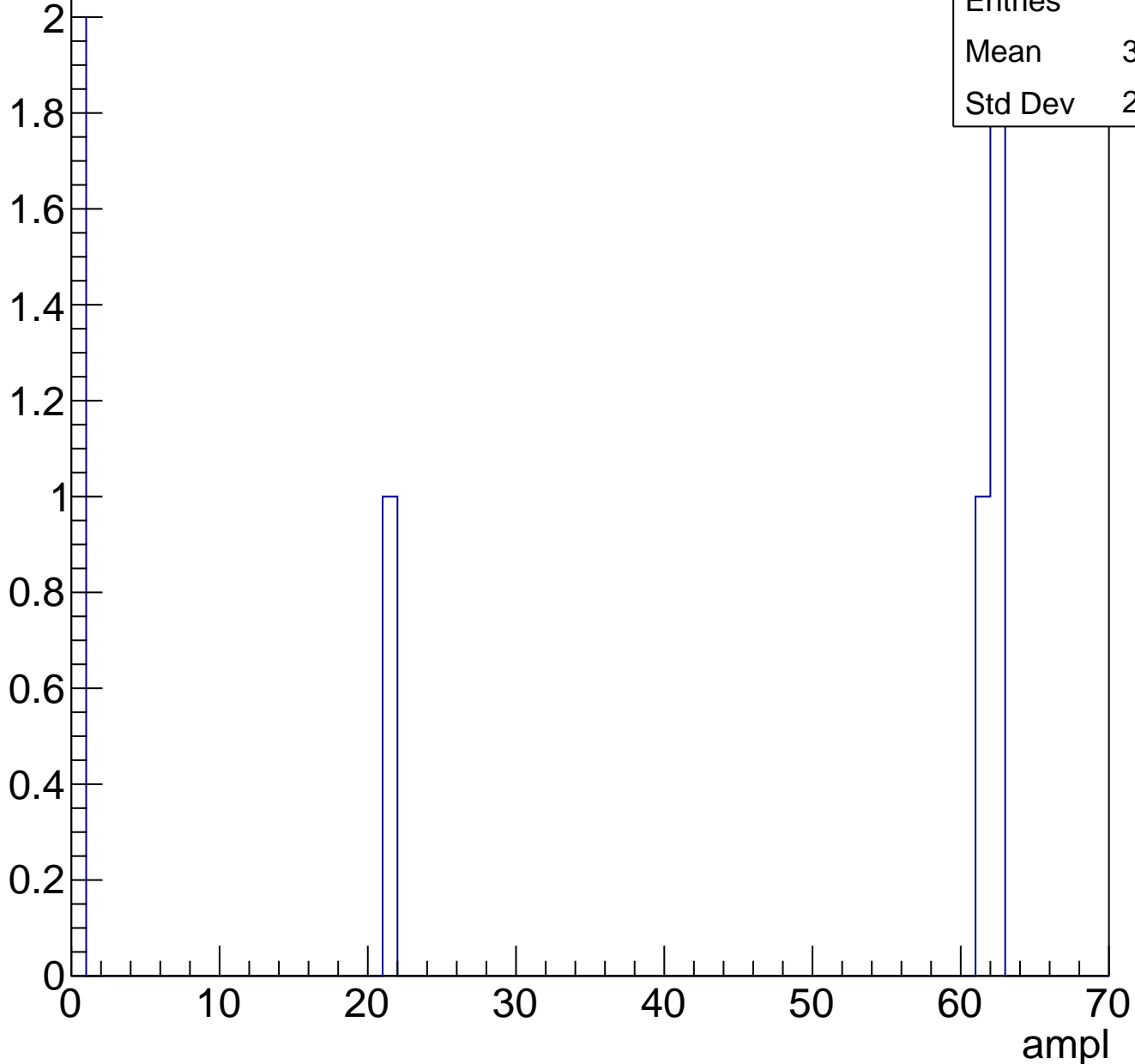
Entries	6
Mean	50.83
Std Dev	22.75



# B1L102S, U24-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	6
Mean	34.33
Std Dev	28.22

# B1L102S, U24-ch75, adc0

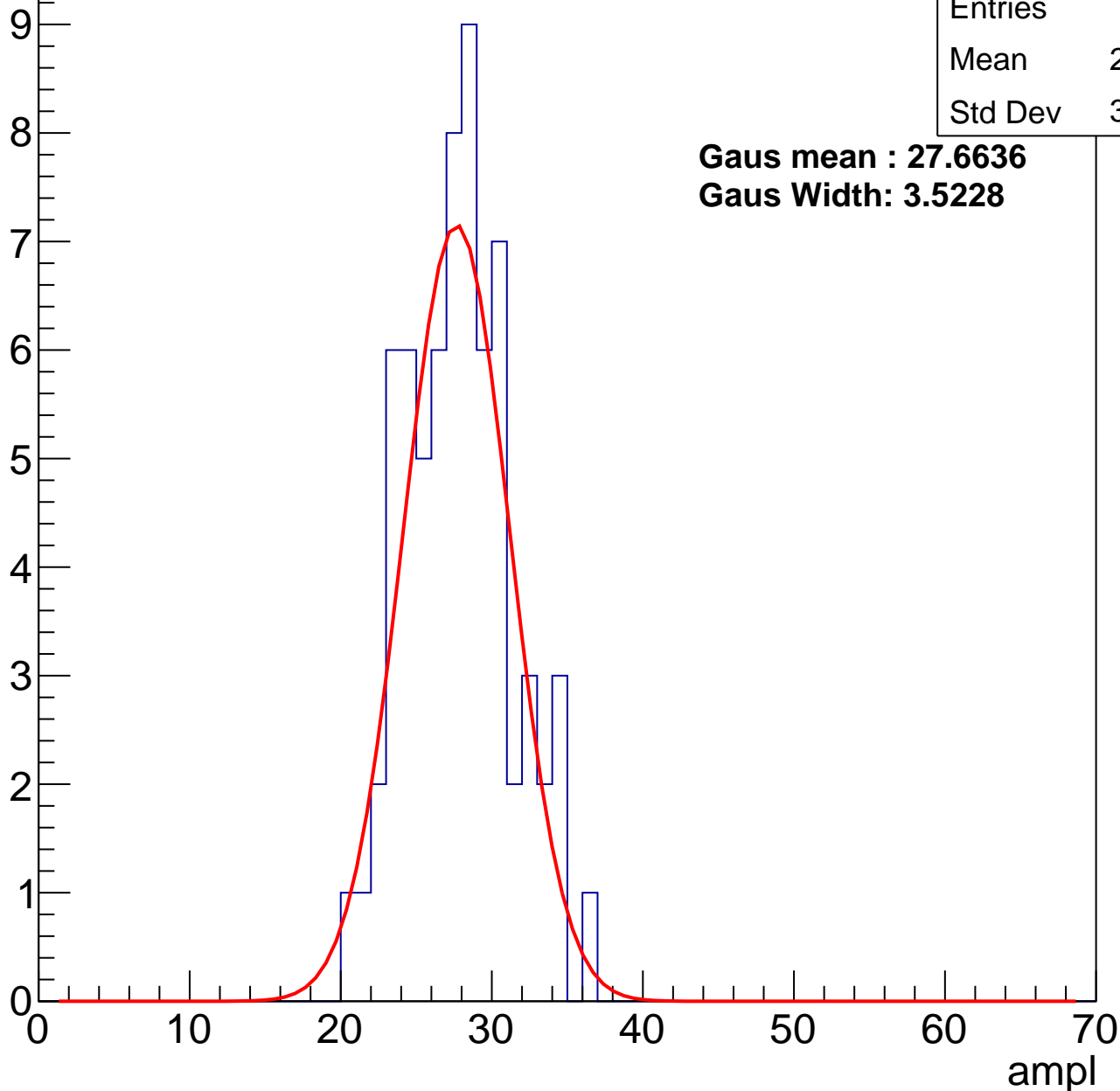
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.38
Std Dev	3.439

**Gaus mean : 27.6636**

**Gaus Width: 3.5228**



# B1L102S, U24-ch75, adc1

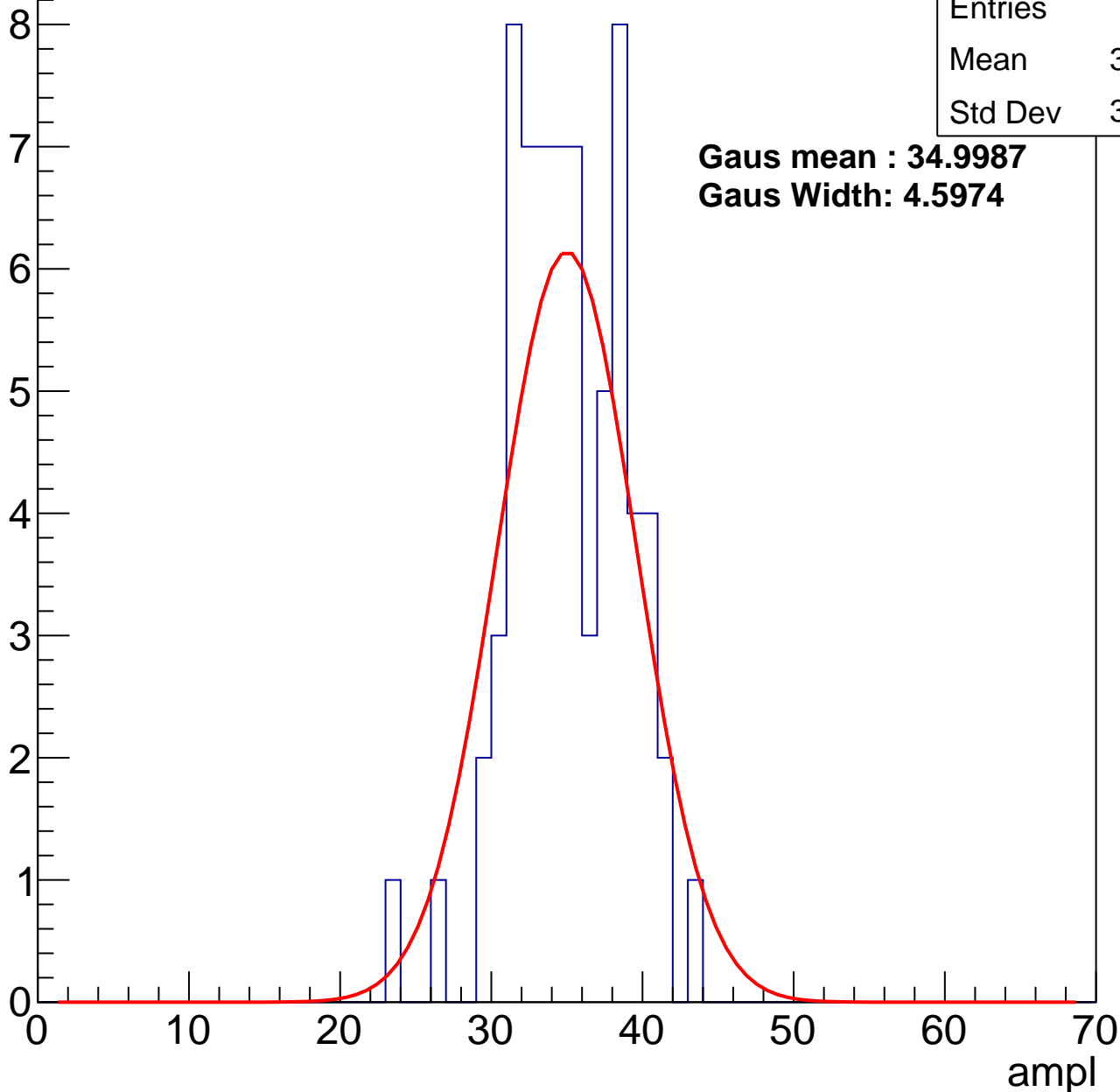
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.59
Std Dev	3.744

**Gaus mean : 34.9987**

**Gaus Width: 4.5974**



# B1L102S, U24-ch75, adc2

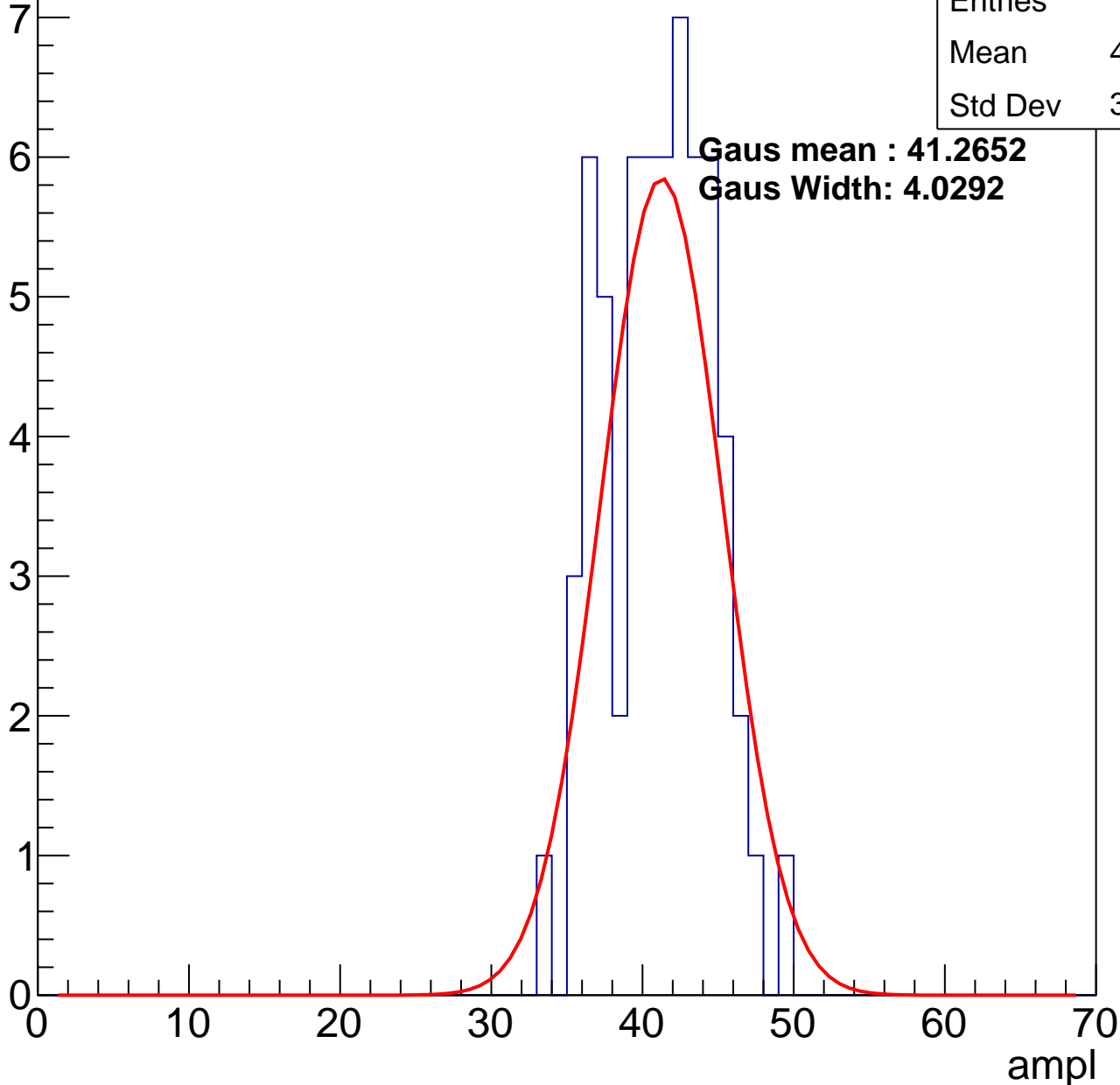
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.63
Std Dev	3.456

**Gaus mean : 41.2652**

**Gaus Width: 4.0292**

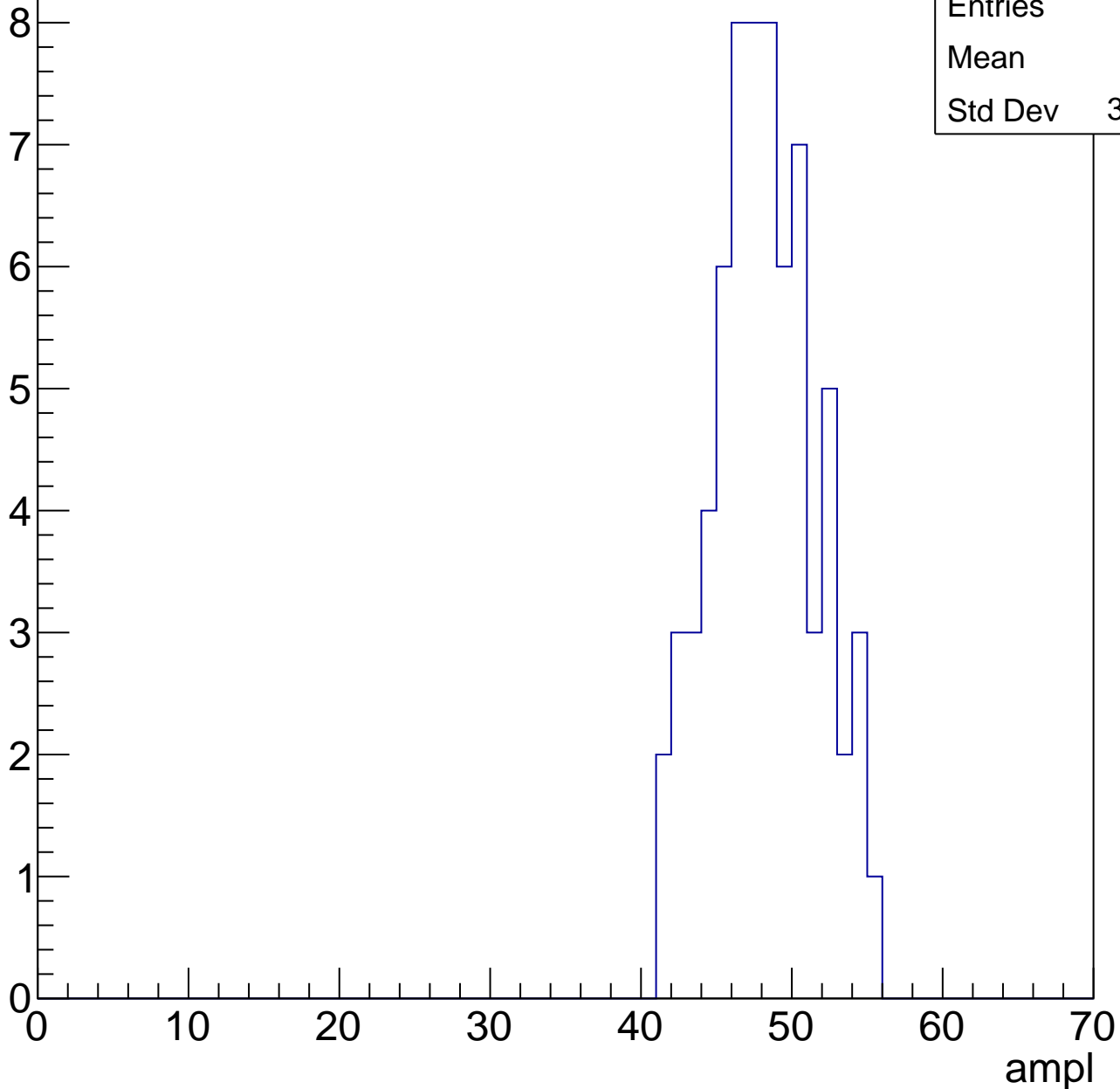


# B1L102S, U24-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.7
Std Dev	3.364

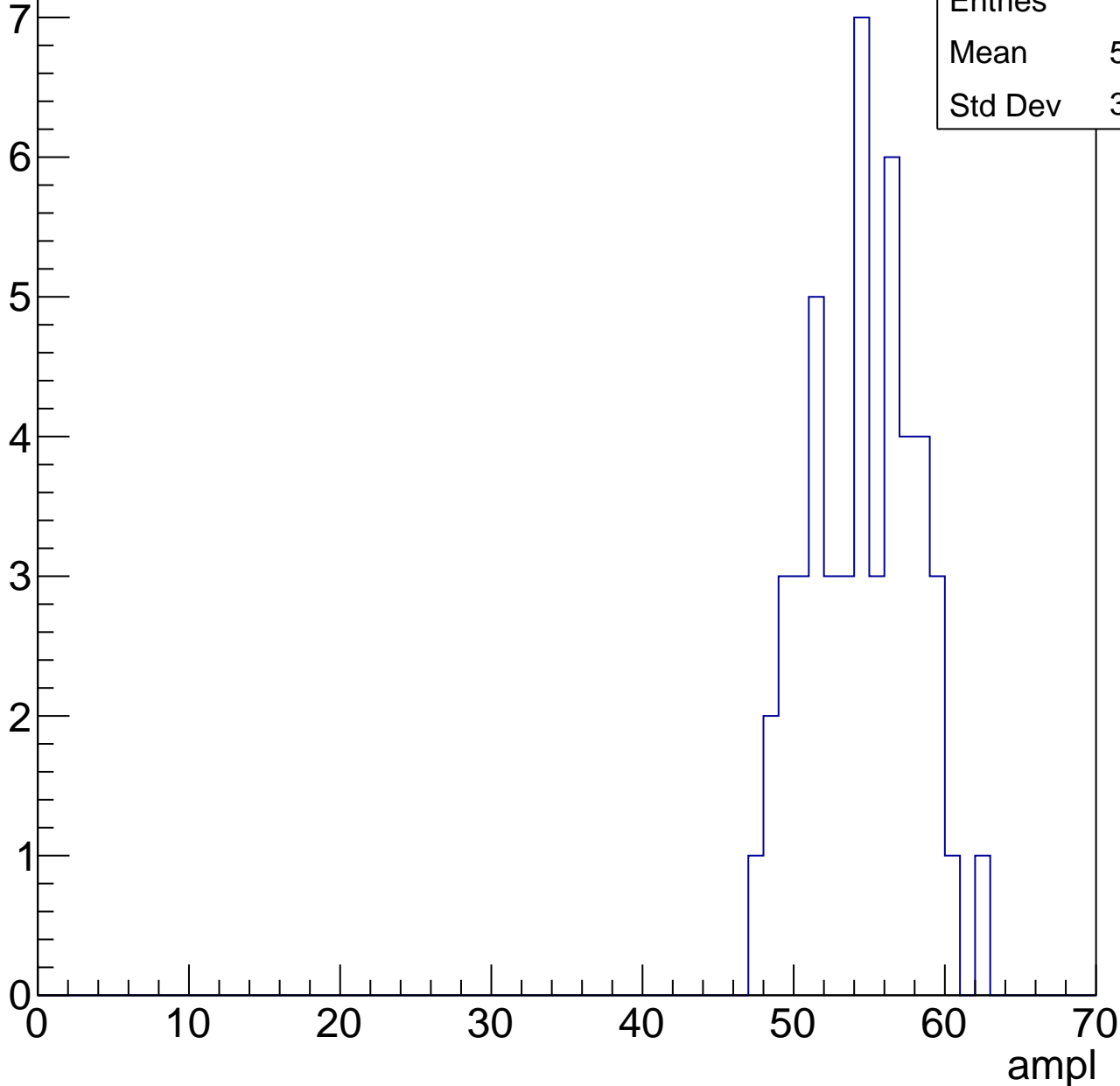


# B1L102S, U24-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.04
Std Dev	3.516

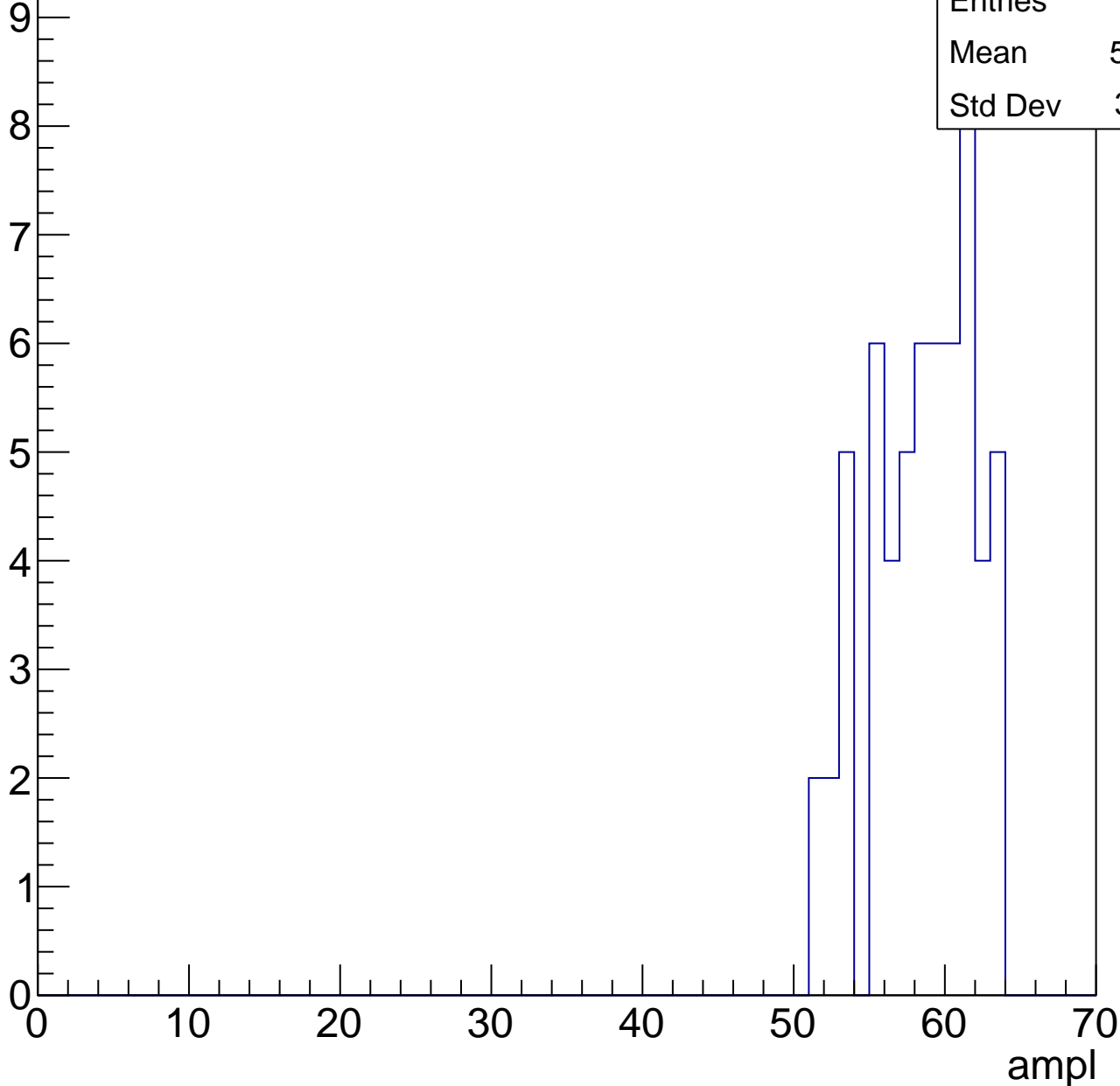


# B1L102S, U24-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.07
Std Dev	3.341

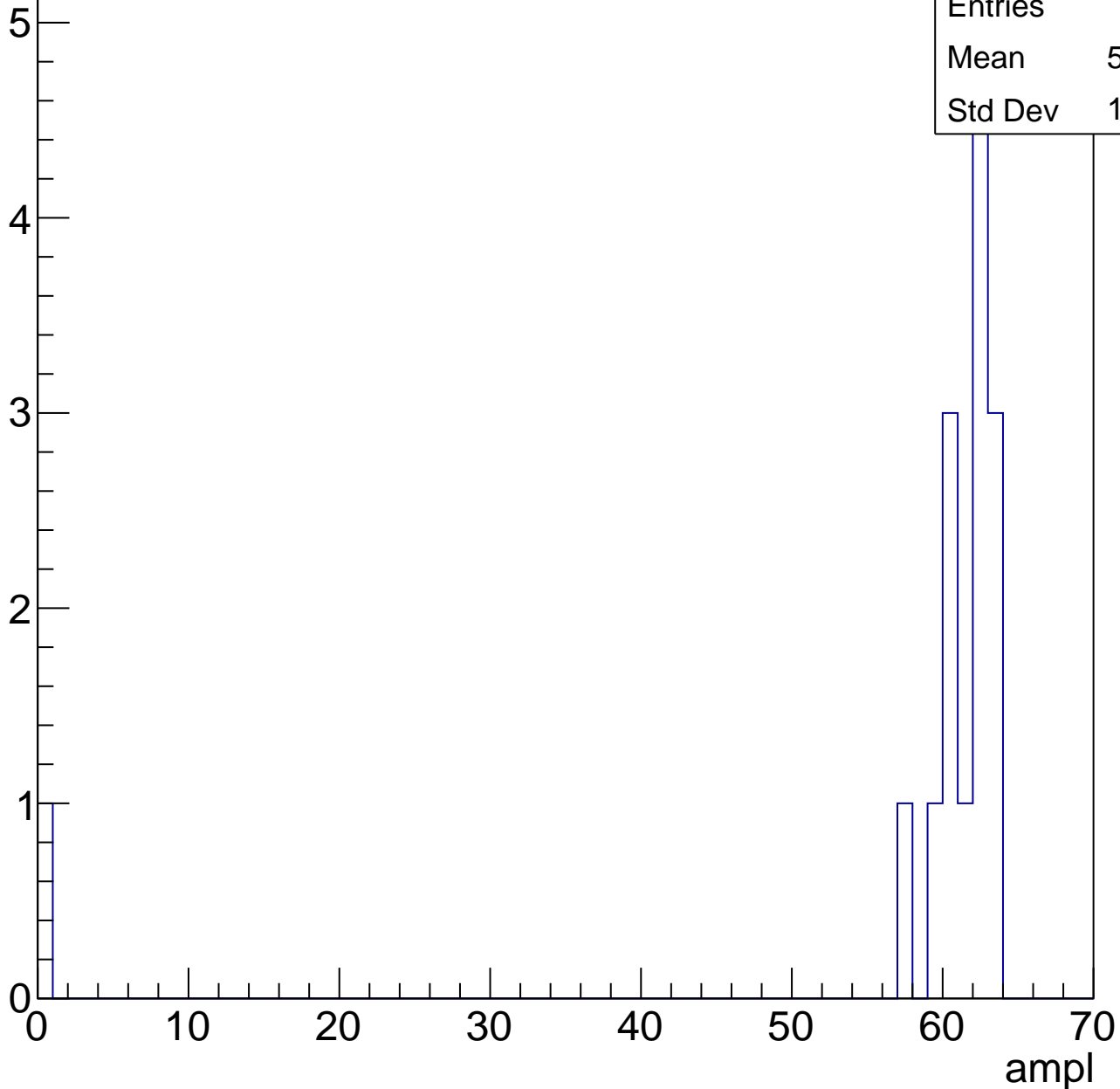


# B1L102S, U24-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.07
Std Dev	15.34





# B1L102S, U24-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch76, adc0

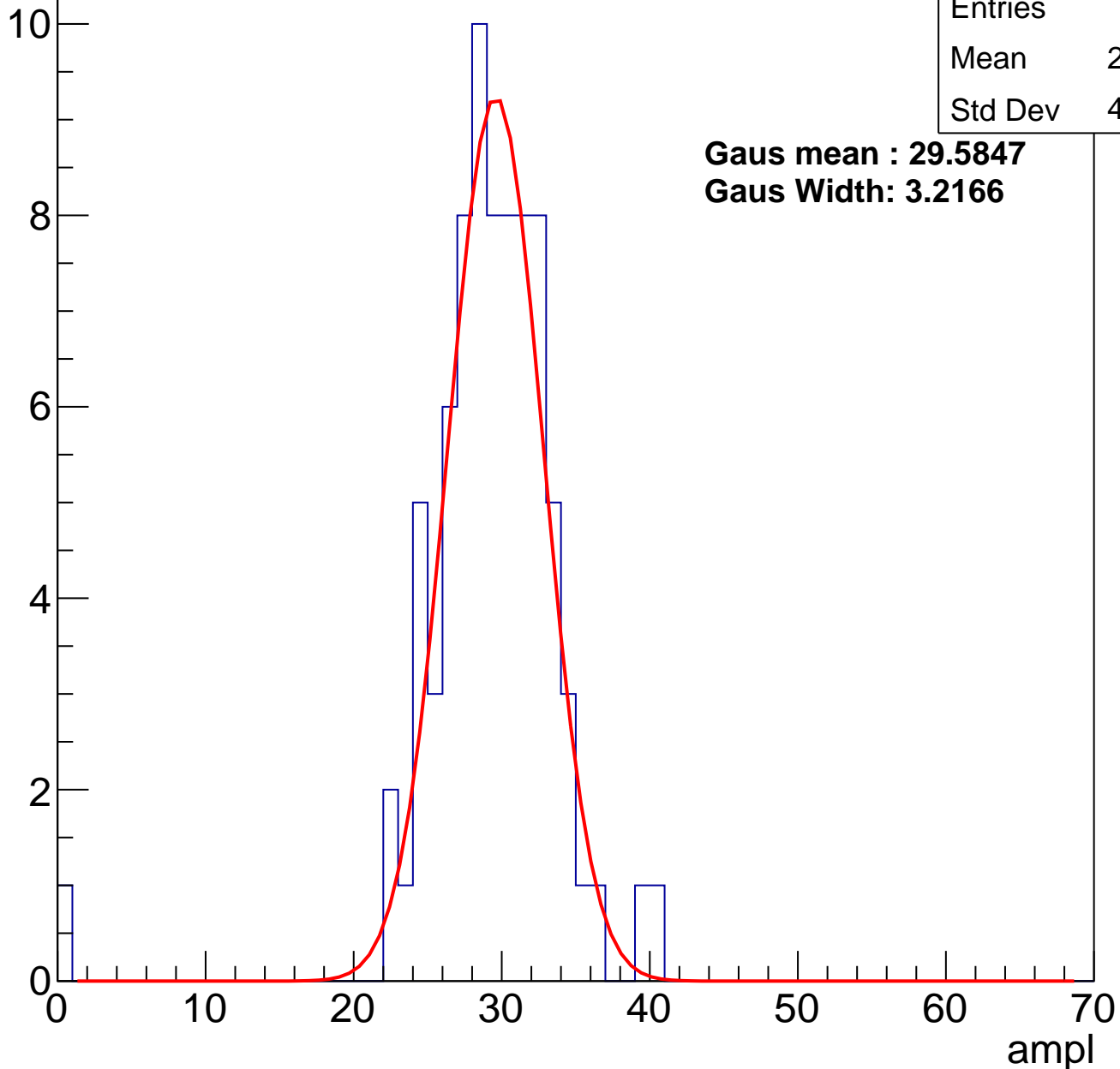
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	28.84
Std Dev	4.763

**Gaus mean : 29.5847**

**Gaus Width: 3.2166**

Entry



# B1L102S, U24-ch76, adc1

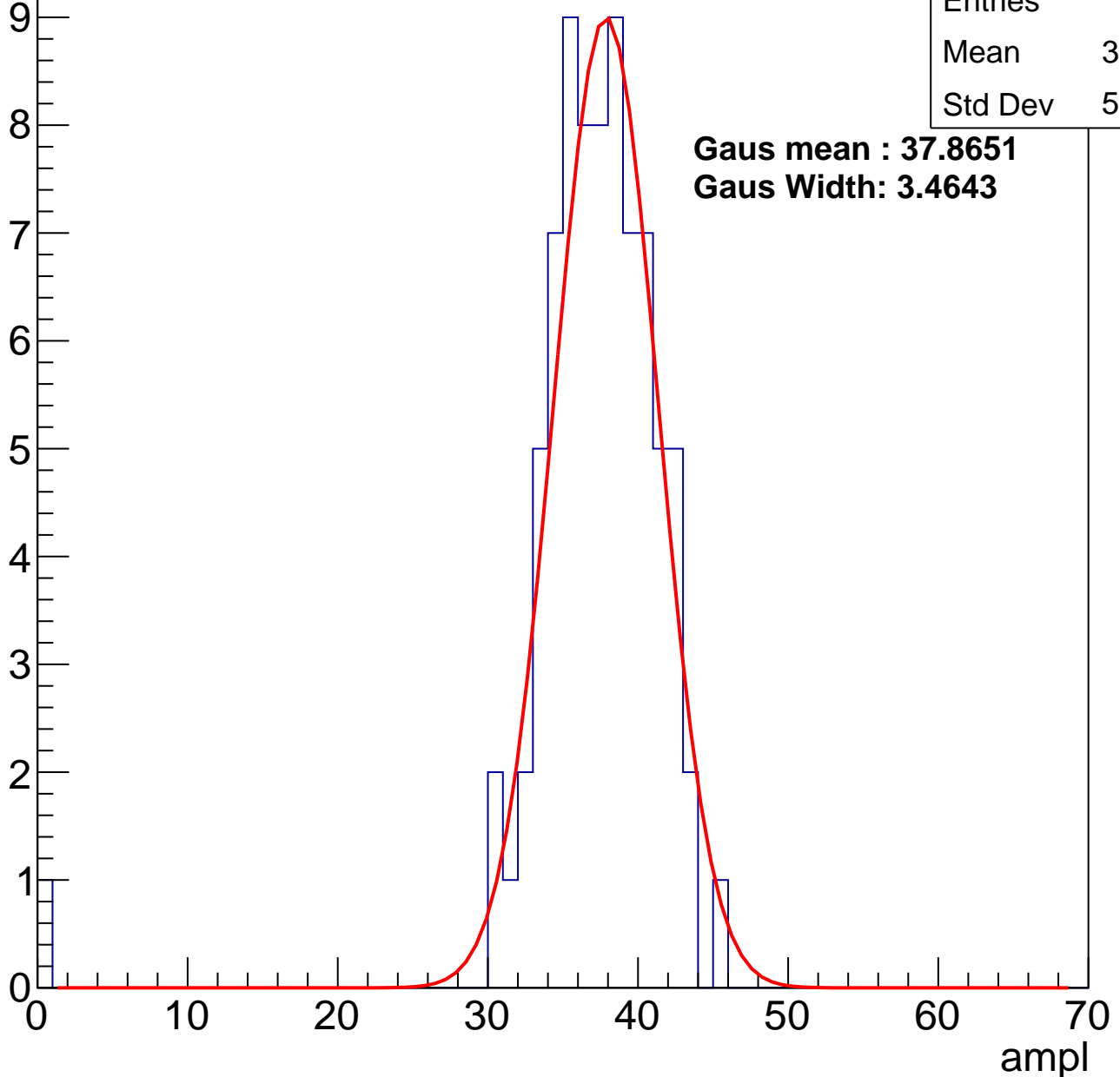
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.68
Std Dev	5.245

**Gaus mean : 37.8651**

**Gaus Width: 3.4643**



# B1L102S, U24-ch76, adc2

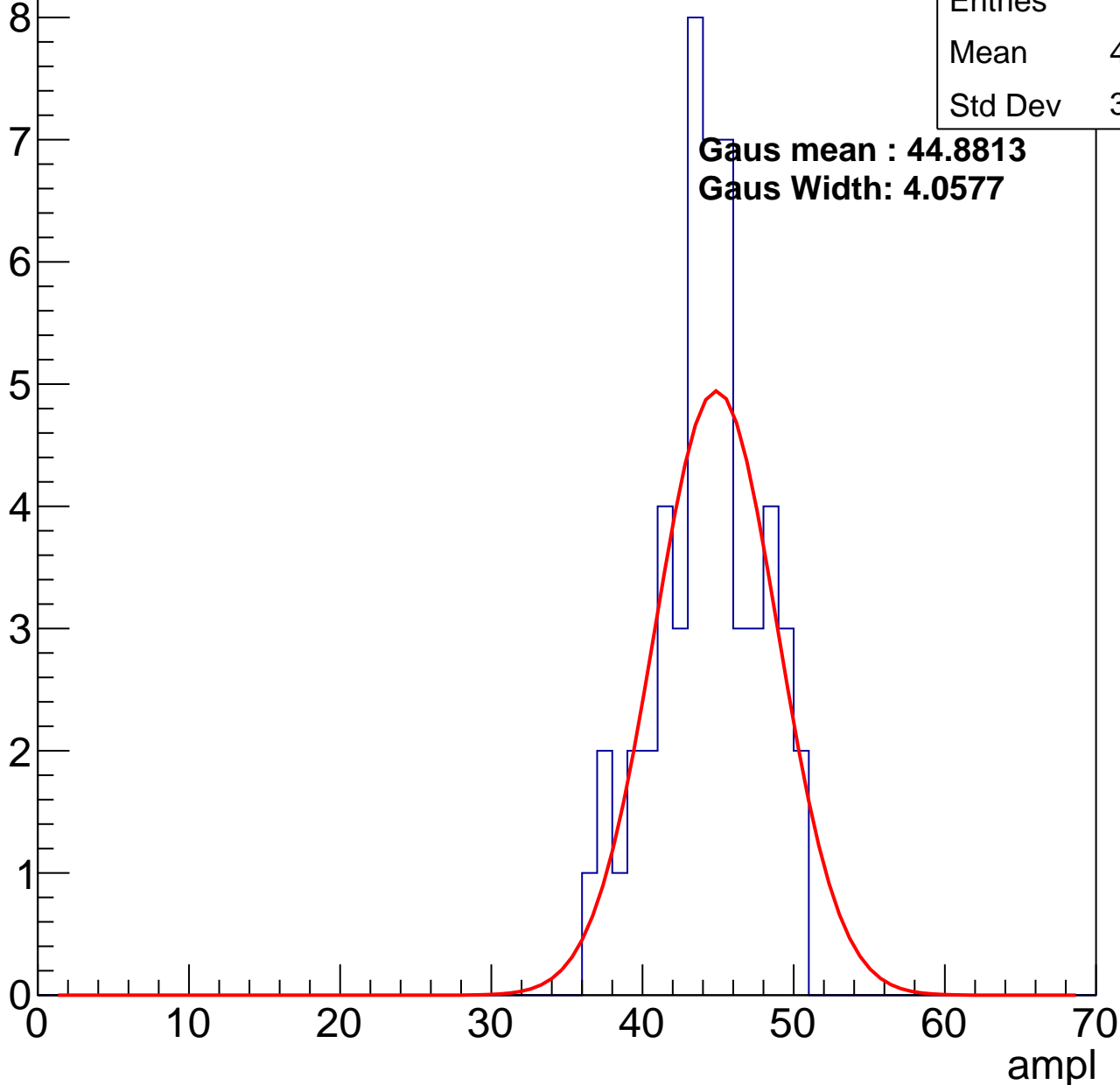
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.87
Std Dev	3.374

**Gaus mean : 44.8813**

**Gaus Width: 4.0577**

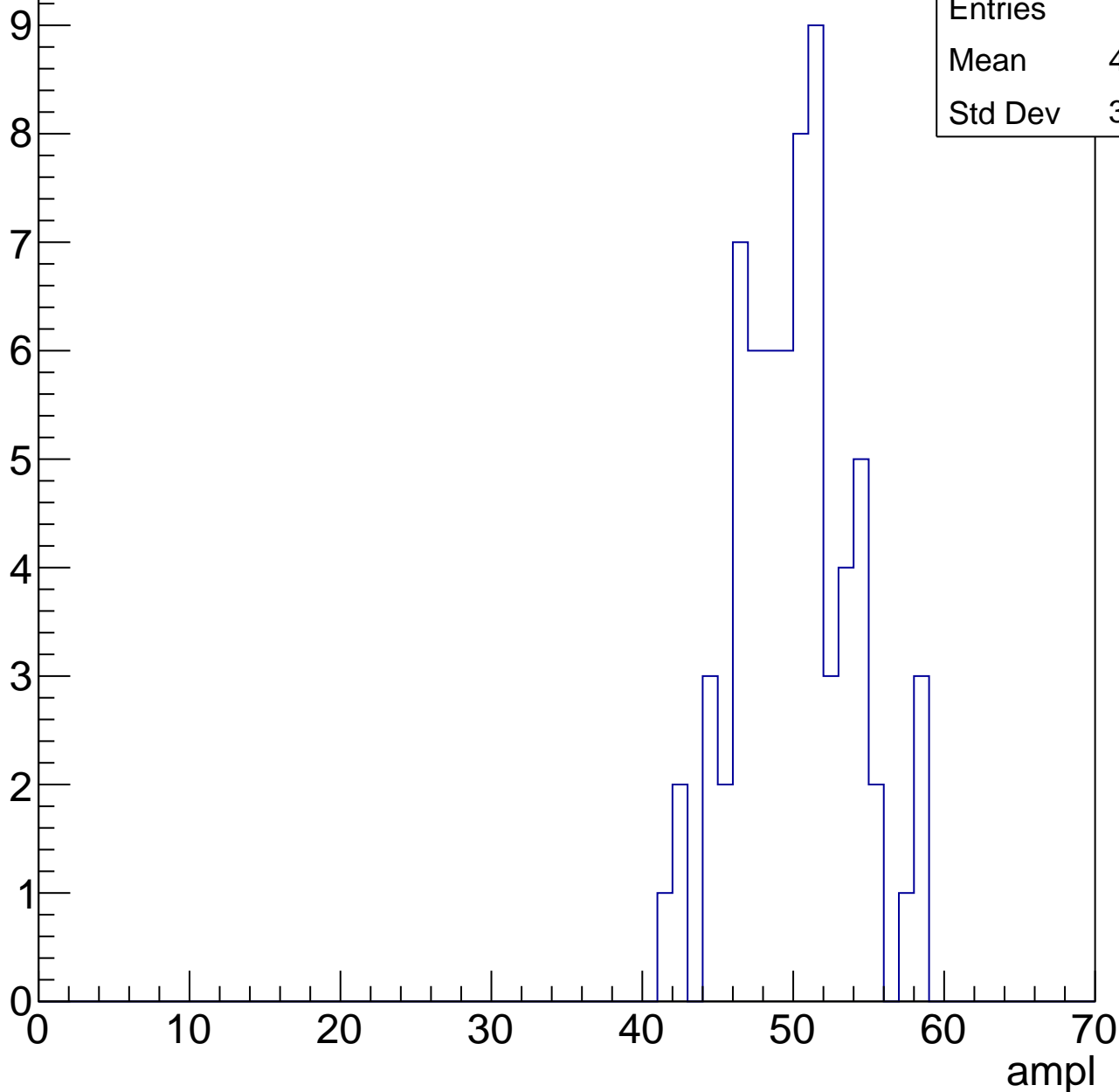


# B1L102S, U24-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

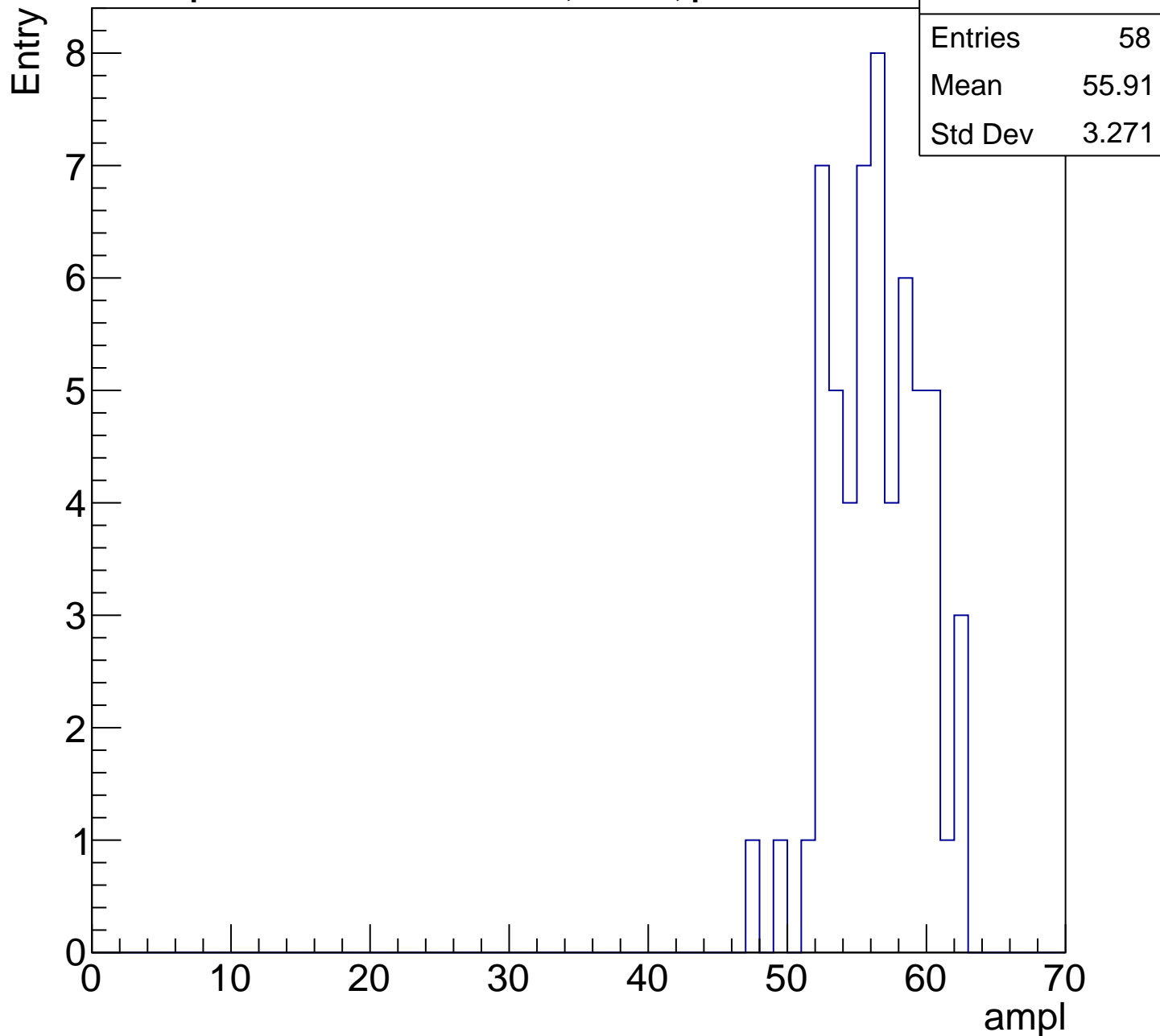
Entry

Entries	68
Mean	49.57
Std Dev	3.794



# B1L102S, U24-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

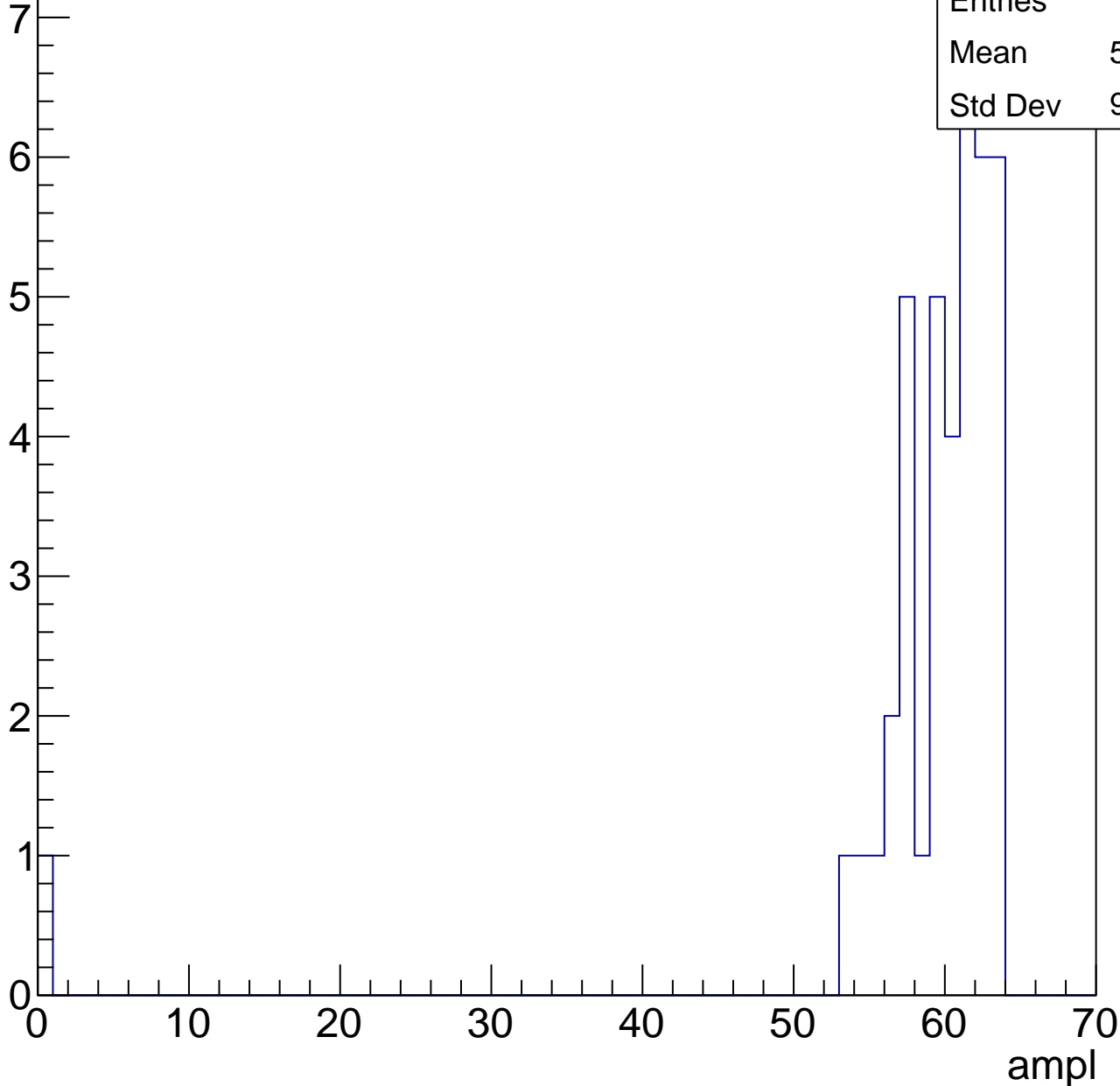


# B1L102S, U24-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.23
Std Dev	9.689



# B1L102S, U24-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch77, adc0

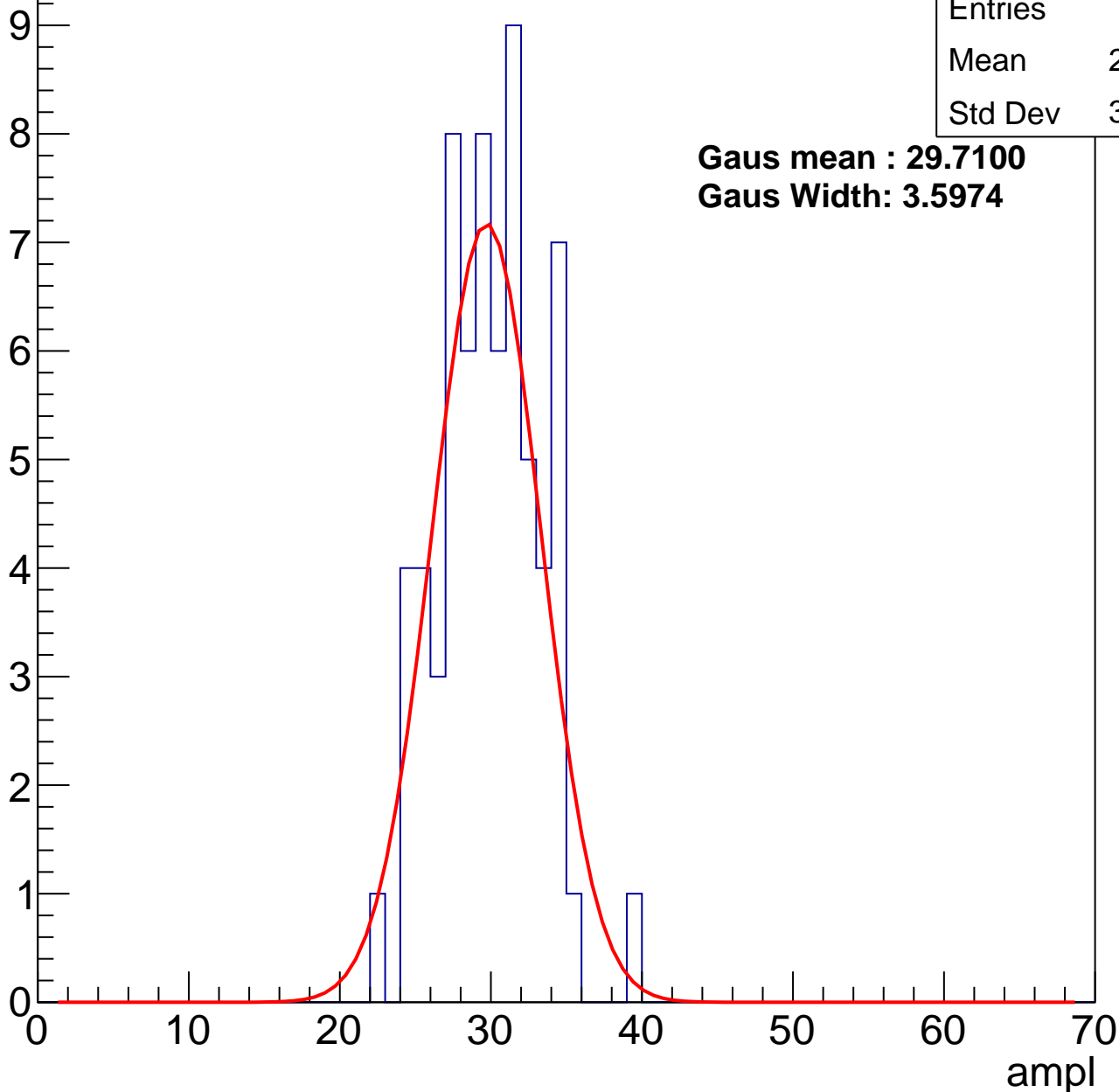
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.48
Std Dev	3.289

**Gaus mean : 29.7100**

**Gaus Width: 3.5974**



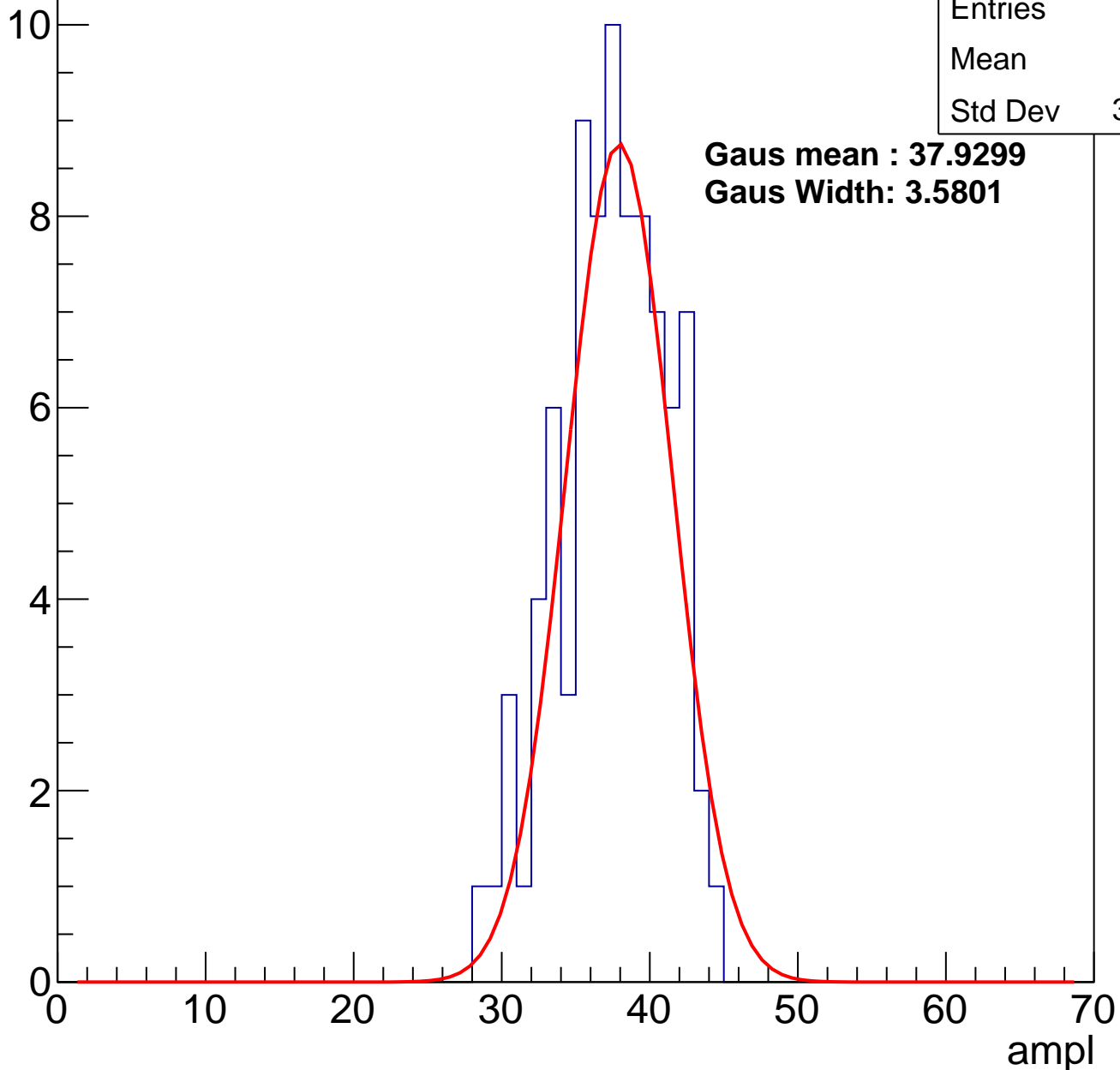
# B1L102S, U24-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	37
Std Dev	3.581

**Gaus mean : 37.9299**  
**Gaus Width: 3.5801**

Entry



# B1L102S, U24-ch77, adc2

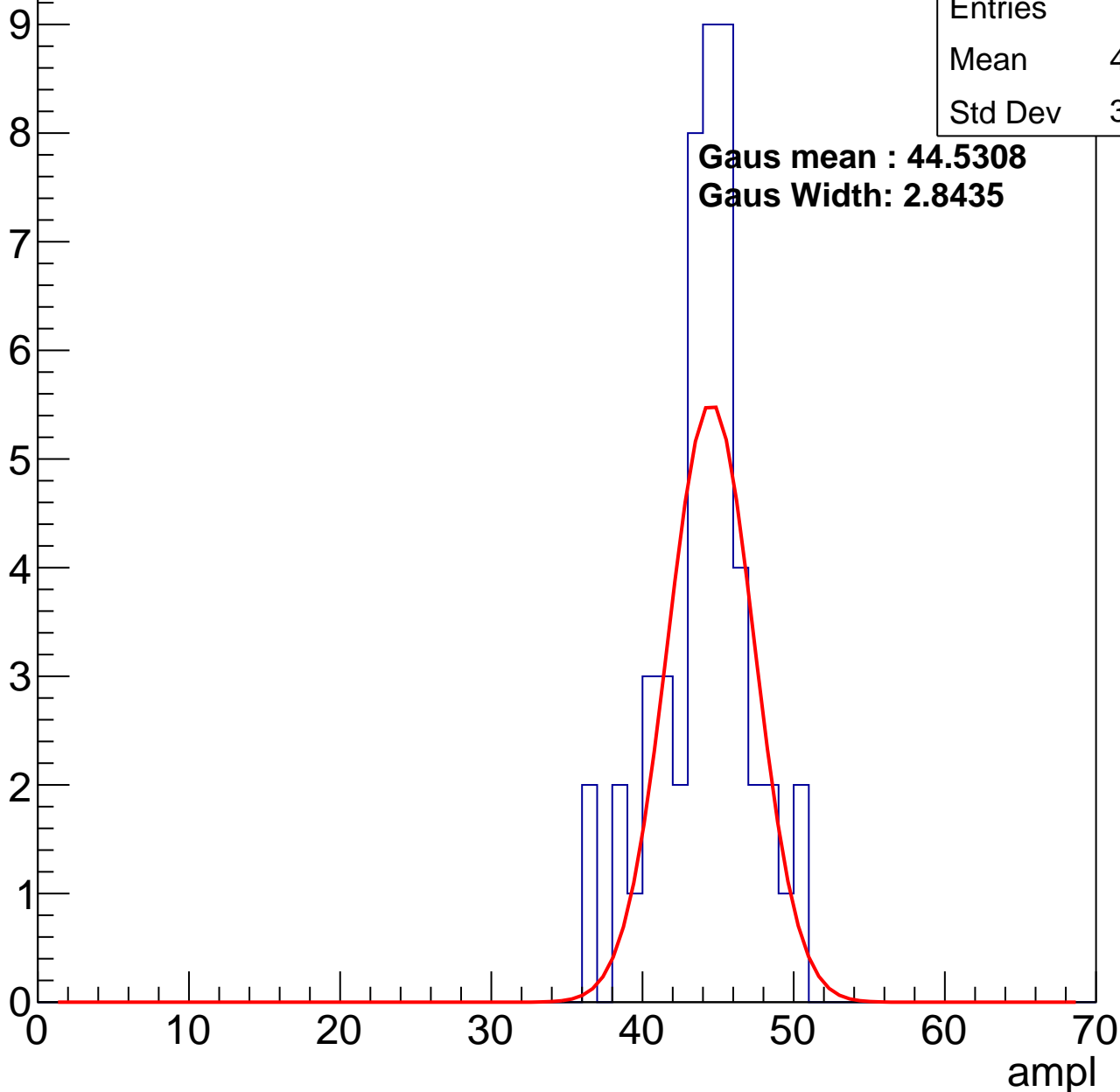
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.64
Std Dev	3.103

**Gaus mean : 44.5308**

**Gaus Width: 2.8435**



# B1L102S, U24-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	50.11
Std Dev	3.415

Entry

10

8

6

4

2

0

0

10

20

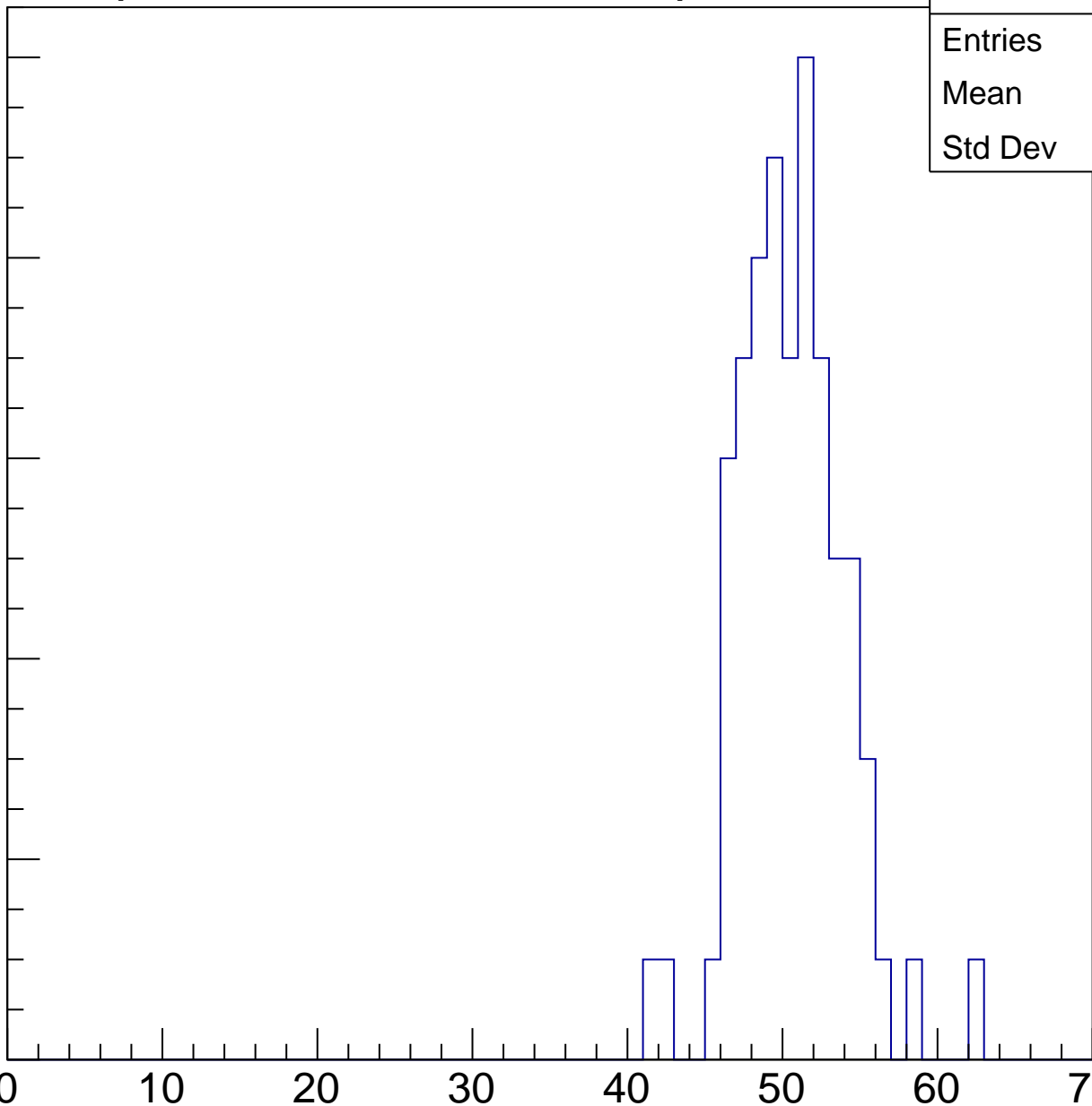
30

40

50

60

ampl

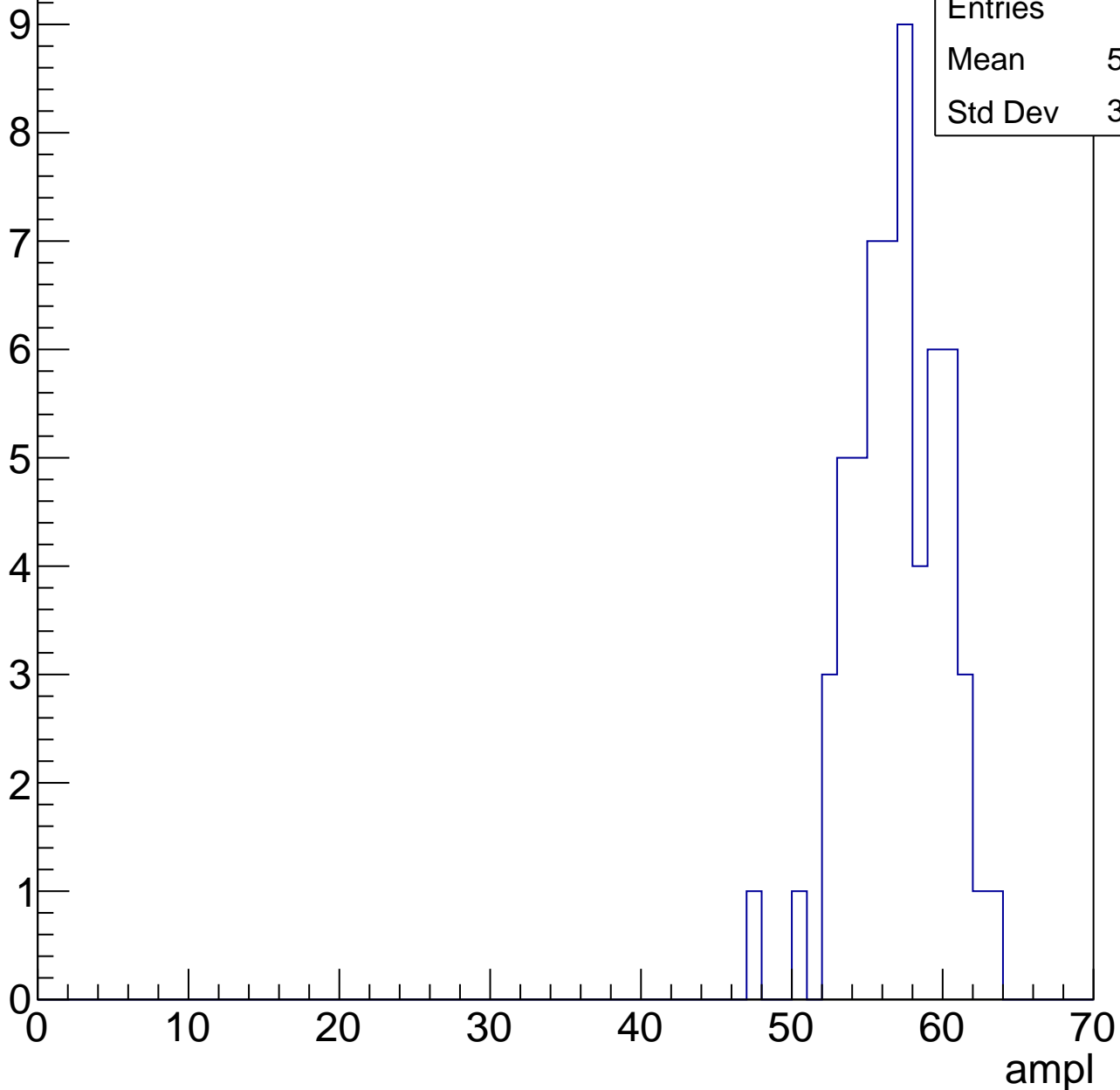


# B1L102S, U24-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.47
Std Dev	3.077

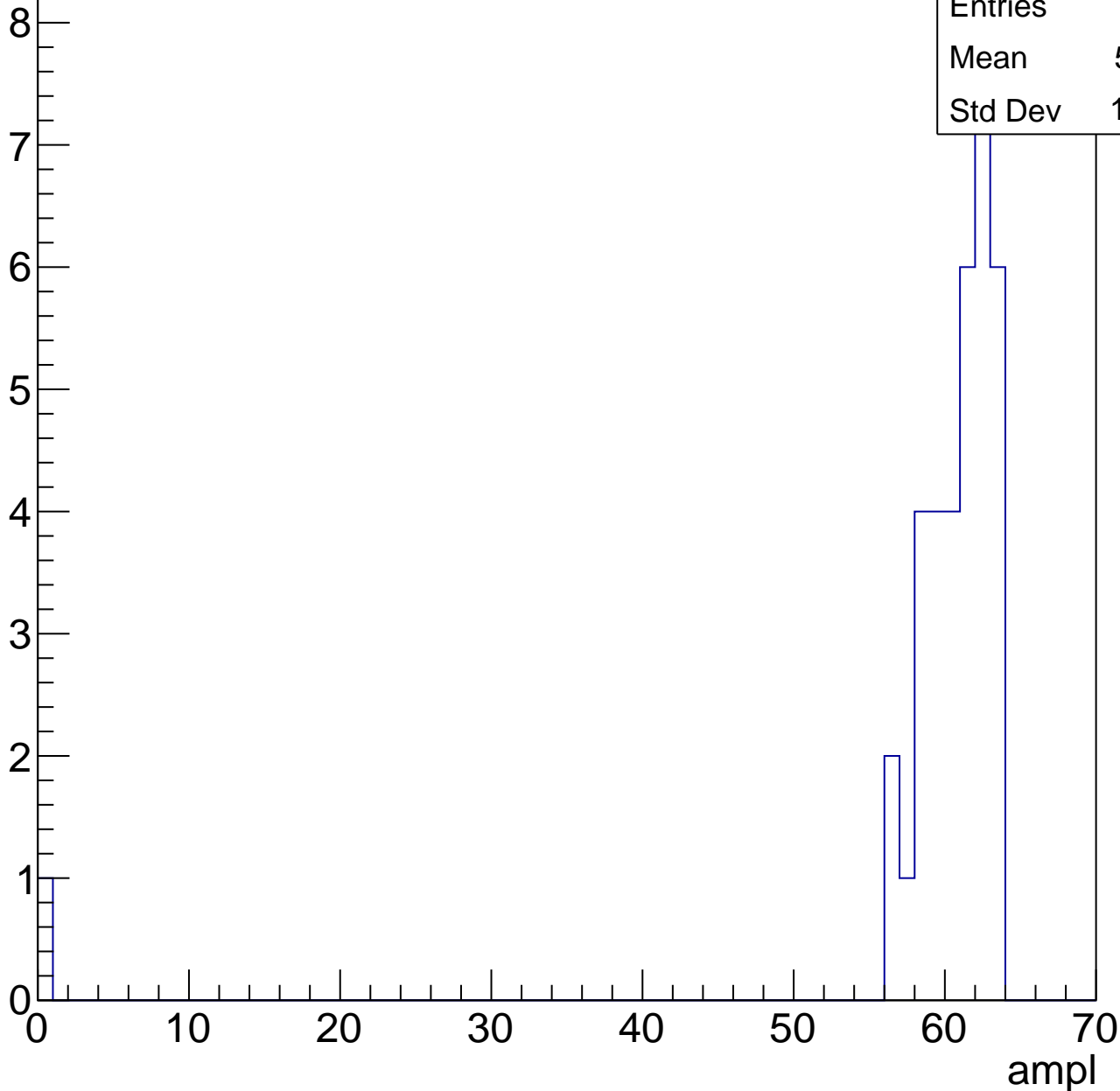


# B1L102S, U24-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

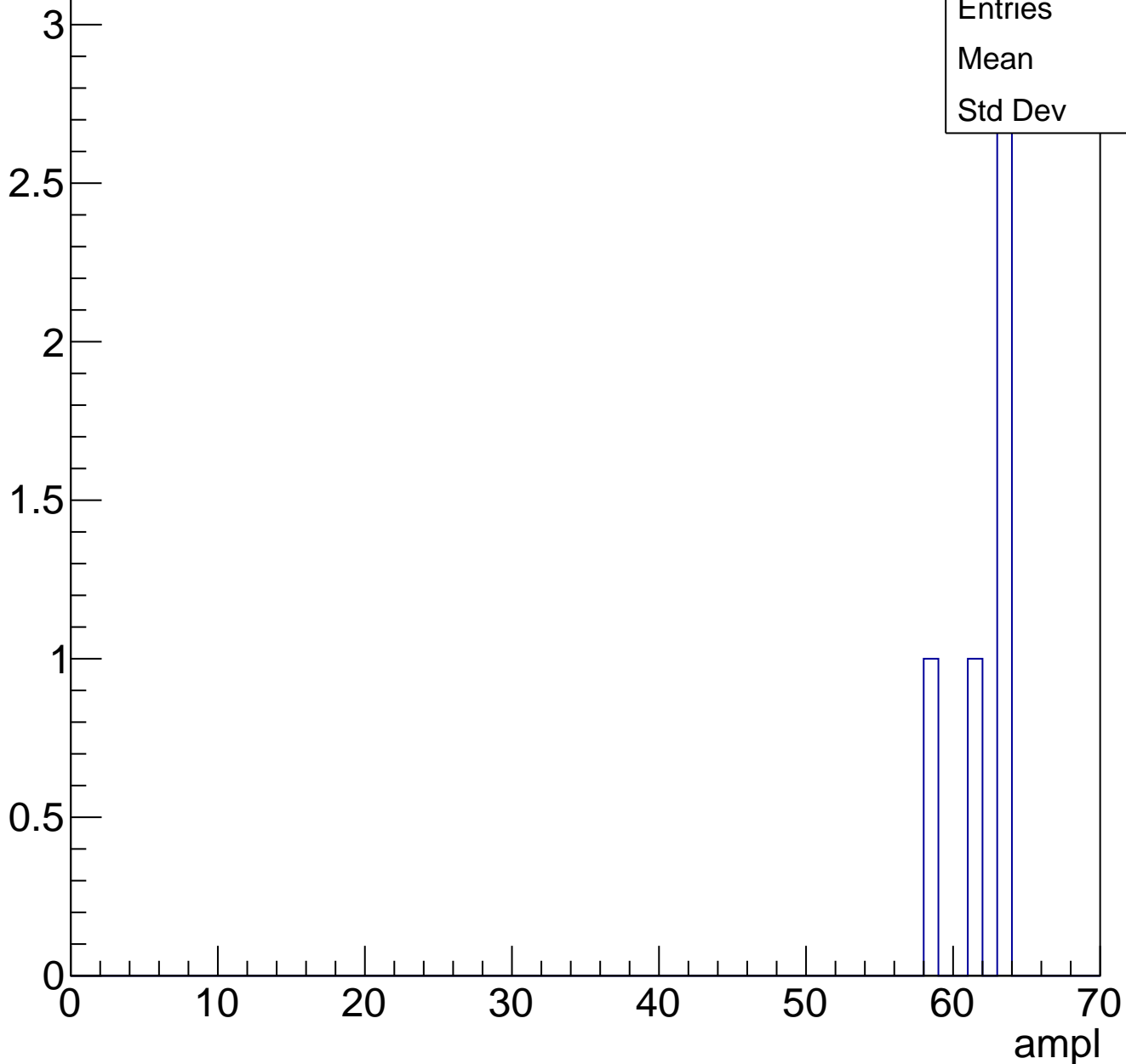
Entries	36
Mean	58.81
Std Dev	10.14



# B1L102S, U24-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch78, adc0

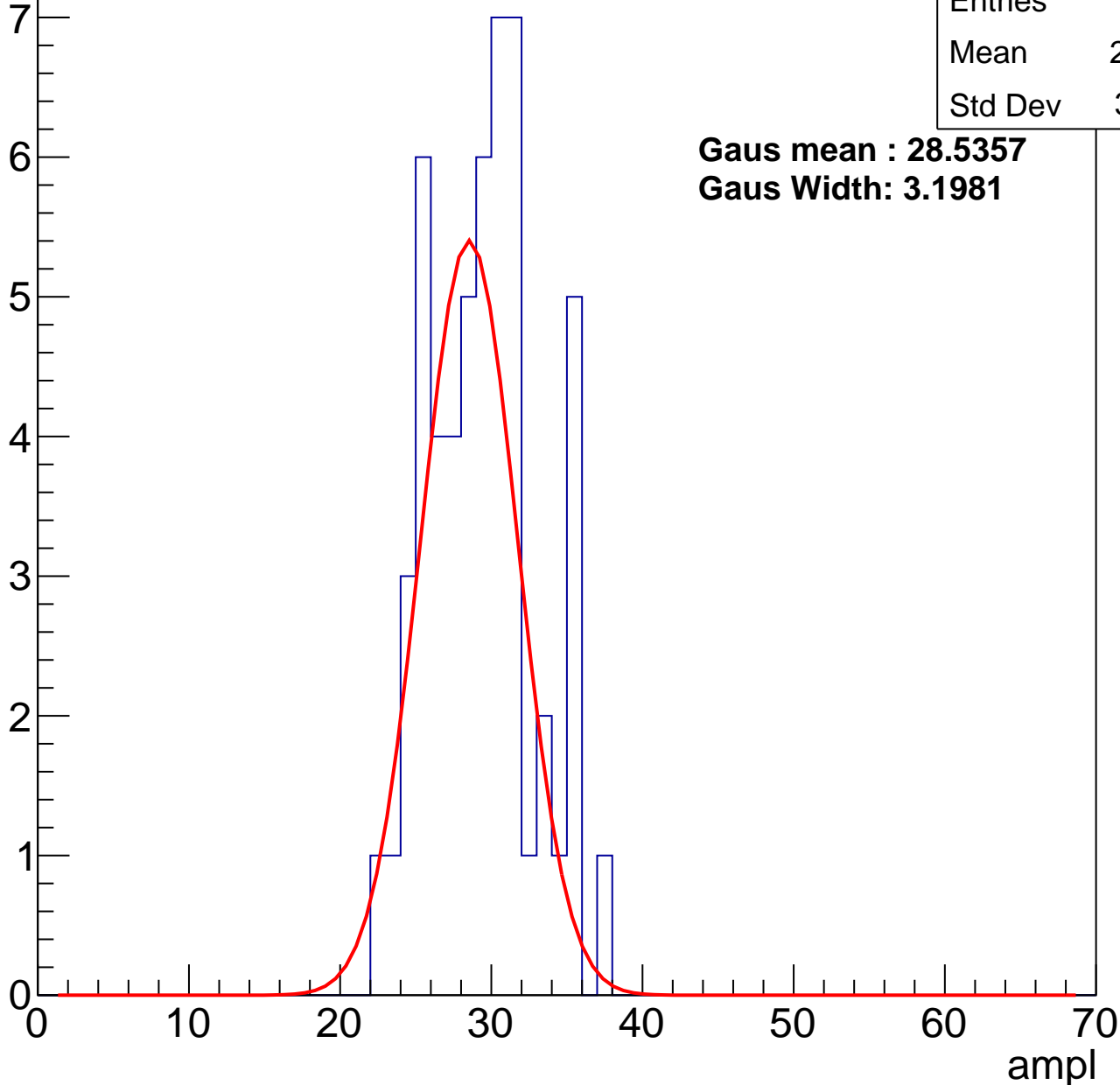
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	28.96
Std Dev	3.491

**Gaus mean : 28.5357**

**Gaus Width: 3.1981**



# B1L102S, U24-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

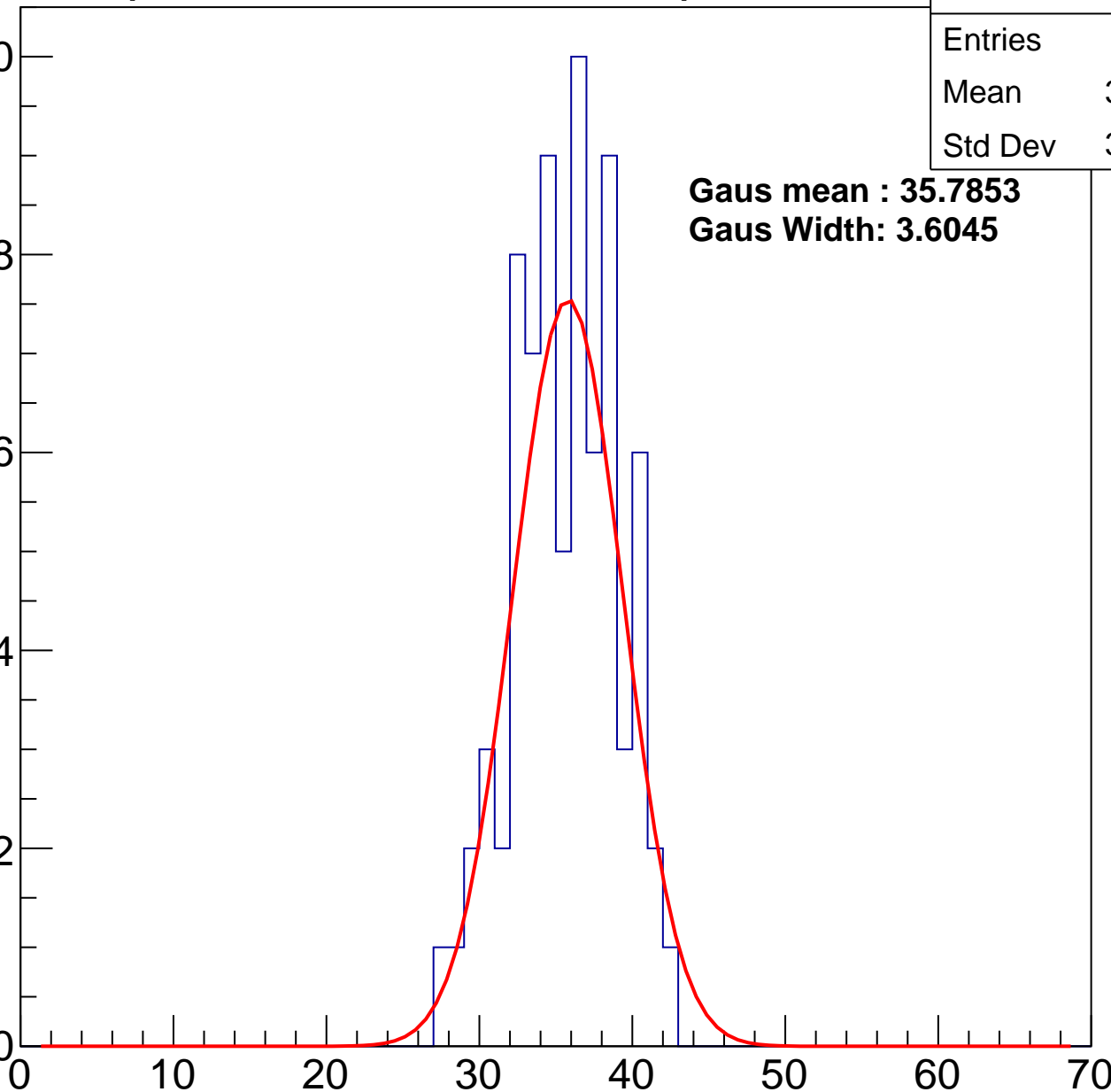
Entries	75
Mean	35.17
Std Dev	3.332

**Gaus mean : 35.7853**

**Gaus Width: 3.6045**

10  
8  
6  
4  
2  
0

ampl



# B1L102S, U24-ch78, adc2

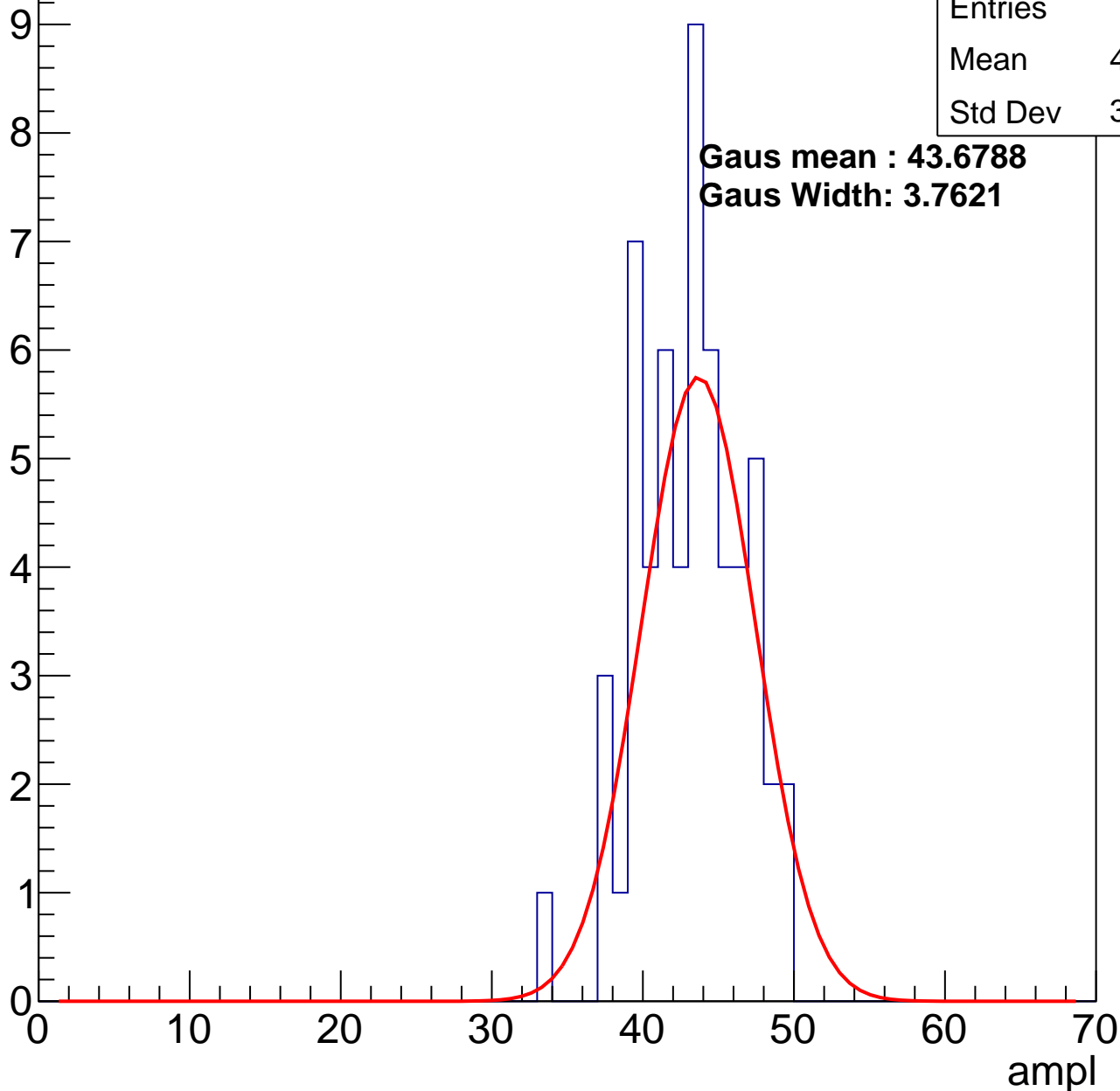
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.64
Std Dev	3.377

**Gaus mean : 43.6788**

**Gaus Width: 3.7621**

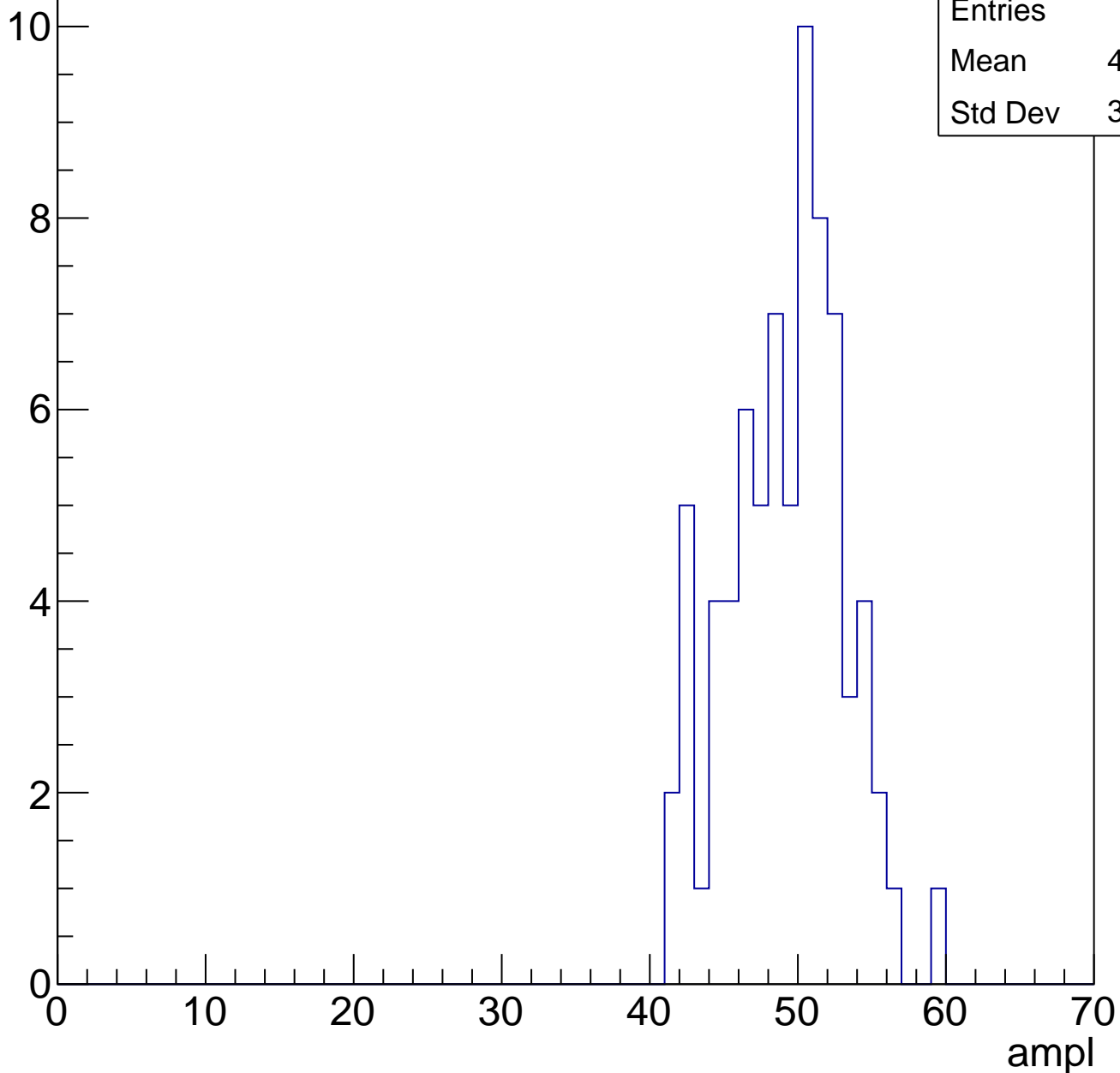


# B1L102S, U24-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.73
Std Dev	3.869

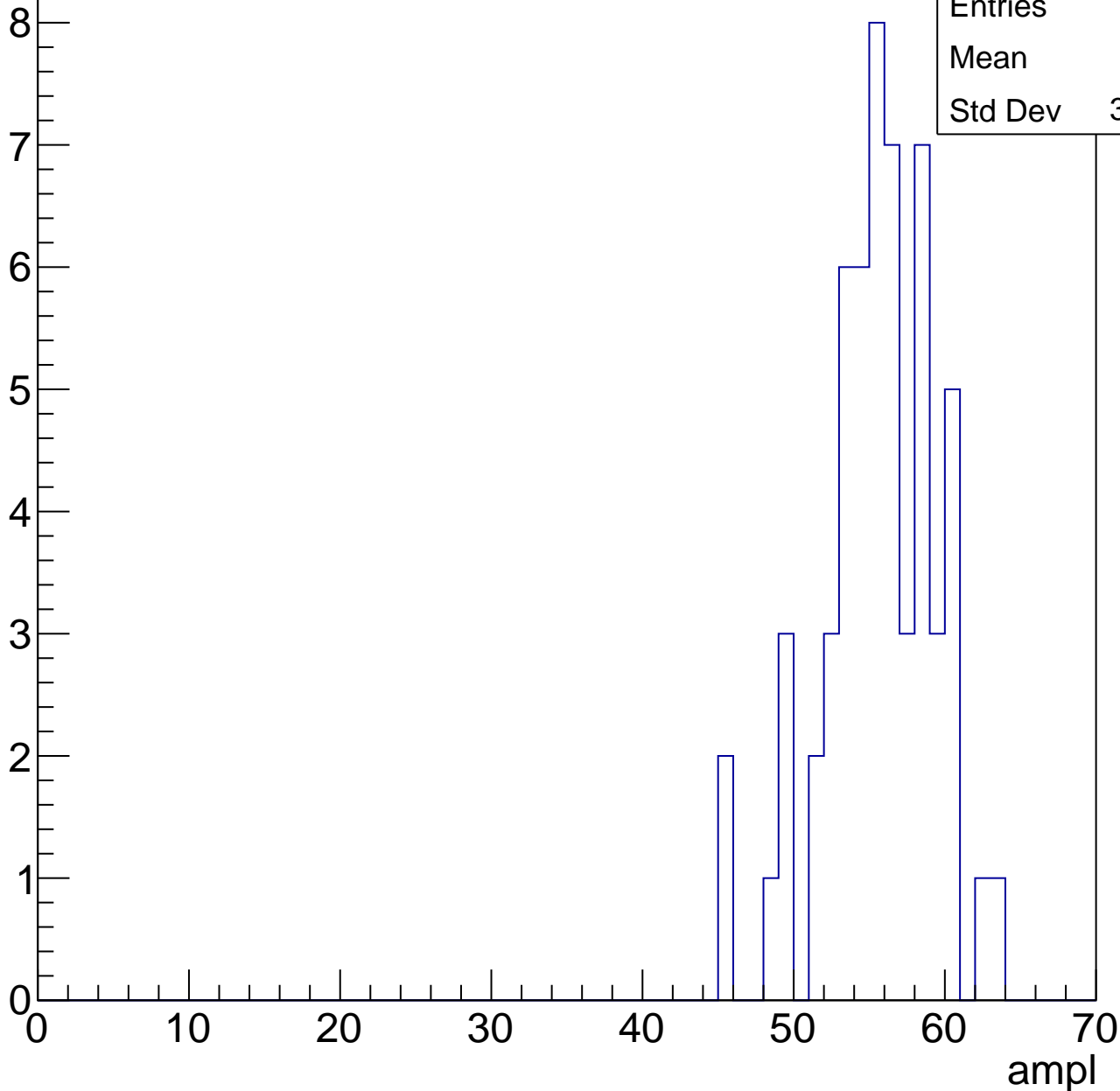


# B1L102S, U24-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.1
Std Dev	3.745

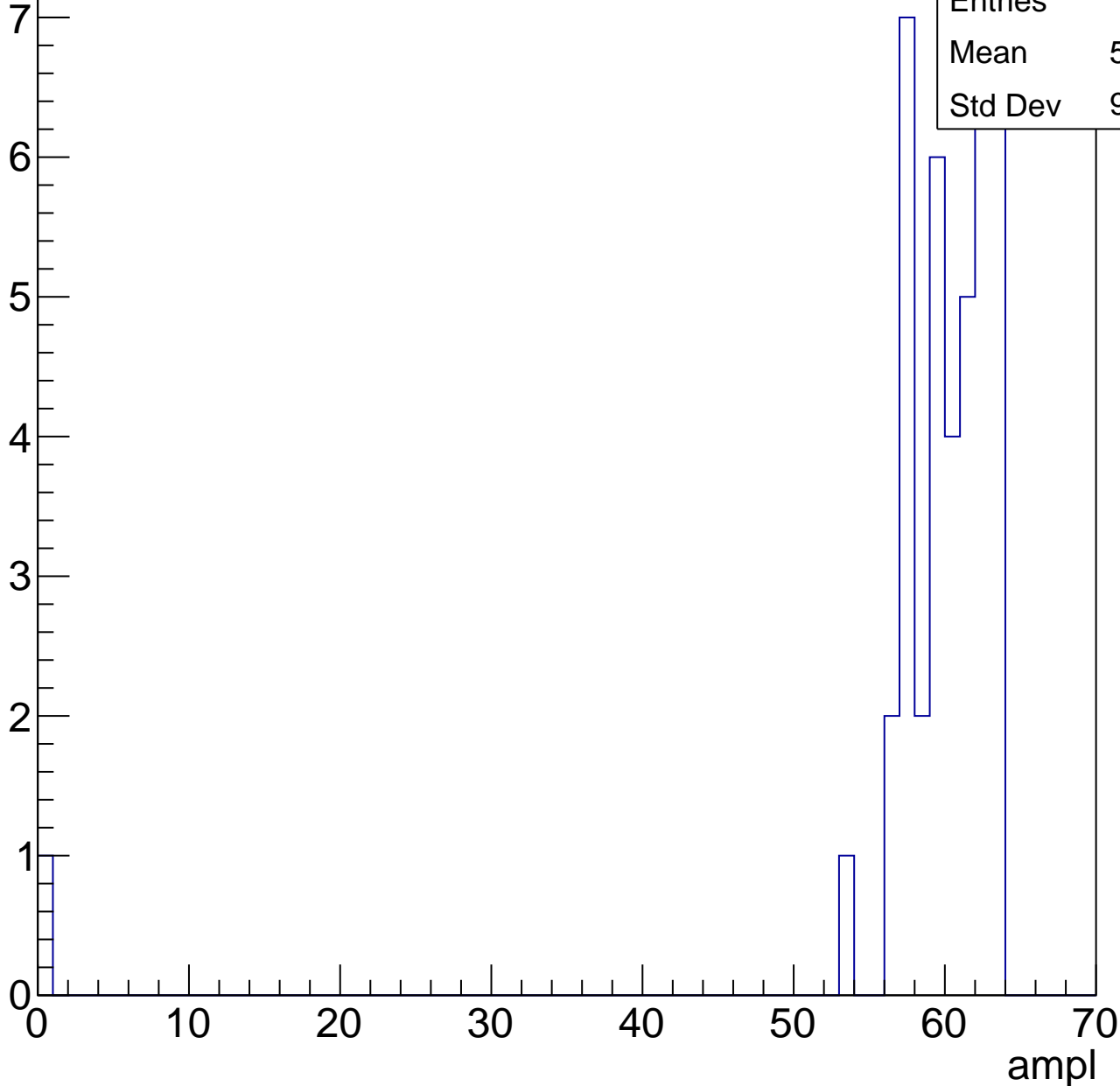


# B1L102S, U24-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.43
Std Dev	9.449

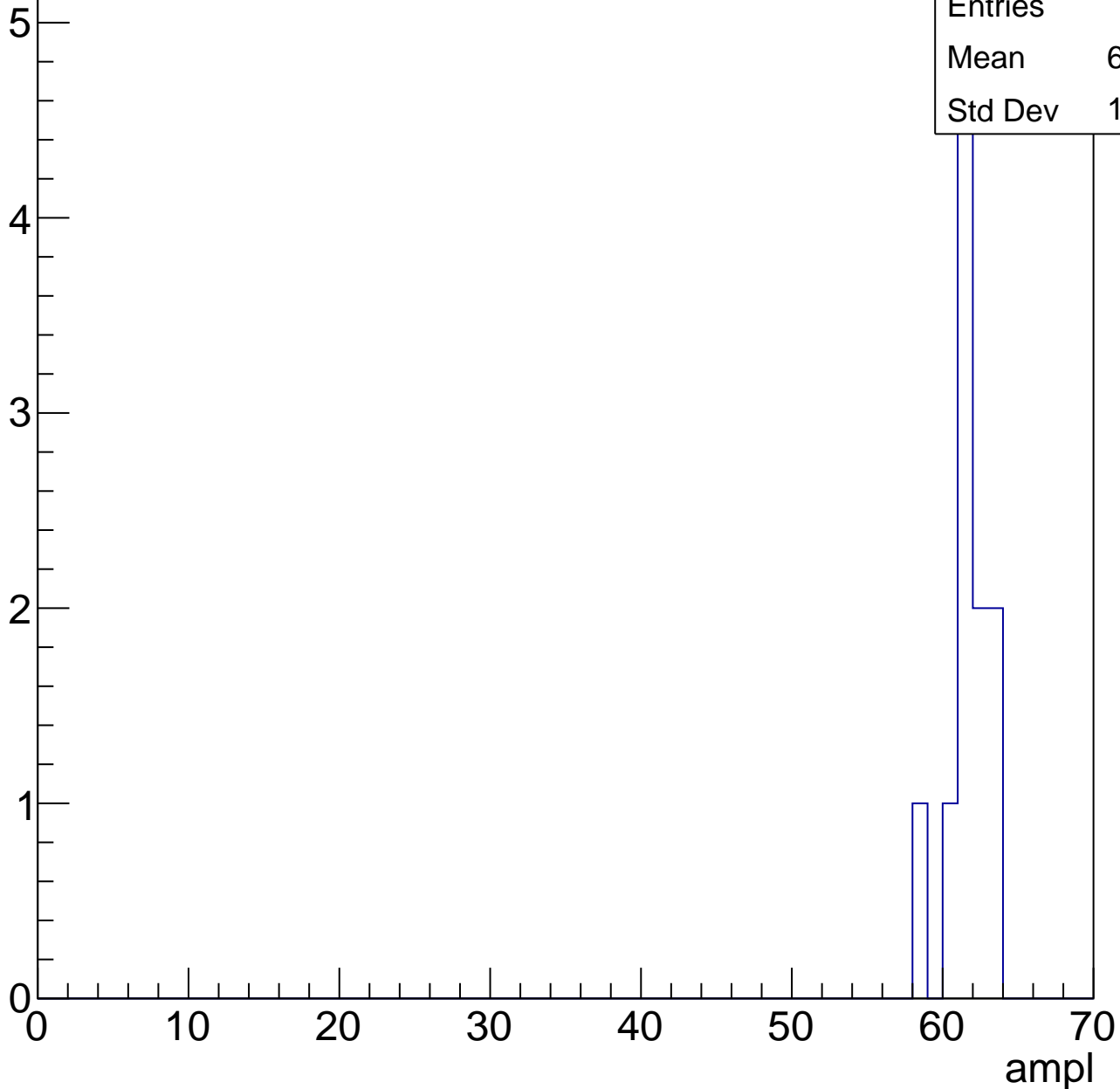


# B1L102S, U24-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.18
Std Dev	1.336





# B1L102S, U24-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L102S, U24-ch79, adc0

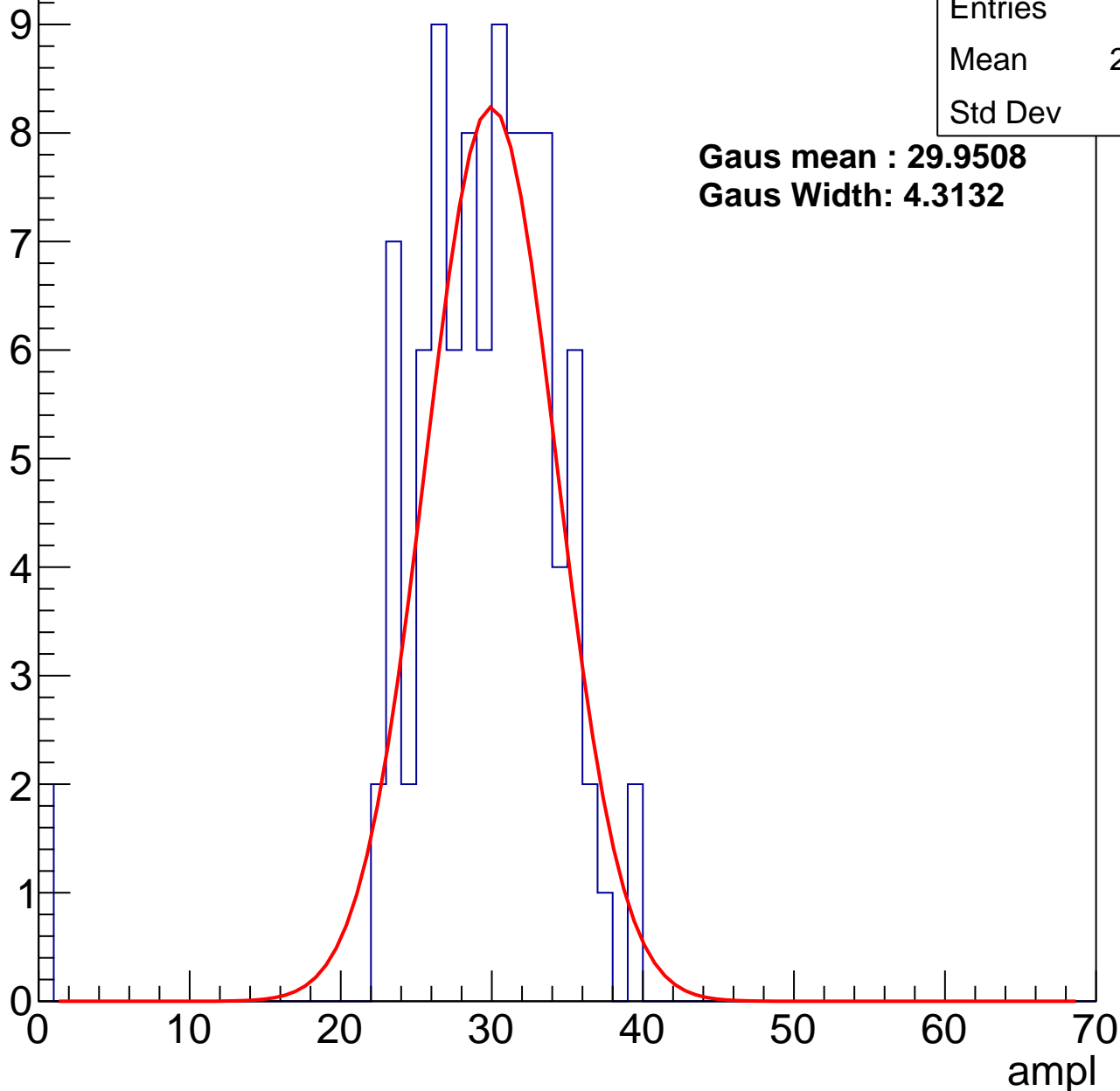
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	96
Mean	28.83
Std Dev	5.78

**Gaus mean : 29.9508**

**Gaus Width: 4.3132**



# B1L102S, U24-ch79, adc1

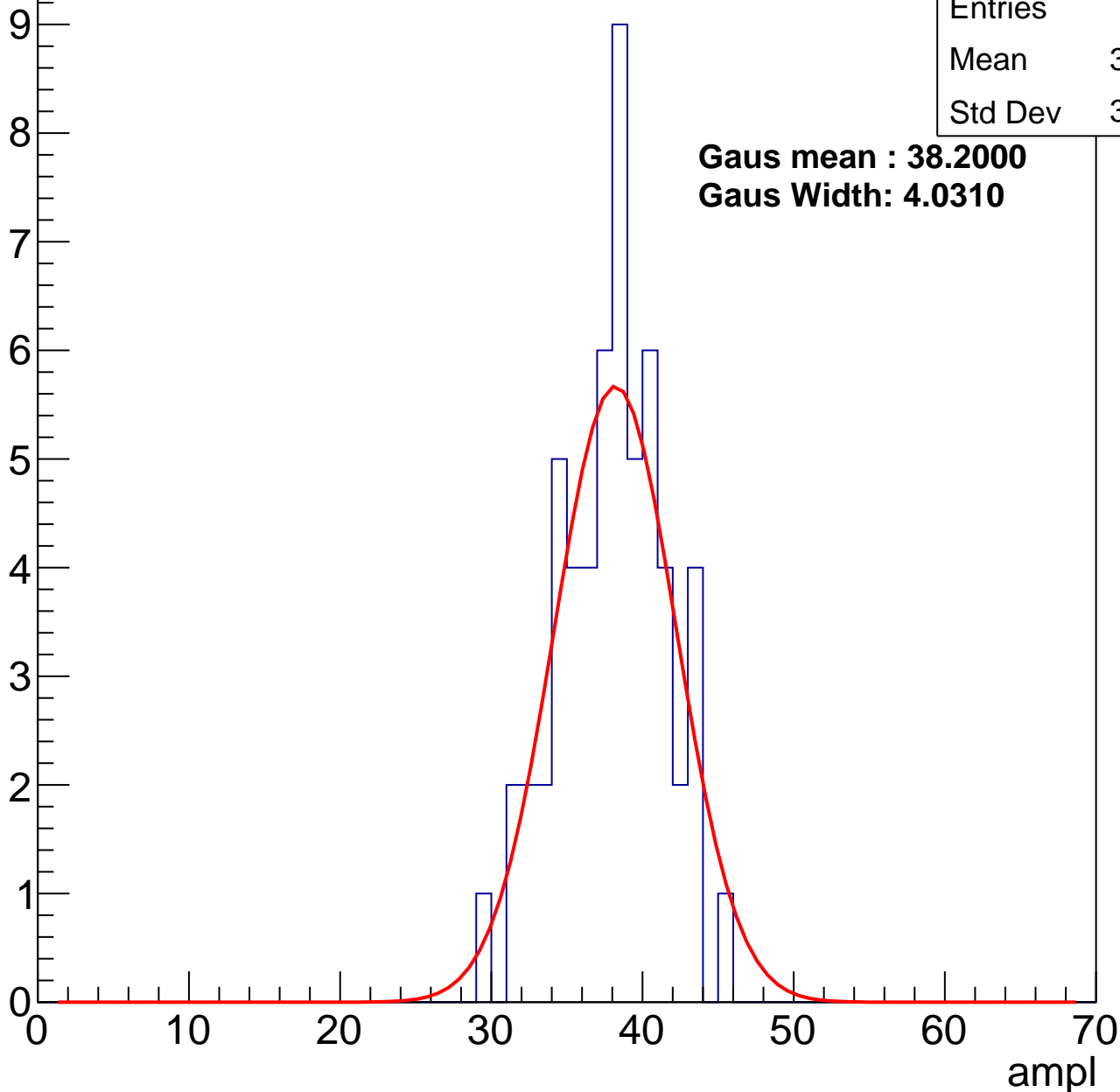
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	37.53
Std Dev	3.434

**Gaus mean : 38.2000**

**Gaus Width: 4.0310**



# B1L102S, U24-ch79, adc2

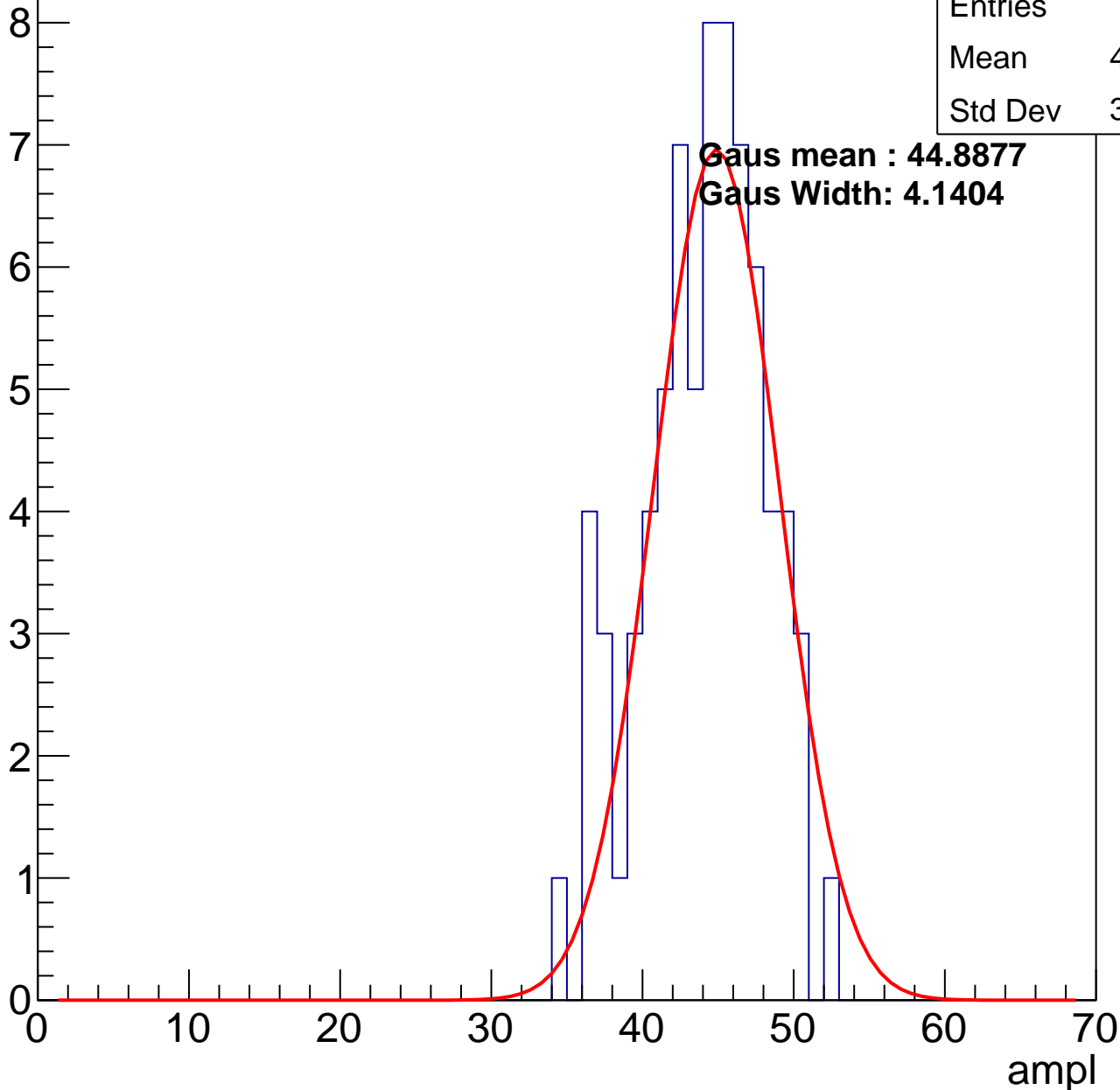
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.57
Std Dev	3.973

Gaus mean : 44.8877

Gaus Width: 4.1404

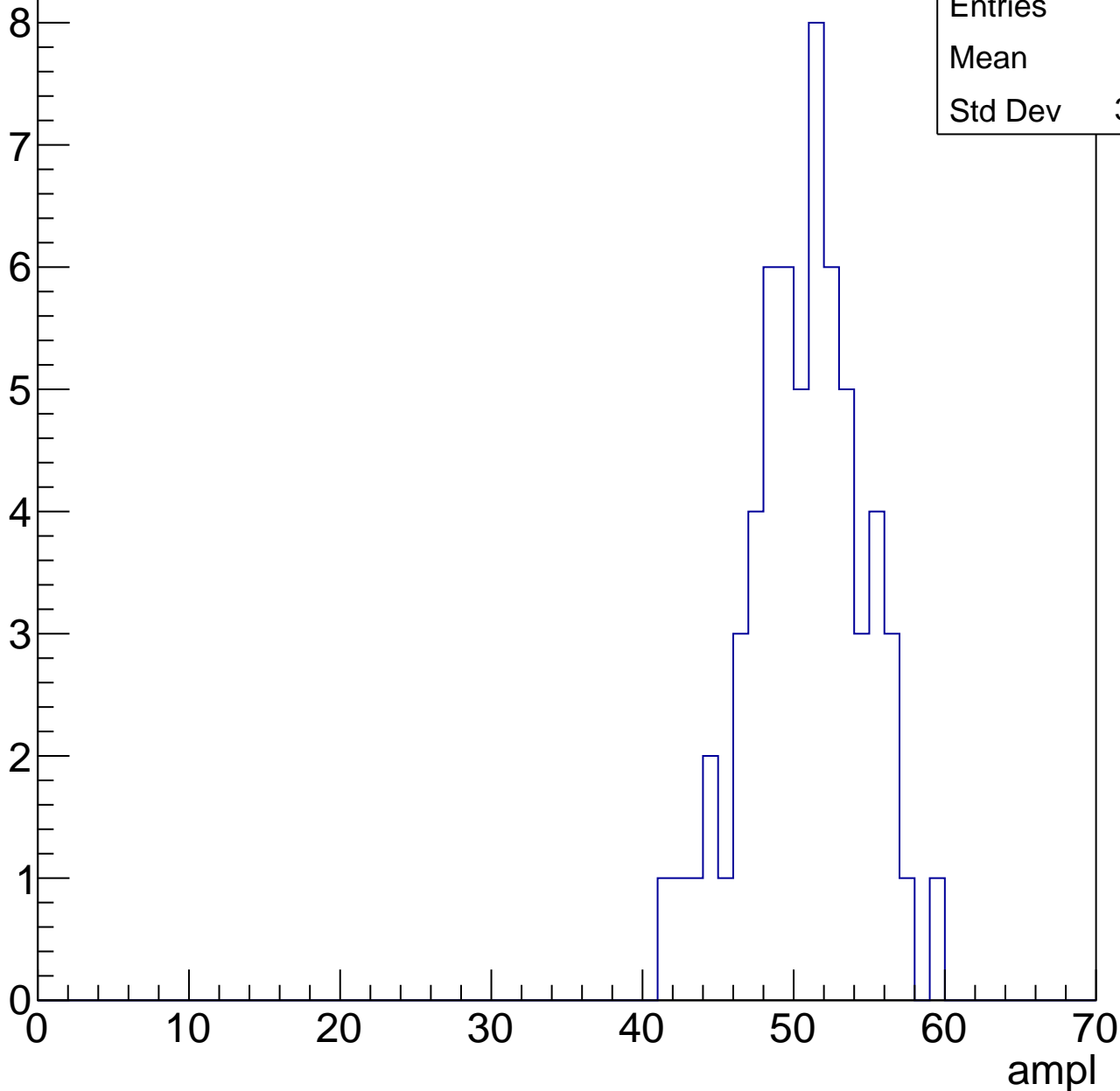


# B1L102S, U24-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.3
Std Dev	3.761

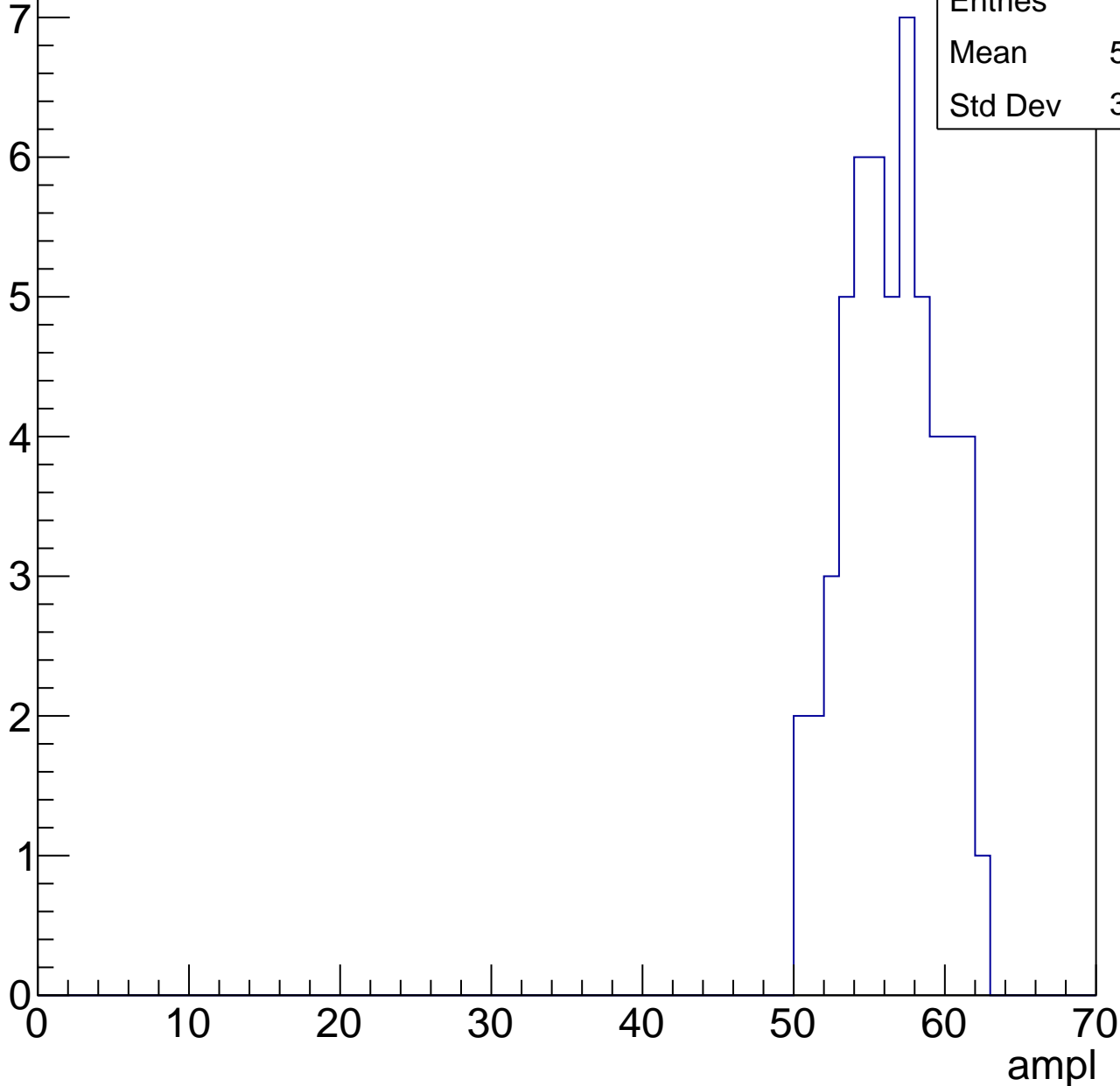


# B1L102S, U24-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.07
Std Dev	3.066

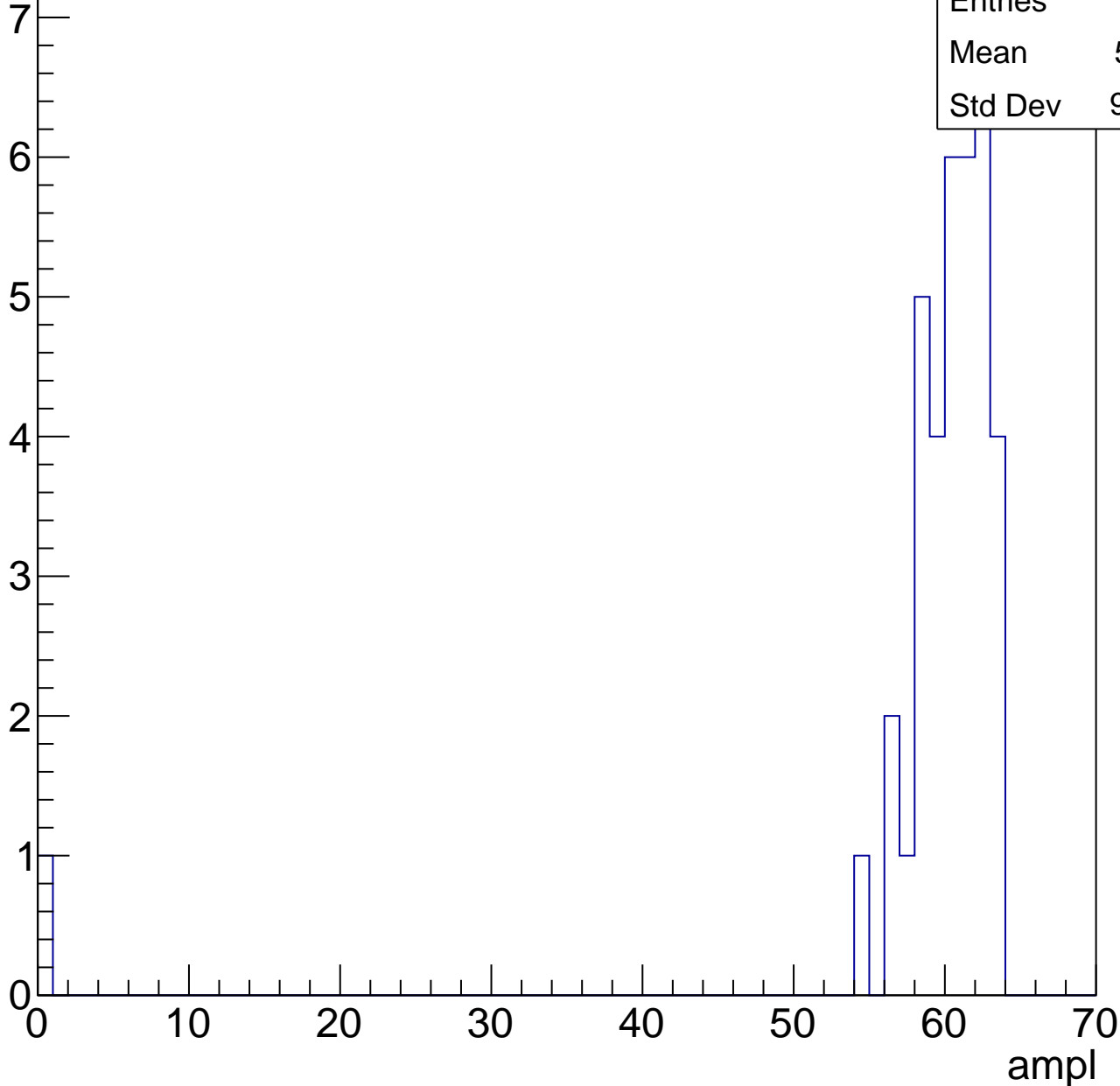


# B1L102S, U24-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.41
Std Dev	9.969

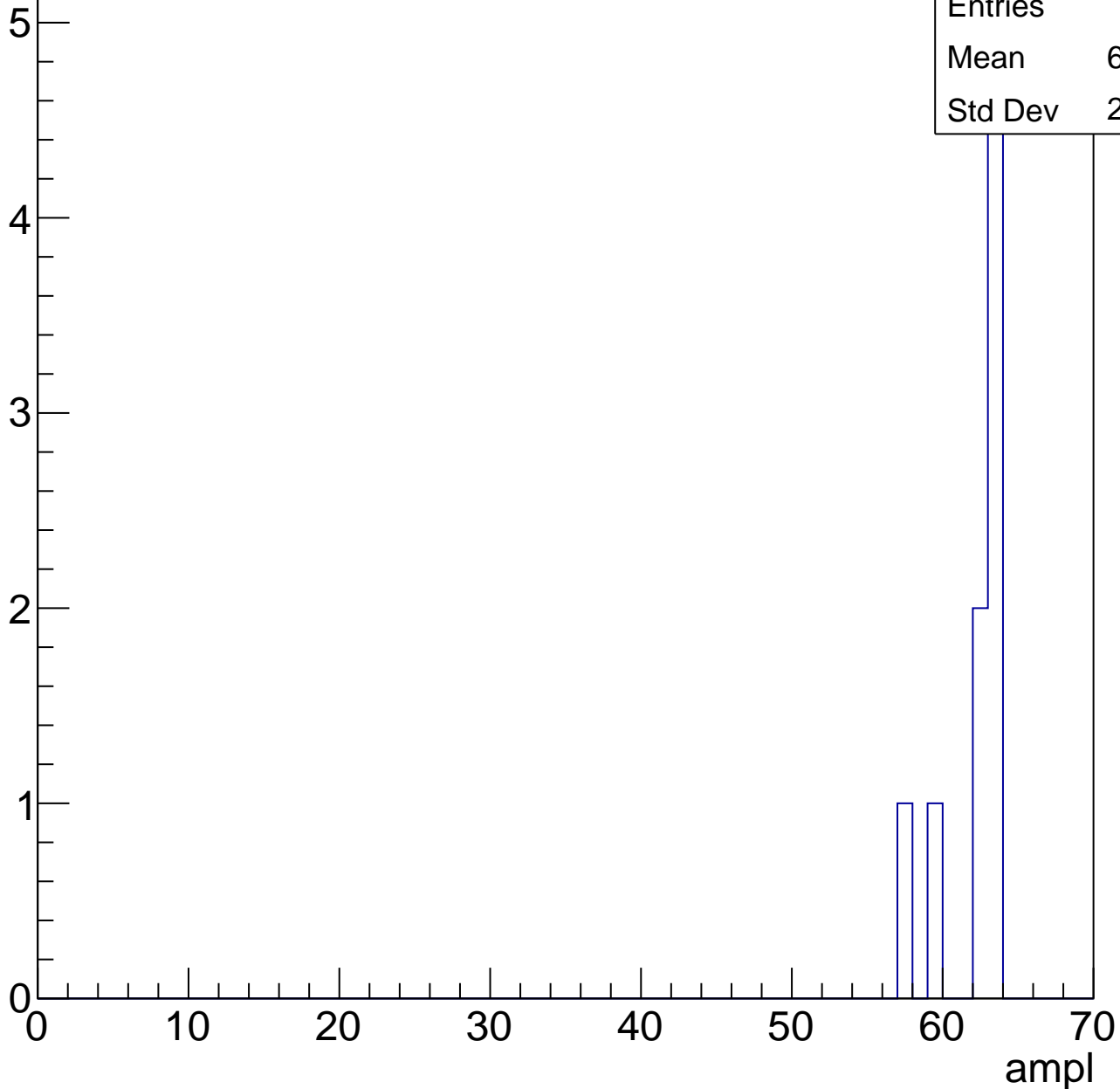


# B1L102S, U24-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.67
Std Dev	2.055





# B1L102S, U24-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

# B1L102S, U24-ch80, adc0

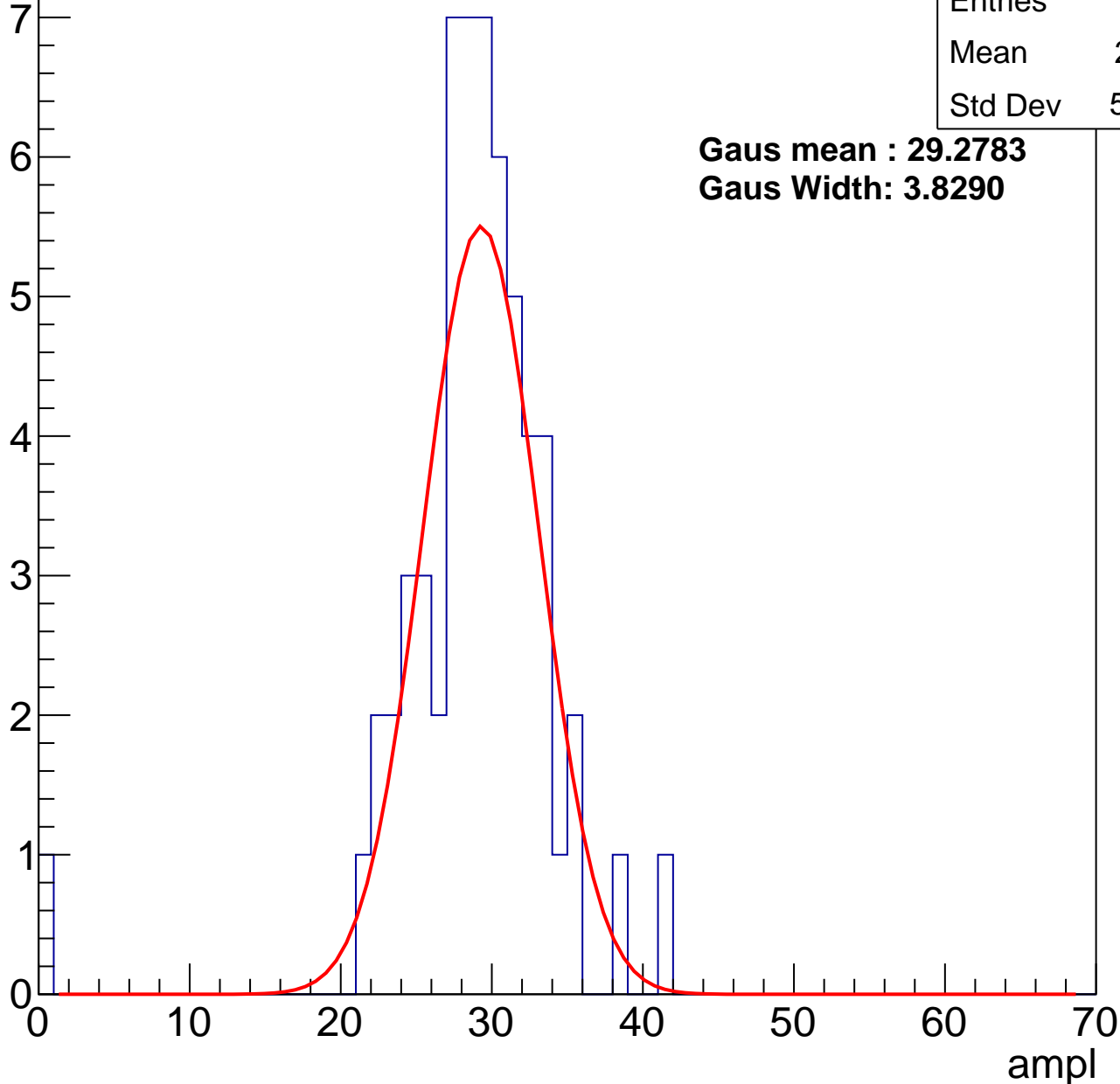
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.41
Std Dev	5.333

**Gaus mean : 29.2783**

**Gaus Width: 3.8290**



# B1L102S, U24-ch80, adc1

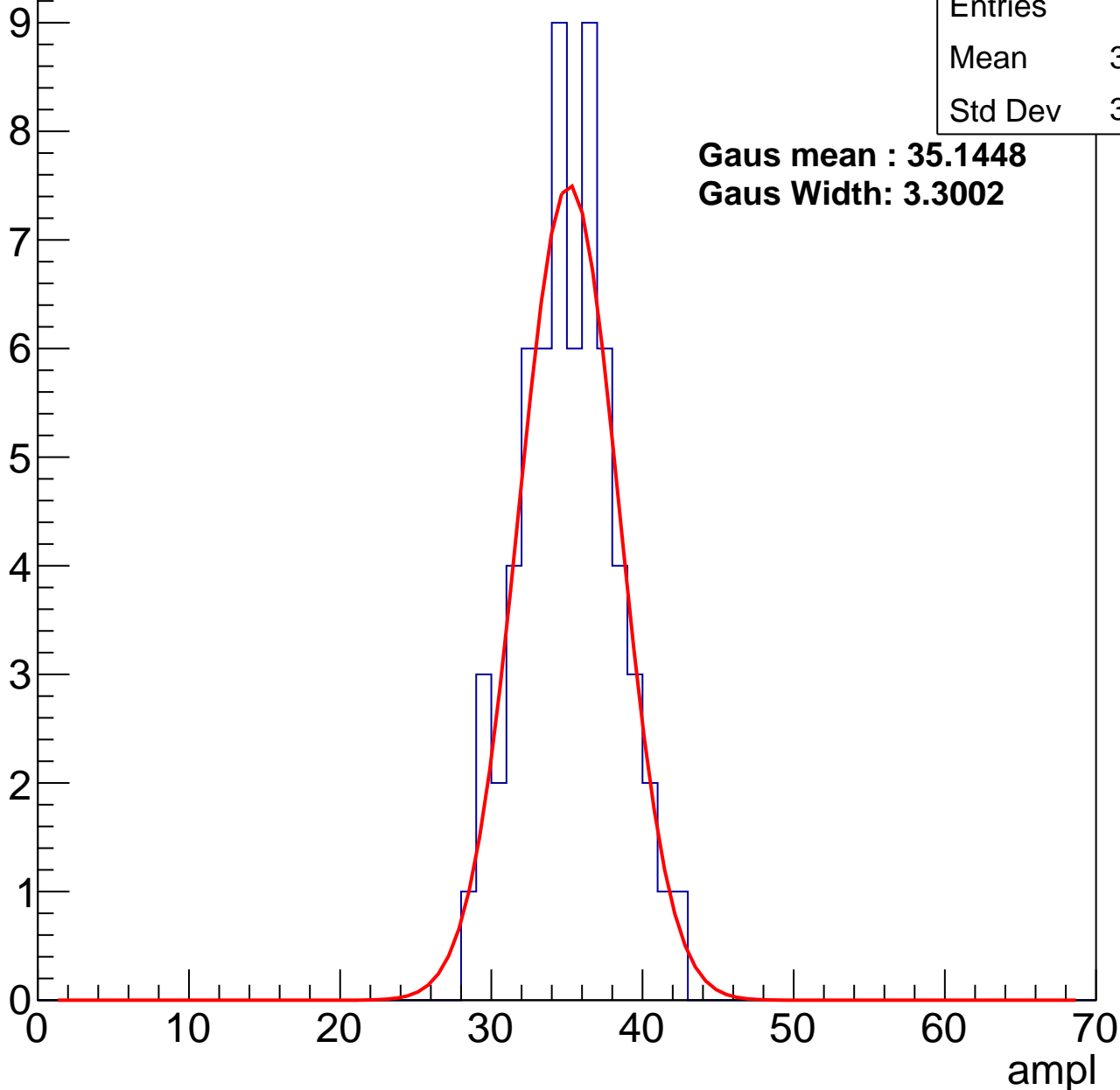
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.65
Std Dev	3.092

**Gaus mean : 35.1448**

**Gaus Width: 3.3002**



# B1L102S, U24-ch80, adc2

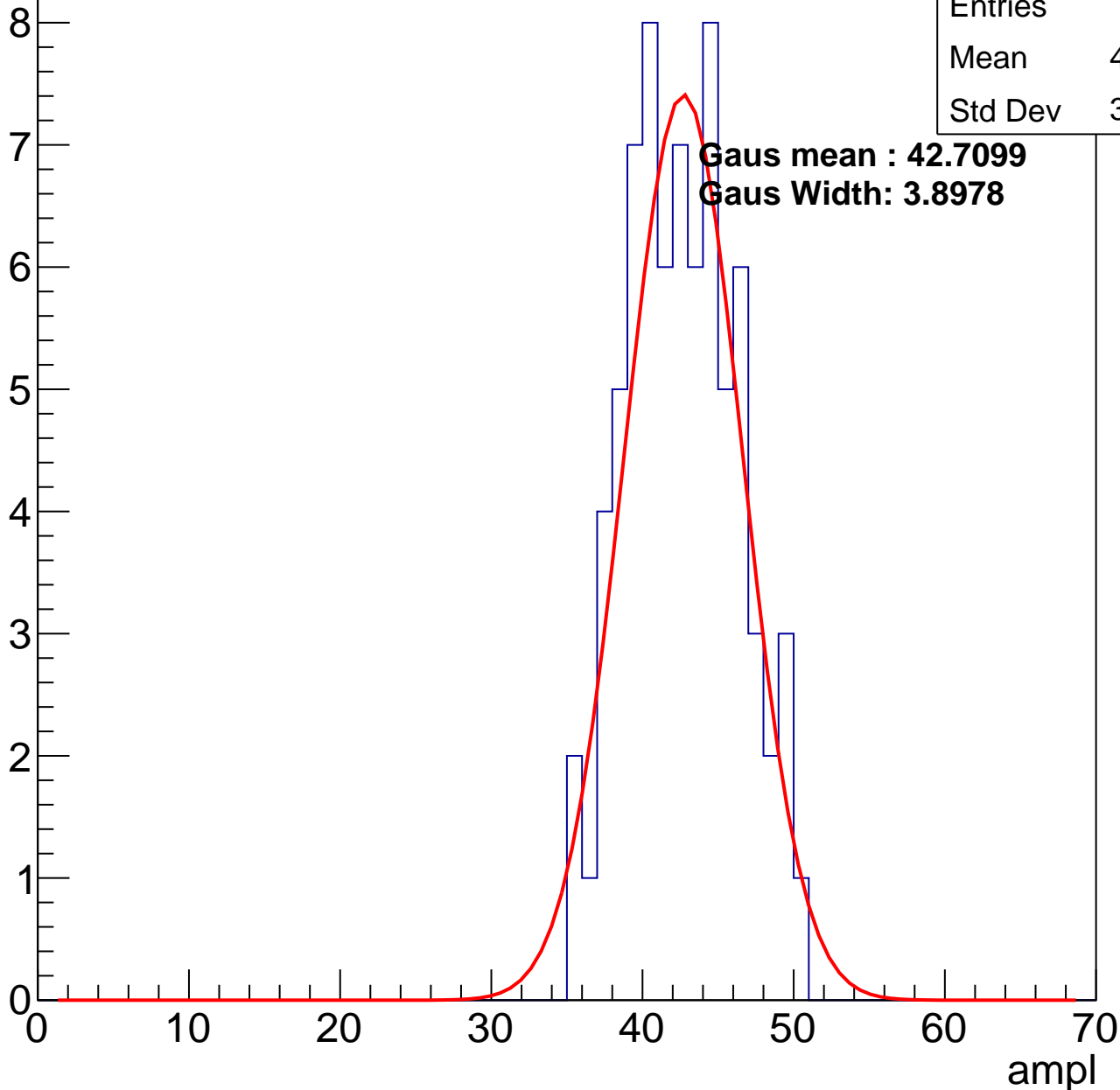
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.19
Std Dev	3.582

**Gaus mean : 42.7099**

**Gaus Width: 3.8978**

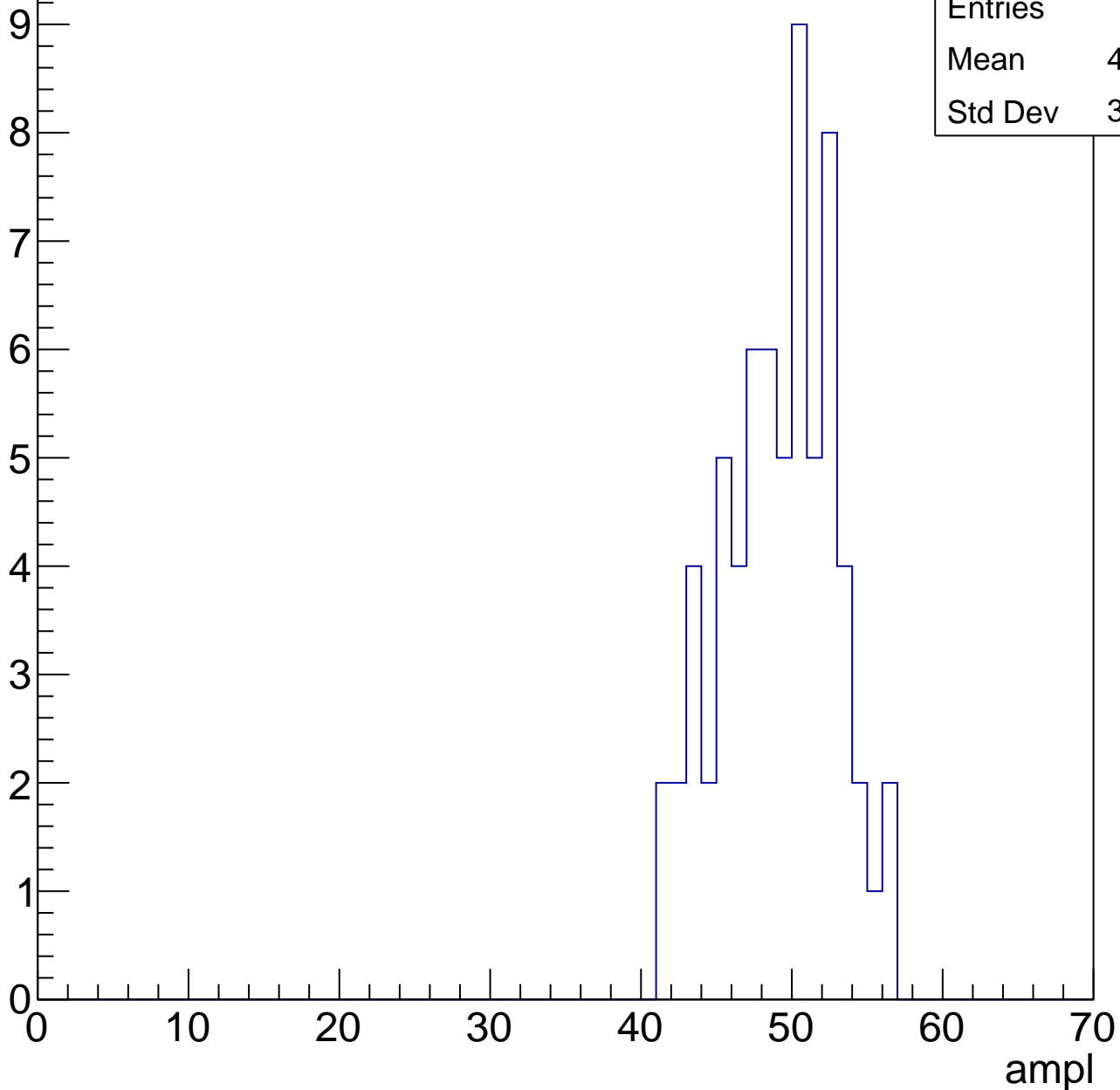


# B1L102S, U24-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.63
Std Dev	3.677

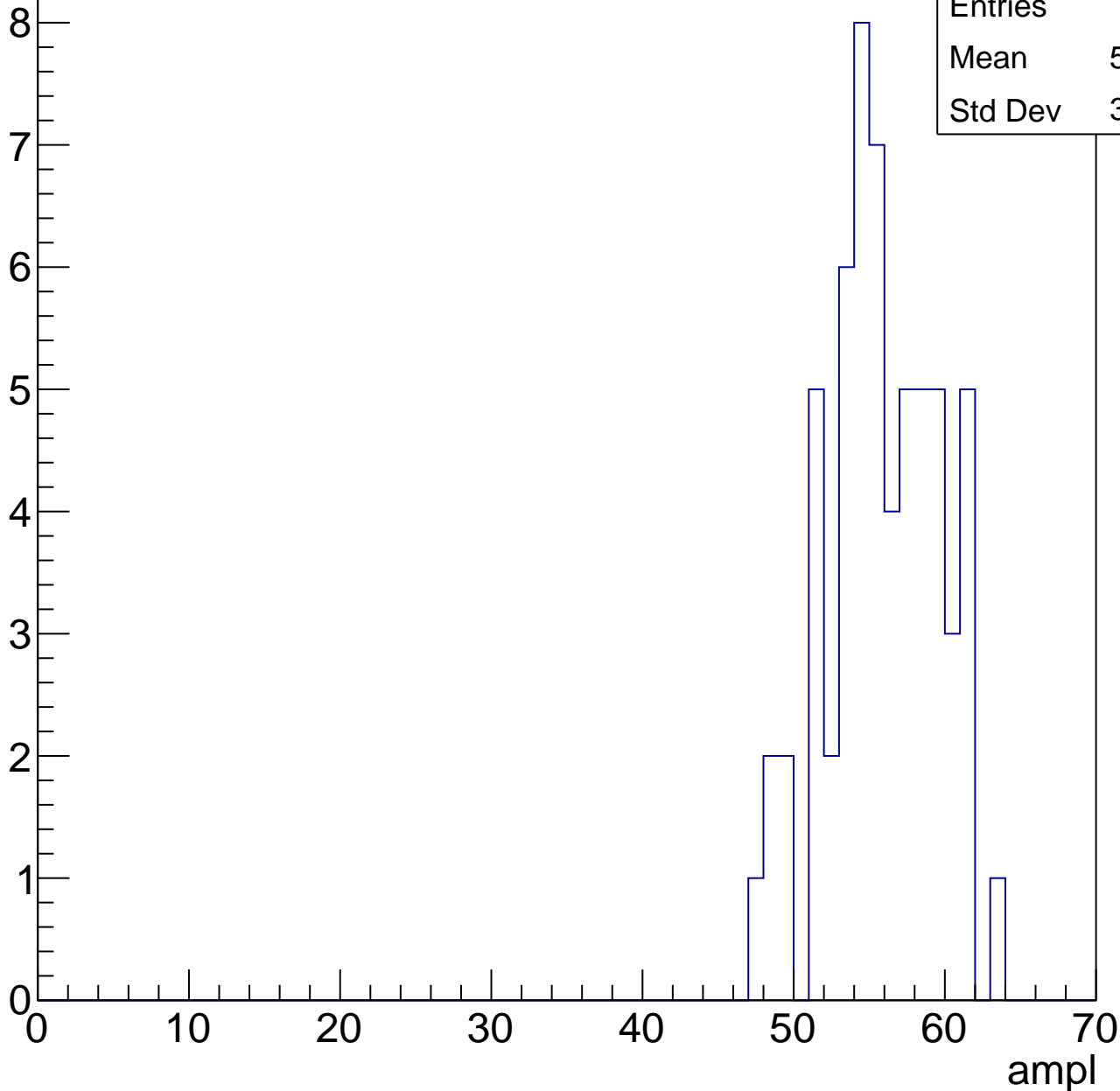


# B1L102S, U24-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.36
Std Dev	3.675

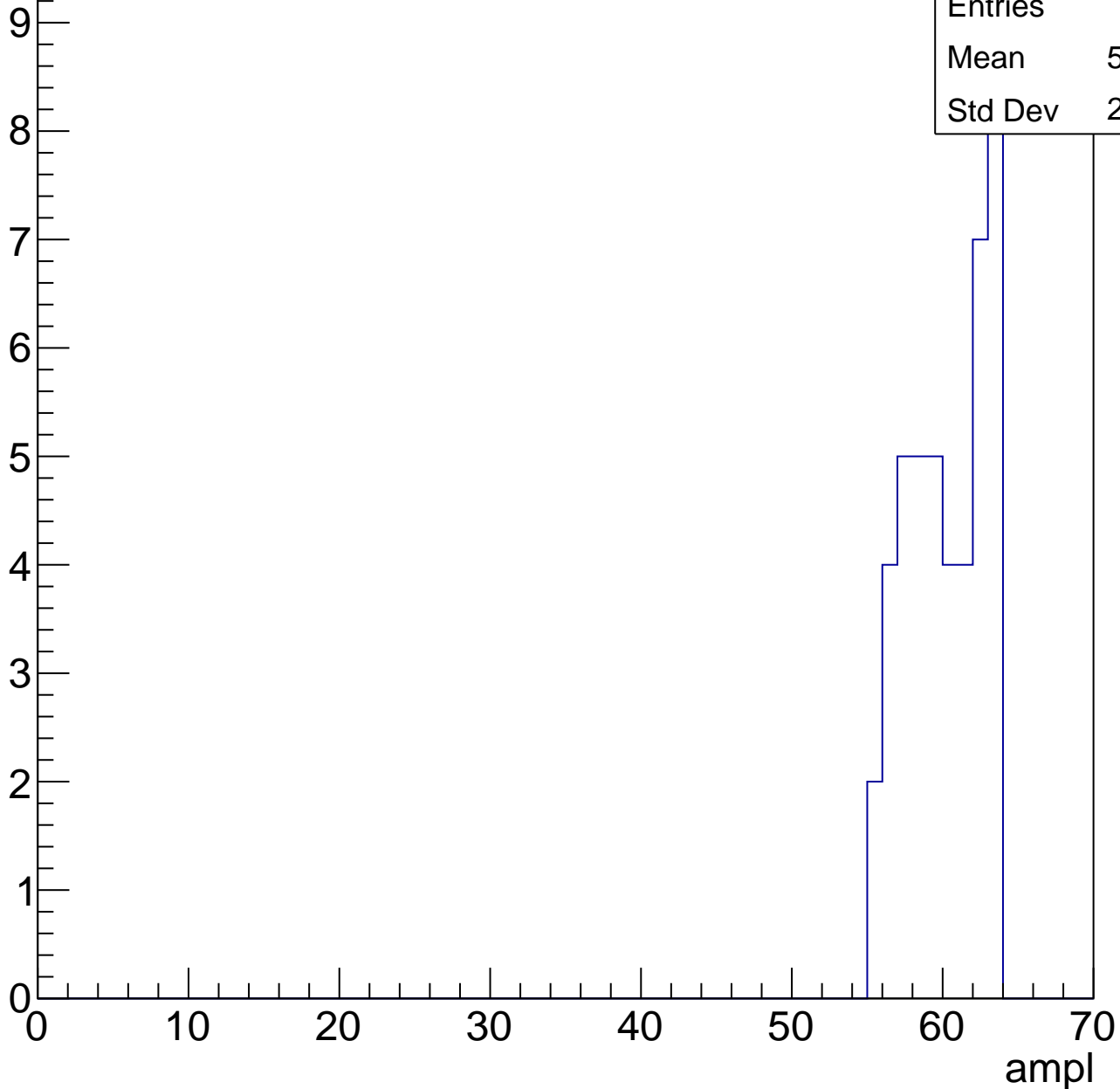


# B1L102S, U24-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

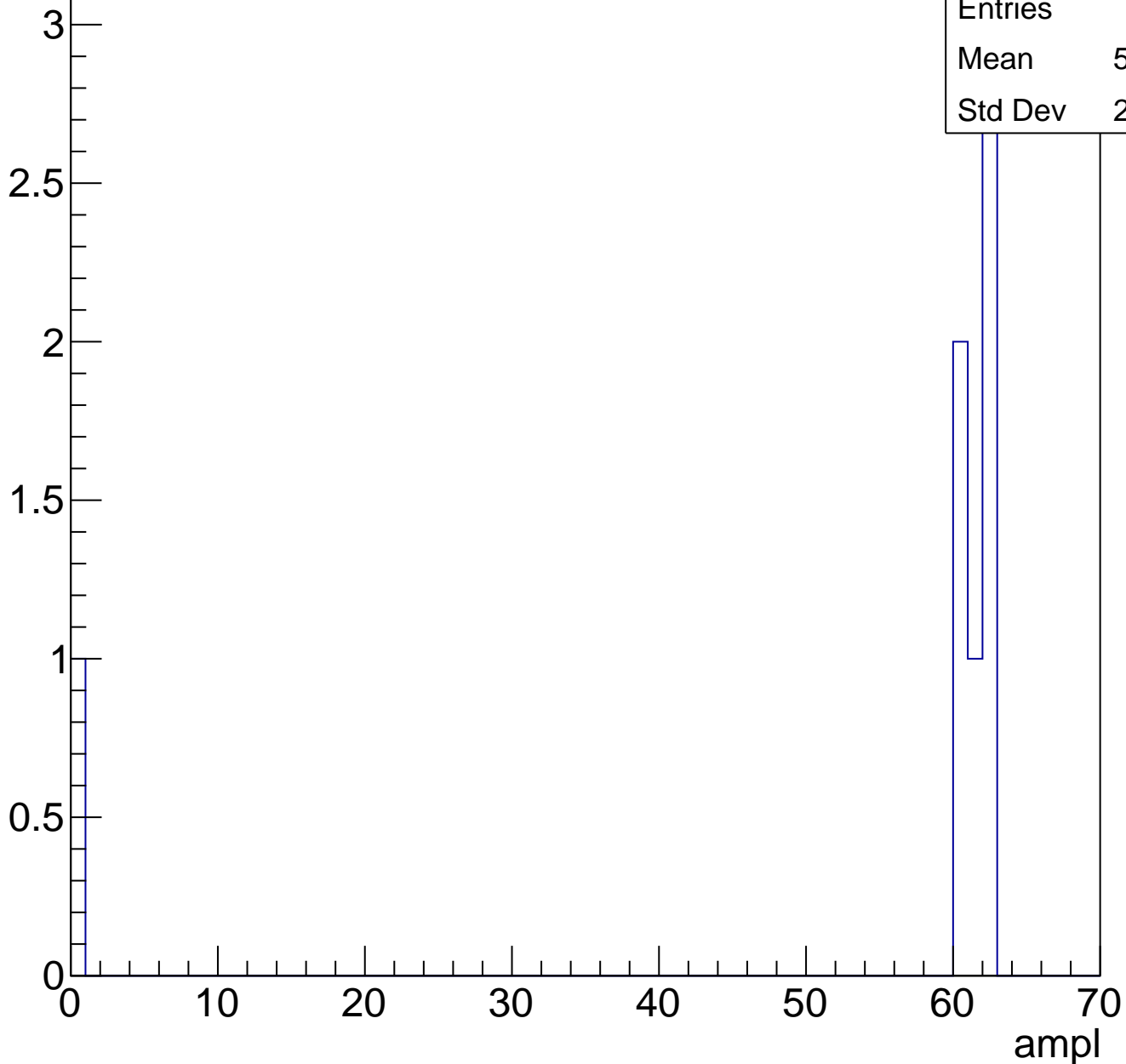
Entries	45
Mean	59.76
Std Dev	2.557



# B1L102S, U24-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

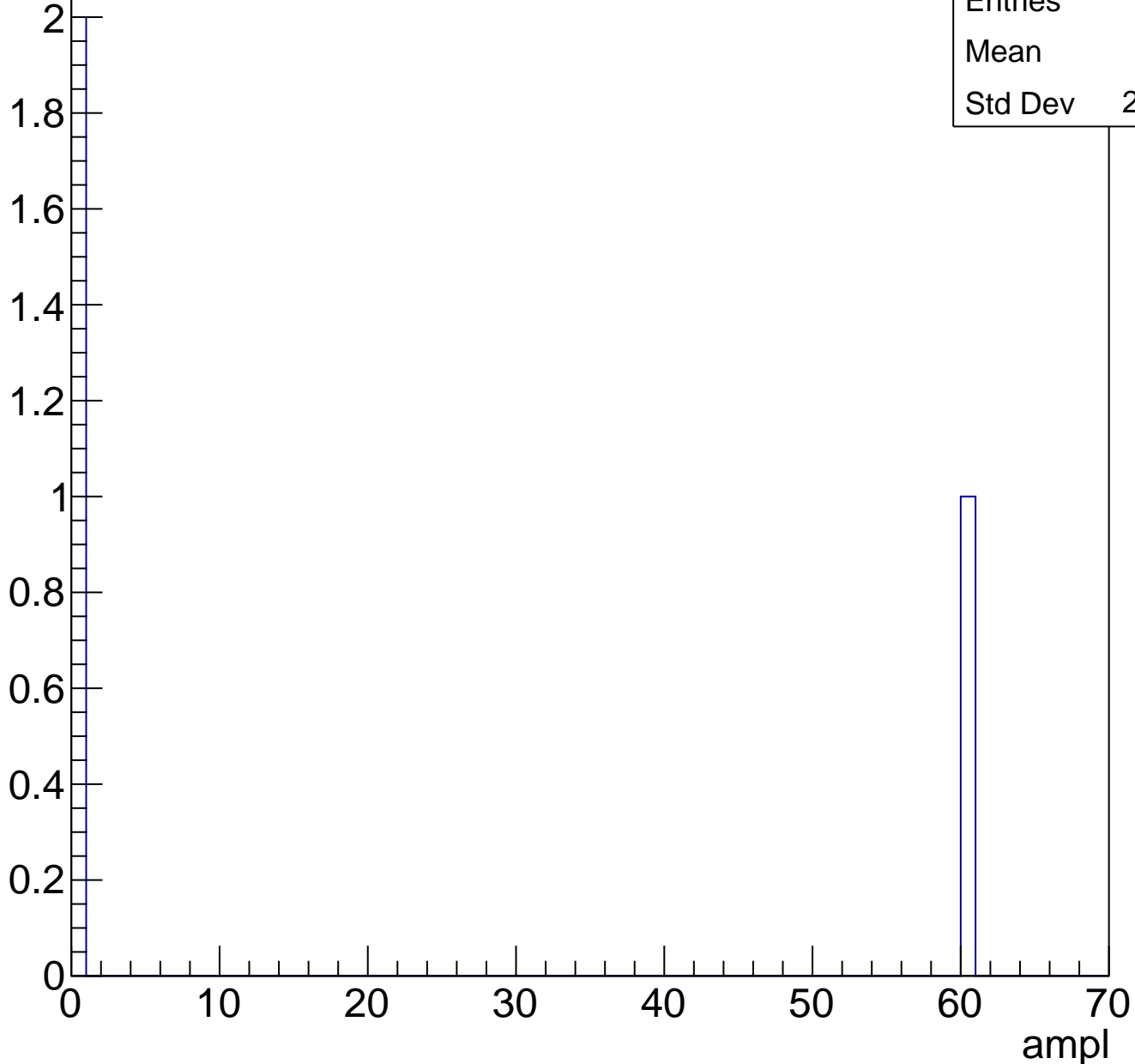




# B1L102S, U24-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20
Std Dev	28.28

# B1L102S, U24-ch81, adc0

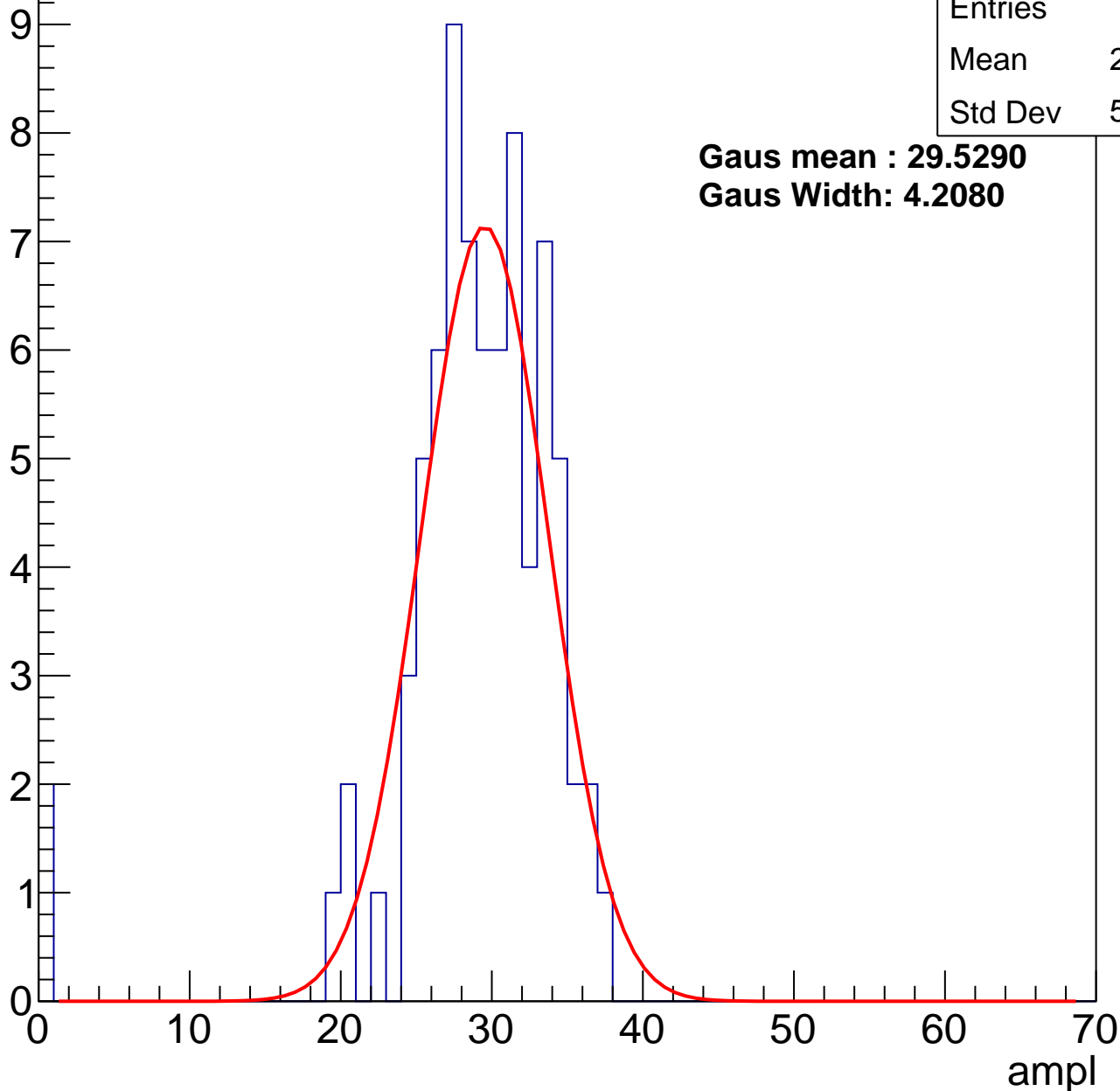
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.35
Std Dev	5.984

**Gaus mean : 29.5290**

**Gaus Width: 4.2080**



# B1L102S, U24-ch81, adc1

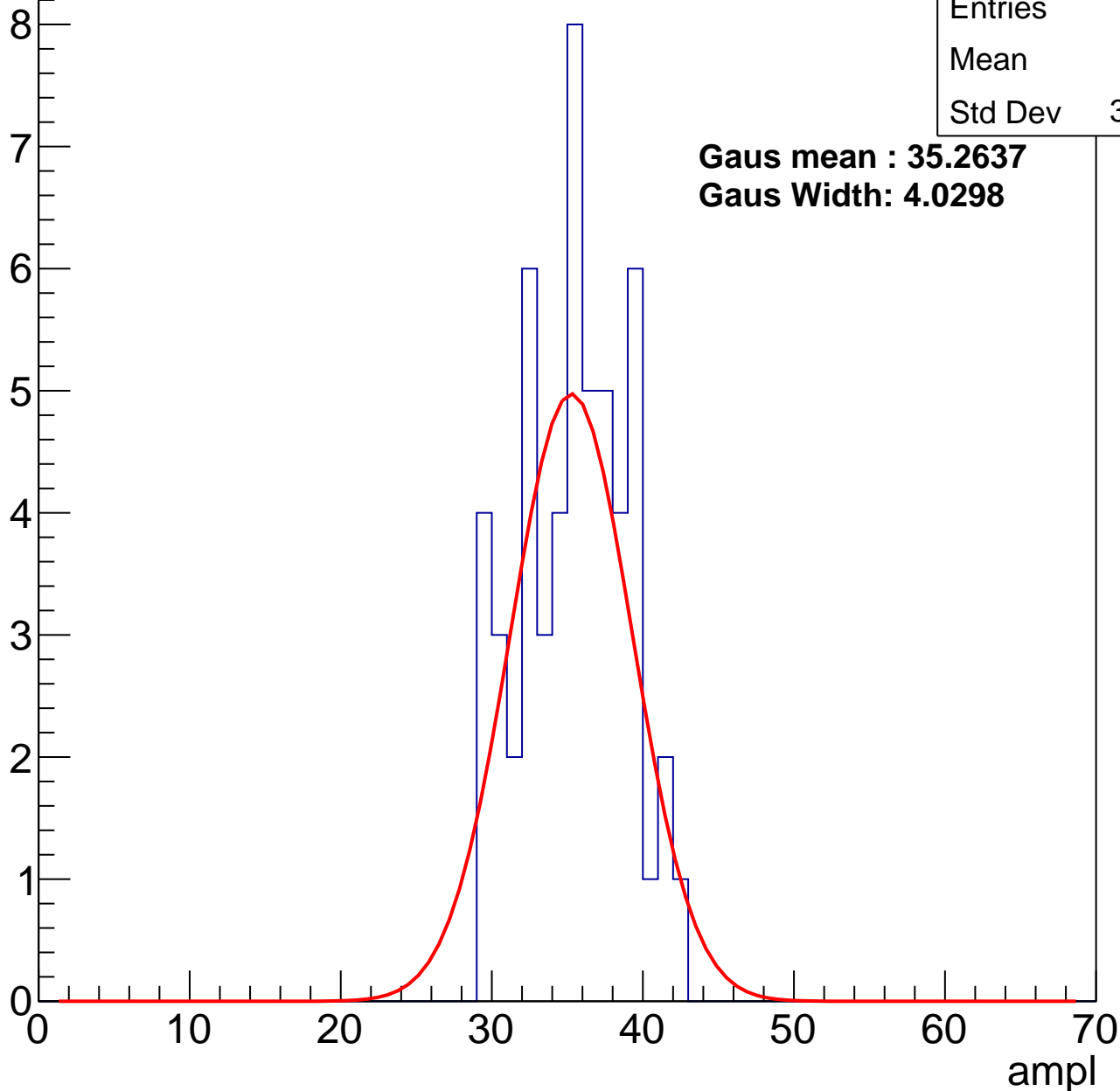
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35
Std Dev	3.399

**Gaus mean : 35.2637**

**Gaus Width: 4.0298**



# B1L102S, U24-ch81, adc2

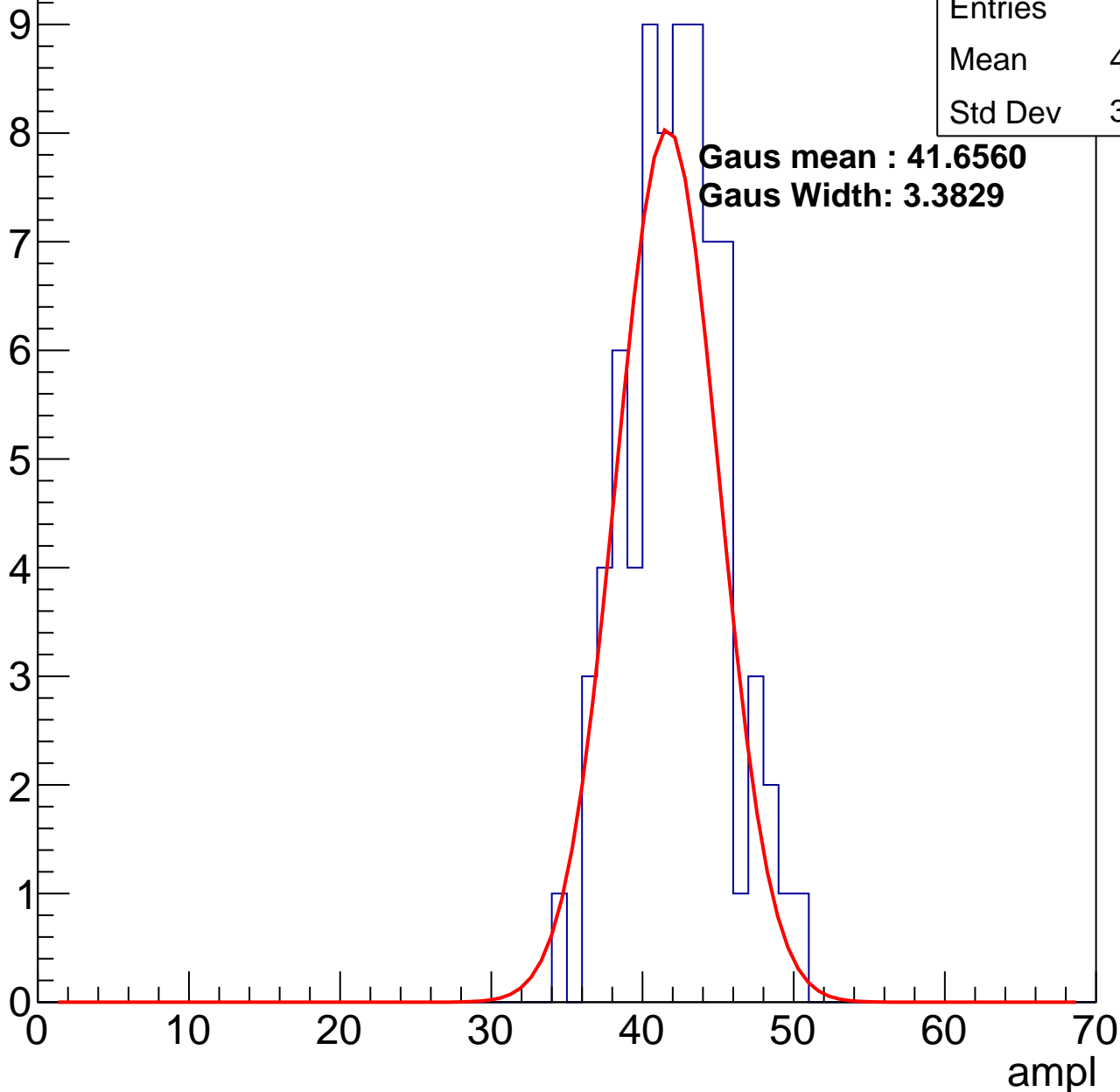
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.76
Std Dev	3.306

**Gaus mean : 41.6560**

**Gaus Width: 3.3829**

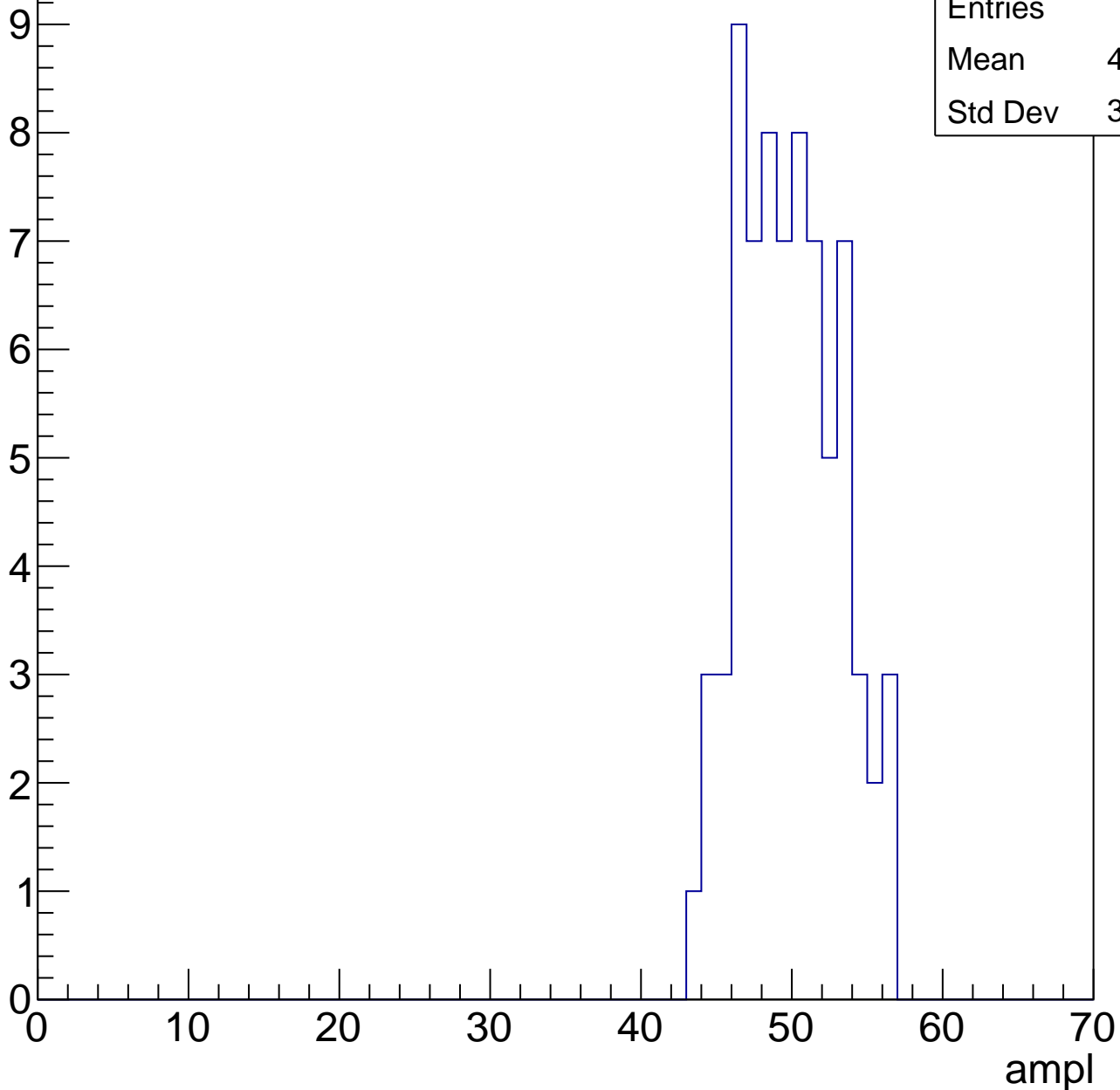


# B1L102S, U24-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

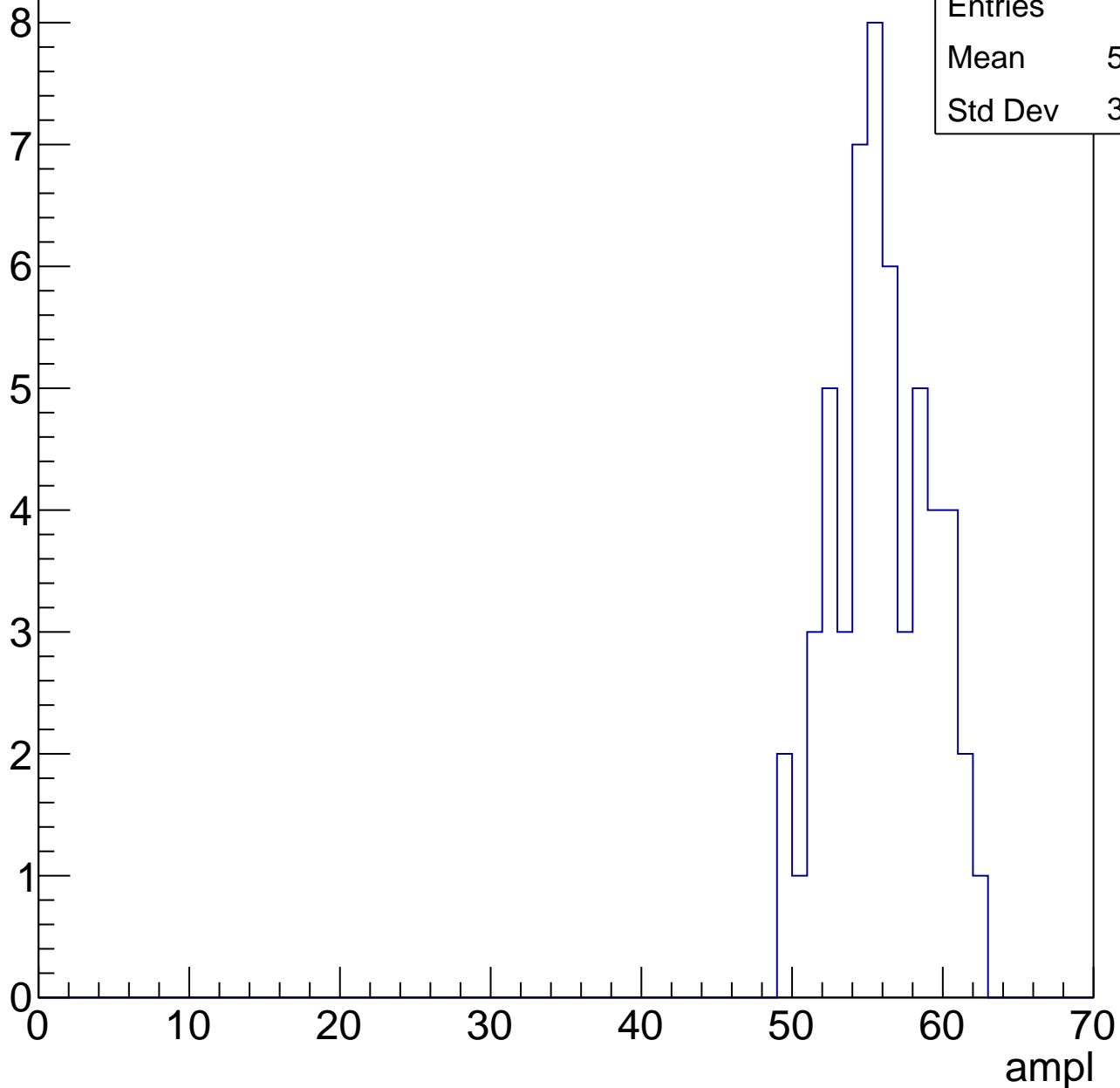
Entries	73
Mean	49.42
Std Dev	3.205



# B1L102S, U24-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

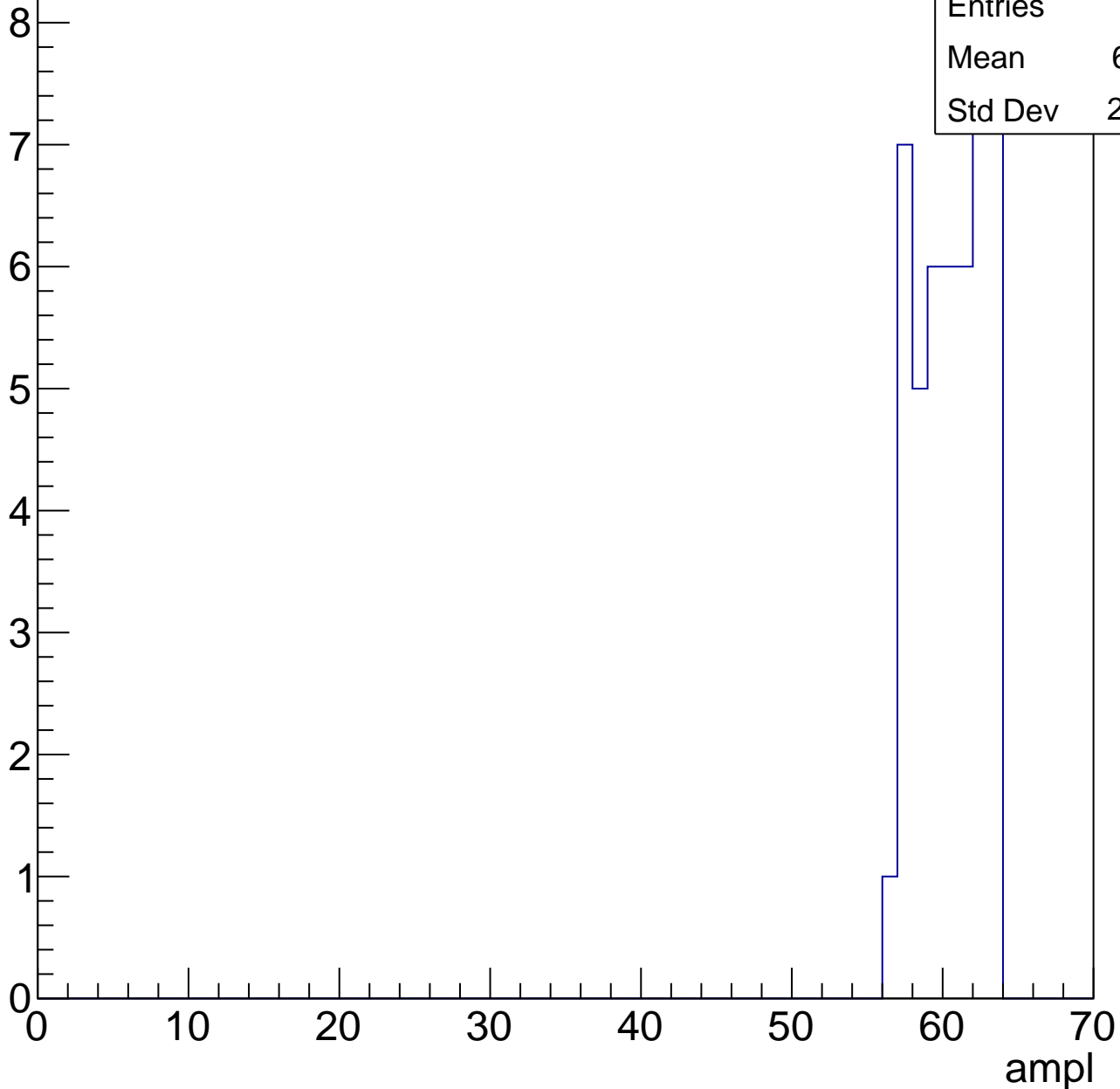


# B1L102S, U24-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	60.11
Std Dev	2.136



# B1L102S, U24-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

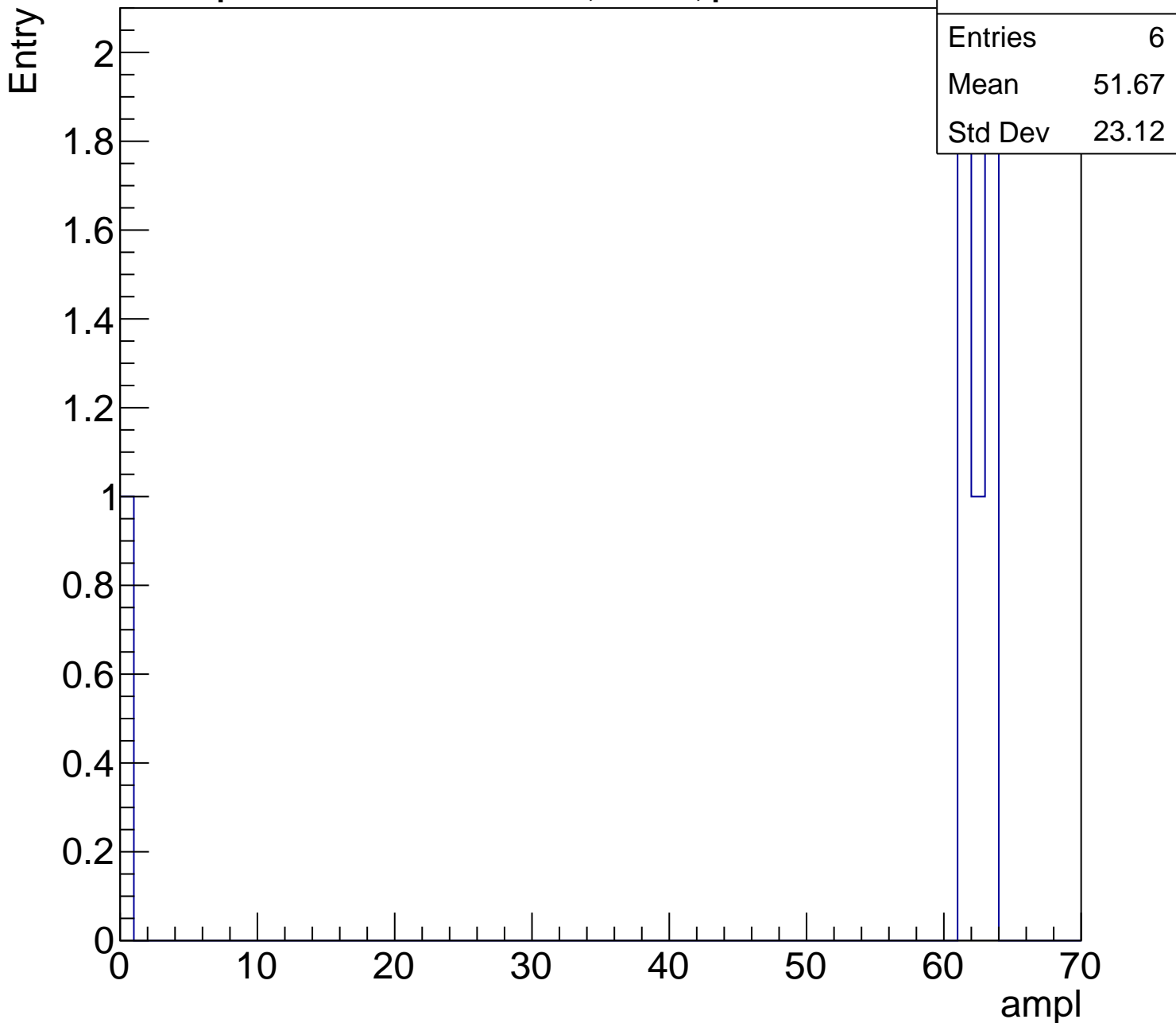
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.67
Std Dev	23.12

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch82, adc0

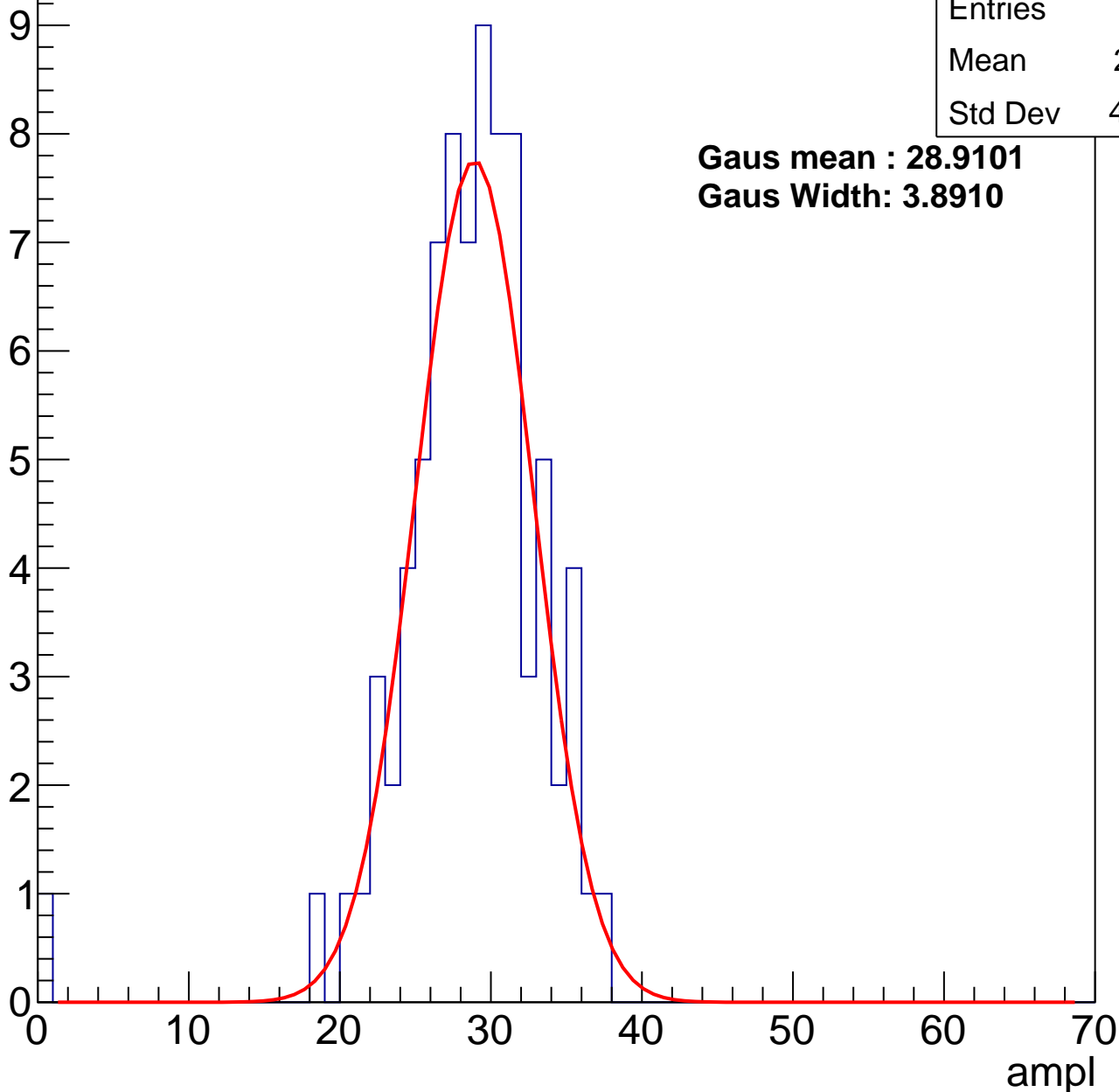
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.11
Std Dev	4.967

**Gaus mean : 28.9101**

**Gaus Width: 3.8910**



# B1L102S, U24-ch82, adc1

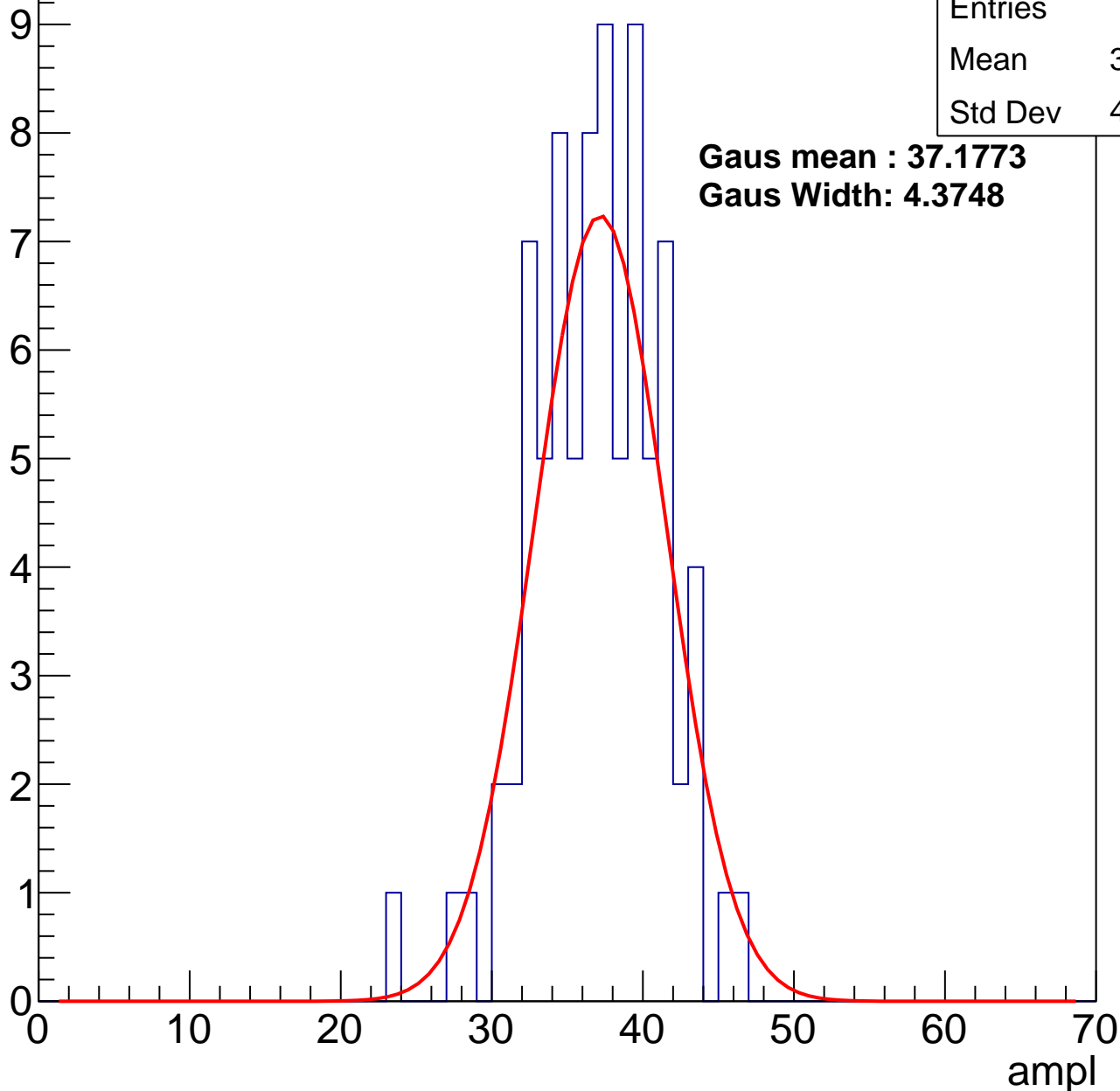
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.53
Std Dev	4.137

**Gaus mean : 37.1773**

**Gaus Width: 4.3748**



# B1L102S, U24-ch82, adc2

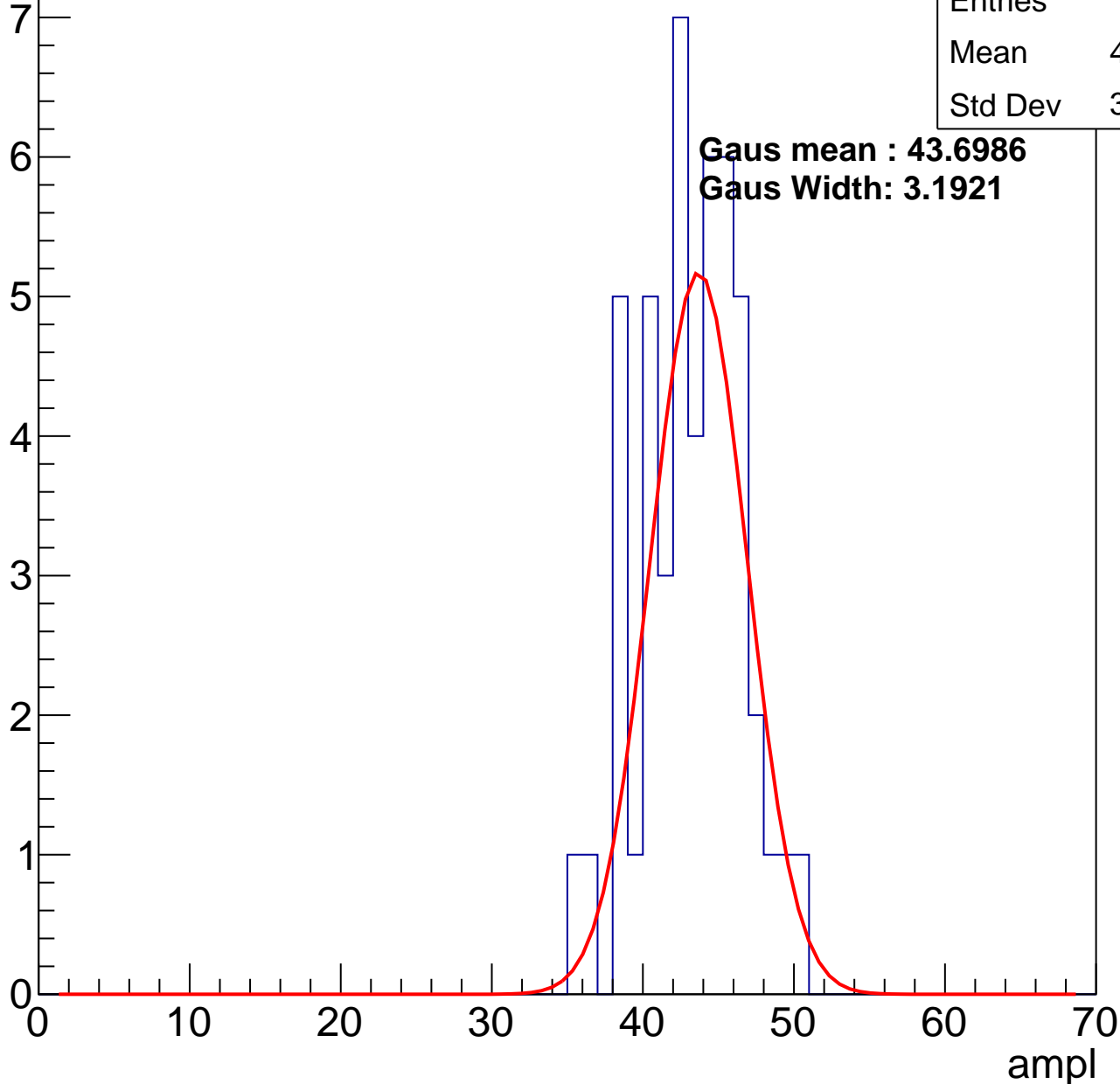
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.73
Std Dev	3.294

**Gaus mean : 43.6986**

**Gaus Width: 3.1921**

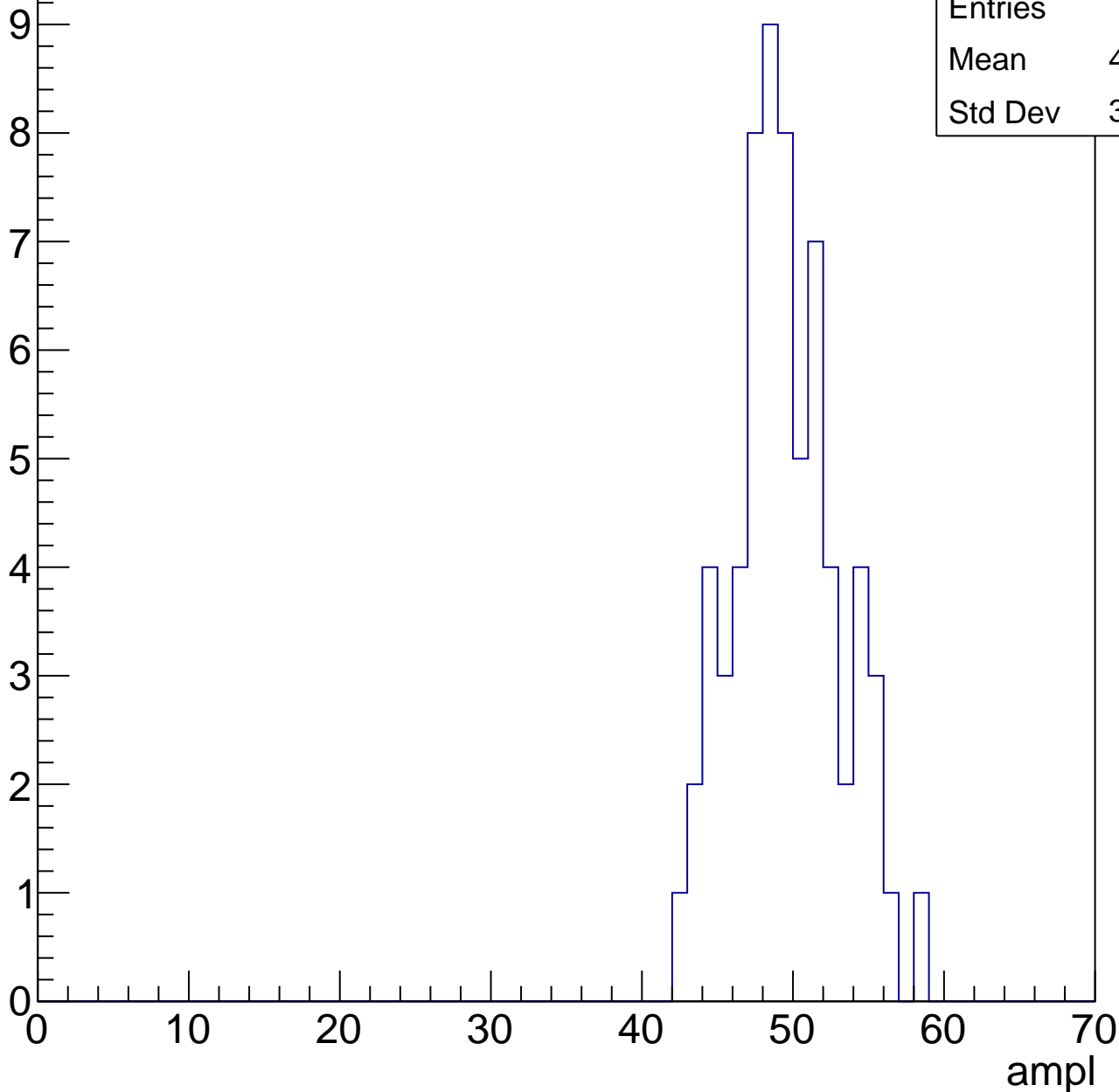


# B1L102S, U24-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.08
Std Dev	3.448

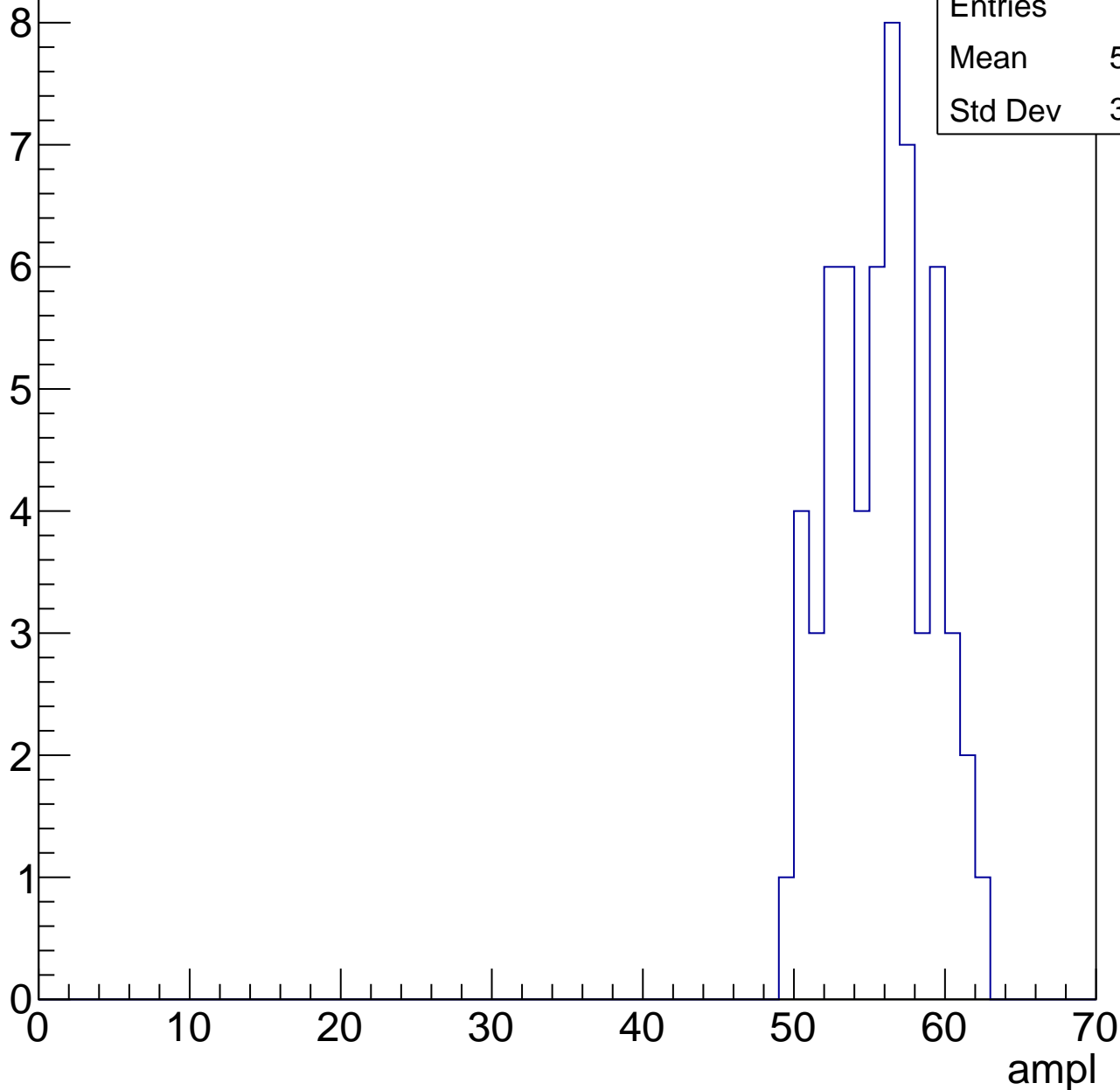


# B1L102S, U24-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.28
Std Dev	3.205

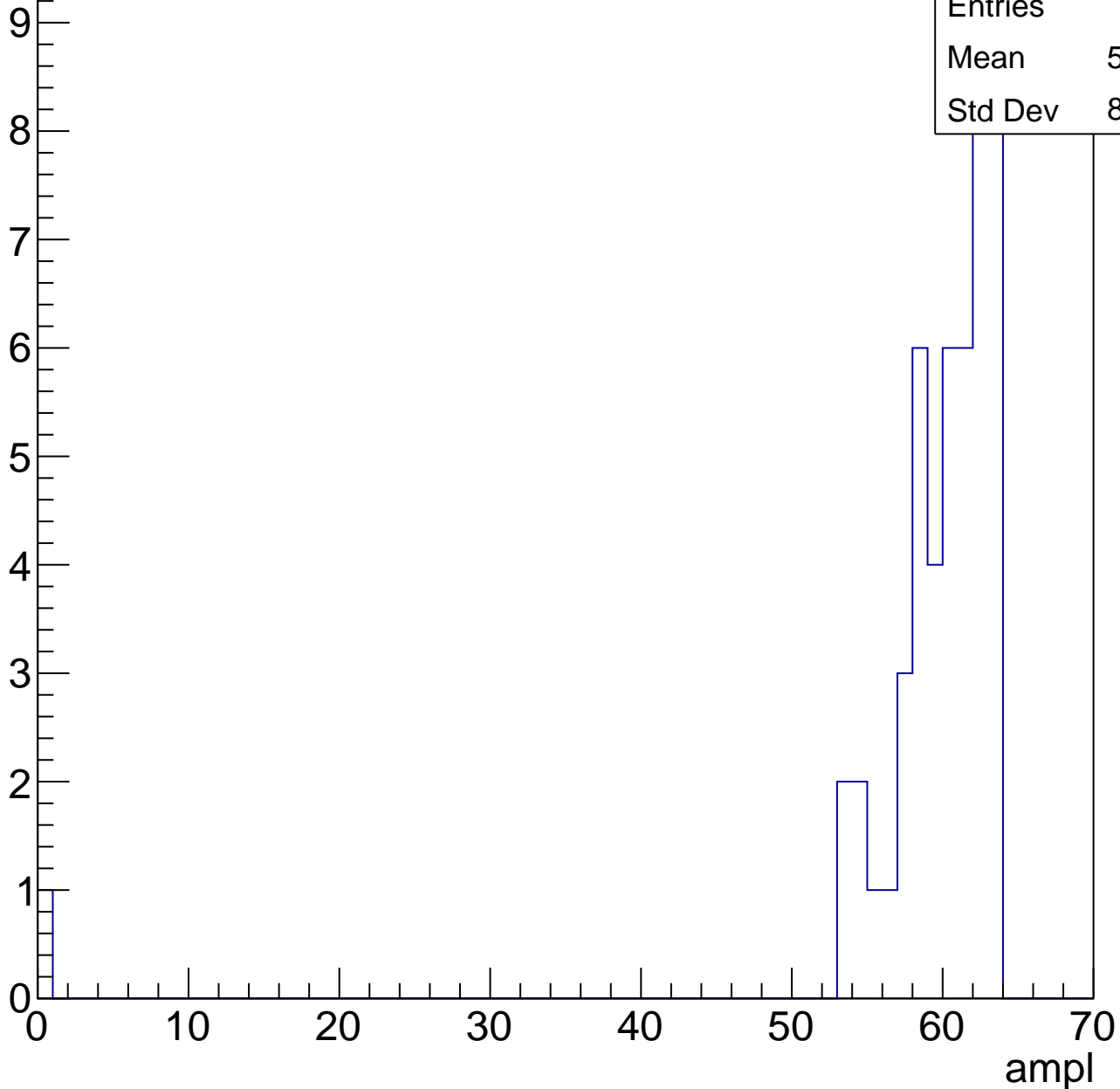


# B1L102S, U24-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

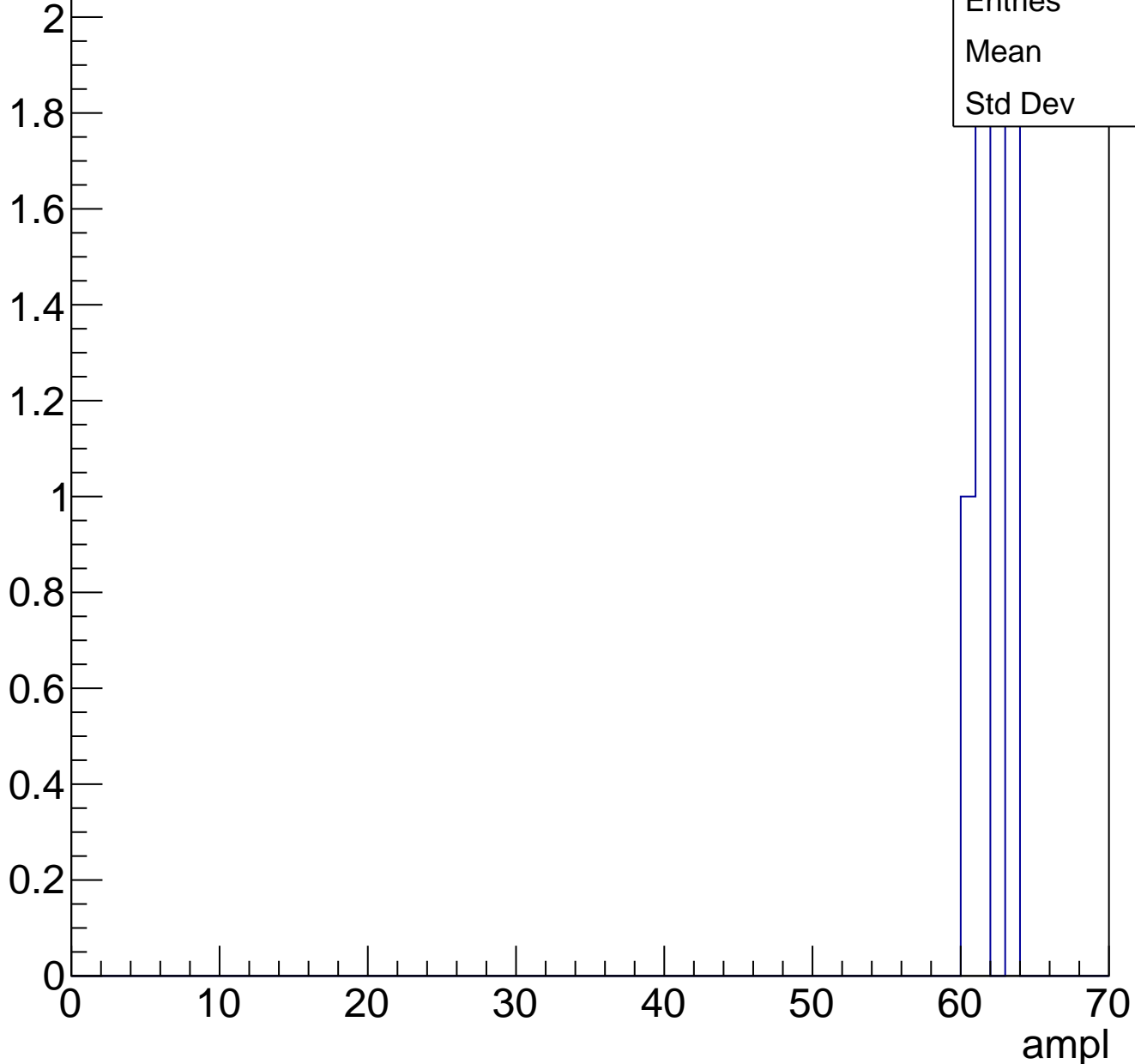
Entries	49
Mean	58.53
Std Dev	8.892



# B1L102S, U24-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch83, adc0

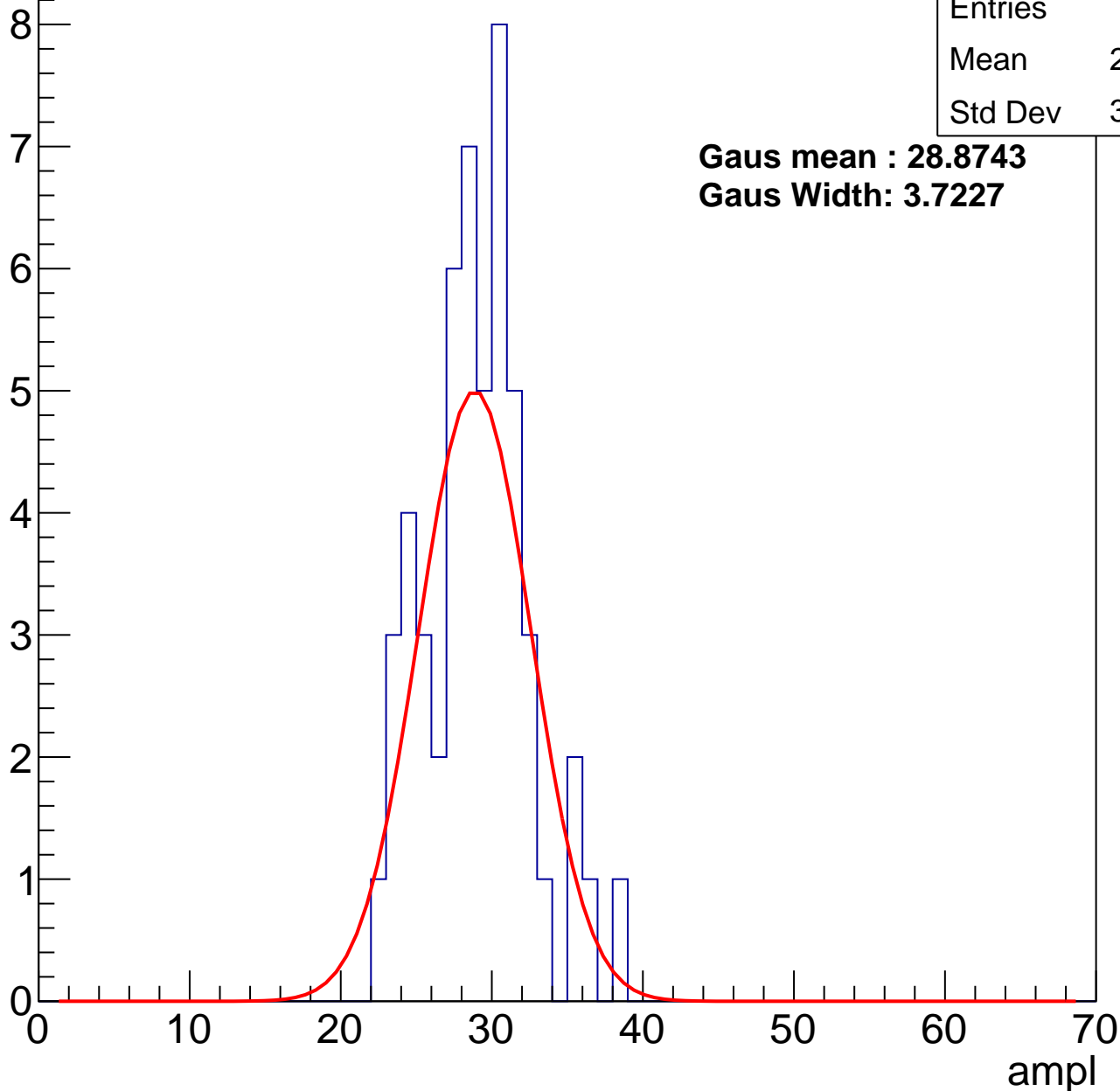
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	28.56
Std Dev	3.444

**Gaus mean : 28.8743**

**Gaus Width: 3.7227**



# B1L102S, U24-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	35.14
Std Dev	4.251

**Gaus mean : 35.9942**

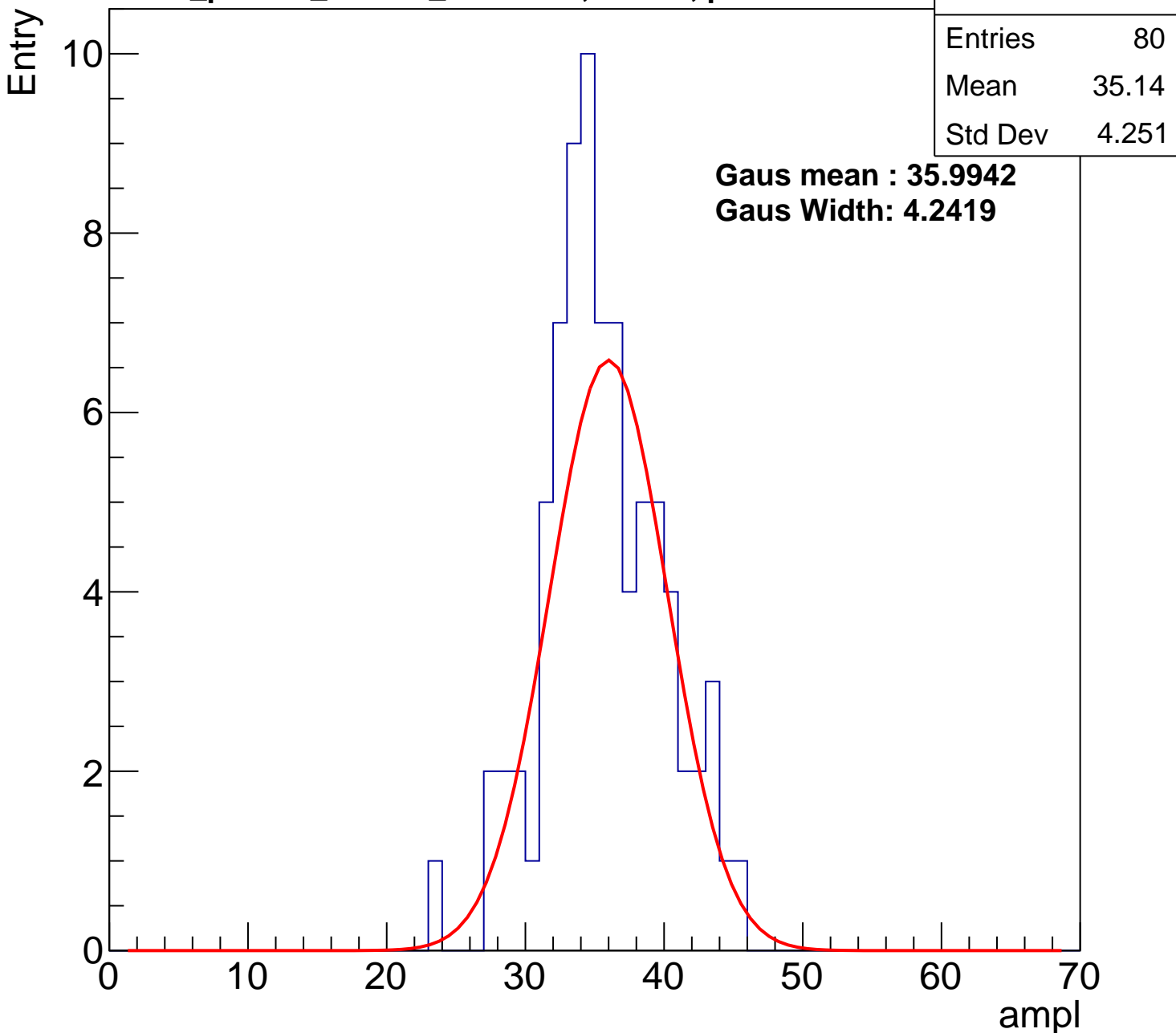
**Gaus Width: 4.2419**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch83, adc2

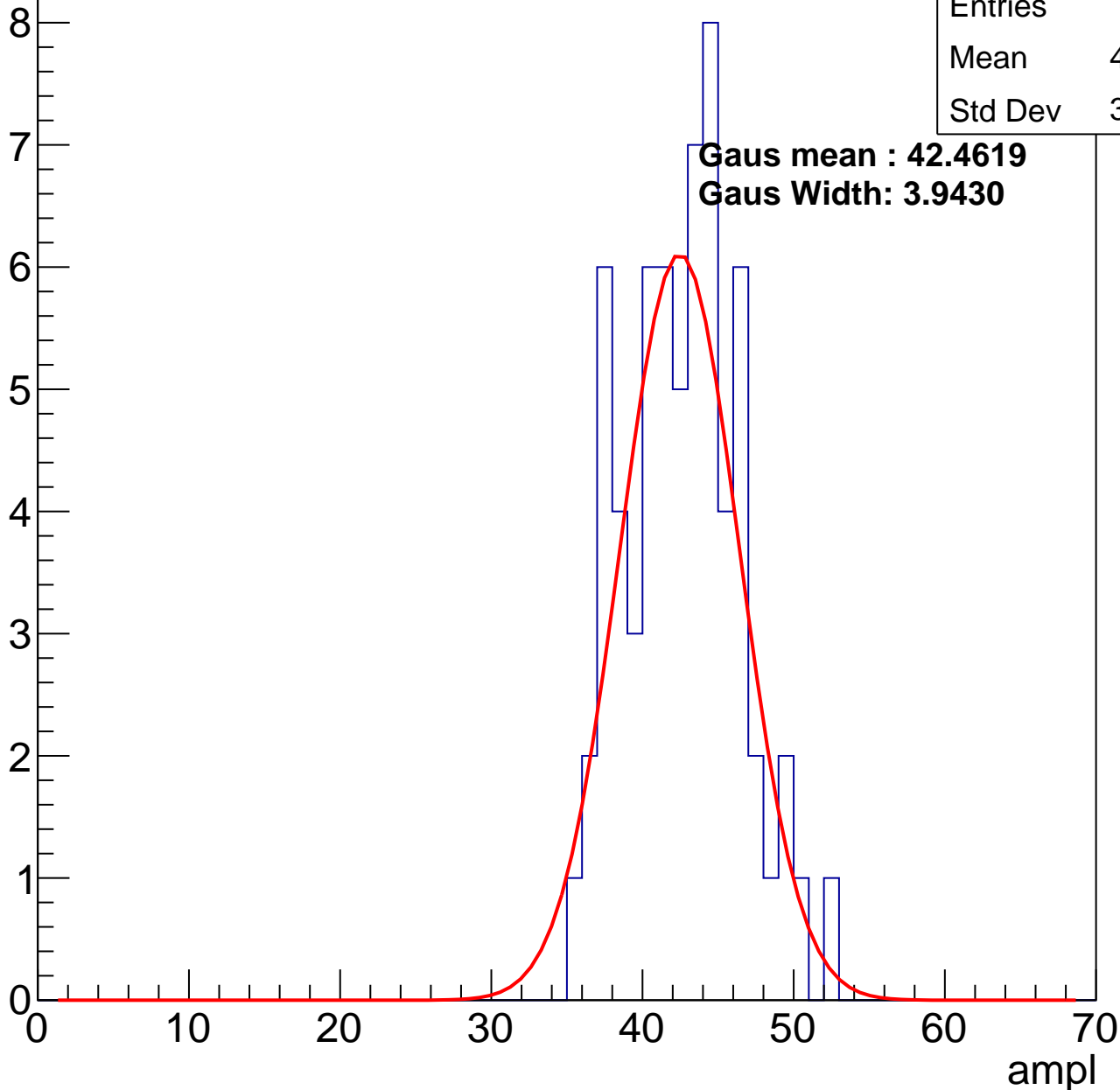
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.23
Std Dev	3.737

**Gaus mean : 42.4619**

**Gaus Width: 3.9430**

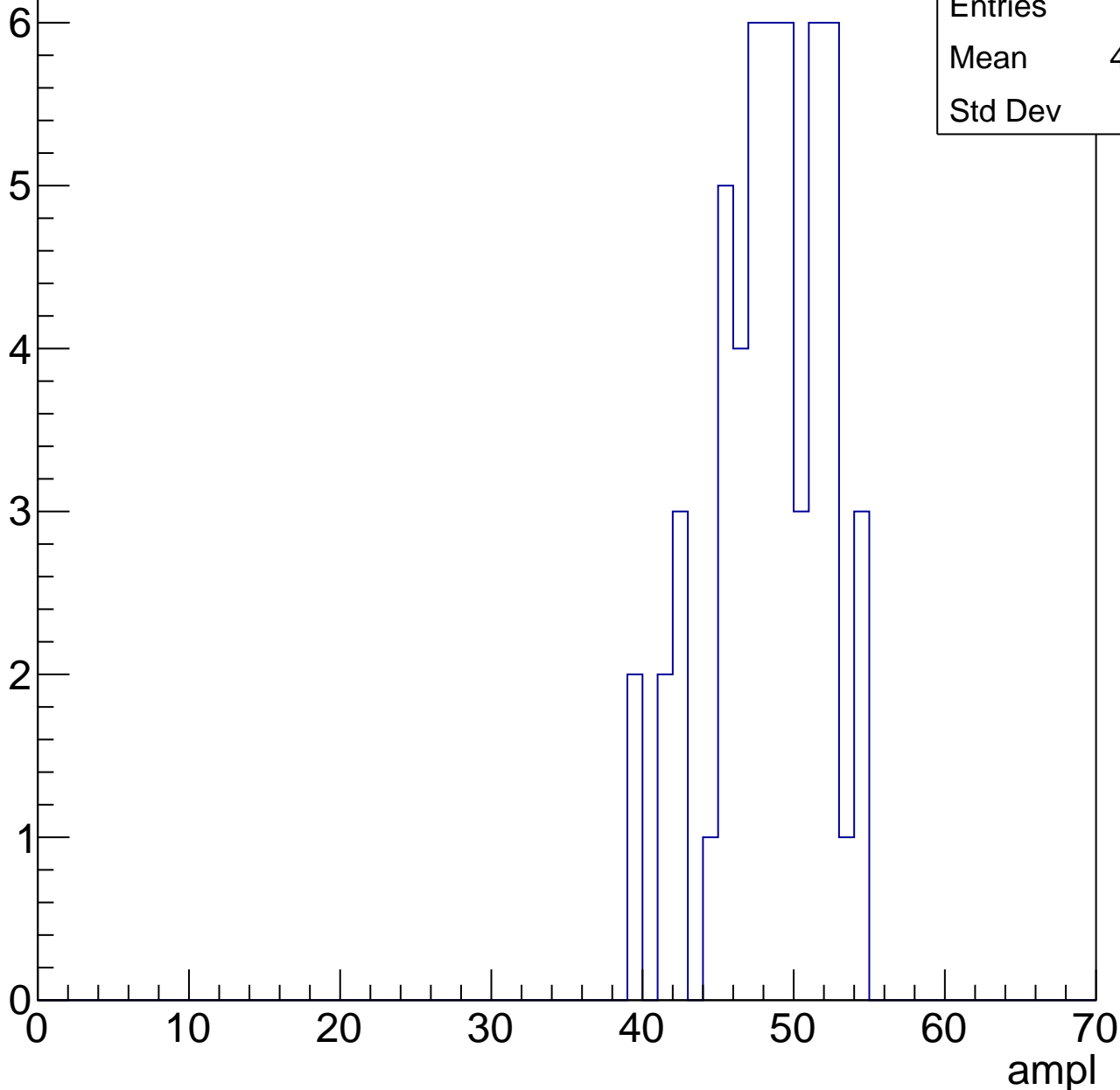


# B1L102S, U24-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

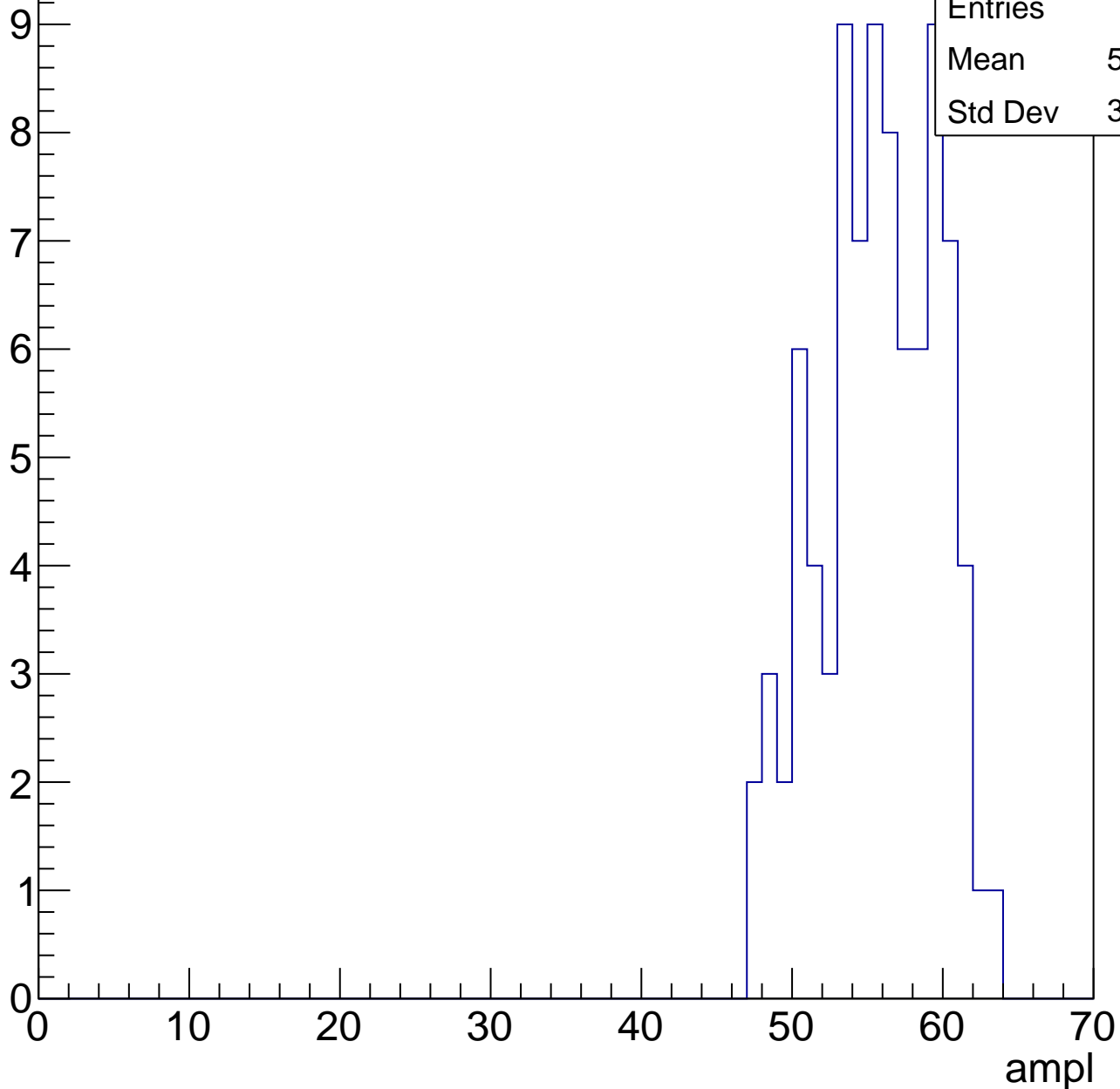
Entries	54
Mean	47.89
Std Dev	3.73



# B1L102S, U24-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

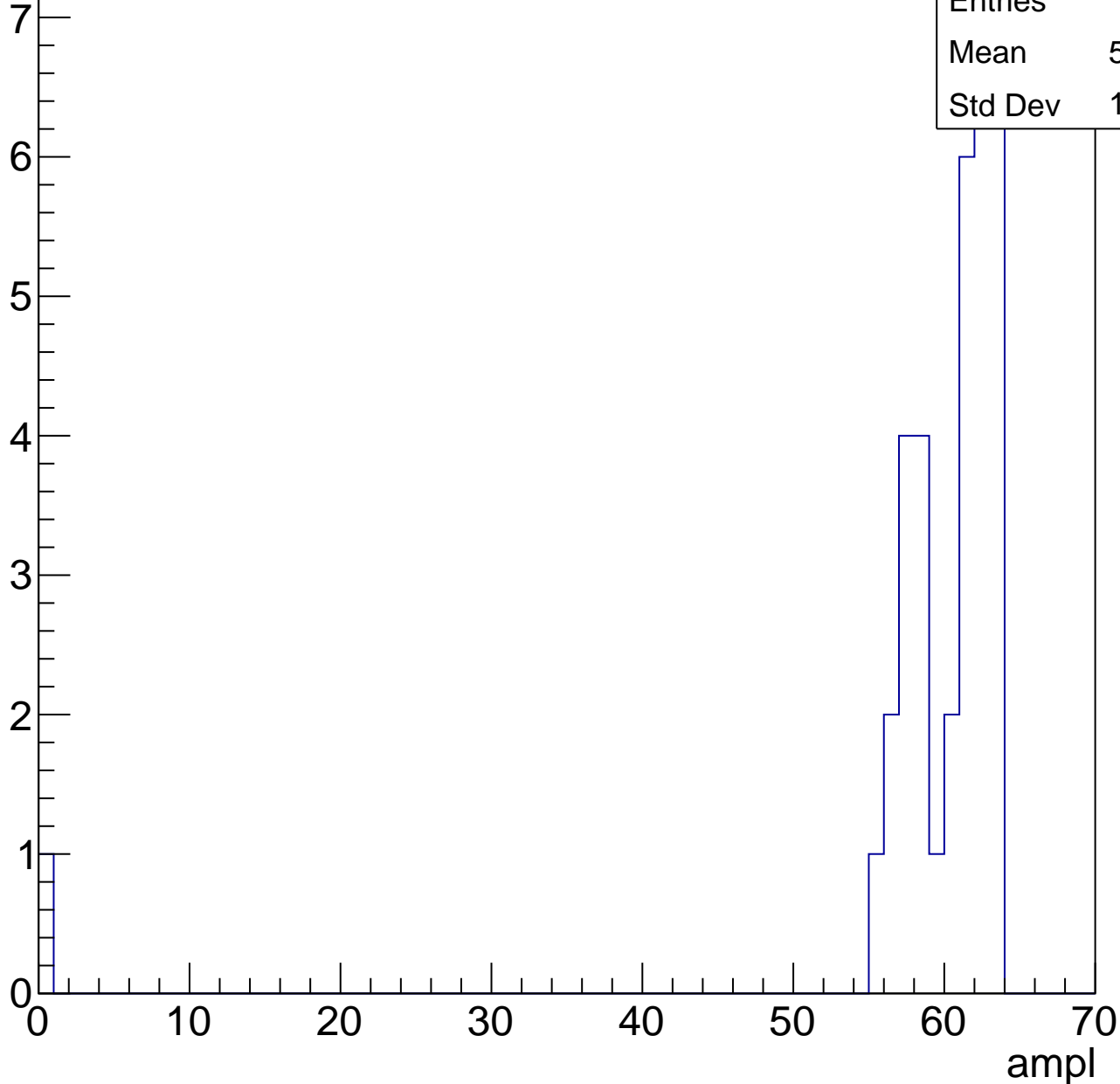


# B1L102S, U24-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

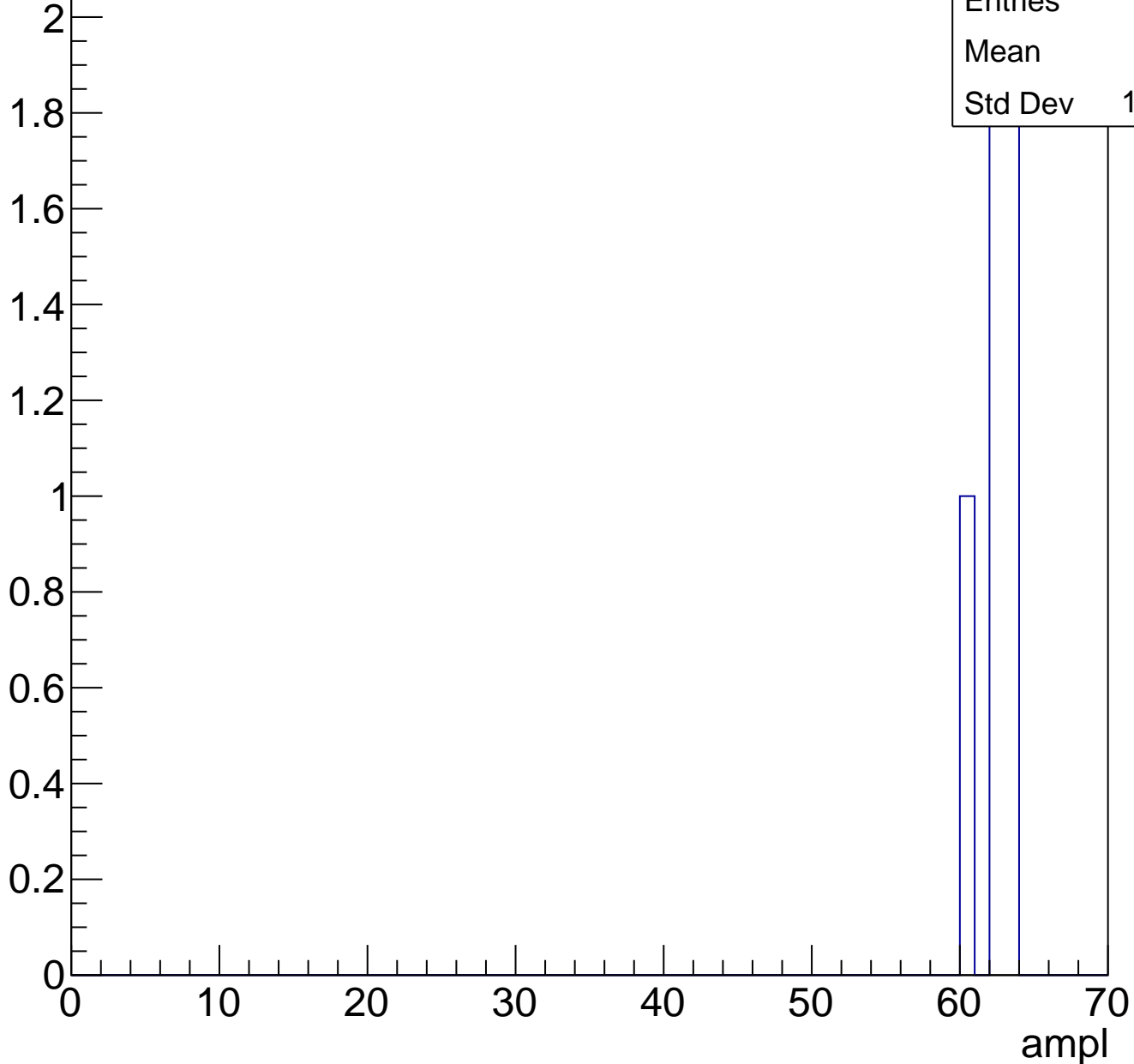
Entries	35
Mean	58.49
Std Dev	10.32



# B1L102S, U24-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch84, adc0

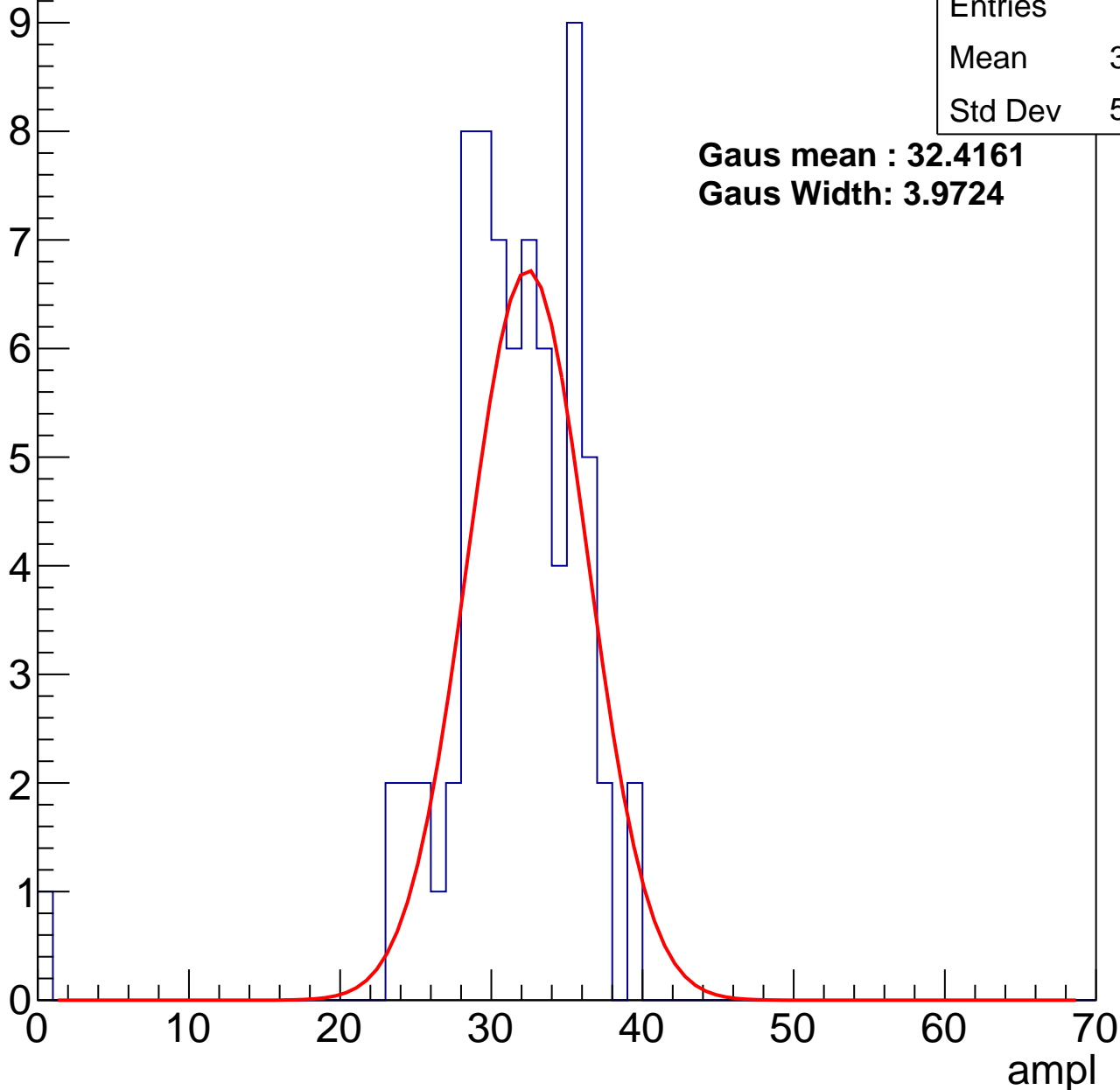
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	30.82
Std Dev	5.147

**Gaus mean : 32.4161**

**Gaus Width: 3.9724**



# B1L102S, U24-ch84, adc1

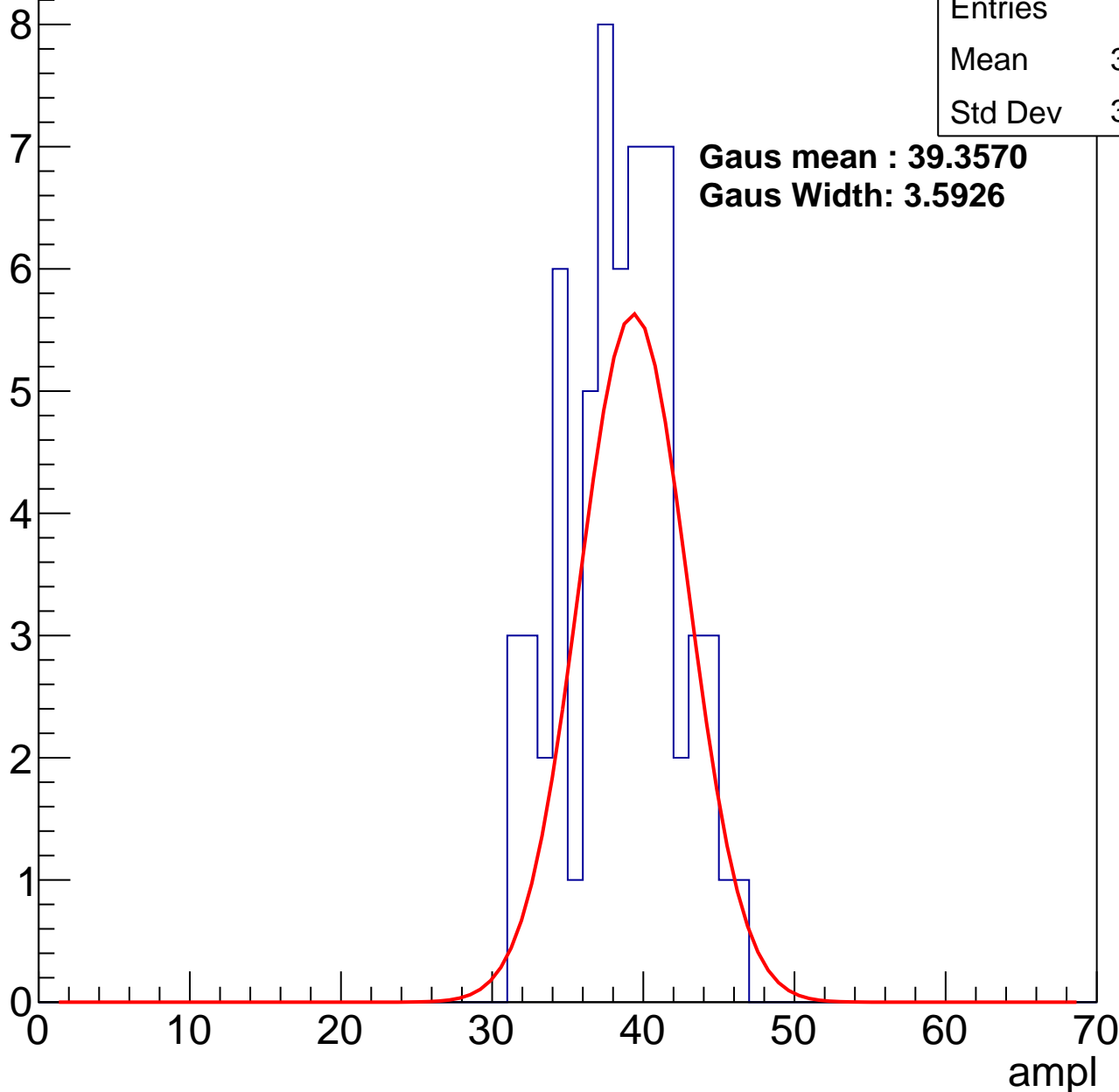
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	38.06
Std Dev	3.645

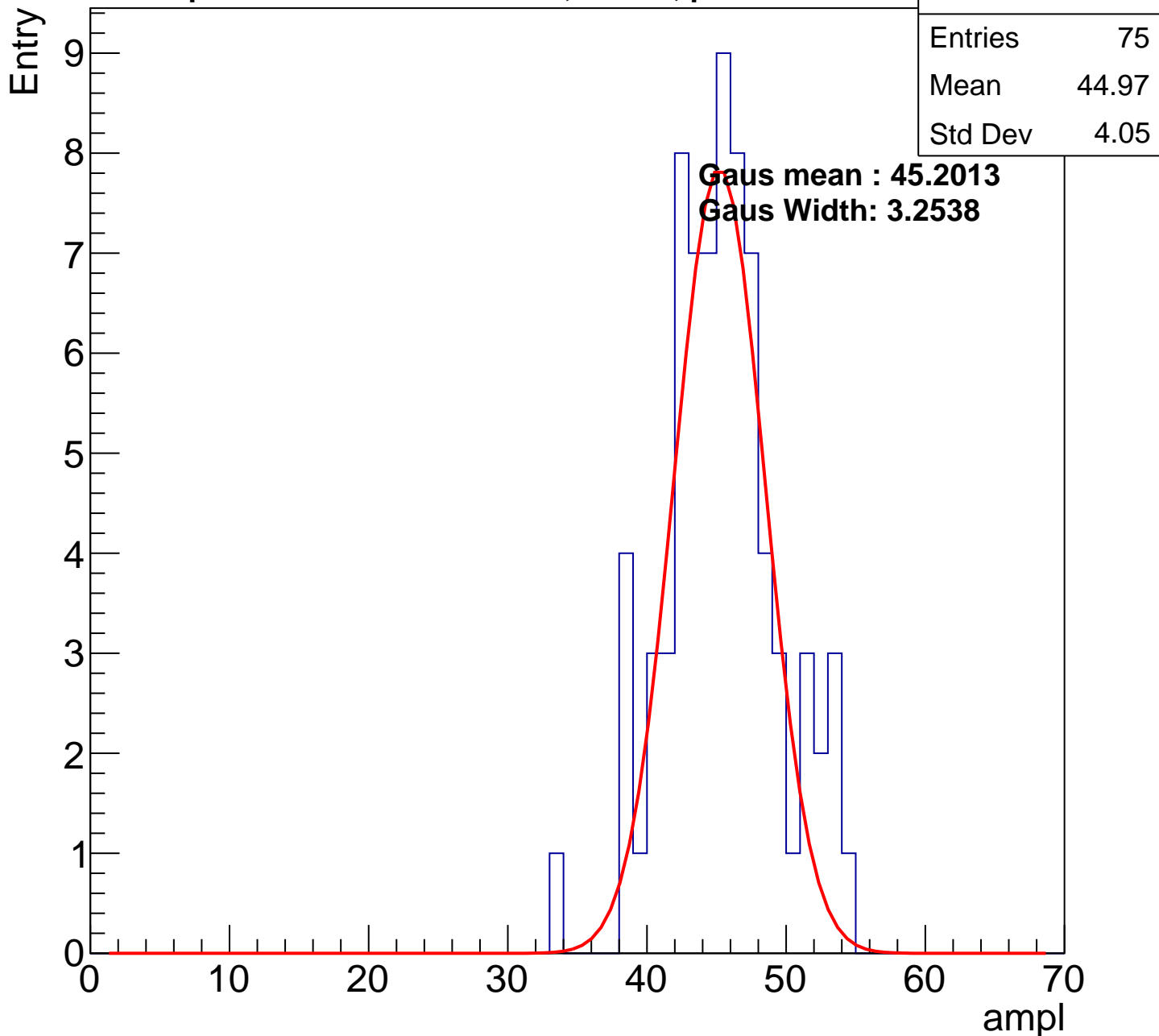
**Gaus mean : 39.3570**

**Gaus Width: 3.5926**



# B1L102S, U24-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

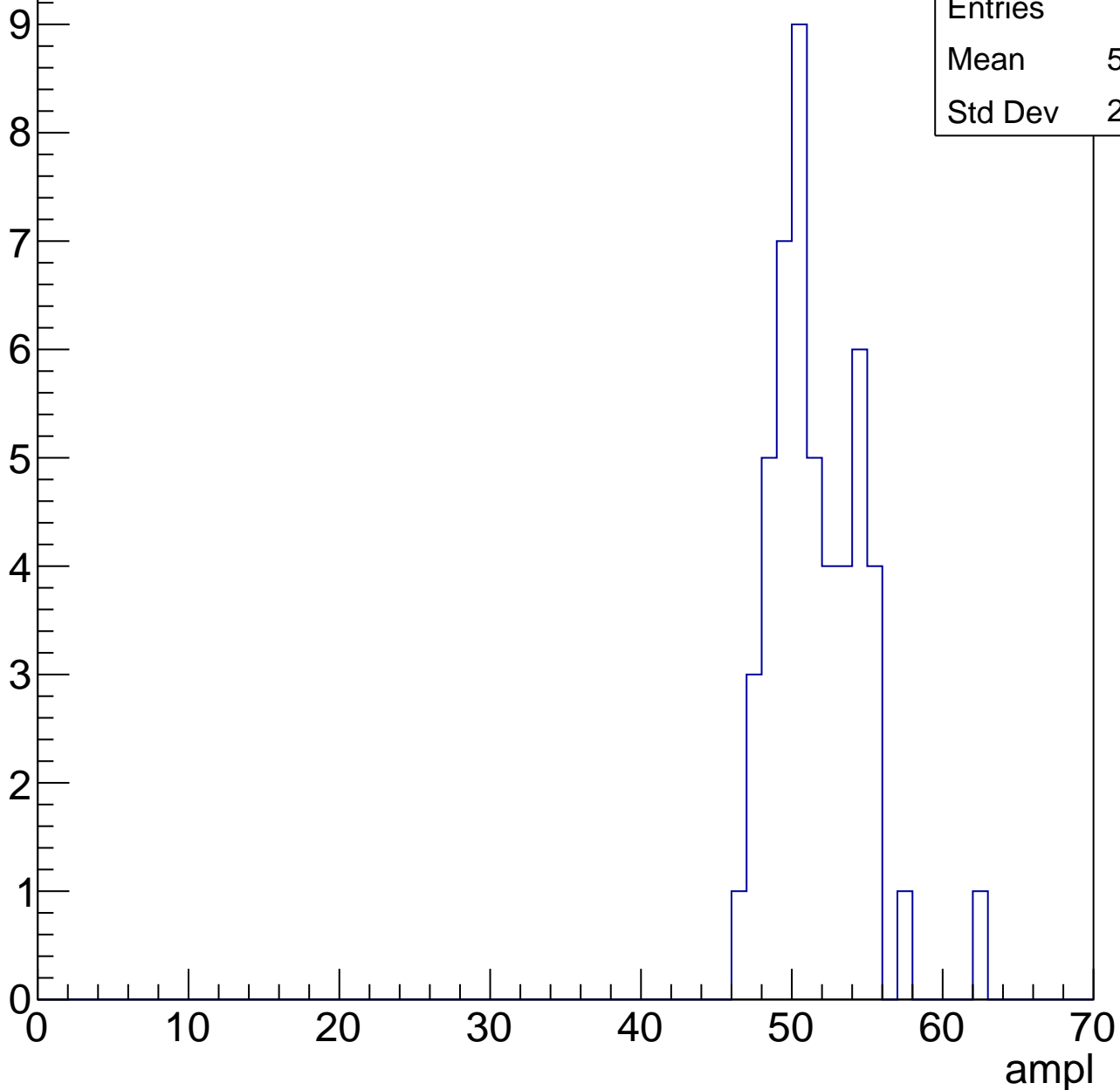


# B1L102S, U24-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	51.16
Std Dev	2.996

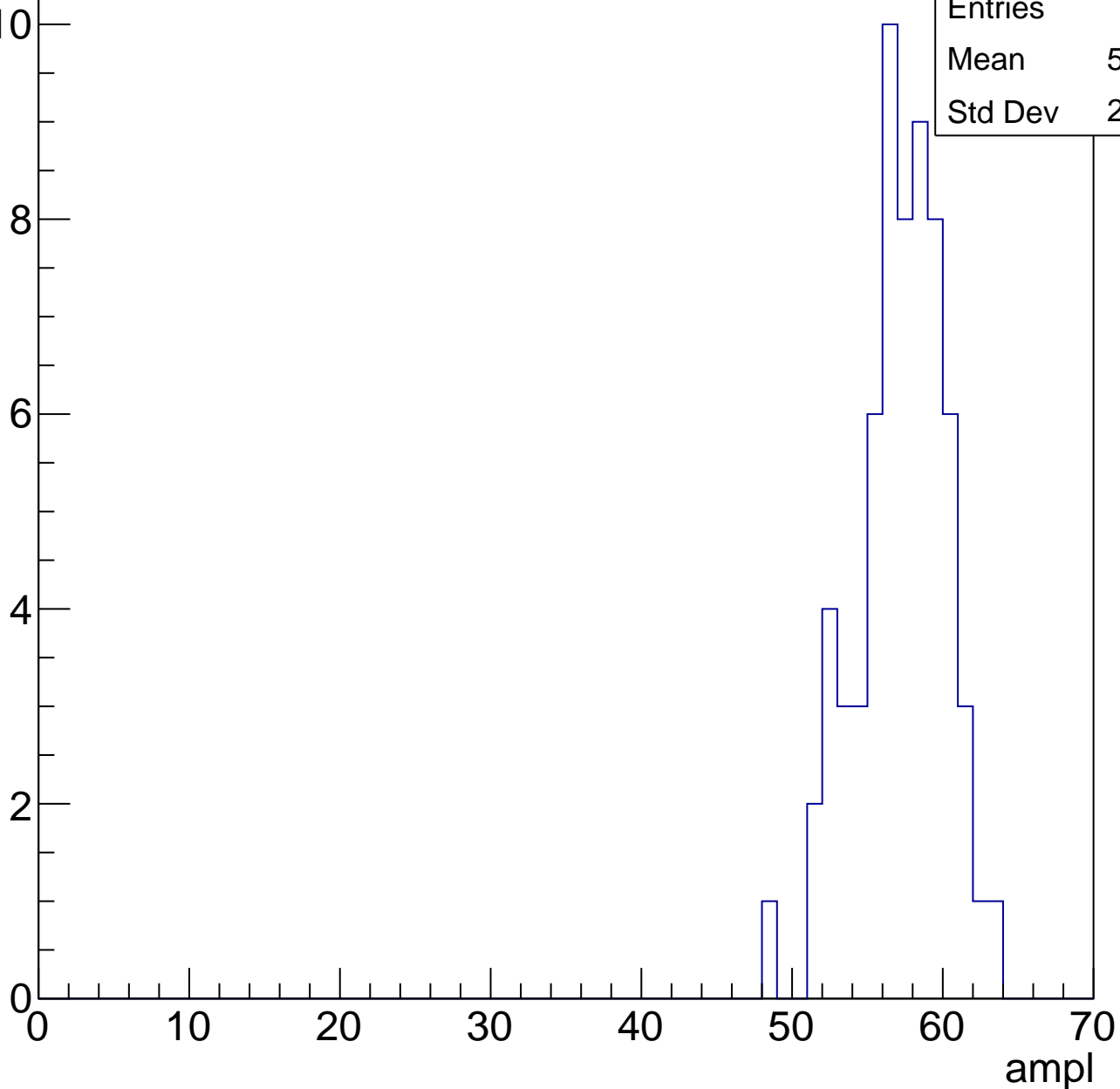


# B1L102S, U24-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	56.72
Std Dev	2.938

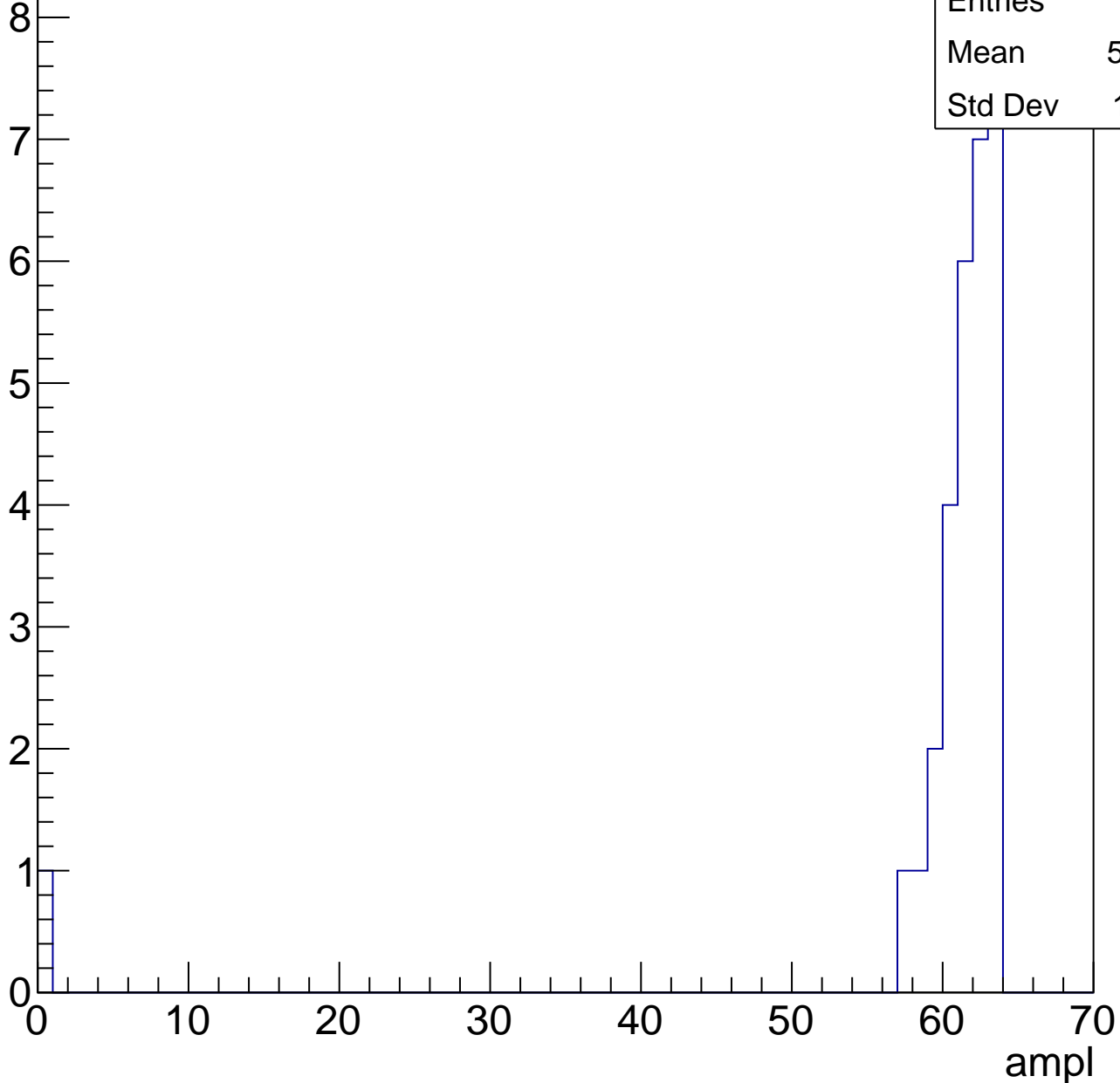


# B1L102S, U24-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	30
Mean	59.23
Std Dev	11.11



# B1L102S, U24-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch85, adc0

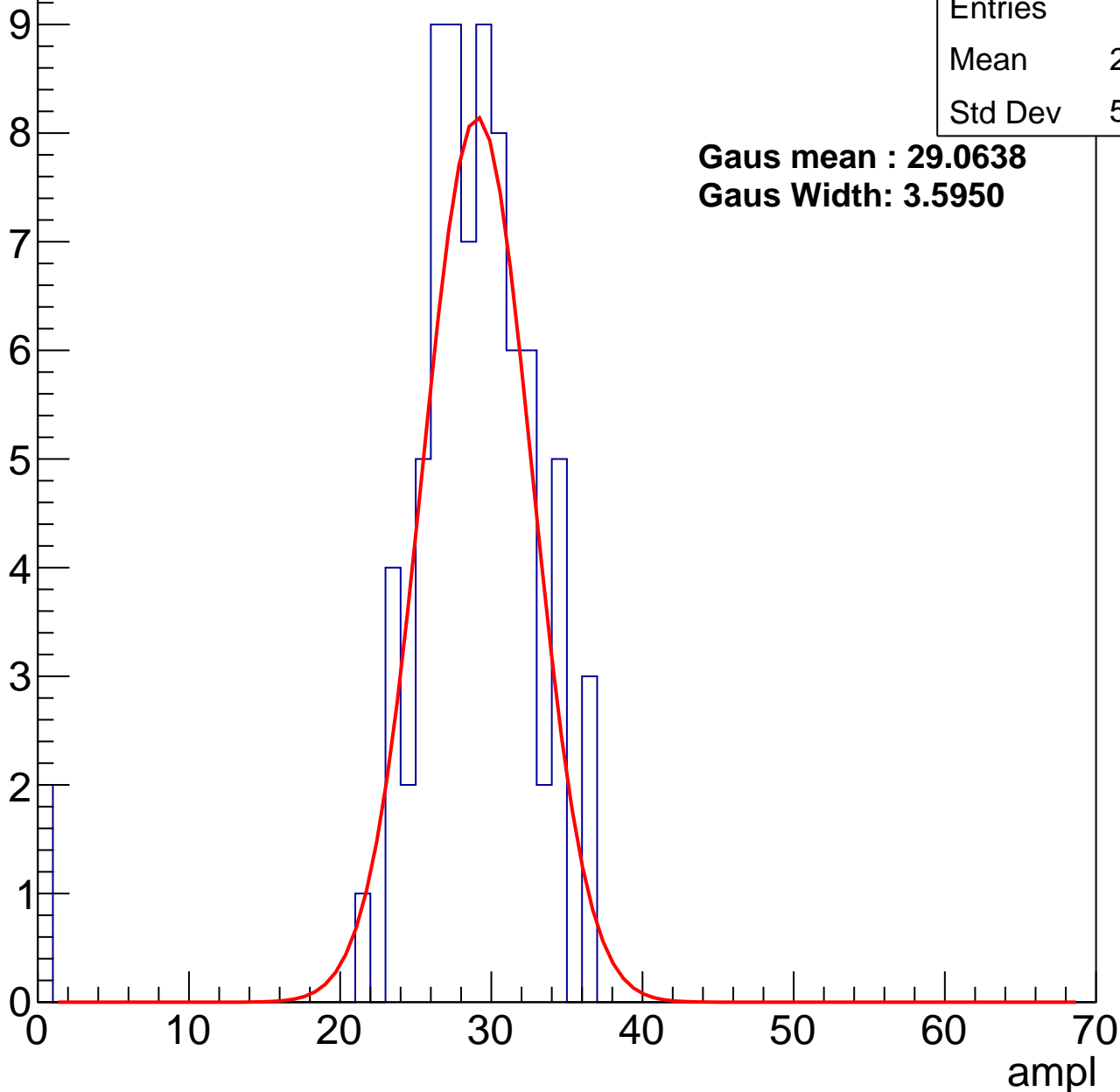
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	27.97
Std Dev	5.607

**Gaus mean : 29.0638**

**Gaus Width: 3.5950**



# B1L102S, U24-ch85, adc1

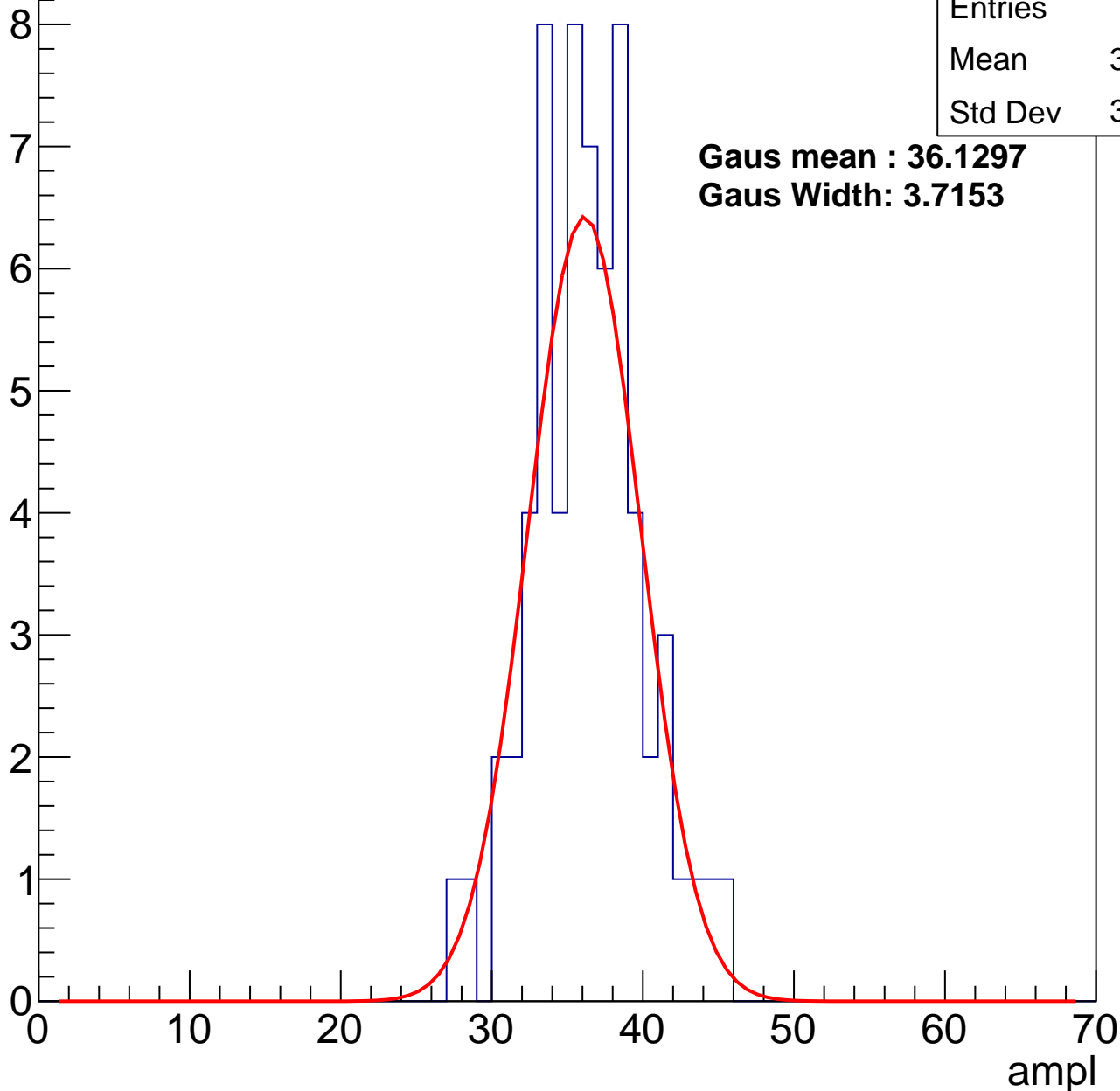
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.88
Std Dev	3.616

**Gaus mean : 36.1297**

**Gaus Width: 3.7153**



# B1L102S, U24-ch85, adc2

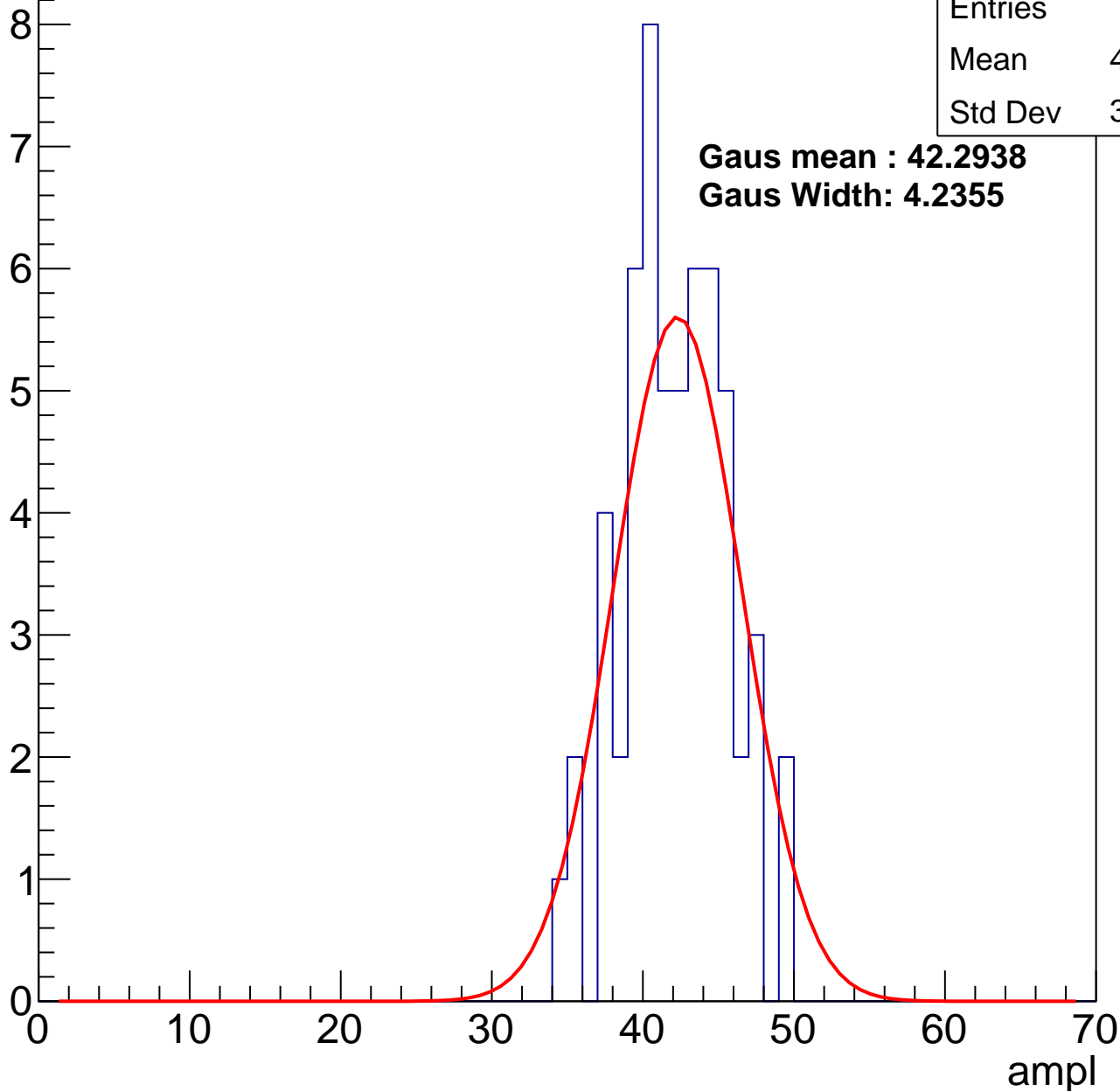
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.67
Std Dev	3.399

**Gaus mean : 42.2938**

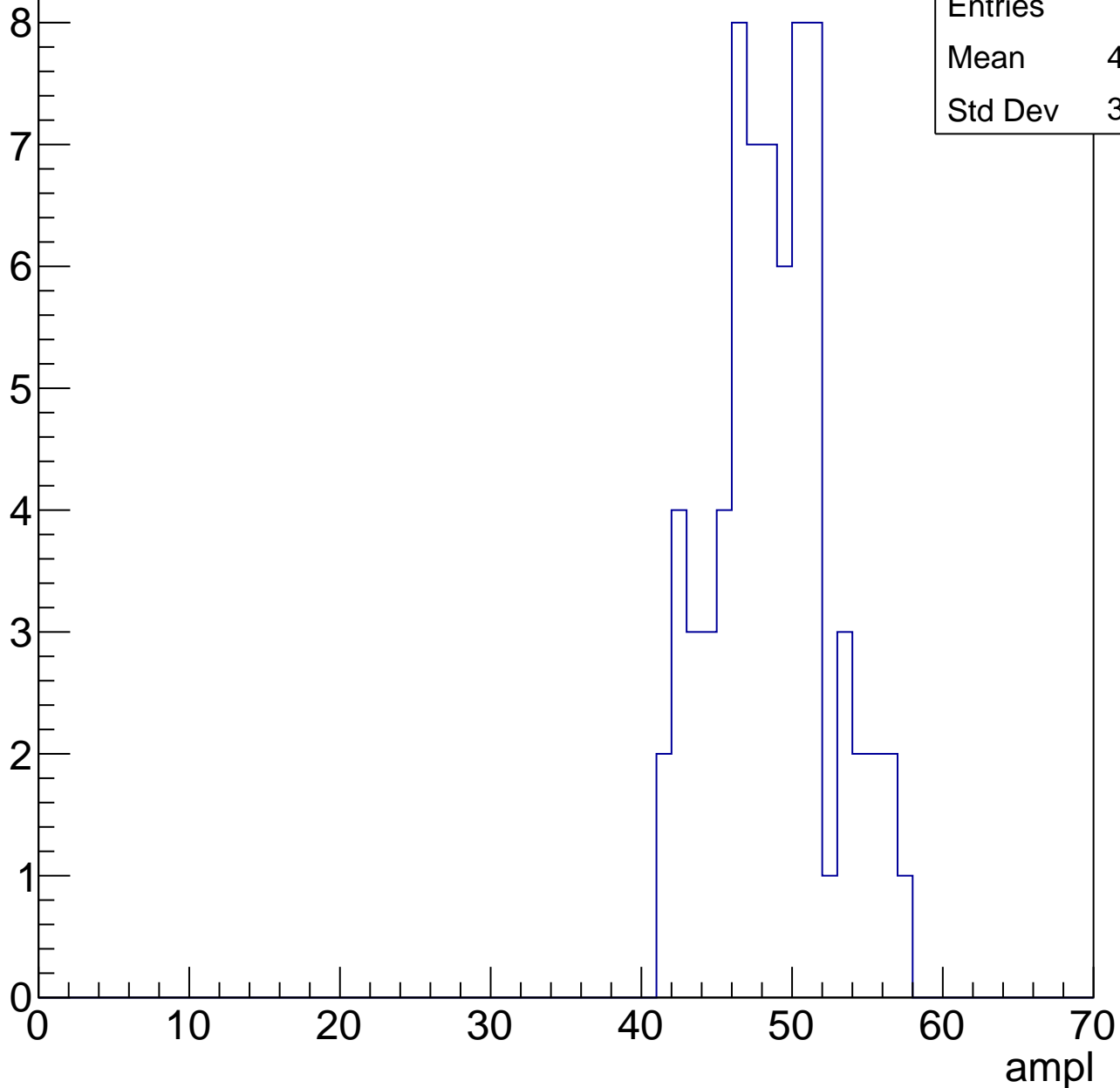
**Gaus Width: 4.2355**



# B1L102S, U24-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



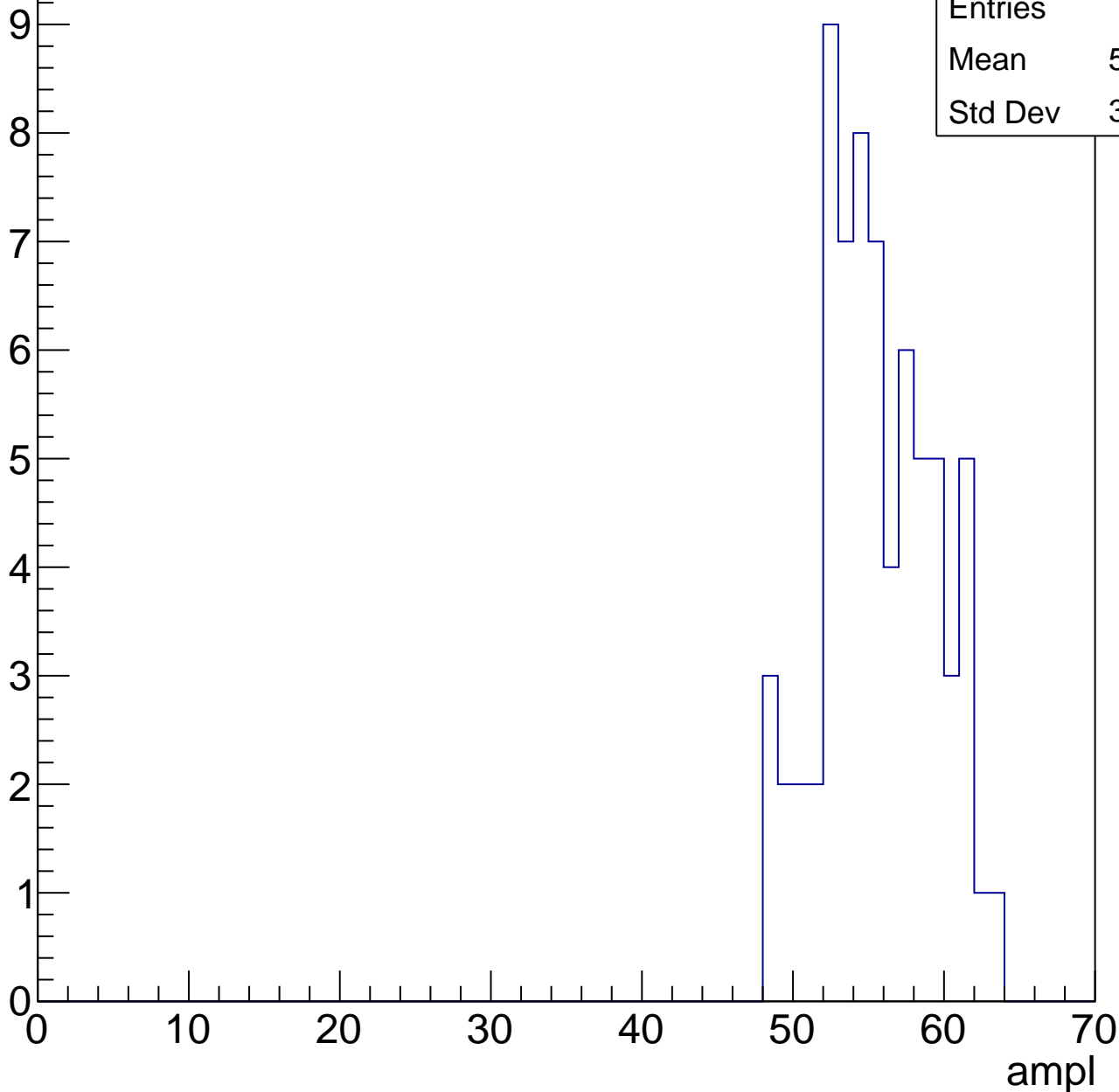
Entries	71
Mean	48.23
Std Dev	3.784

# B1L102S, U24-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.16
Std Dev	3.659

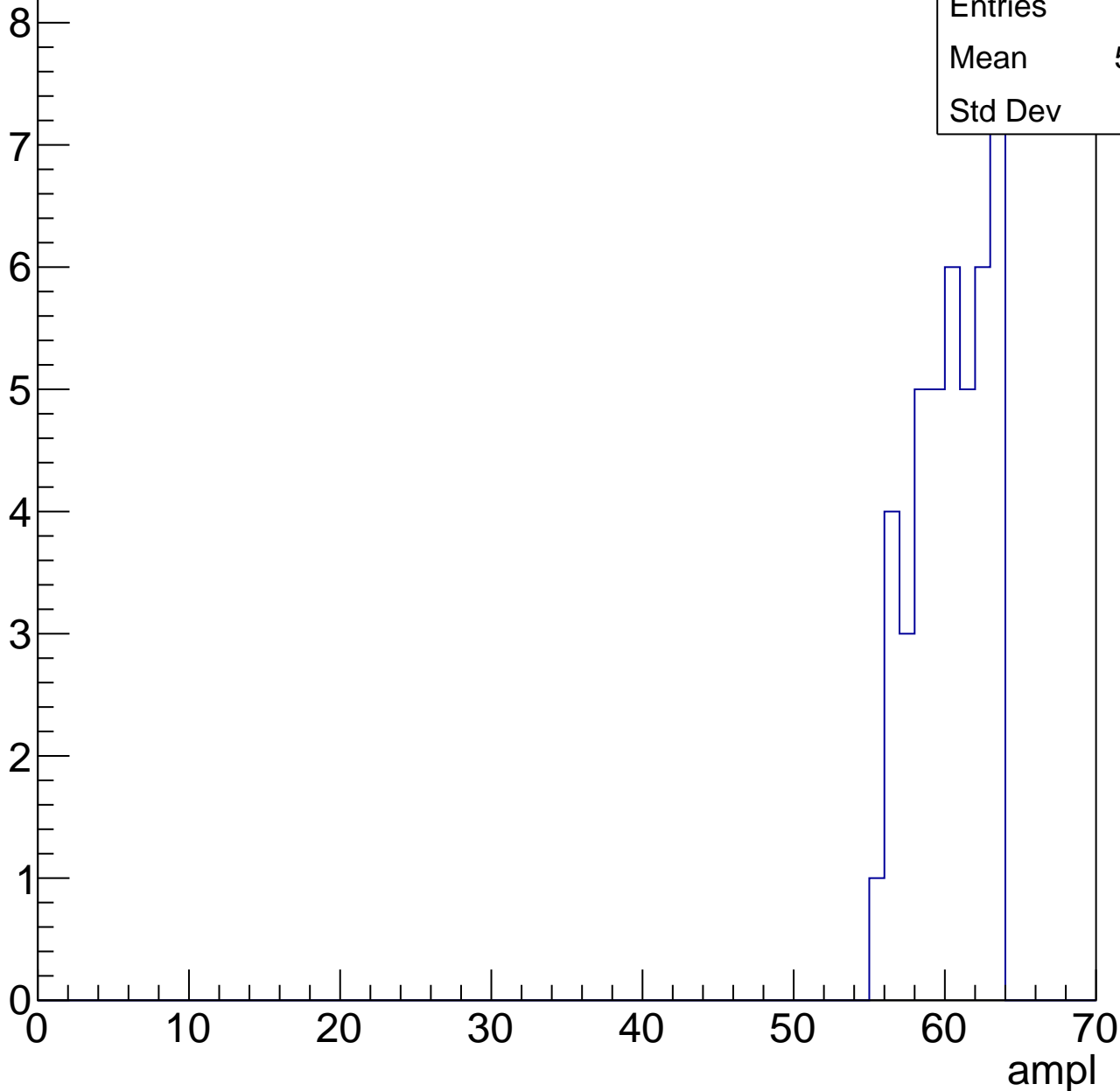


# B1L102S, U24-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

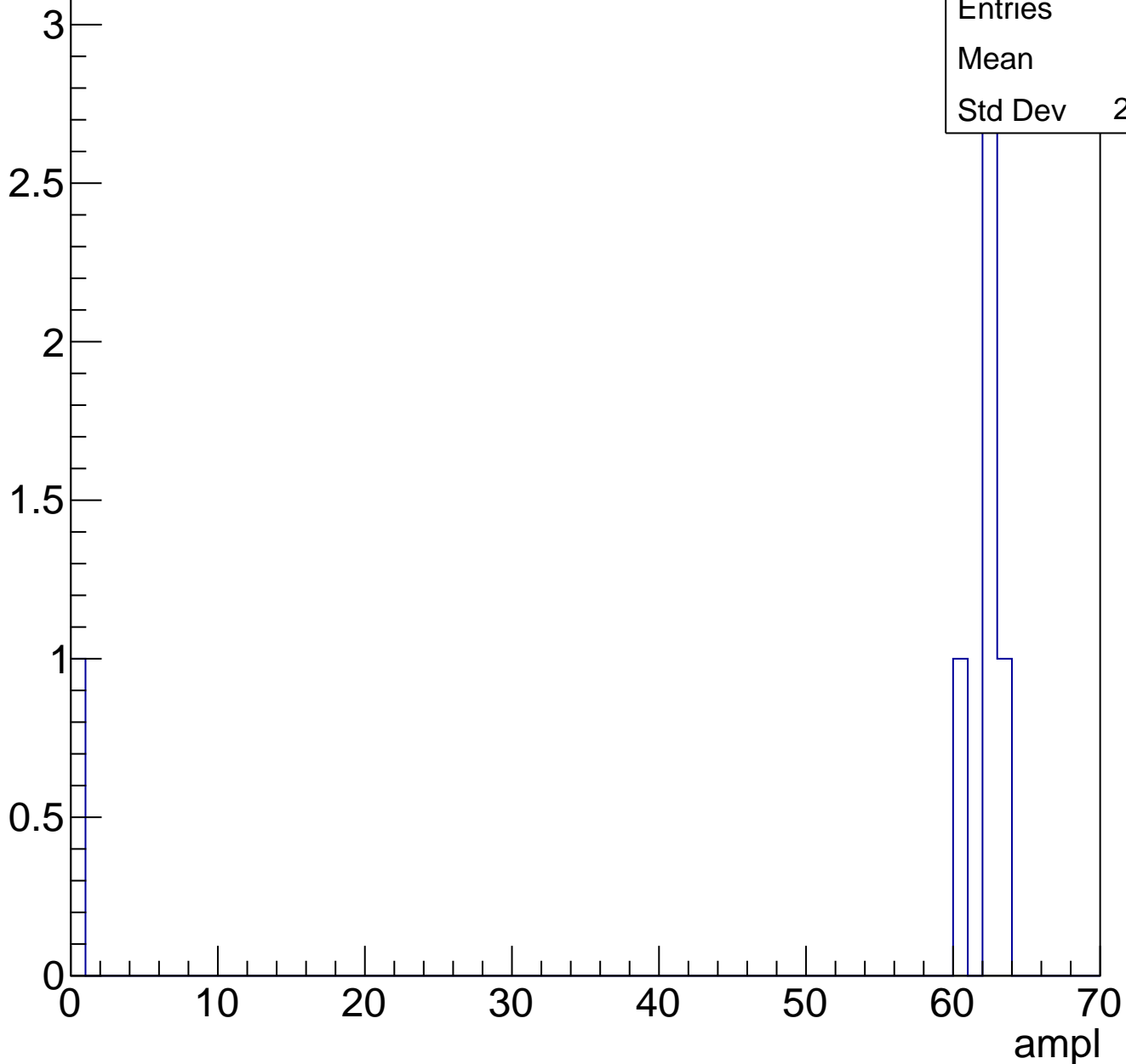
Entries	43
Mean	59.91
Std Dev	2.37



# B1L102S, U24-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch86, adc0

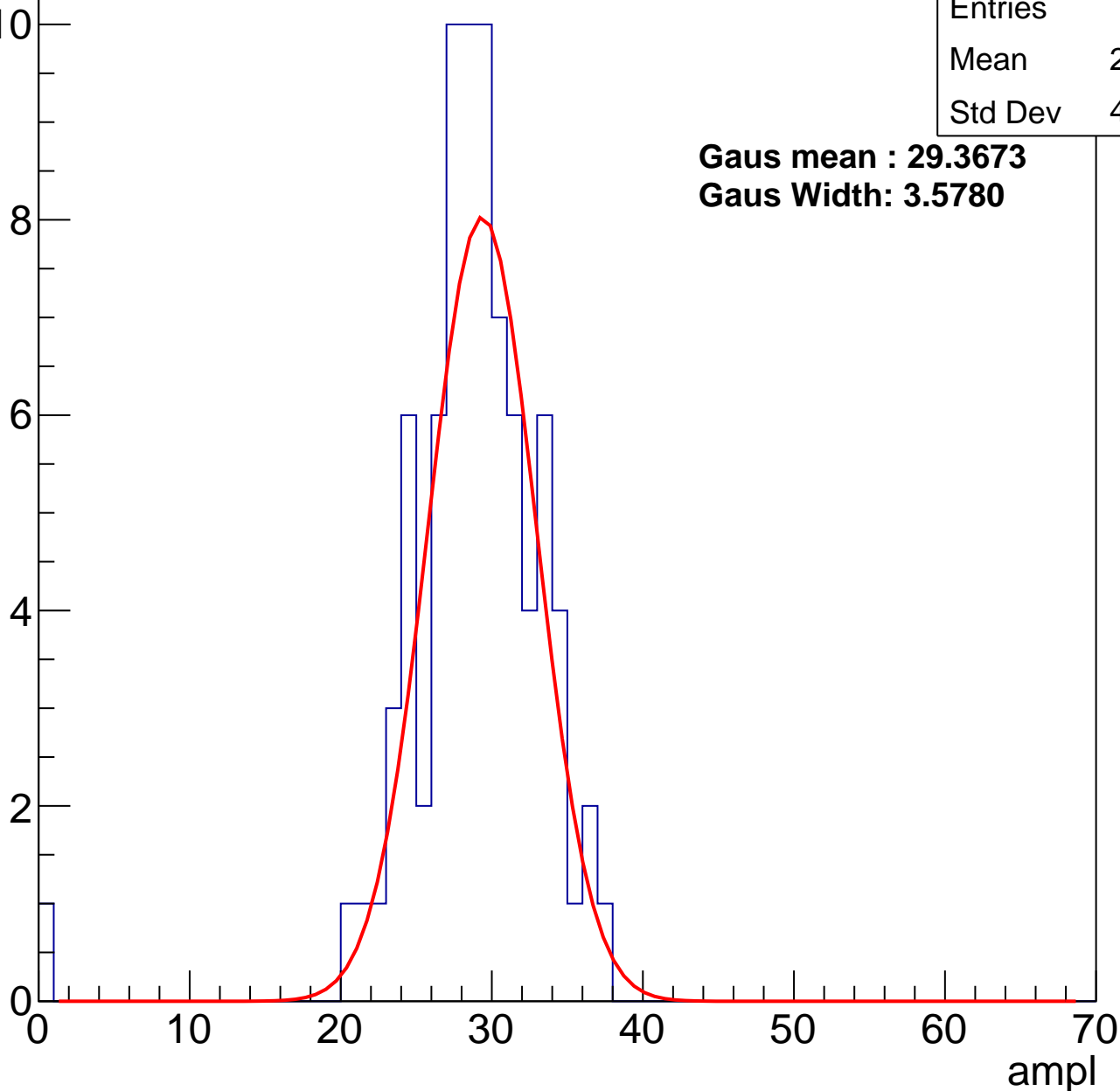
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.34
Std Dev	4.756

**Gaus mean : 29.3673**

**Gaus Width: 3.5780**



# B1L102S, U24-ch86, adc1

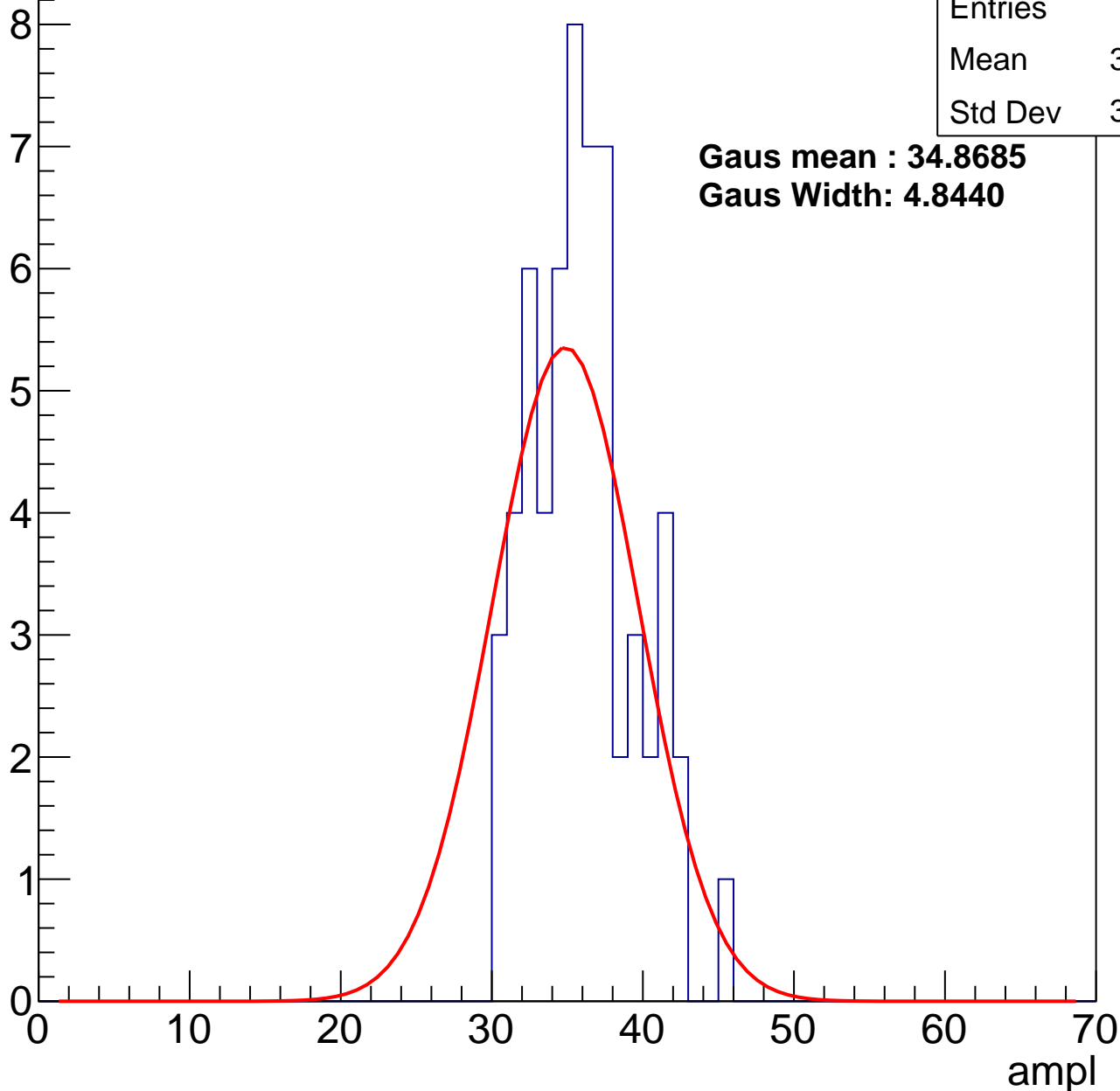
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.58
Std Dev	3.416

**Gaus mean : 34.8685**

**Gaus Width: 4.8440**



# B1L102S, U24-ch86, adc2

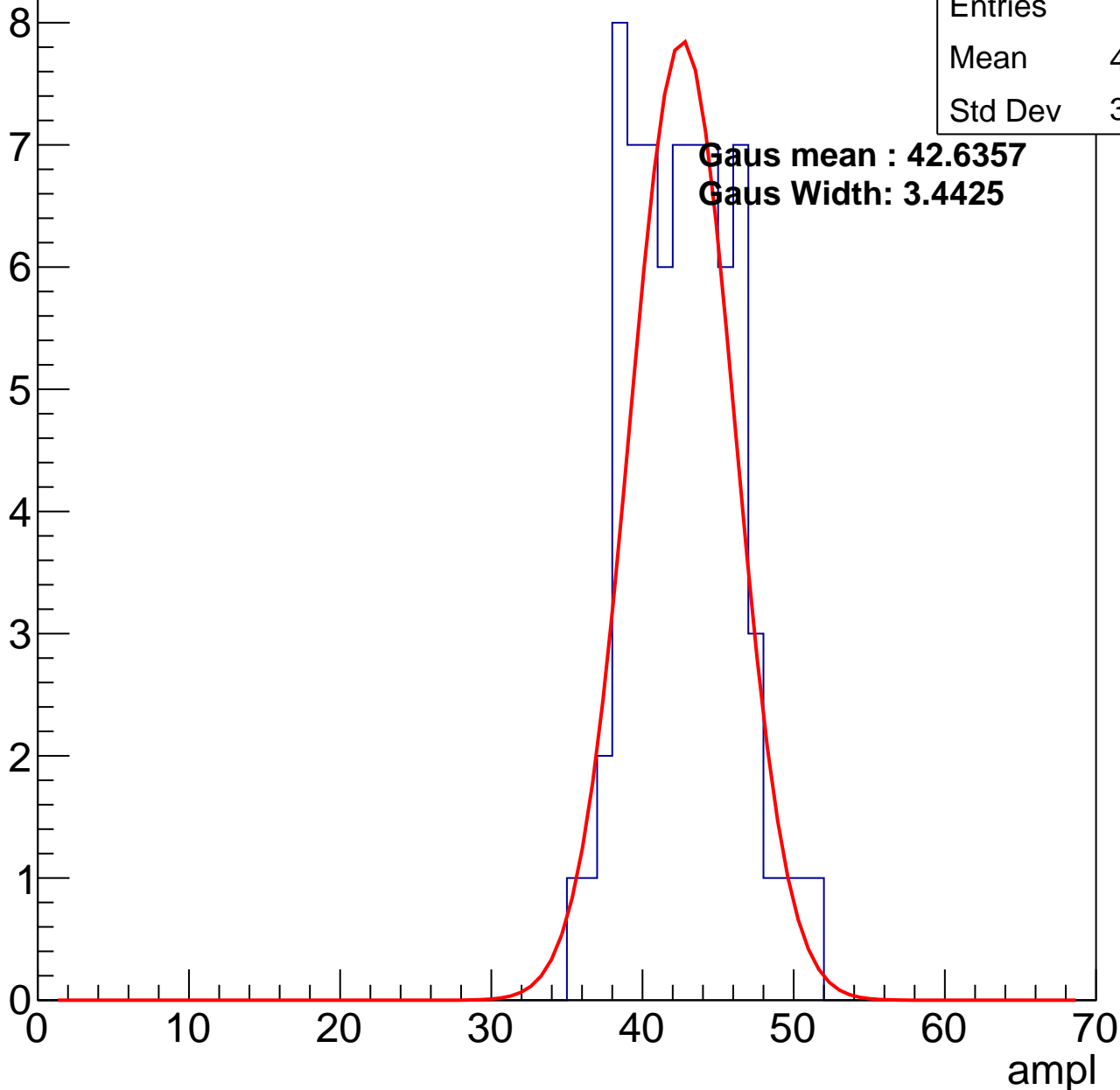
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.22
Std Dev	3.437

**Gaus mean : 42.6357**

**Gaus Width: 3.4425**

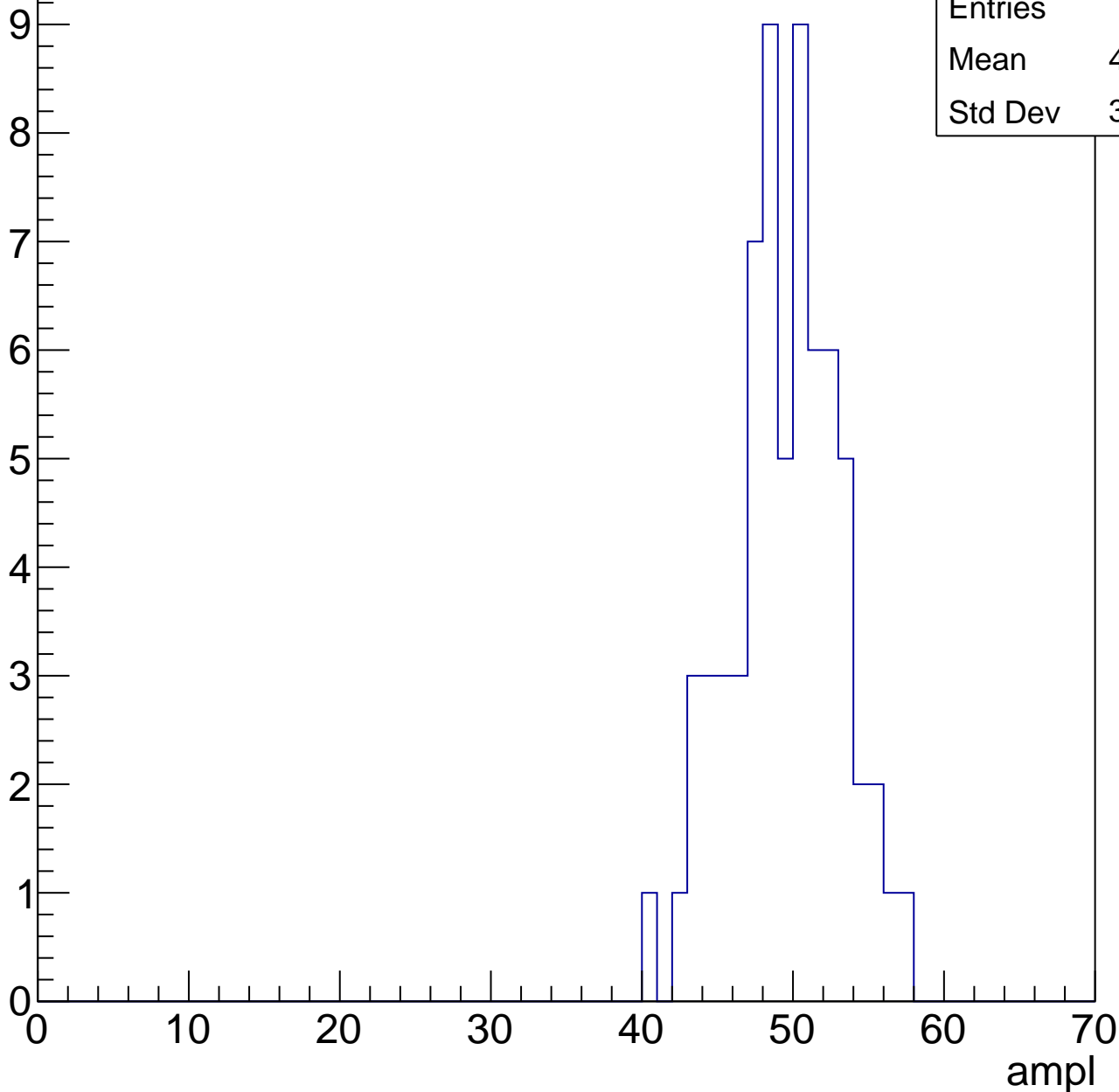


# B1L102S, U24-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.04
Std Dev	3.513

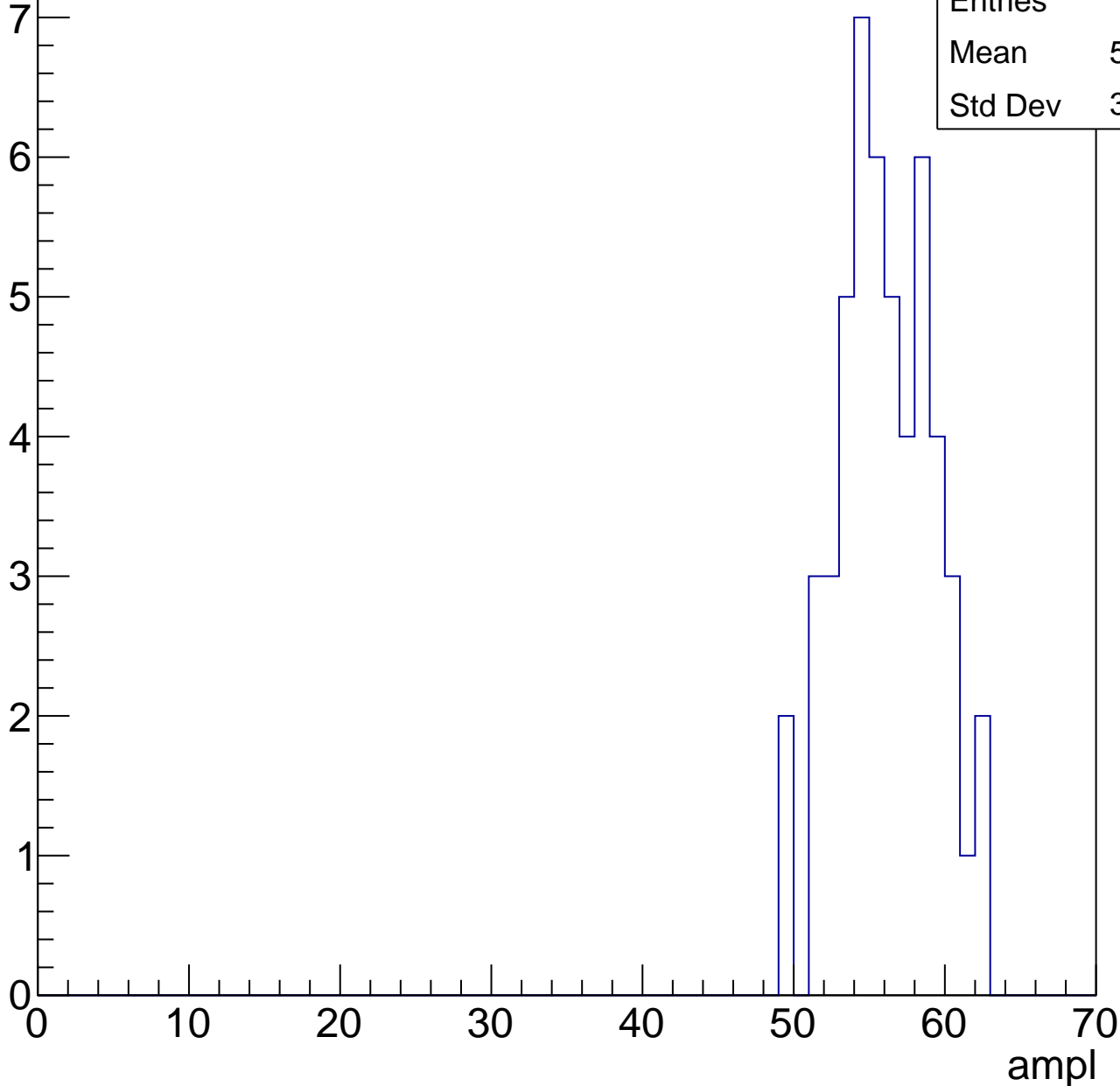


# B1L102S, U24-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.63
Std Dev	3.137

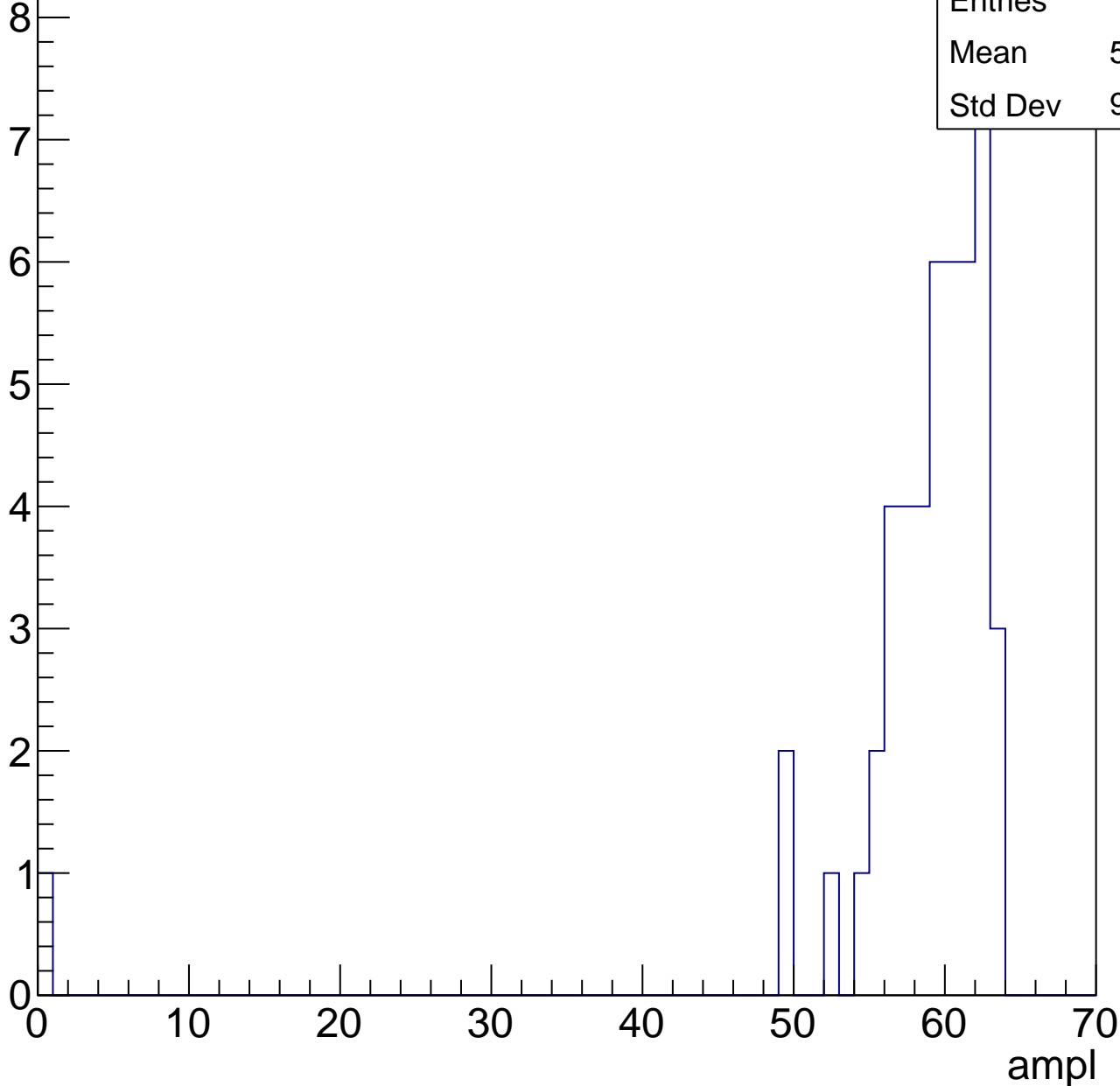


# B1L102S, U24-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.56
Std Dev	9.007

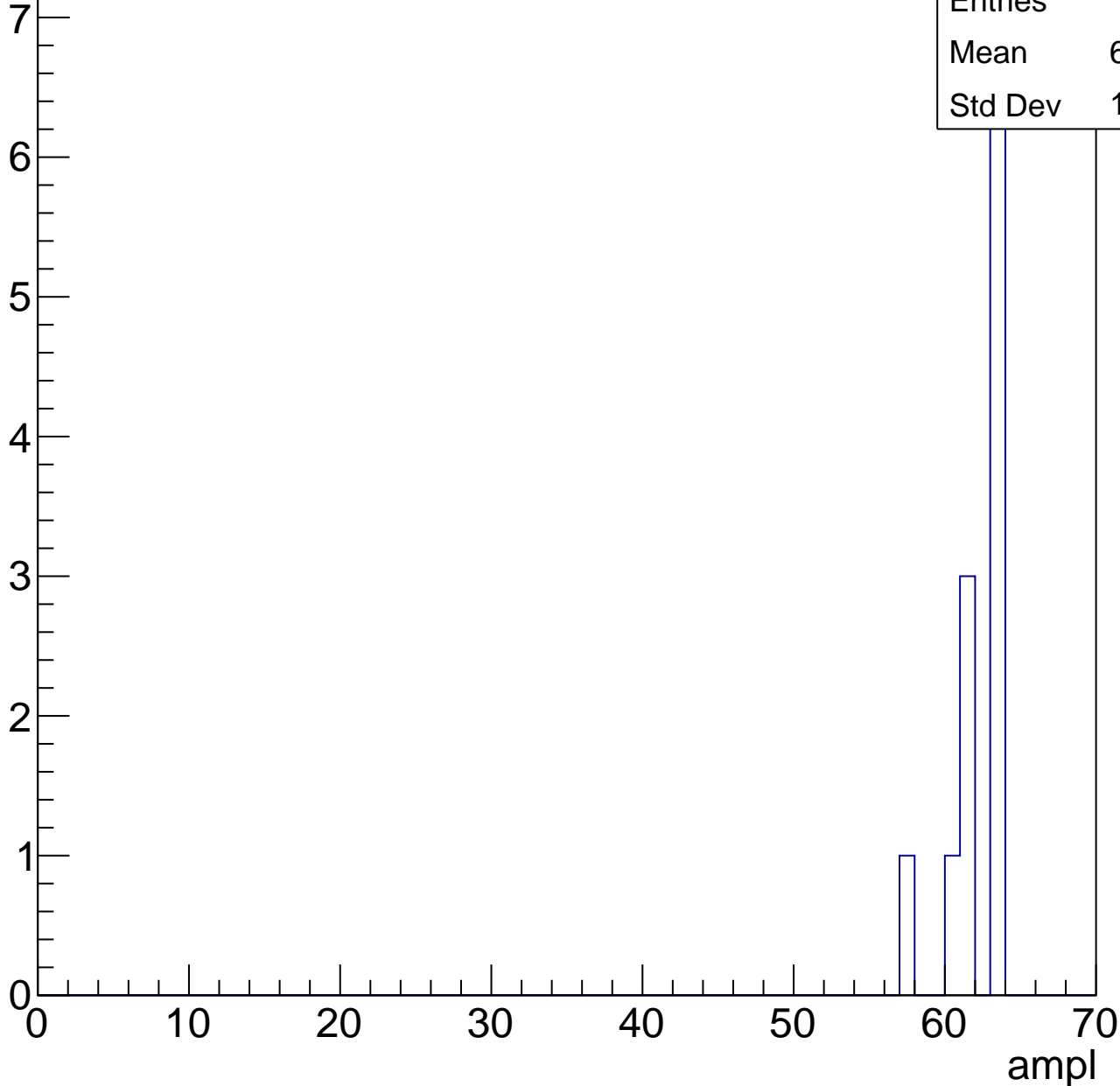


# B1L102S, U24-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.75
Std Dev	1.785





# B1L102S, U24-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U24-ch87, adc0

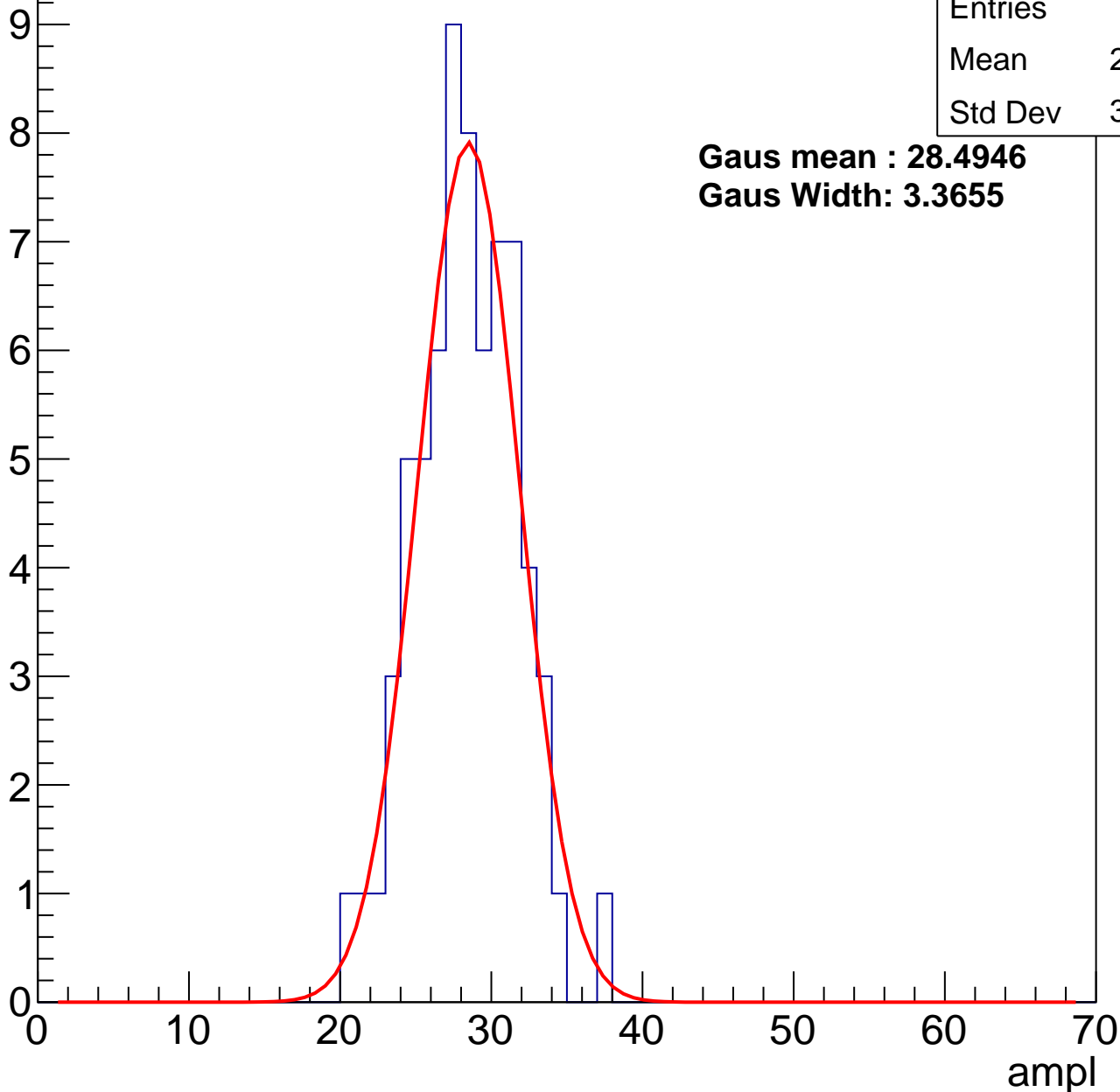
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.93
Std Dev	3.287

**Gaus mean : 28.4946**

**Gaus Width: 3.3655**



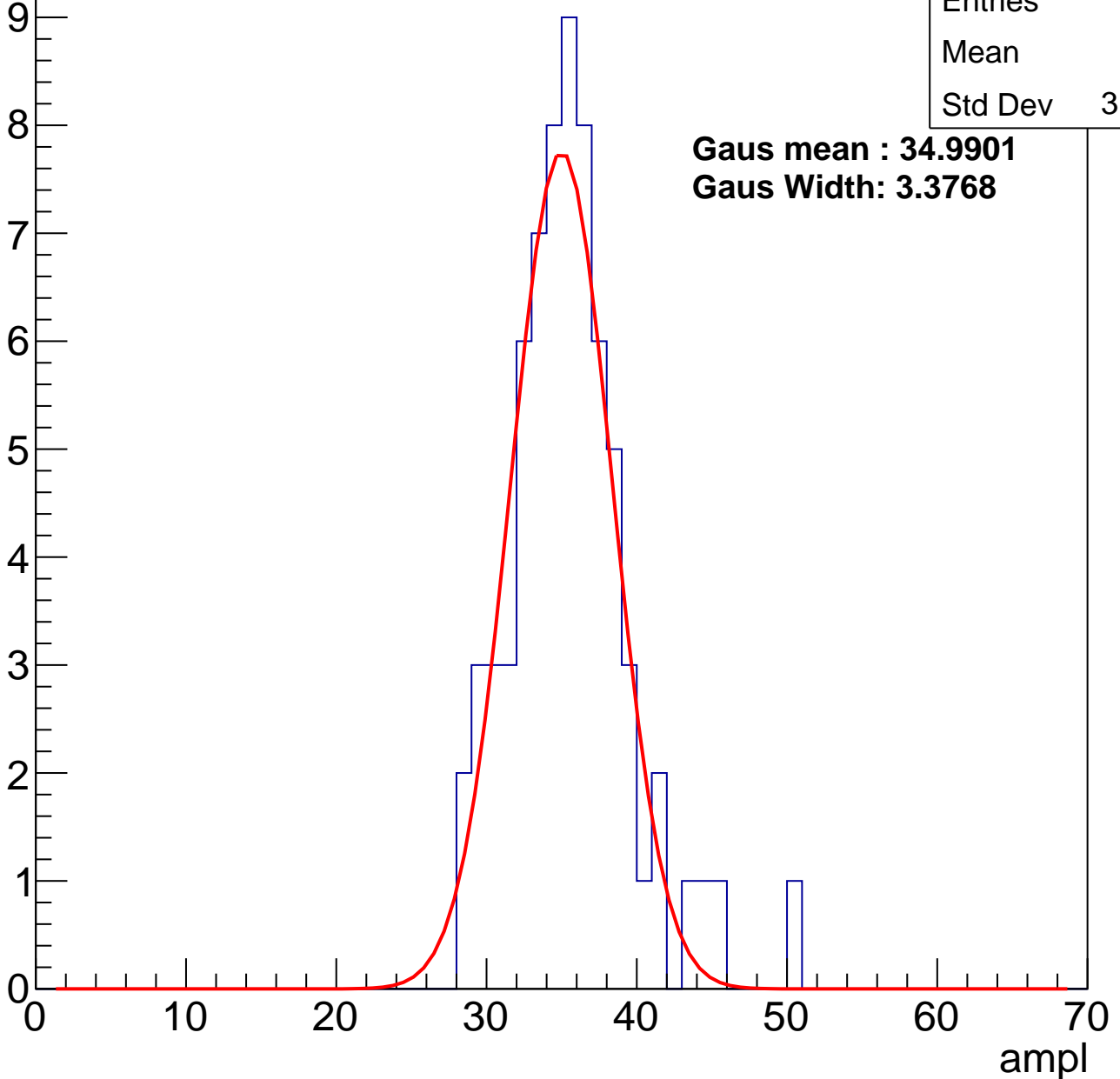
# B1L102S, U24-ch87, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.1
Std Dev	3.983

**Gaus mean : 34.9901**  
**Gaus Width: 3.3768**



# B1L102S, U24-ch87, adc2

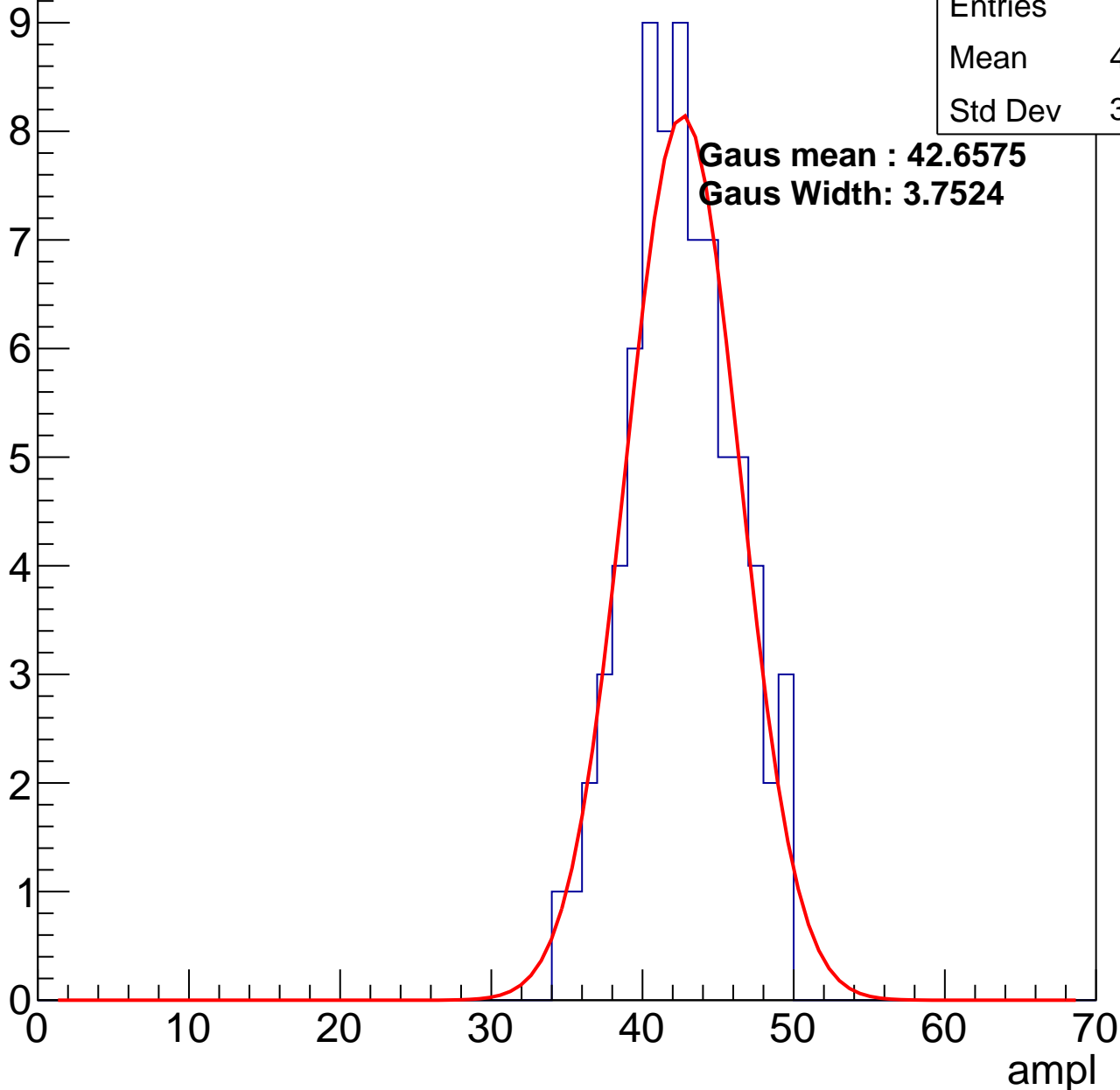
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.09
Std Dev	3.442

**Gaus mean : 42.6575**

**Gaus Width: 3.7524**

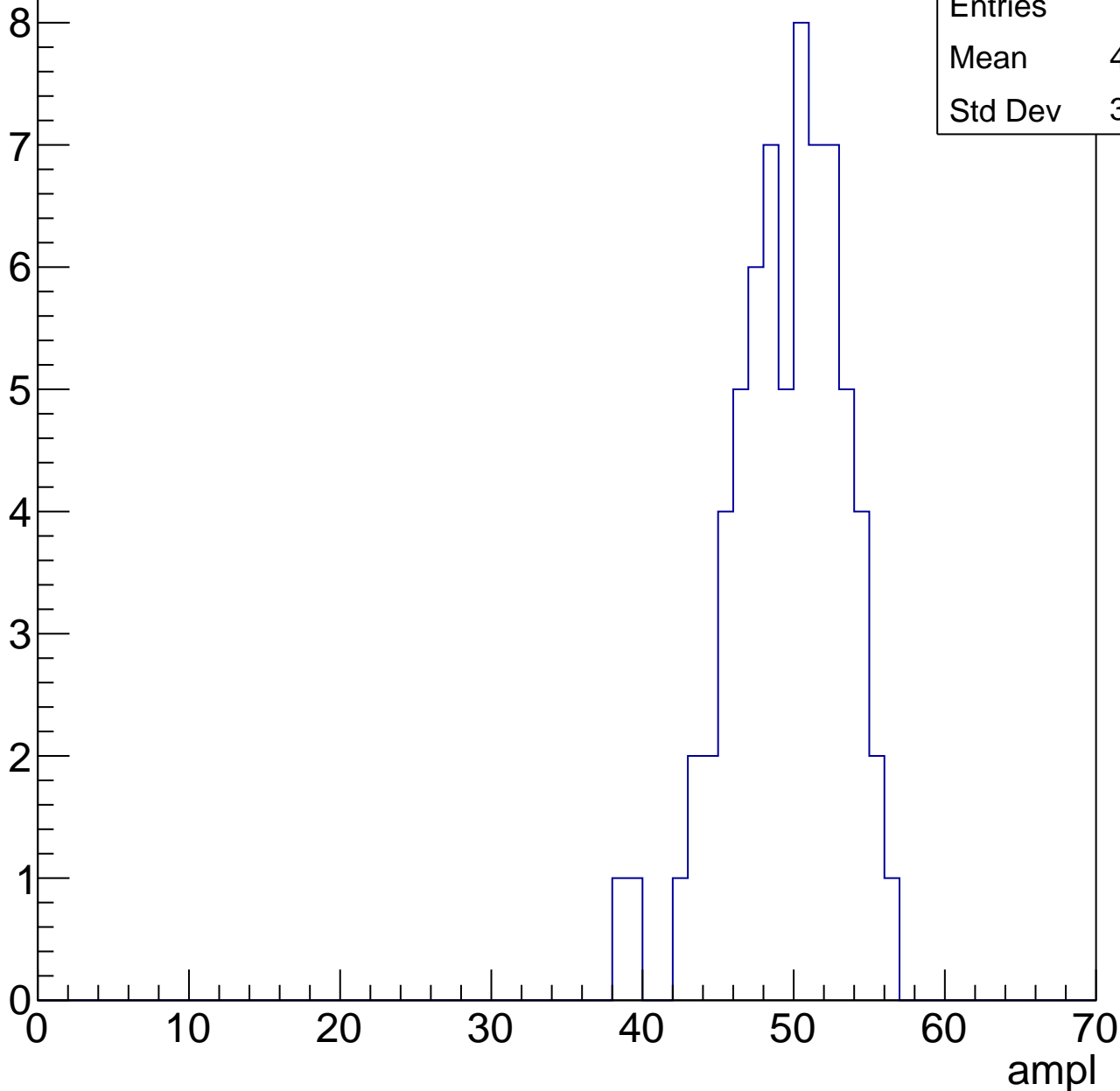


# B1L102S, U24-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.03
Std Dev	3.702

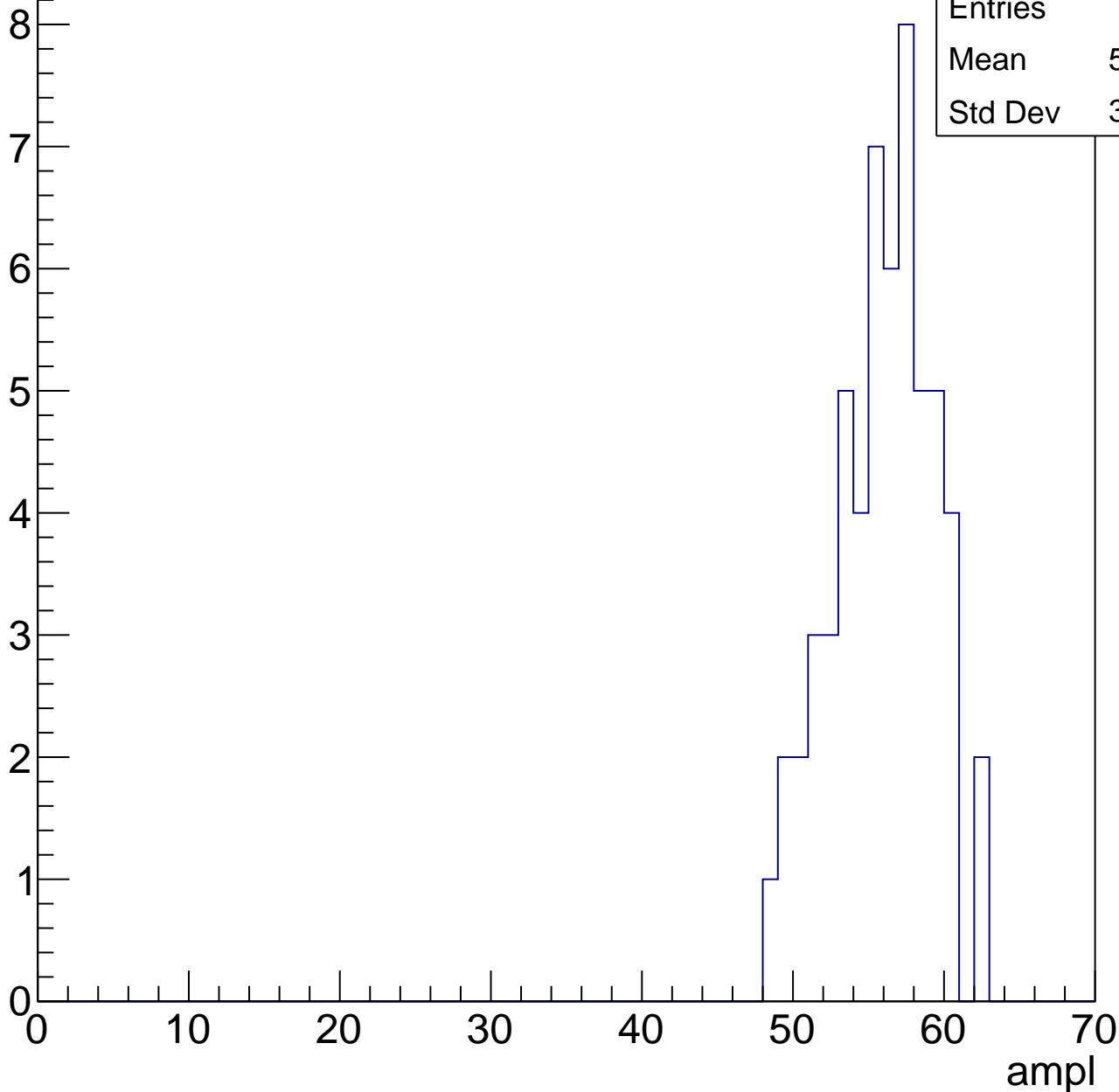


# B1L102S, U24-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

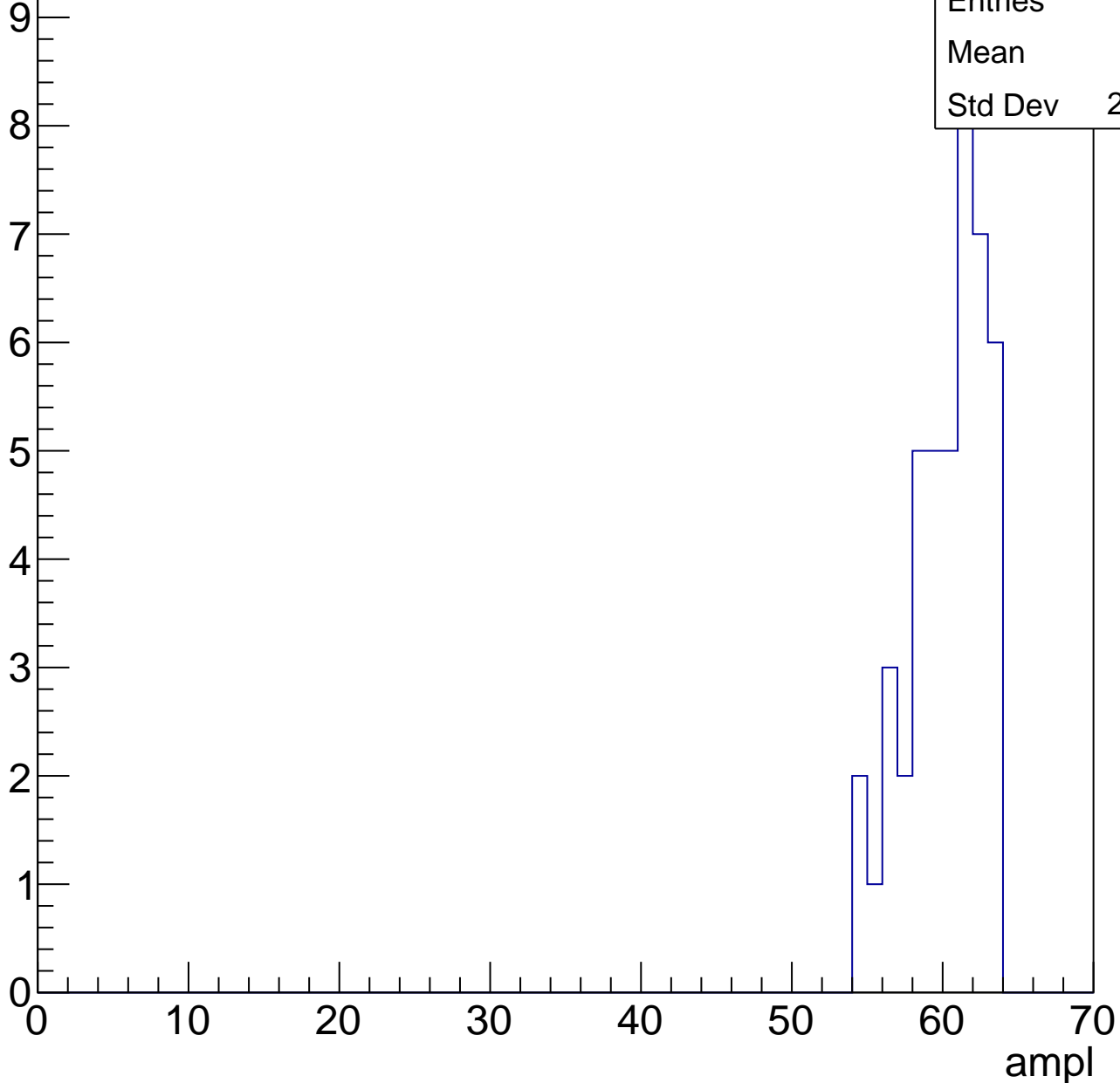
Entries	57
Mean	55.47
Std Dev	3.293



# B1L102S, U24-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

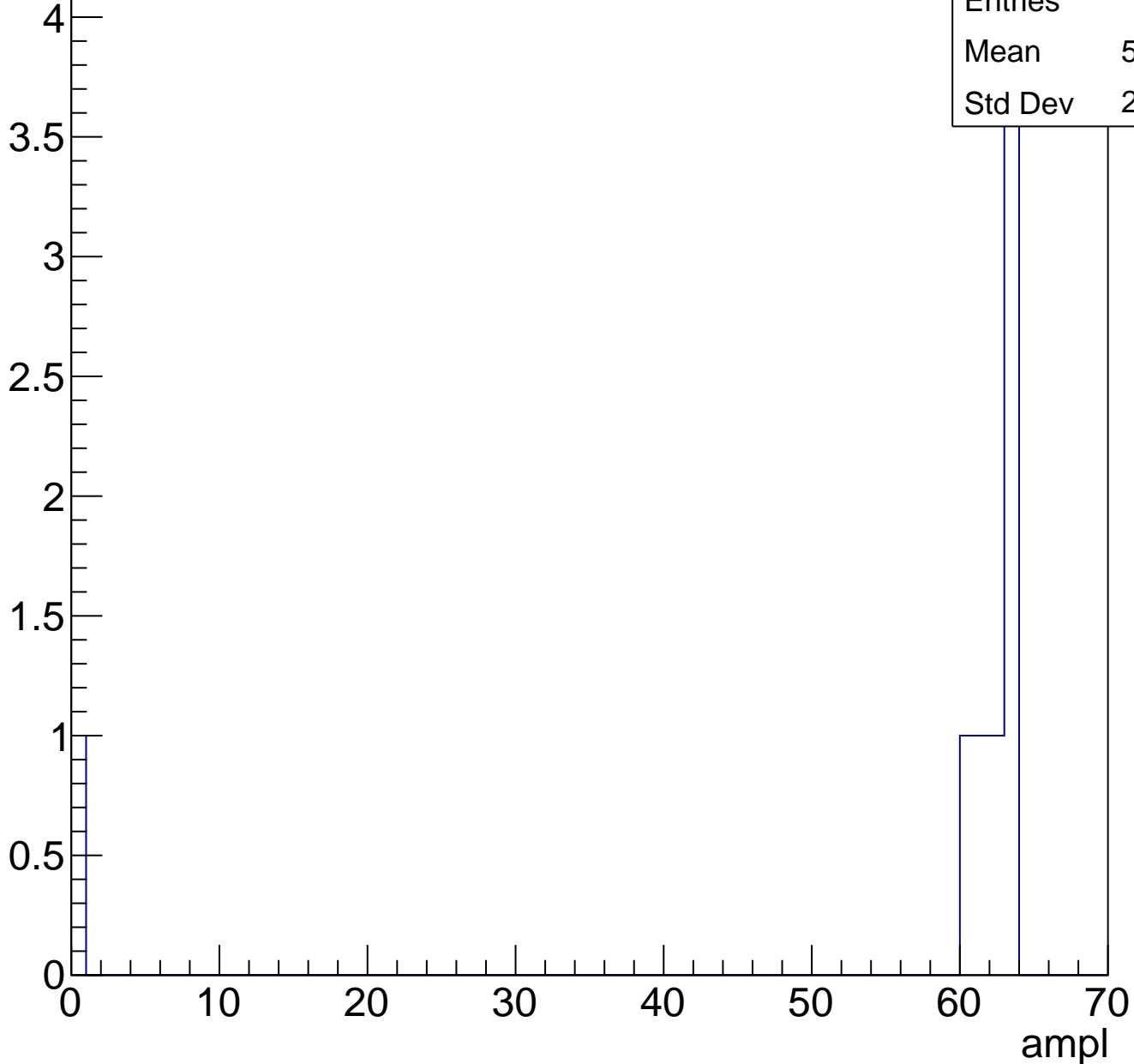
Entry



# B1L102S, U24-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

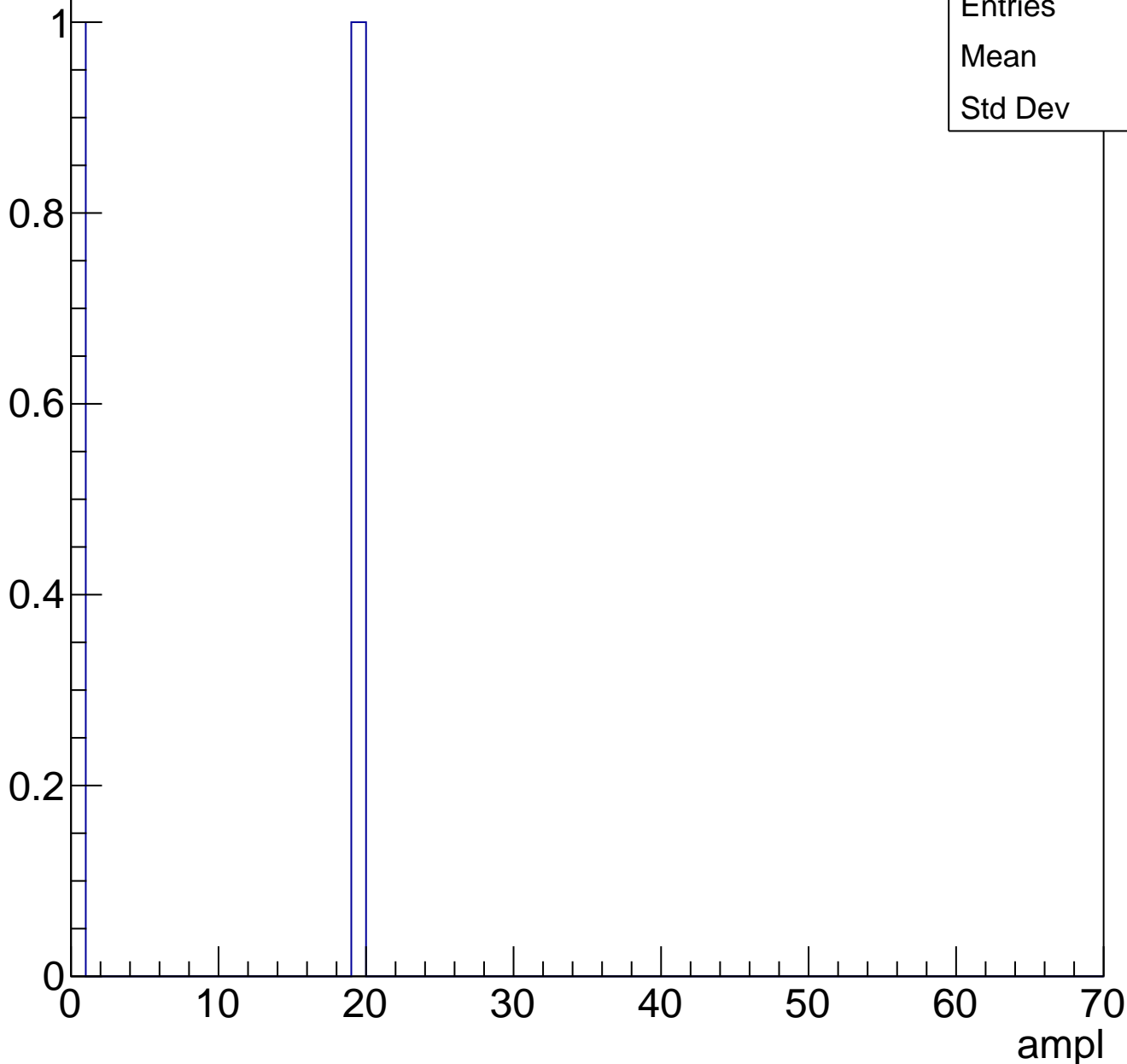




# B1L102S, U24-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L102S, U24-ch88, adc0

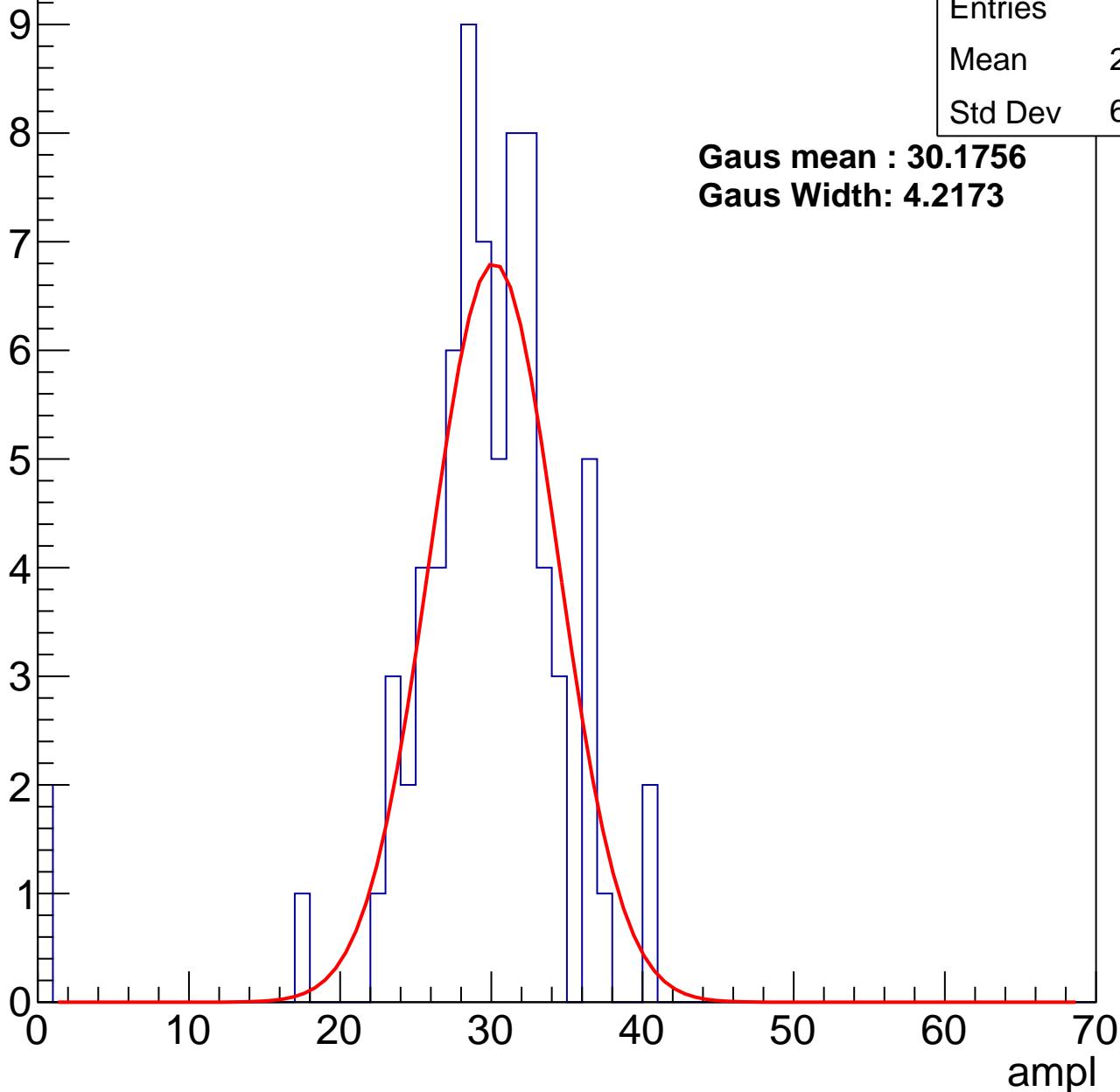
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.83
Std Dev	6.285

**Gaus mean : 30.1756**

**Gaus Width: 4.2173**



# B1L102S, U24-ch88, adc1

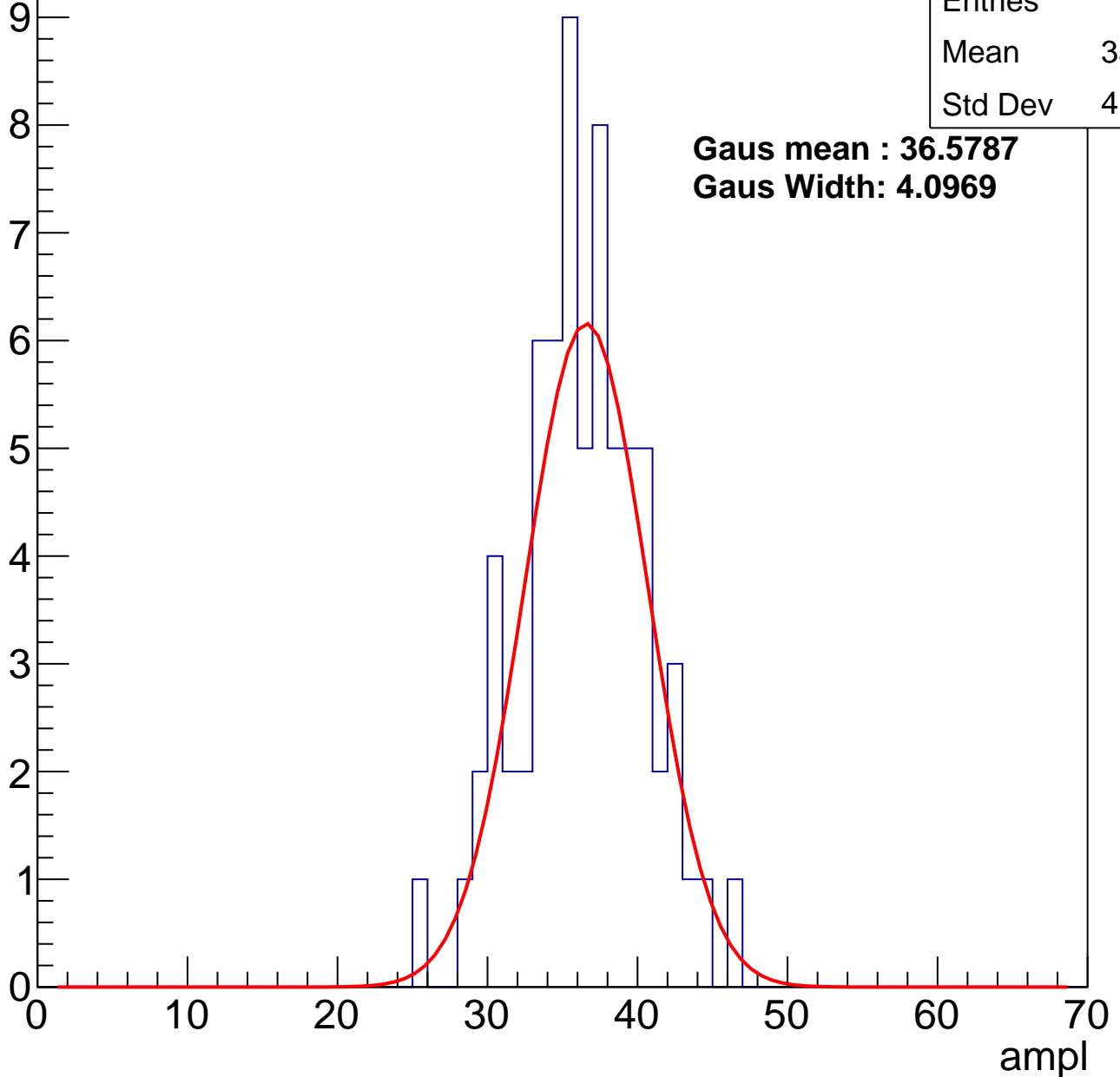
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.88
Std Dev	4.027

**Gaus mean : 36.5787**

**Gaus Width: 4.0969**



# B1L102S, U24-ch88, adc2

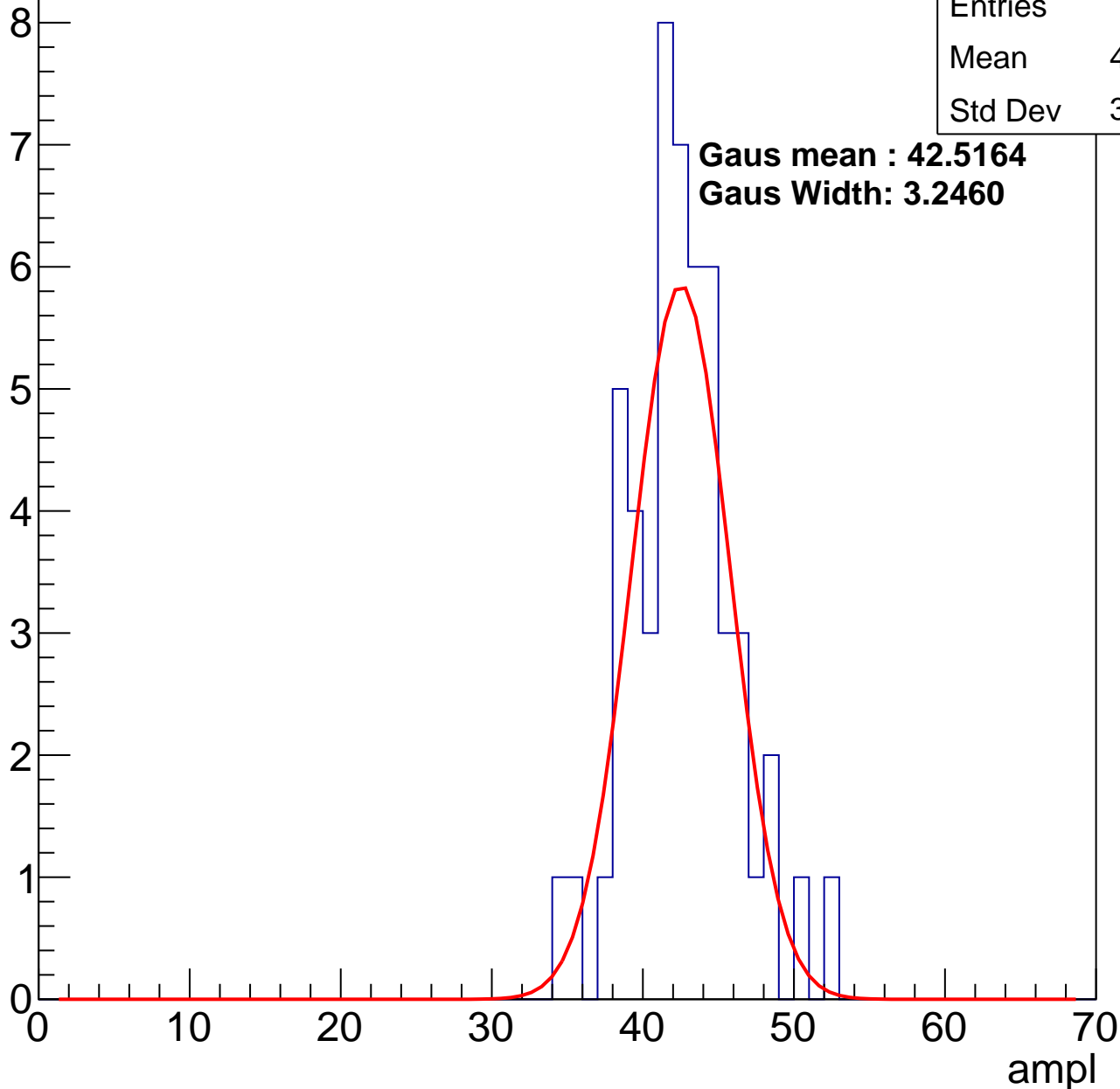
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.15
Std Dev	3.472

**Gaus mean : 42.5164**

**Gaus Width: 3.2460**

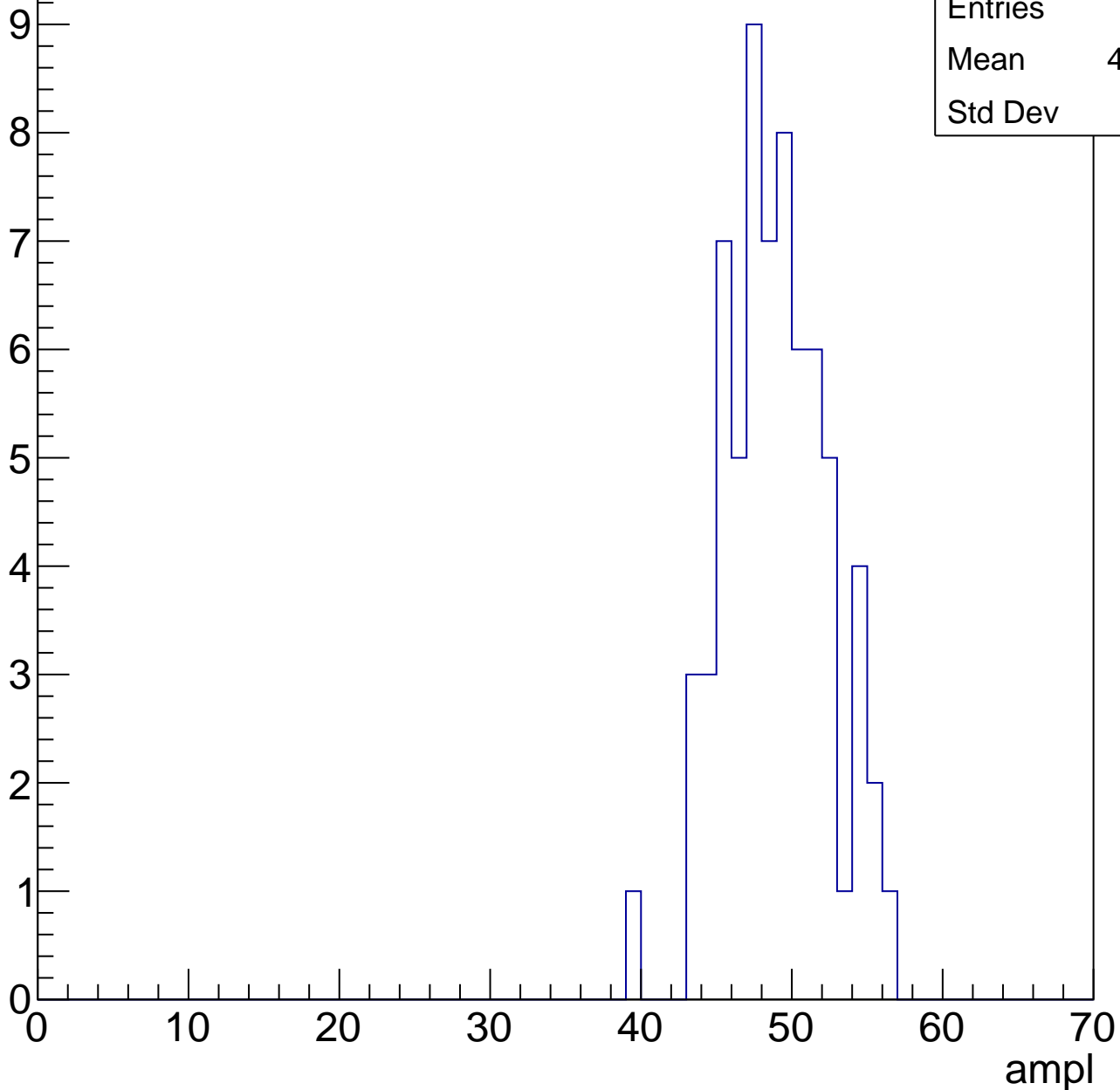


# B1L102S, U24-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.49
Std Dev	3.38

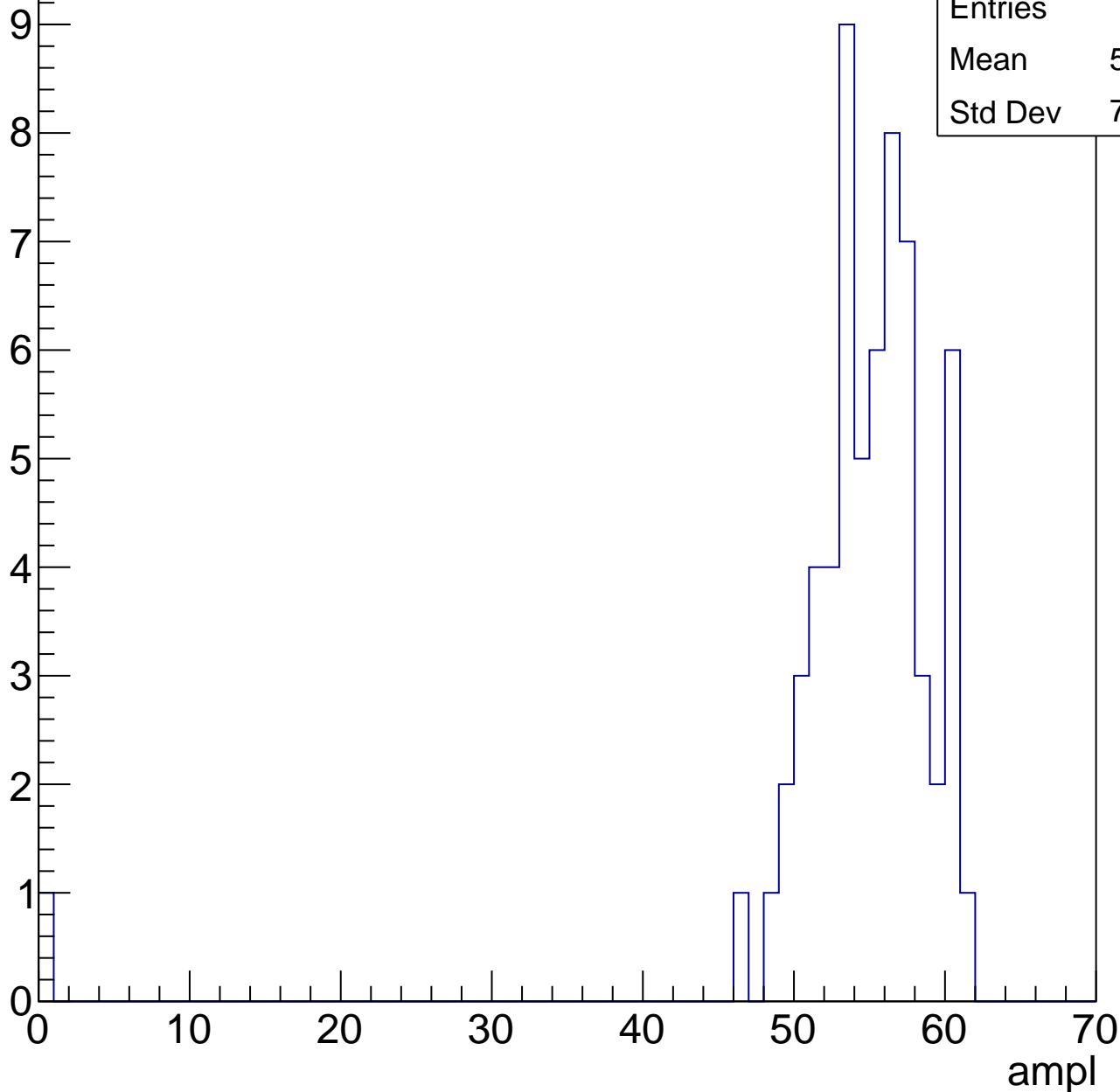


# B1L102S, U24-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.83
Std Dev	7.598

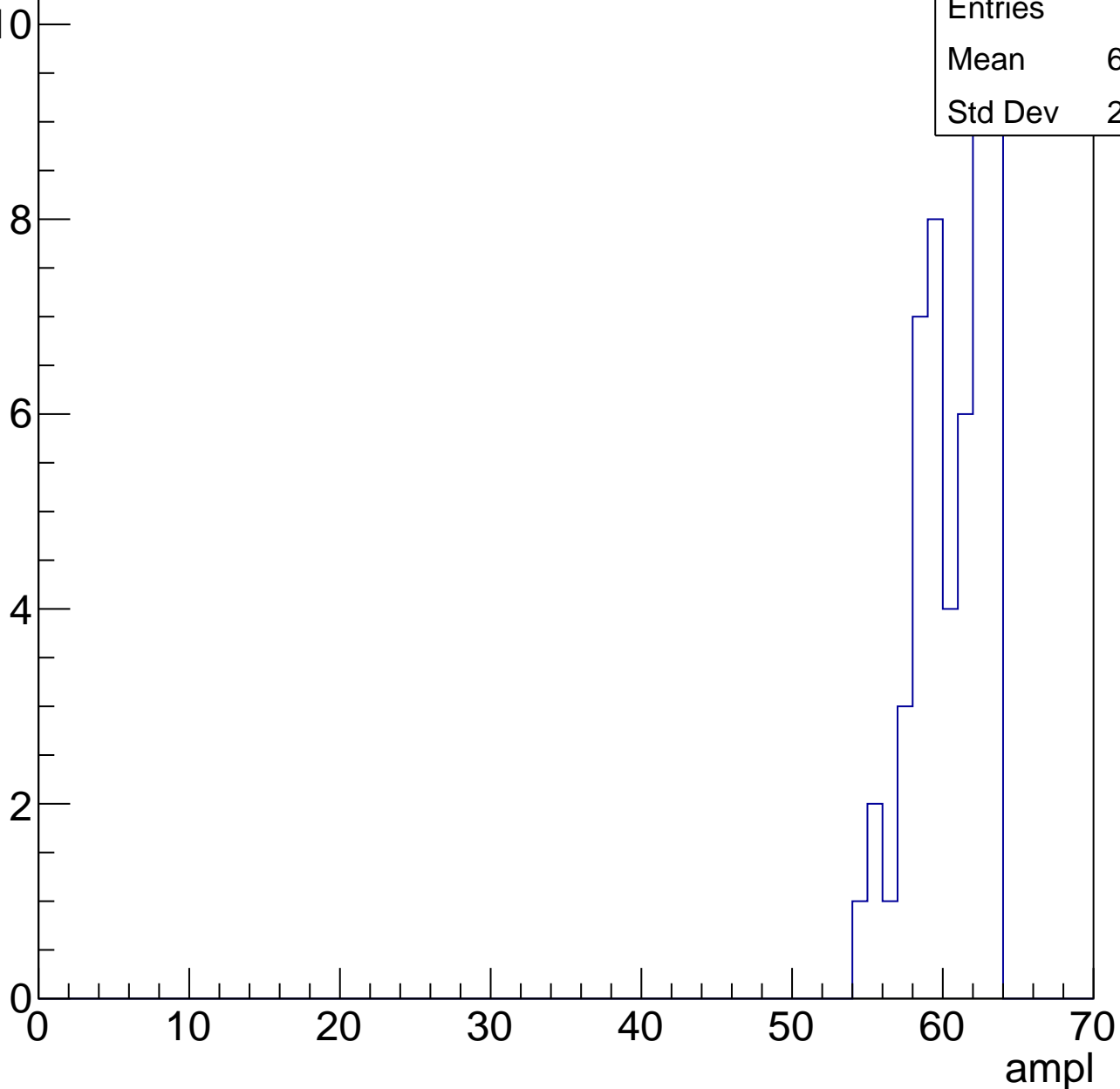


# B1L102S, U24-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

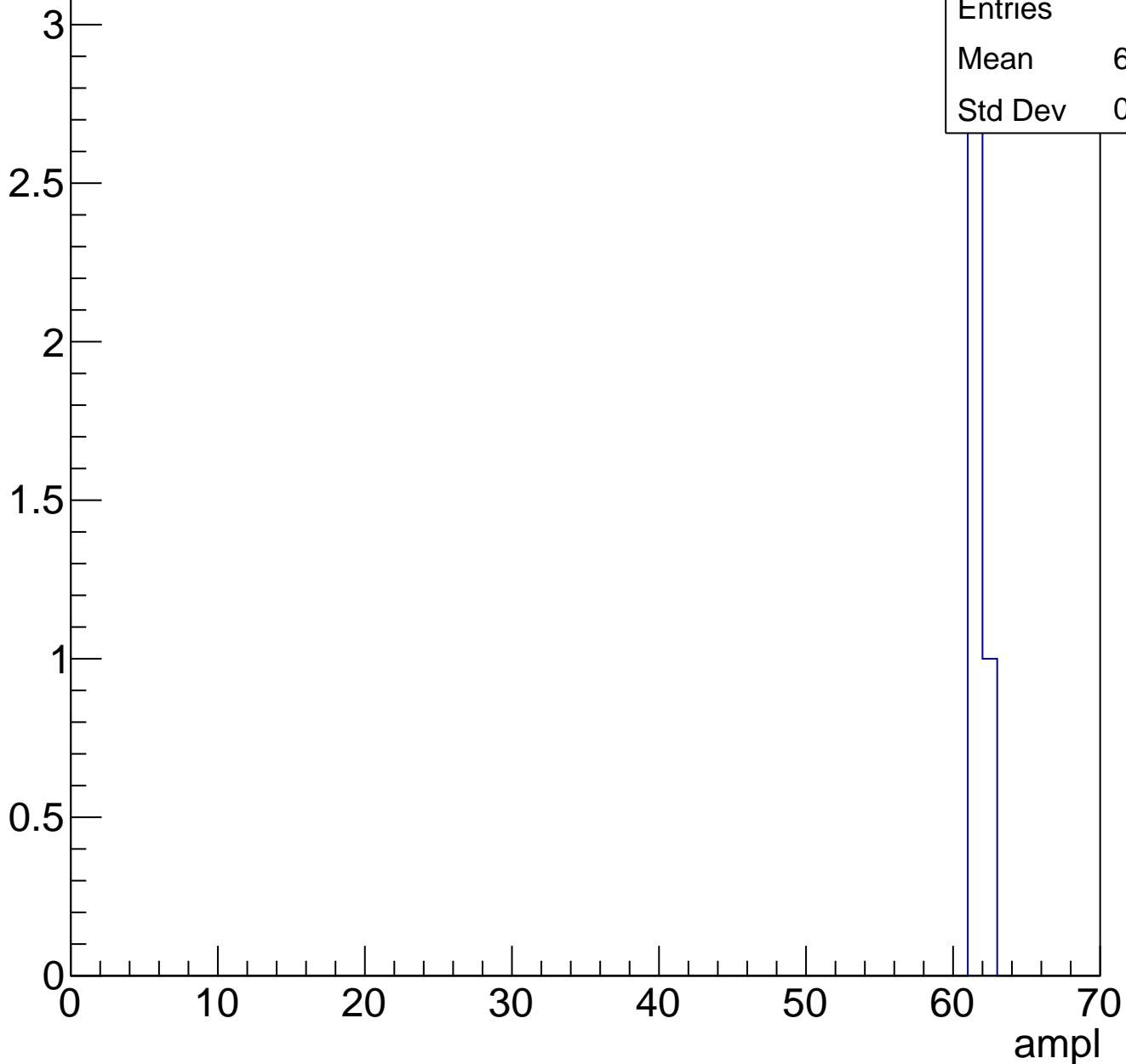
Entries	51
Mean	60.06
Std Dev	2.412



# B1L102S, U24-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch89, adc0

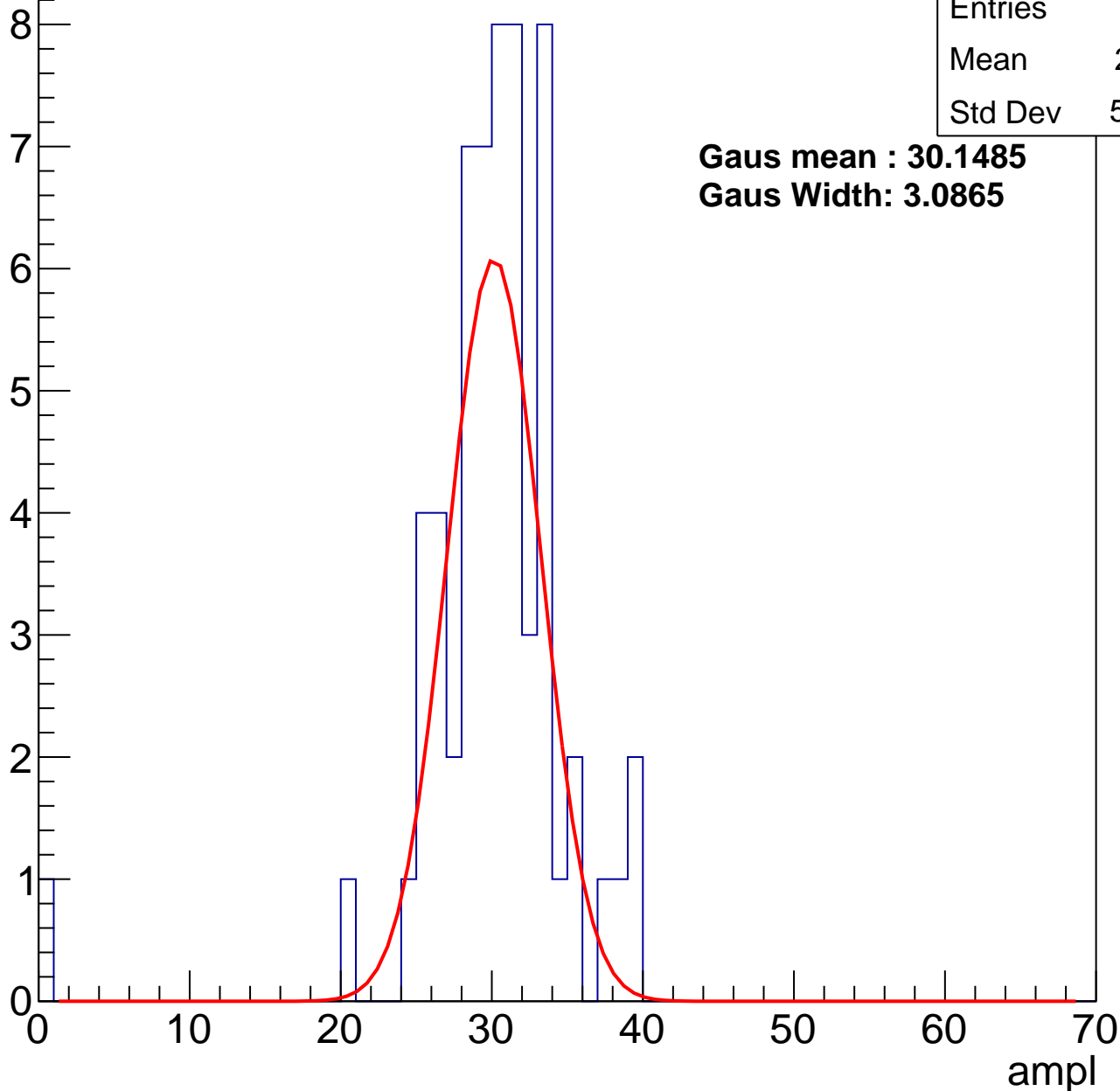
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.61
Std Dev	5.243

**Gaus mean : 30.1485**

**Gaus Width: 3.0865**



# B1L102S, U24-ch89, adc1

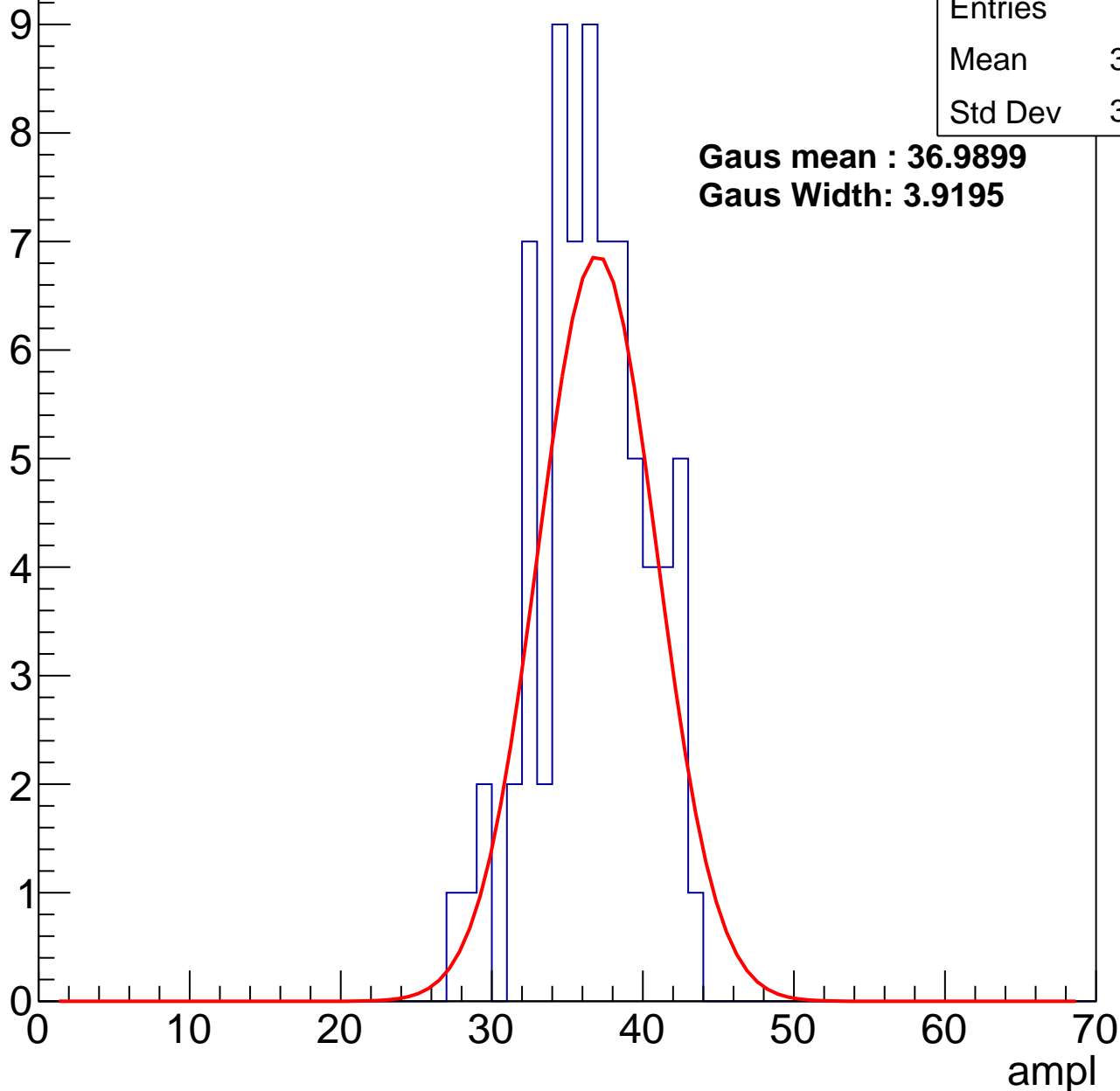
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.12
Std Dev	3.584

**Gaus mean : 36.9899**

**Gaus Width: 3.9195**



# B1L102S, U24-ch89, adc2

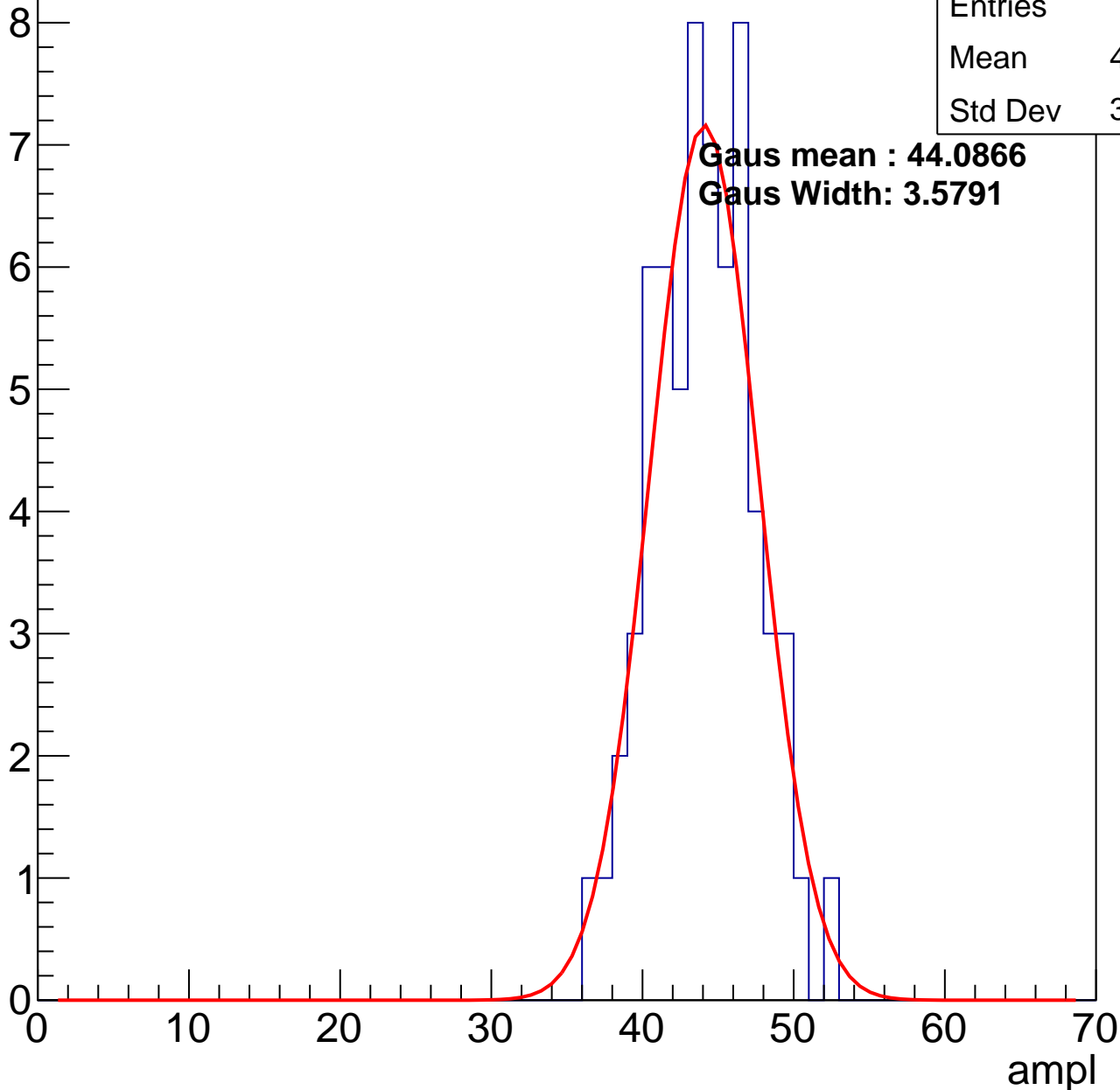
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.58
Std Dev	3.337

**Gaus mean : 44.0866**

**Gaus Width: 3.5791**

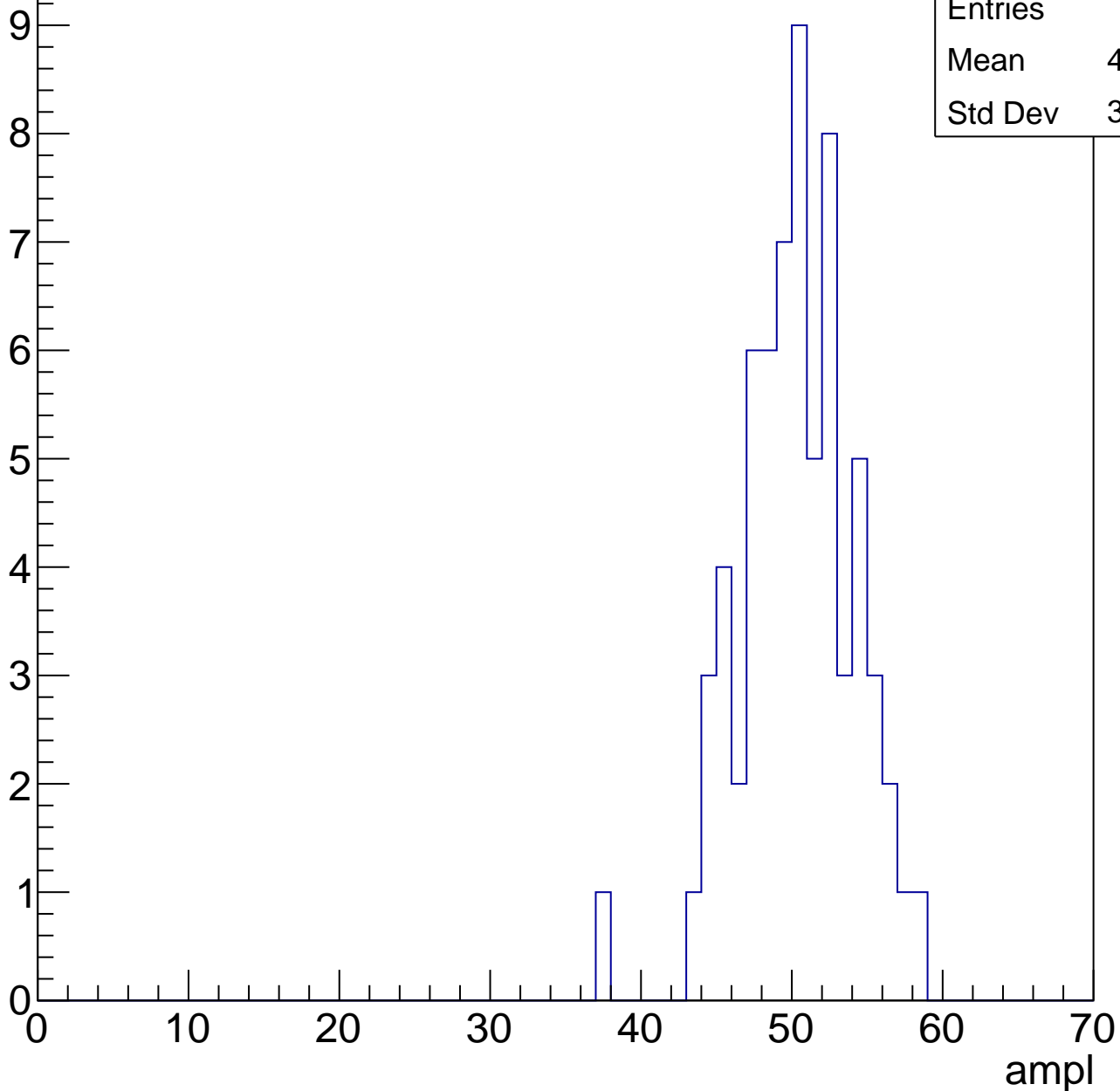


# B1L102S, U24-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.84
Std Dev	3.756

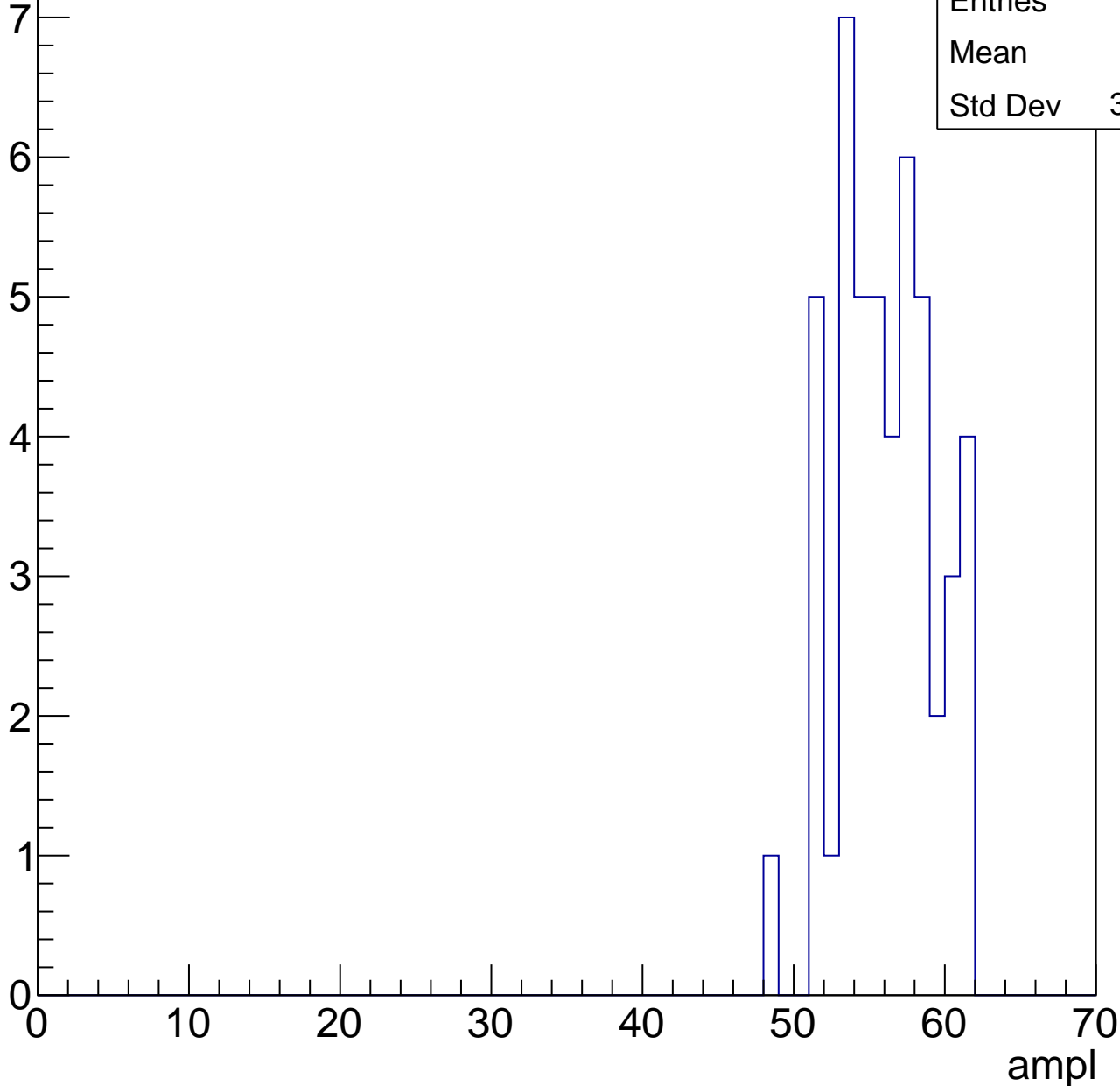


# B1L102S, U24-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.6
Std Dev	3.154



# B1L102S, U24-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	52
Mean	58.92
Std Dev	8.582

Entry

10

8

6

4

2

0

0

10

20

30

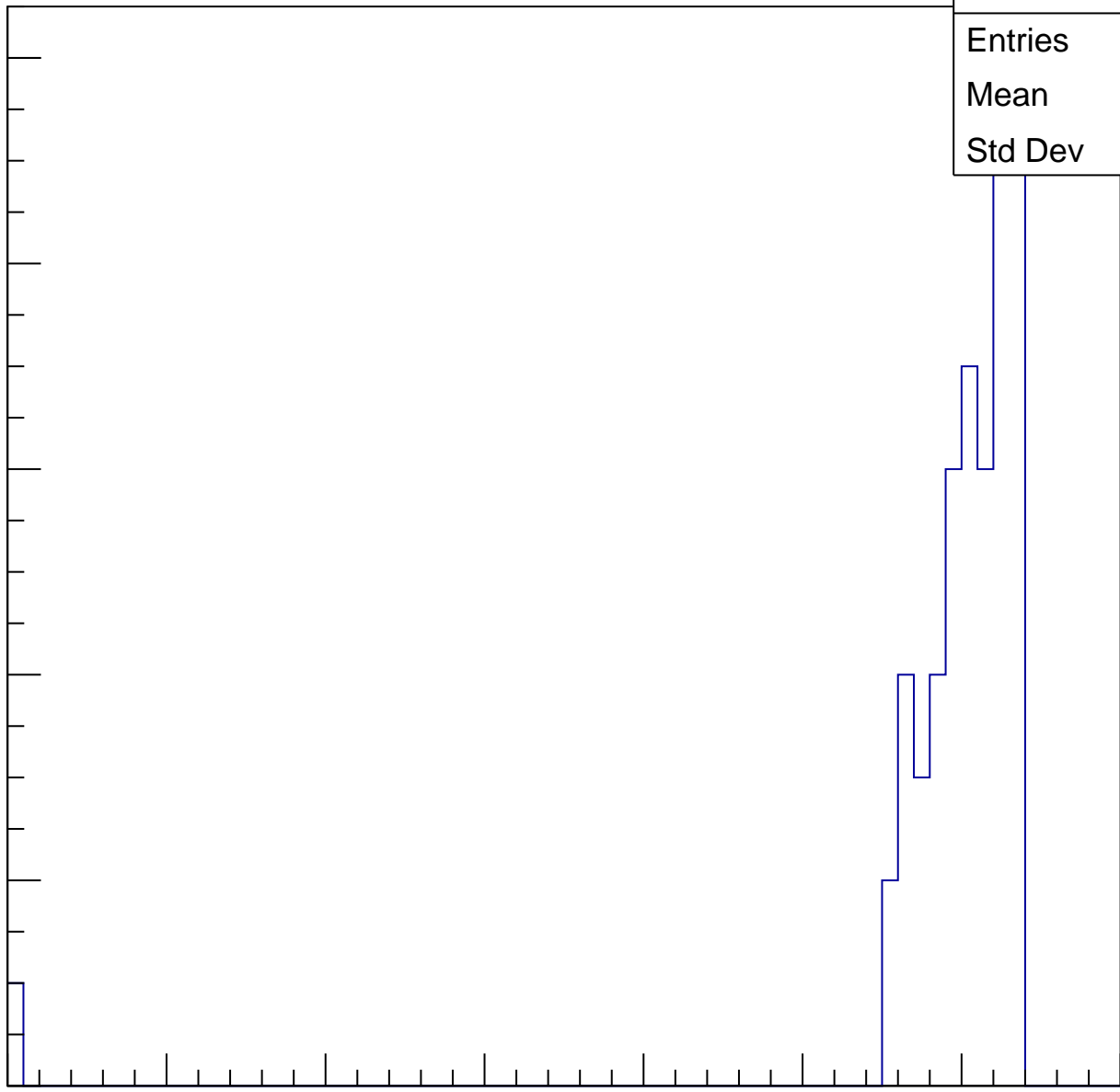
40

50

60

70

ampl



# B1L102S, U24-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

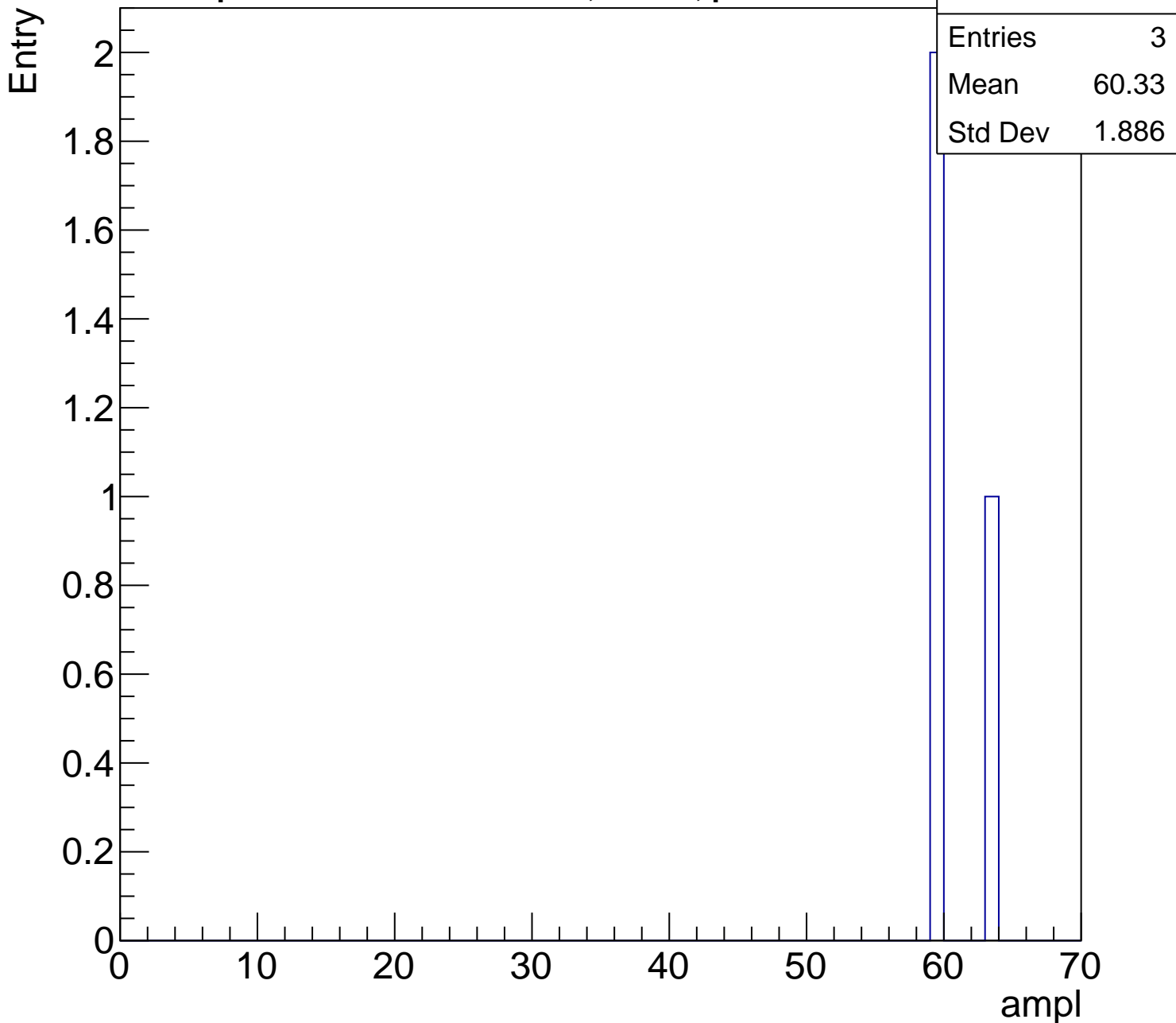
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	60.33
Std Dev	1.886

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch90, adc0

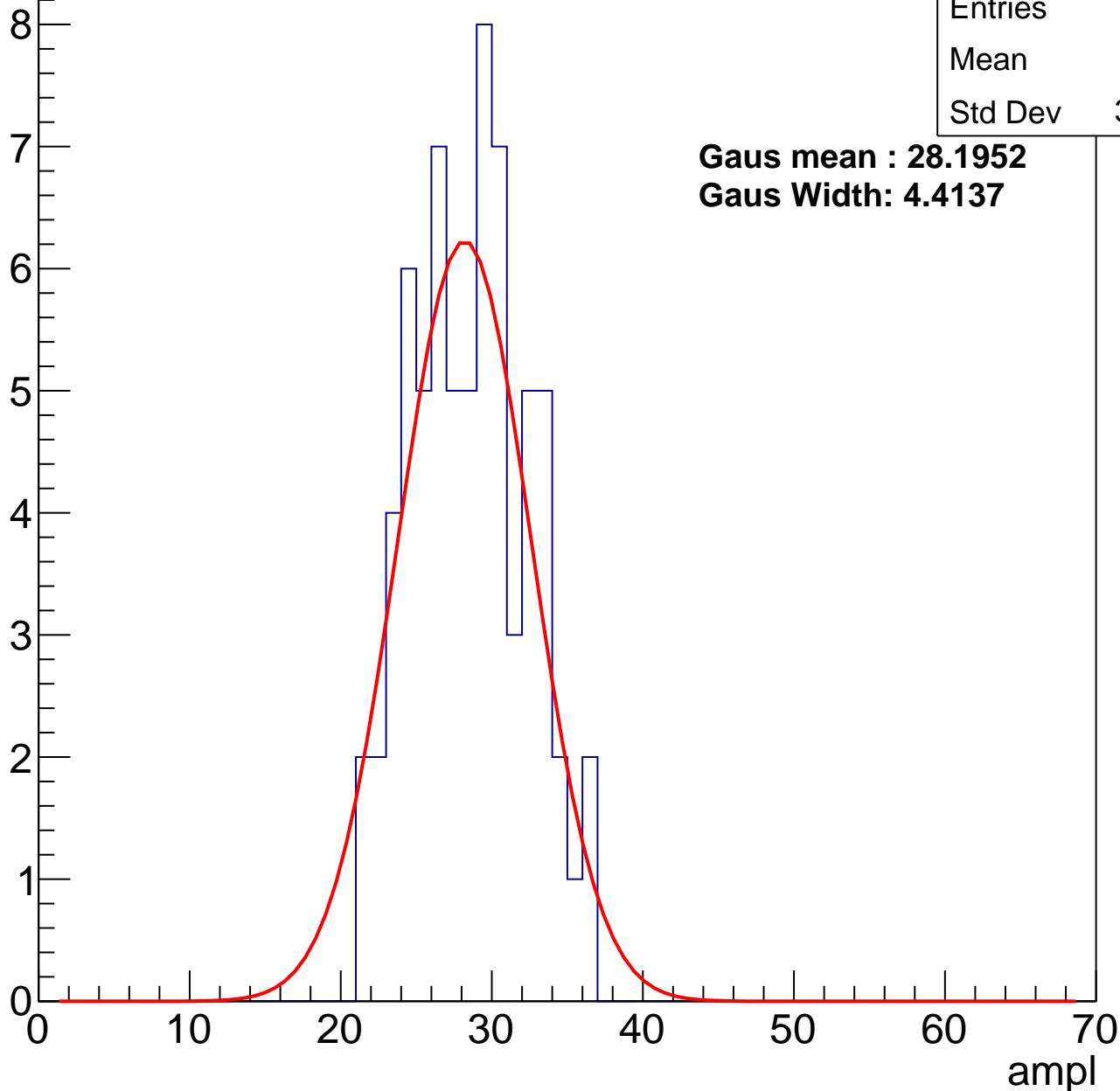
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.1
Std Dev	3.731

**Gaus mean : 28.1952**

**Gaus Width: 4.4137**



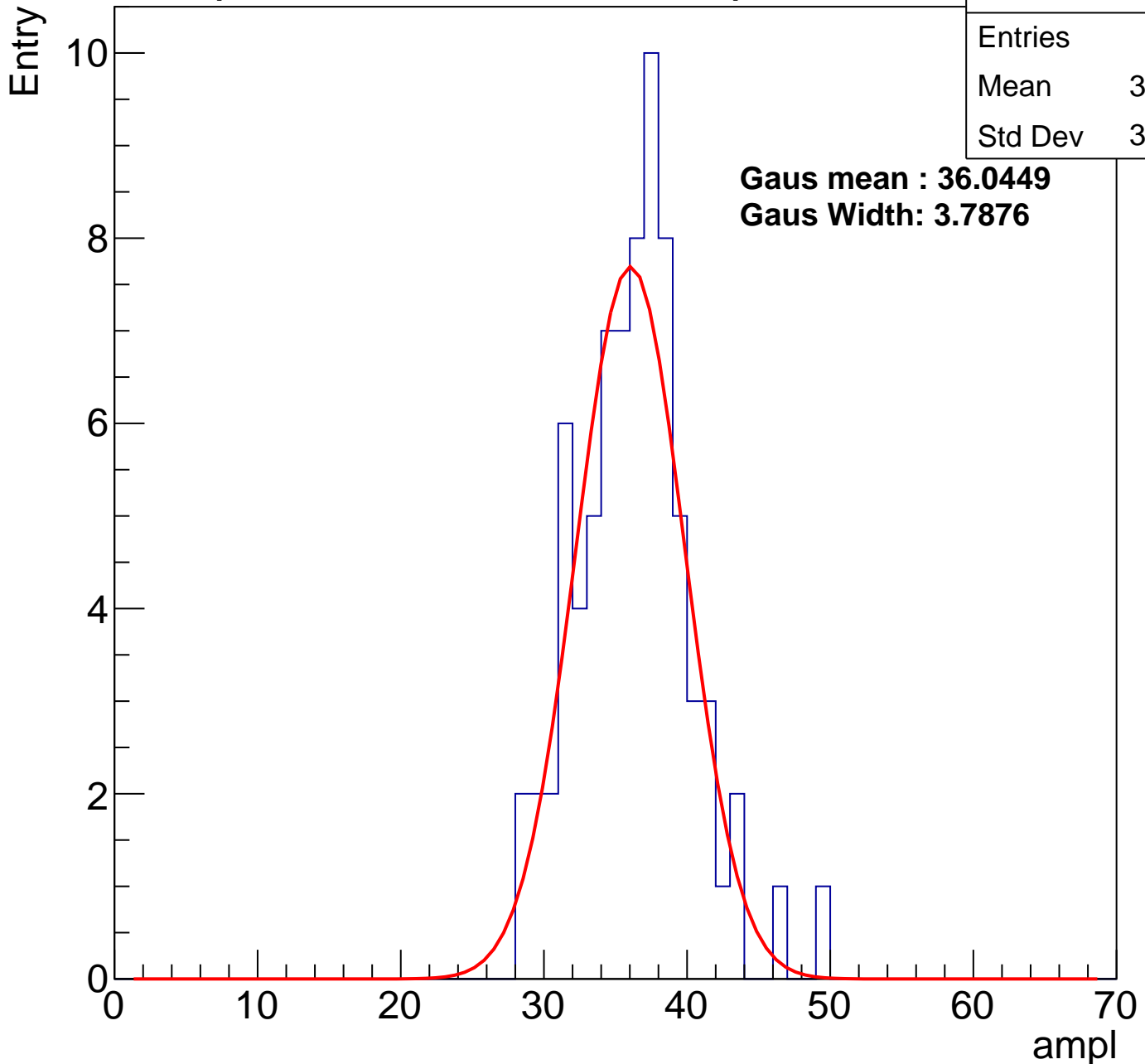
# B1L102S, U24-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	35.83
Std Dev	3.952

**Gaus mean : 36.0449**

**Gaus Width: 3.7876**



# B1L102S, U24-ch90, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	43.33
Std Dev	3.564

**Gaus mean : 43.5080**

**Gaus Width: 3.6259**

10

8

6

4

2

0

0

10

20

30

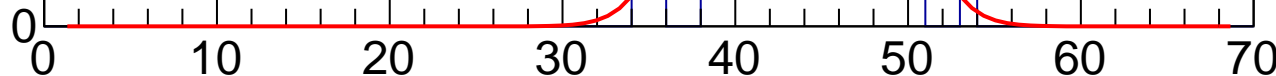
40

50

60

70

ampl

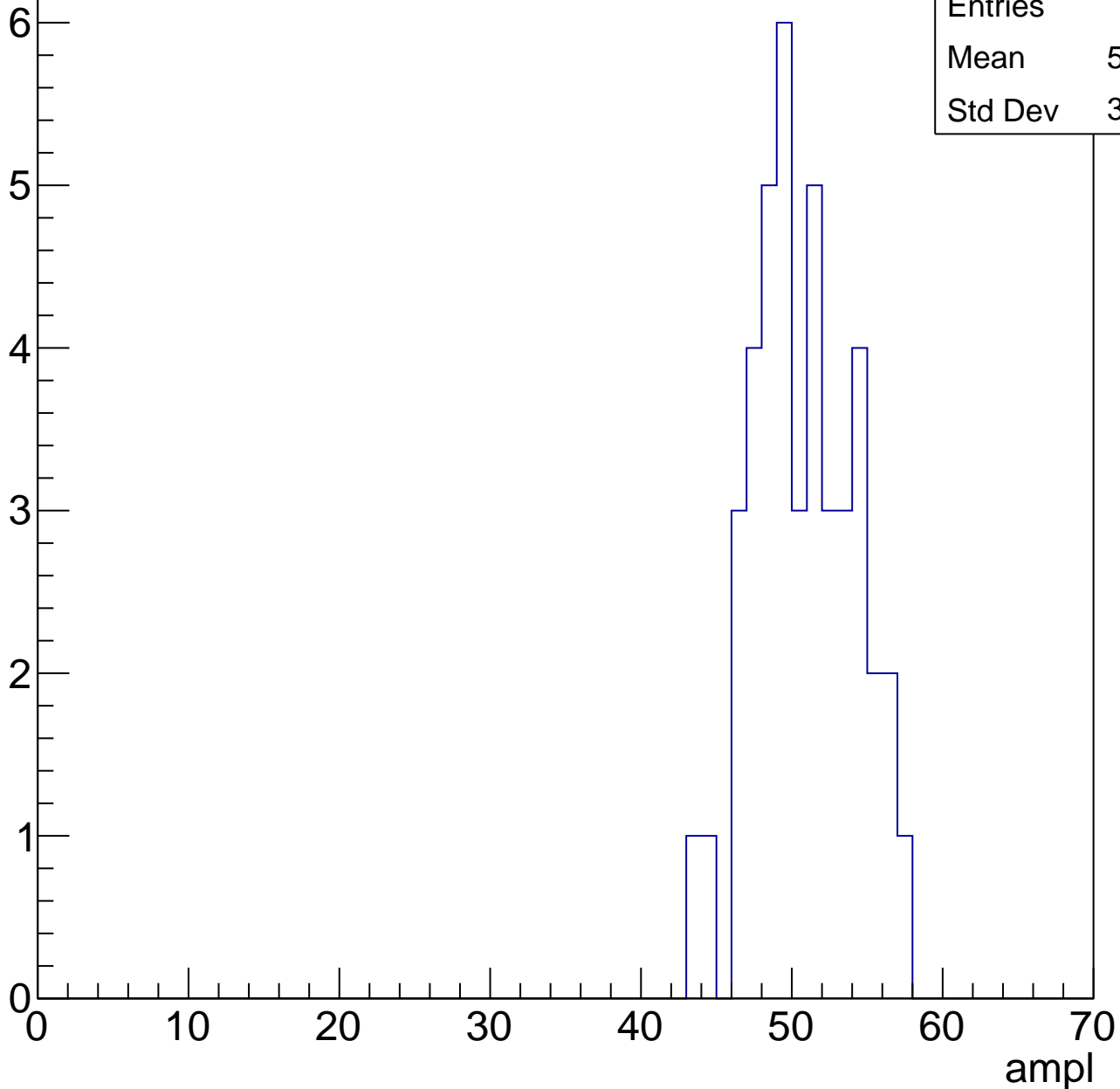


# B1L102S, U24-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	50.28
Std Dev	3.308

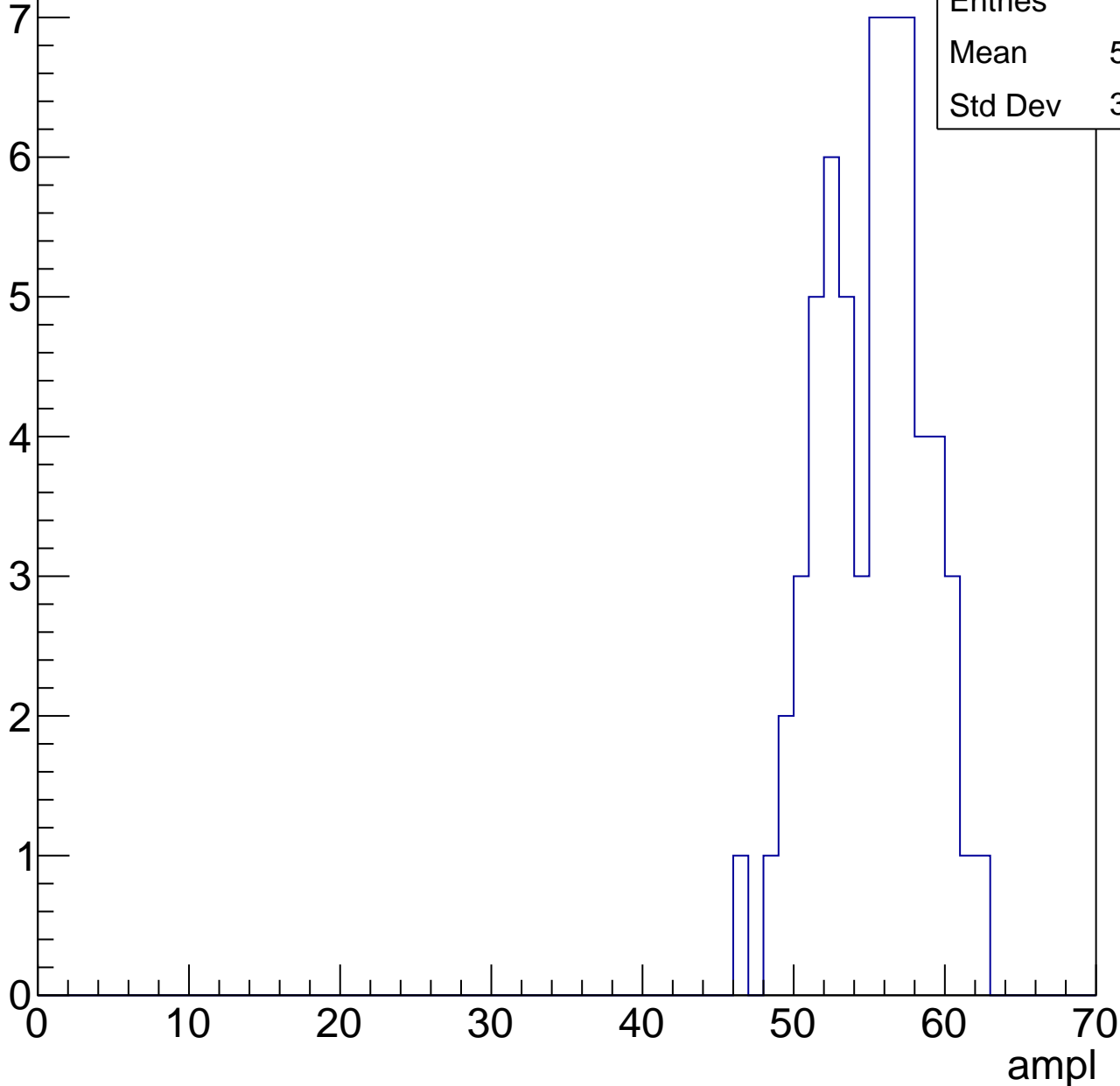


# B1L102S, U24-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.72
Std Dev	3.474

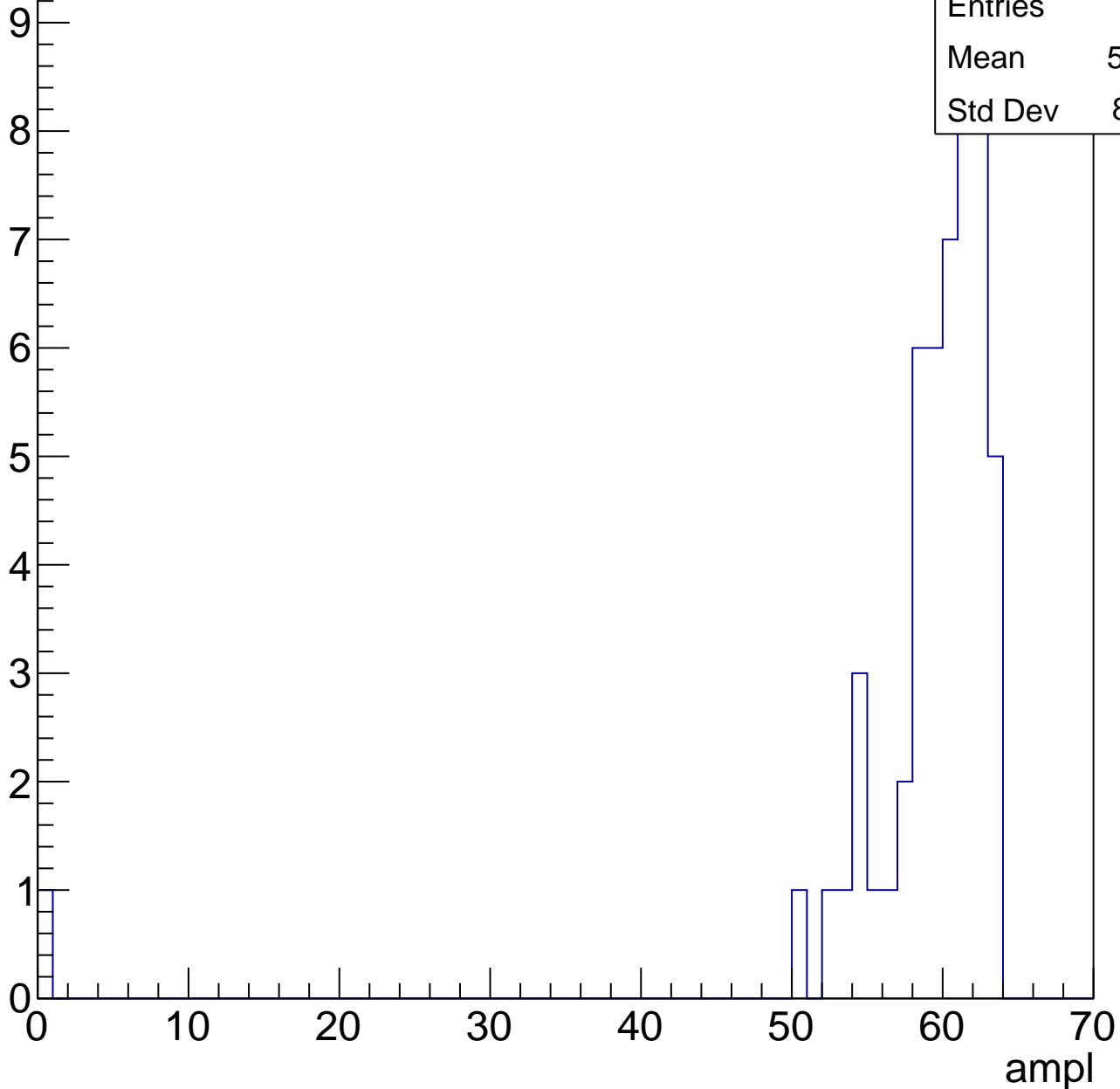


# B1L102S, U24-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.15
Std Dev	8.681



# B1L102S, U24-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch91, adc0

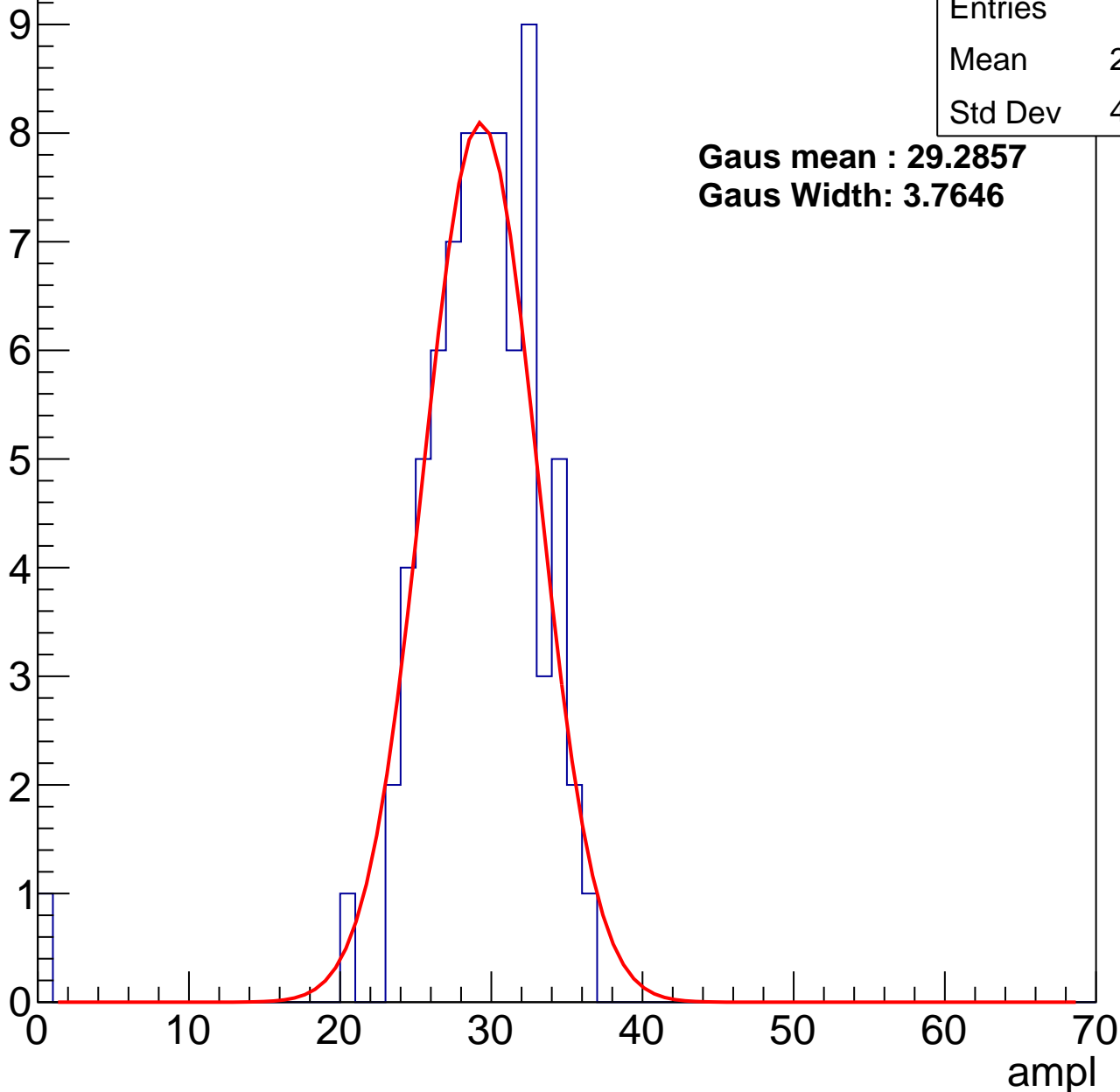
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.64
Std Dev	4.676

**Gaus mean : 29.2857**

**Gaus Width: 3.7646**



# B1L102S, U24-ch91, adc1

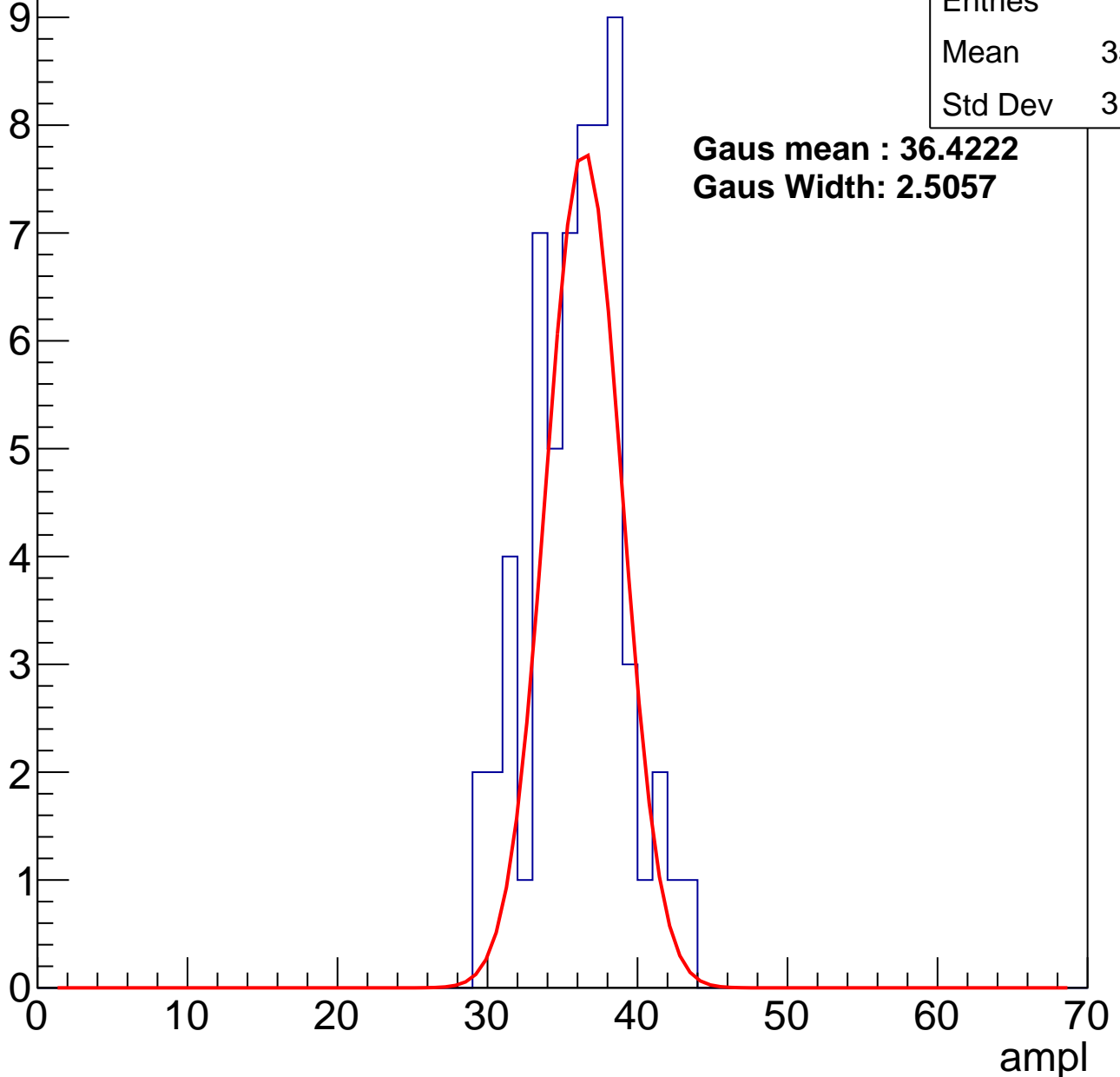
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.57
Std Dev	3.102

**Gaus mean : 36.4222**

**Gaus Width: 2.5057**



# B1L102S, U24-ch91, adc2

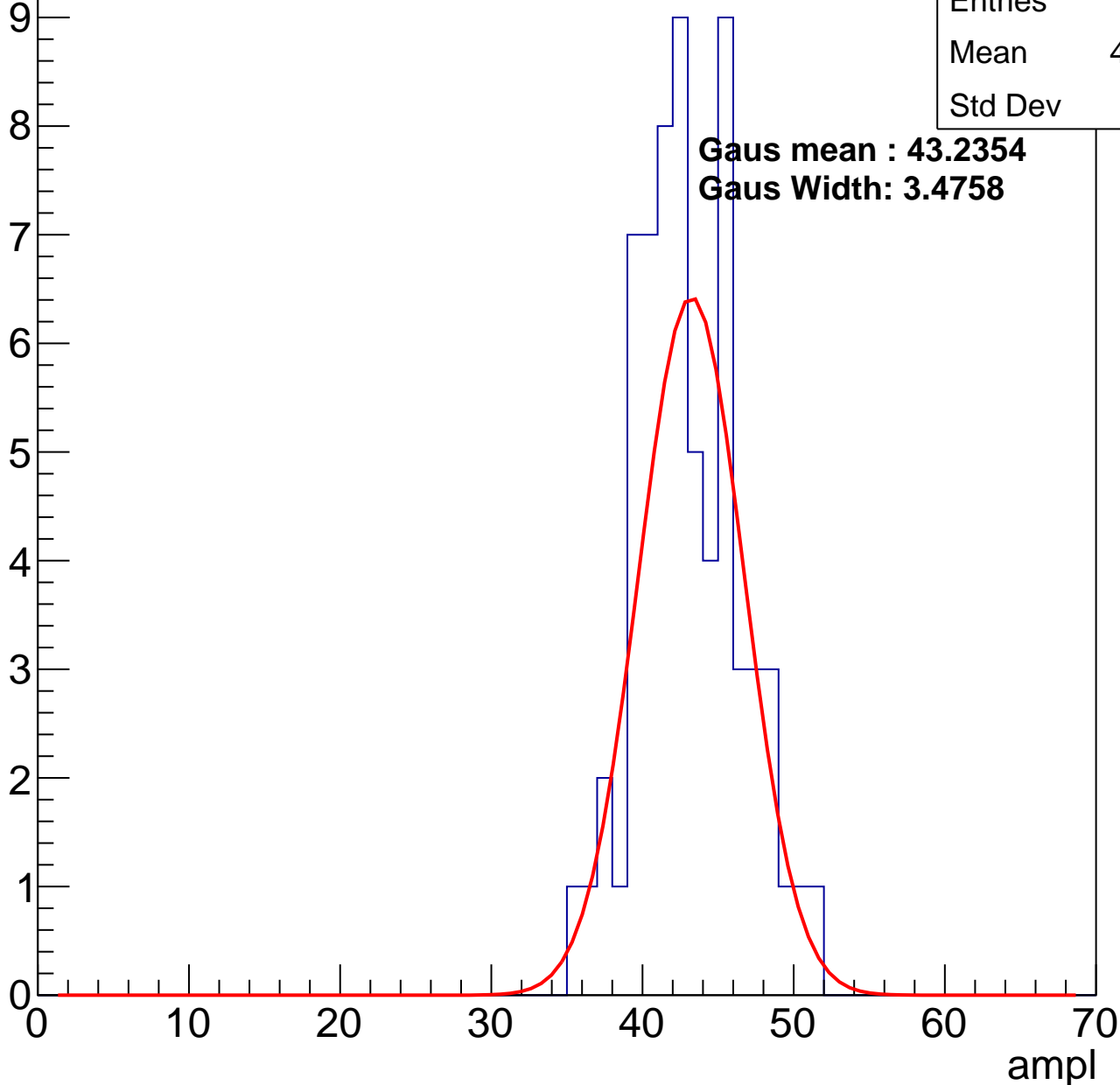
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.59
Std Dev	3.38

**Gaus mean : 43.2354**

**Gaus Width: 3.4758**

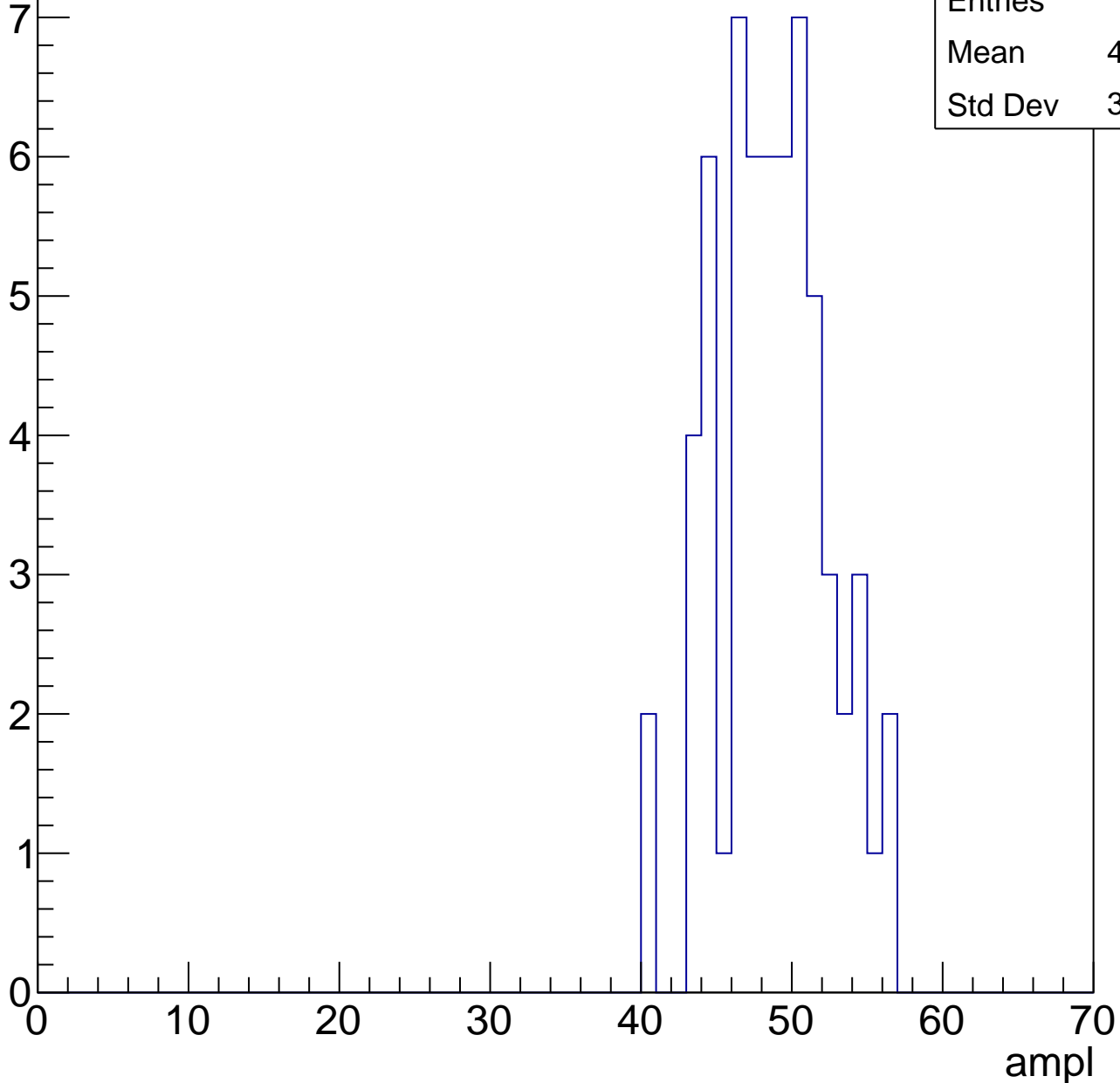


# B1L102S, U24-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.25
Std Dev	3.678



# B1L102S, U24-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

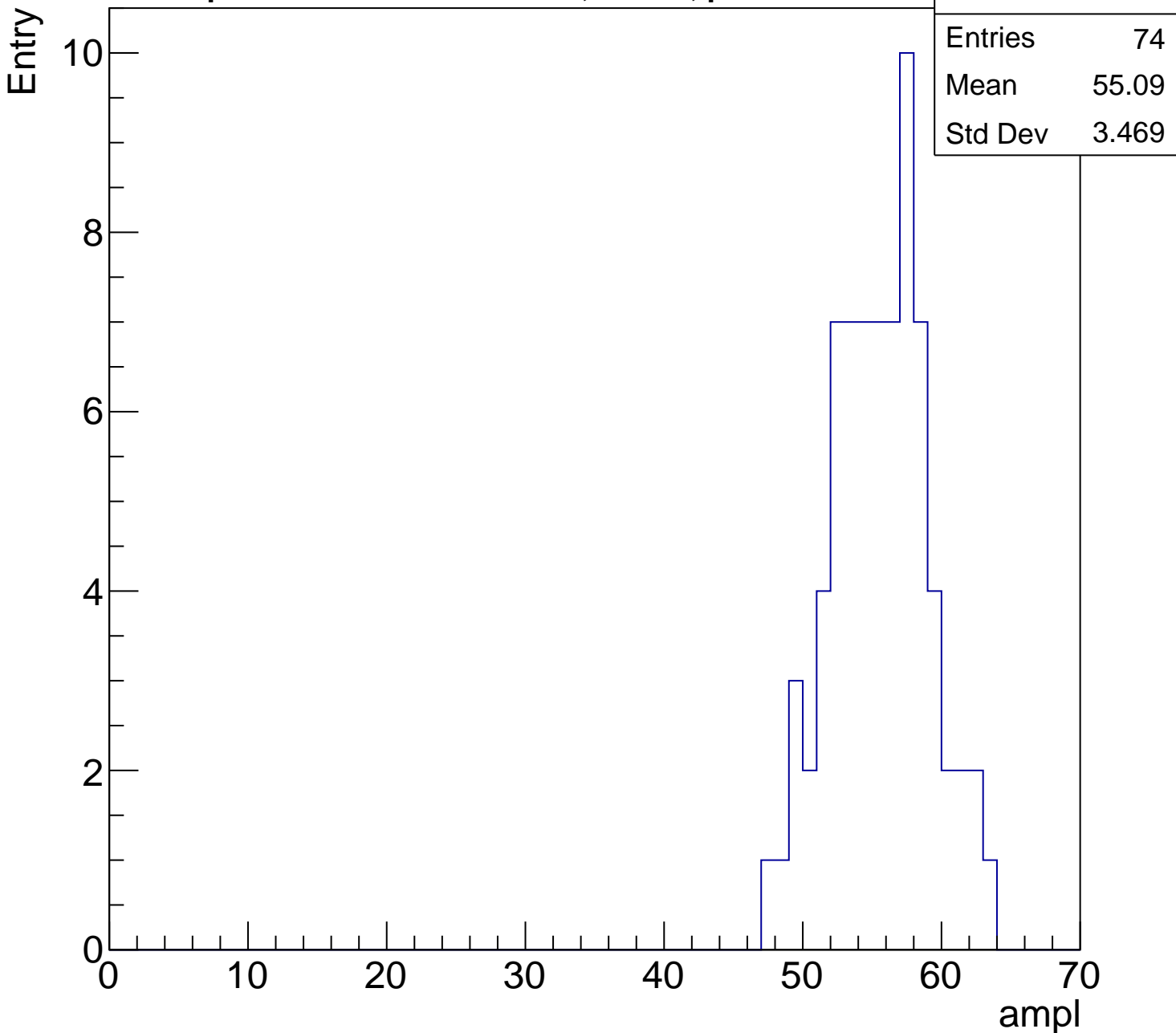
Entries	74
Mean	55.09
Std Dev	3.469

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U24-ch91, adc5

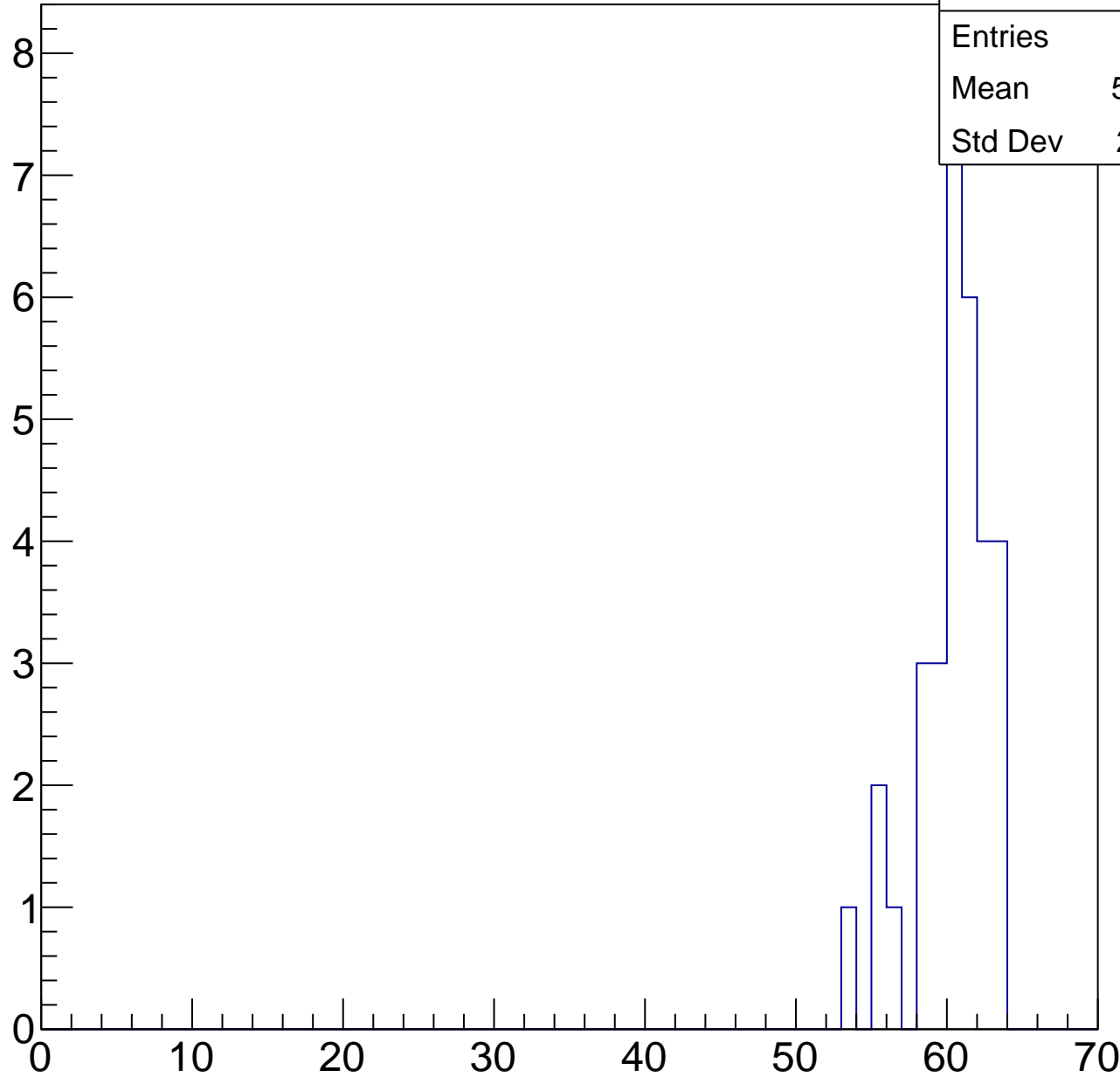
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	59.88
Std Dev	2.421

ampl

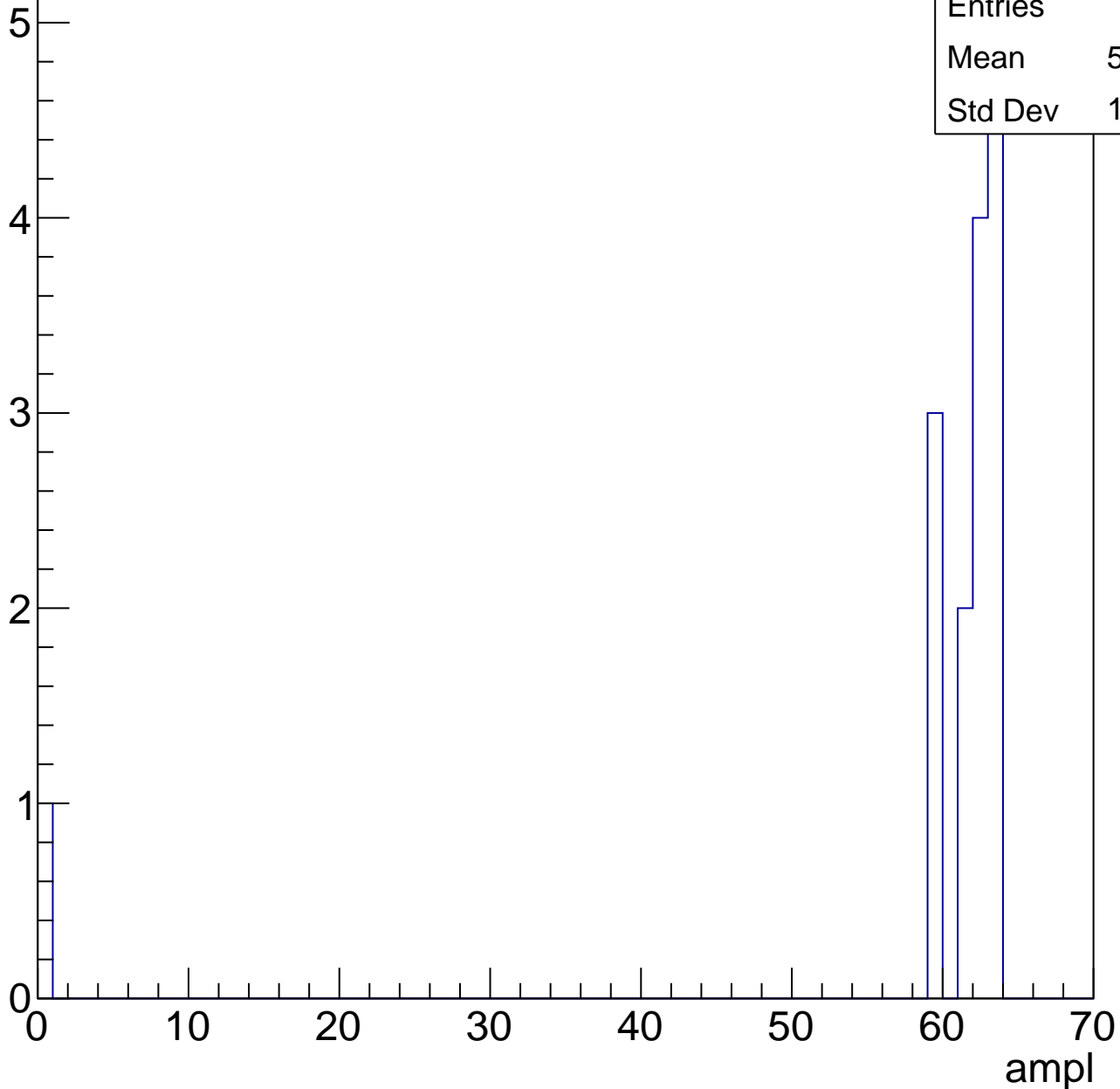


# B1L102S, U24-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.47
Std Dev	15.43





# B1L102S, U24-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch92, adc0

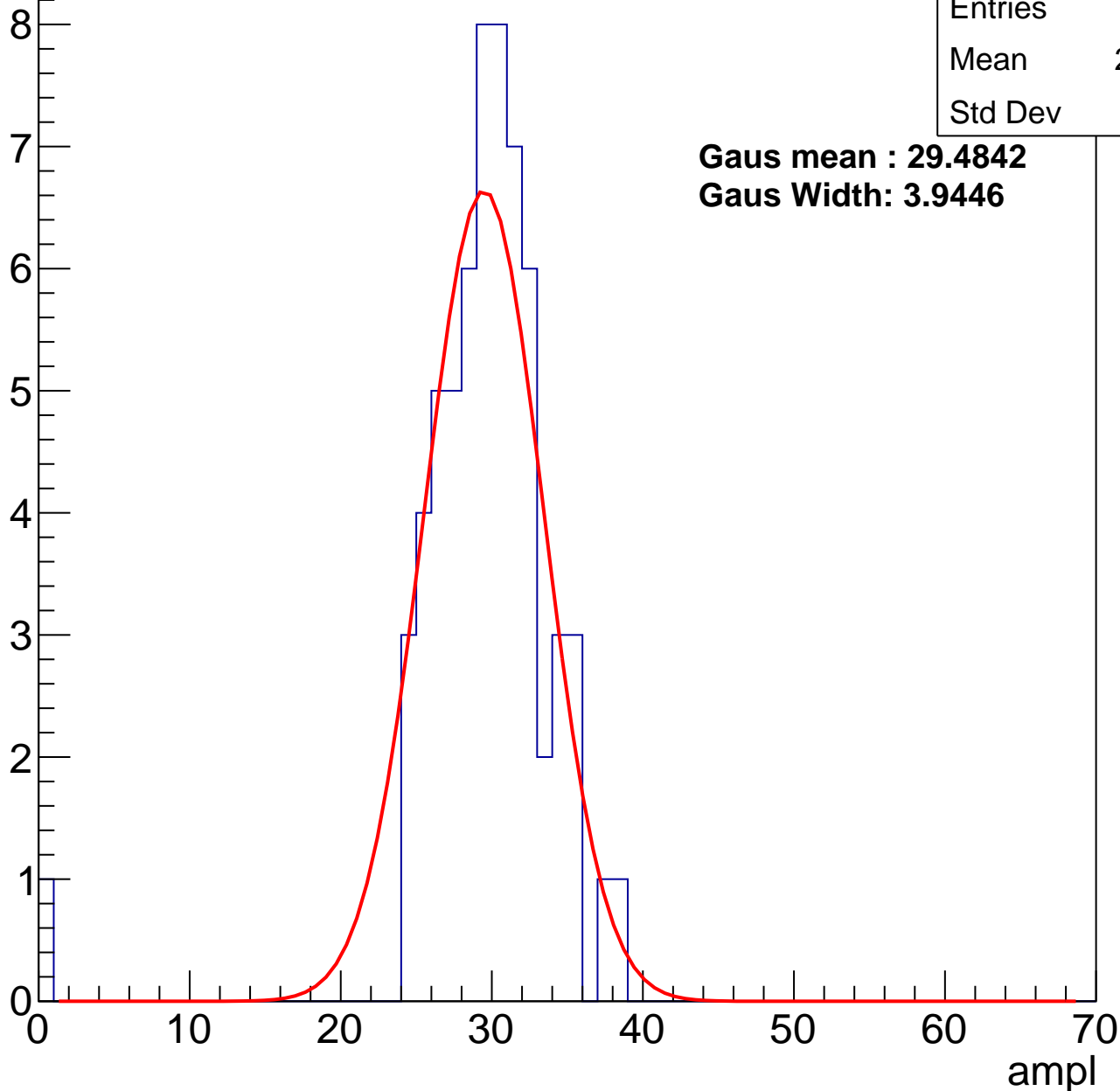
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.11
Std Dev	4.88

**Gaus mean : 29.4842**

**Gaus Width: 3.9446**



# B1L102S, U24-ch92, adc1

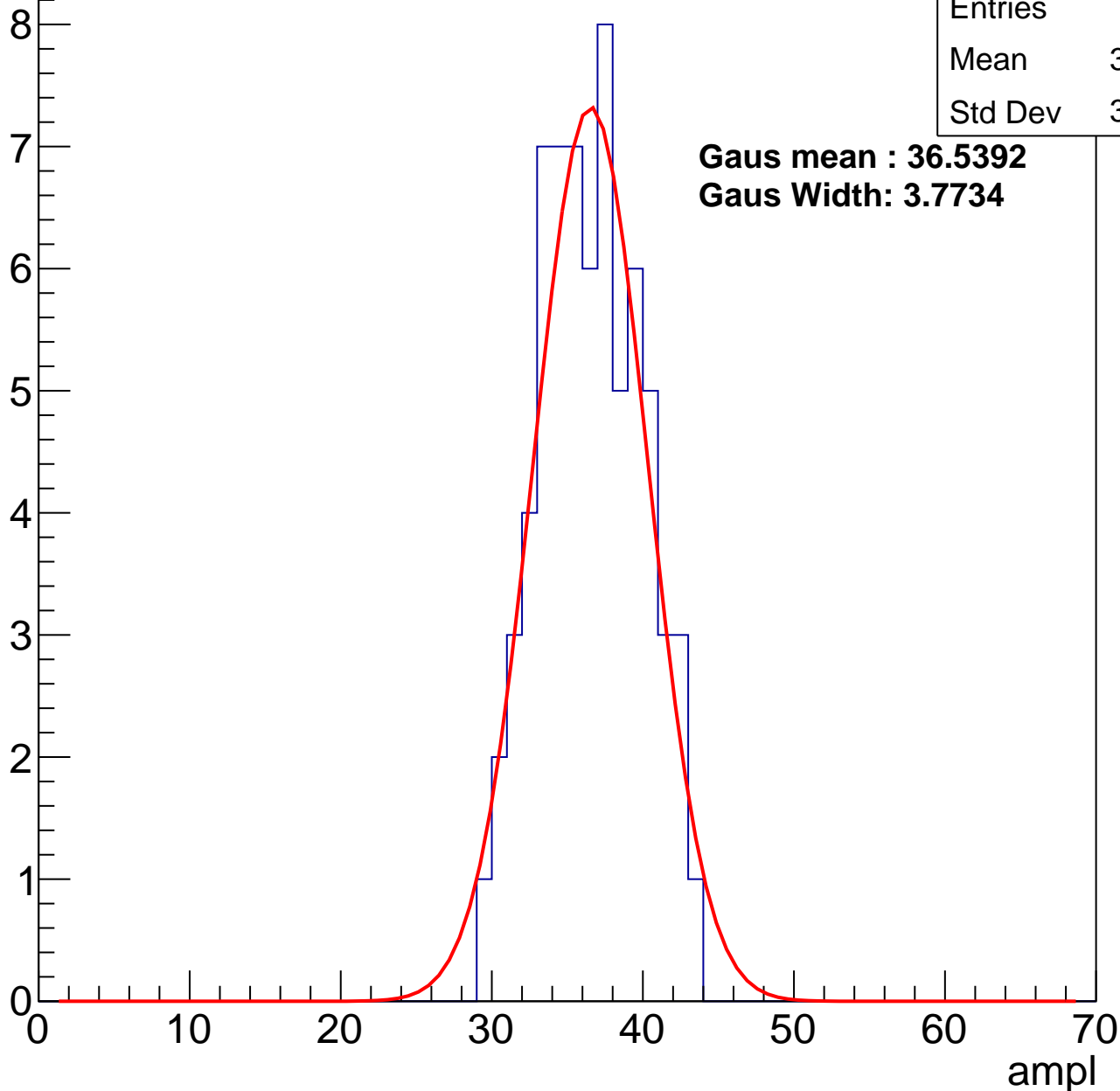
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.06
Std Dev	3.325

**Gaus mean : 36.5392**

**Gaus Width: 3.7734**

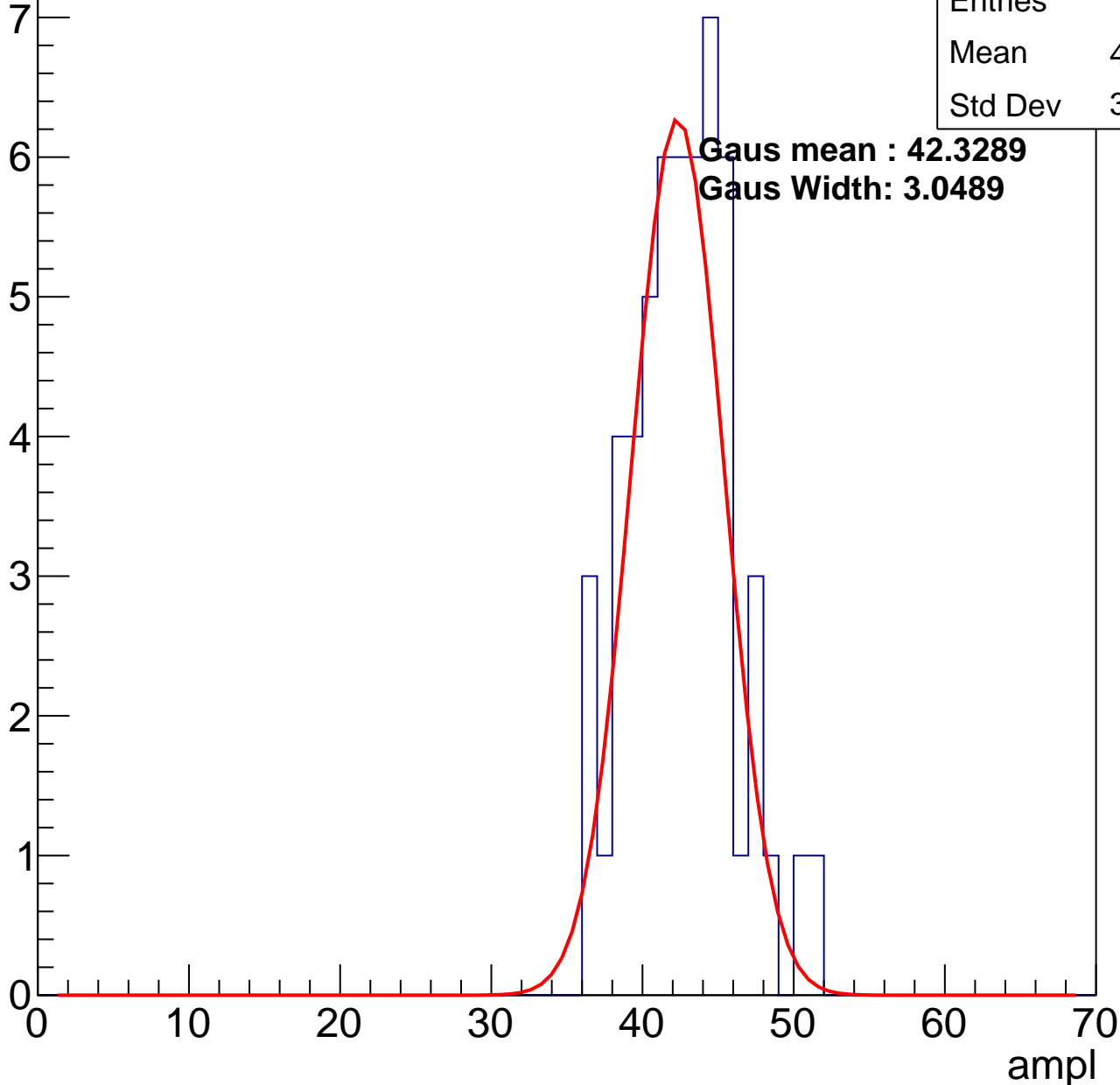


# B1L102S, U24-ch92, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.24
Std Dev	3.346

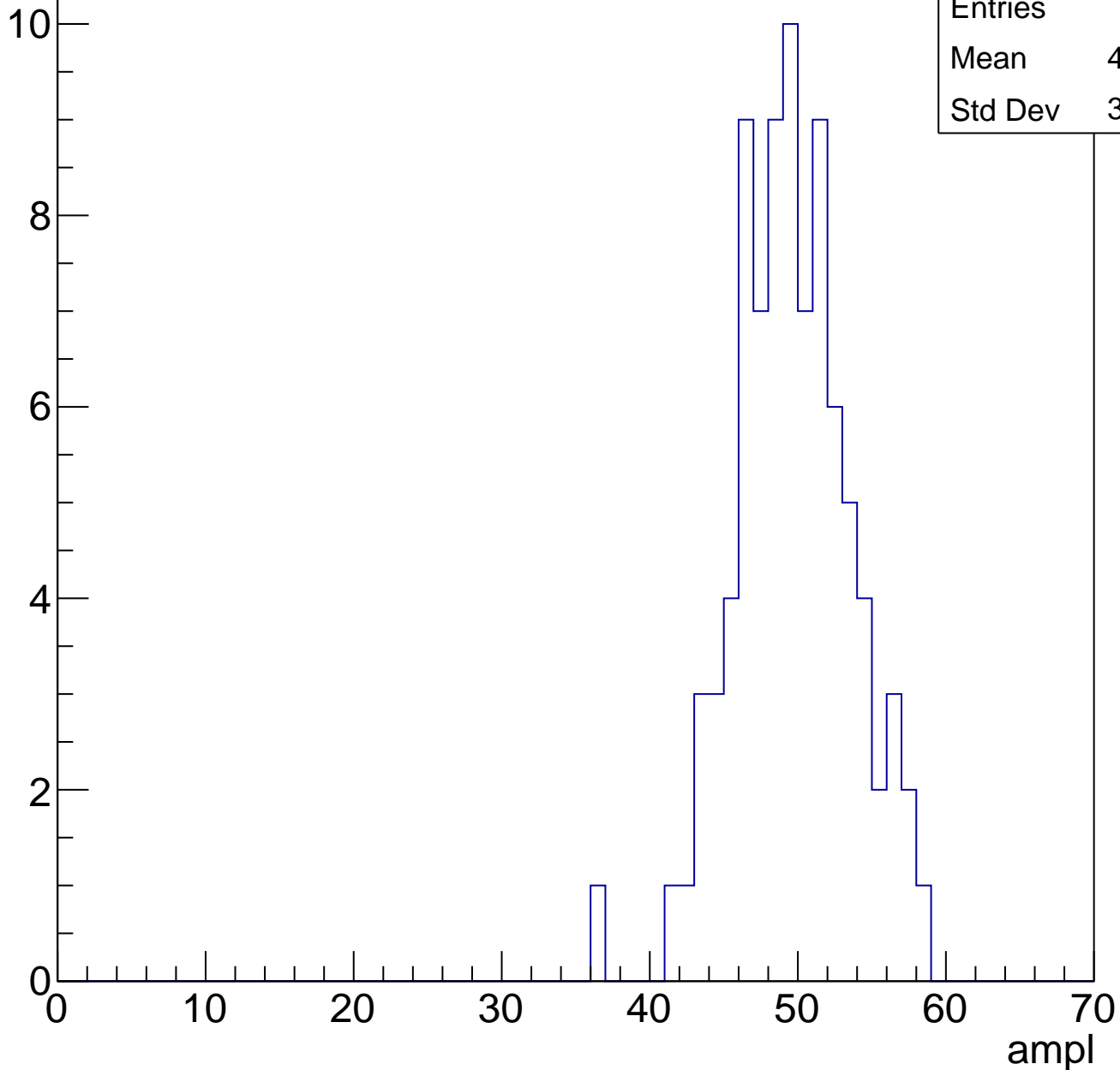


# B1L102S, U24-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	87
Mean	49.16
Std Dev	3.939

Entry

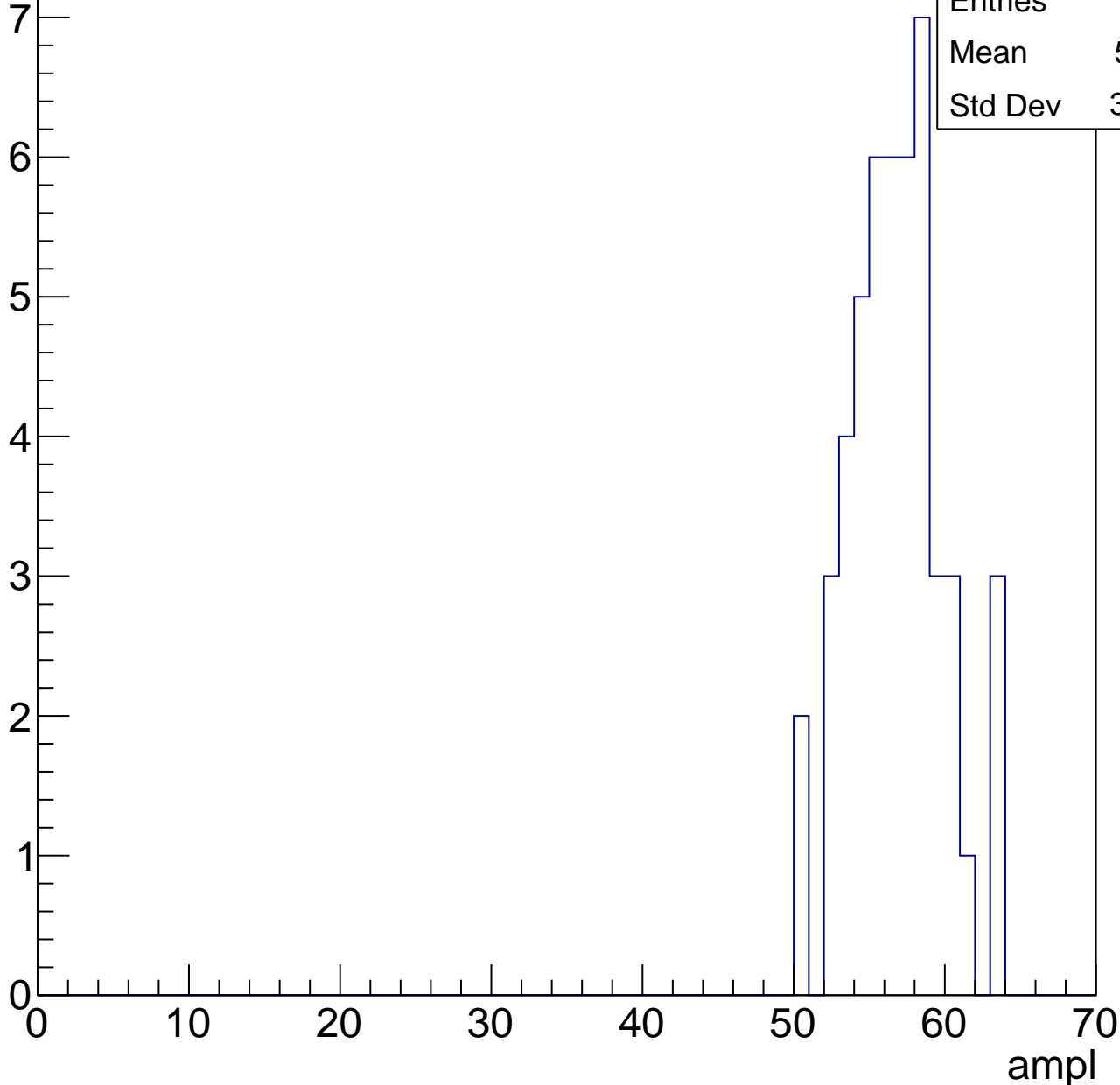


# B1L102S, U24-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.31
Std Dev	3.059

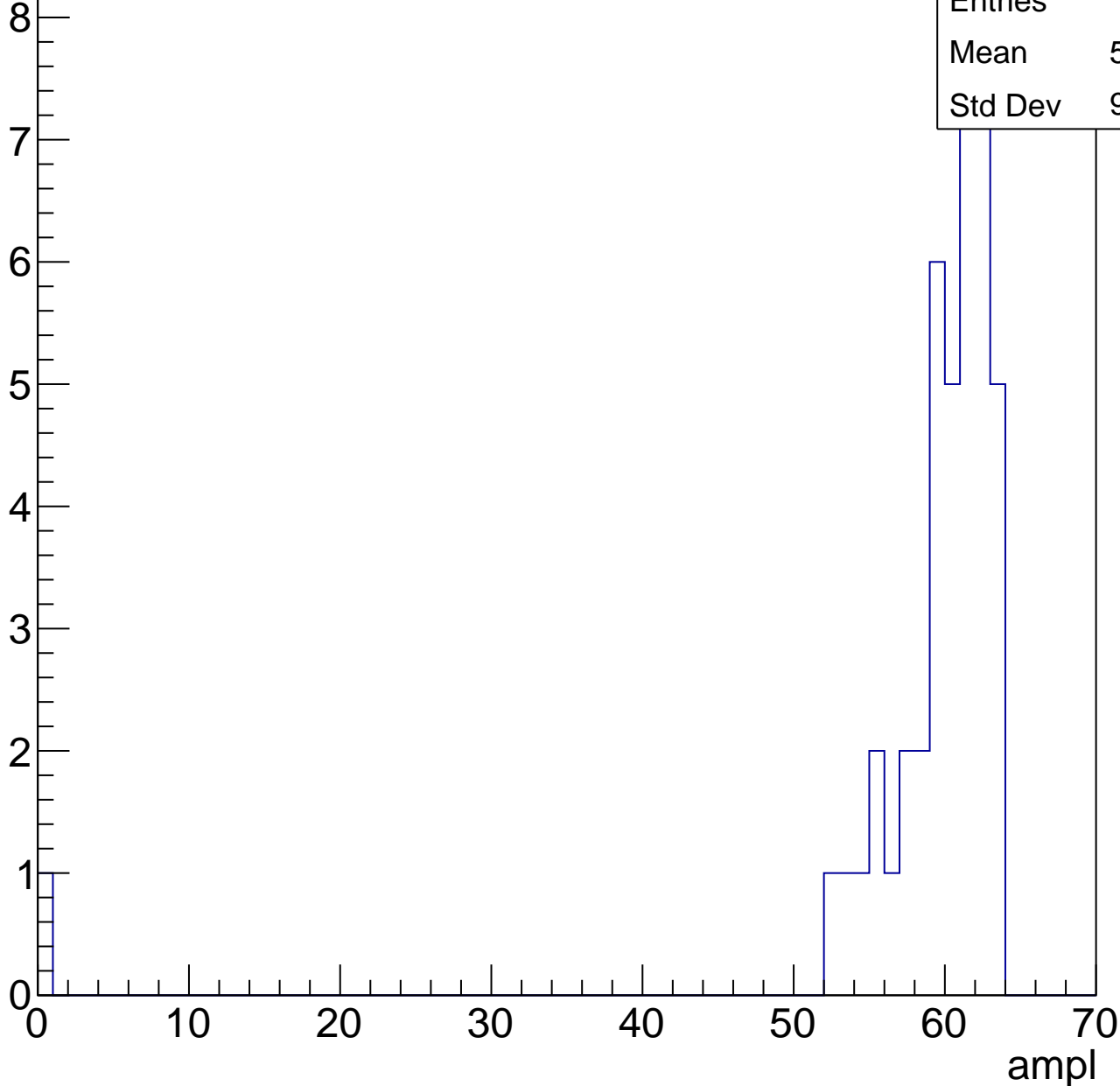


# B1L102S, U24-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.33
Std Dev	9.415



# B1L102S, U24-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	61.25
Std Dev	1.392

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U24-ch93, adc0

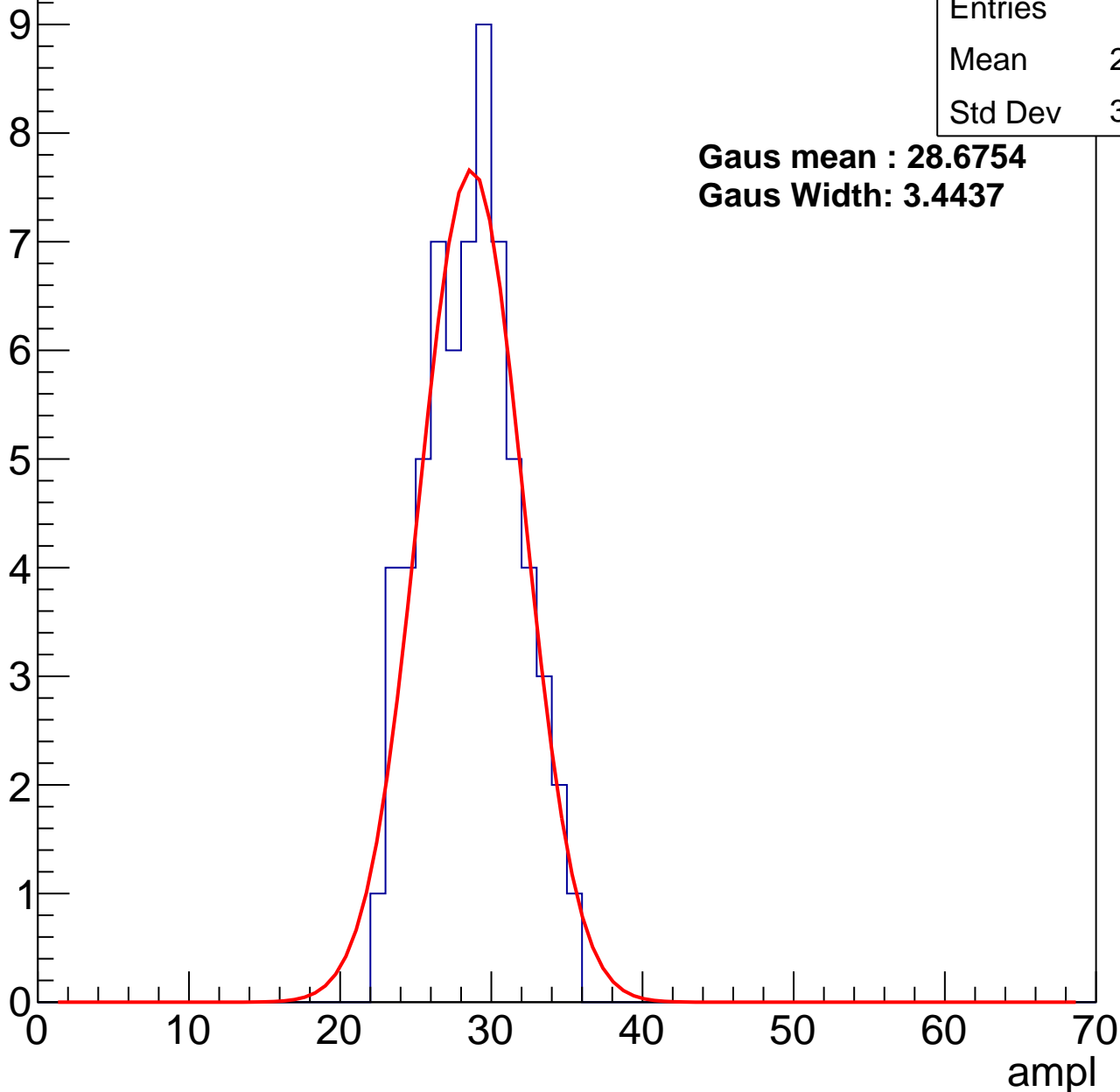
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.17
Std Dev	3.086

**Gaus mean : 28.6754**

**Gaus Width: 3.4437**



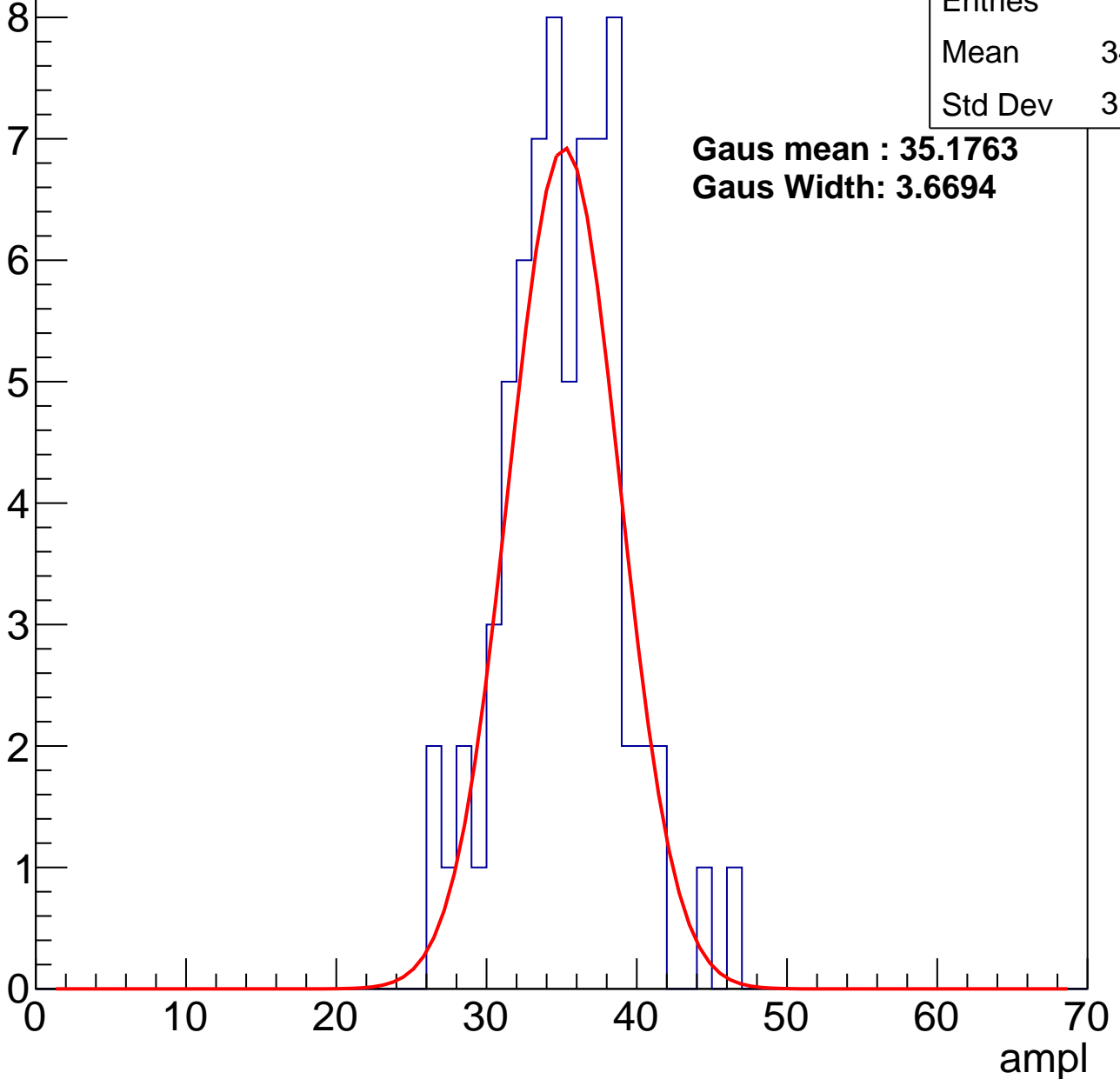
# B1L102S, U24-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.63
Std Dev	3.888

**Gaus mean : 35.1763**  
**Gaus Width: 3.6694**



# B1L102S, U24-ch93, adc2

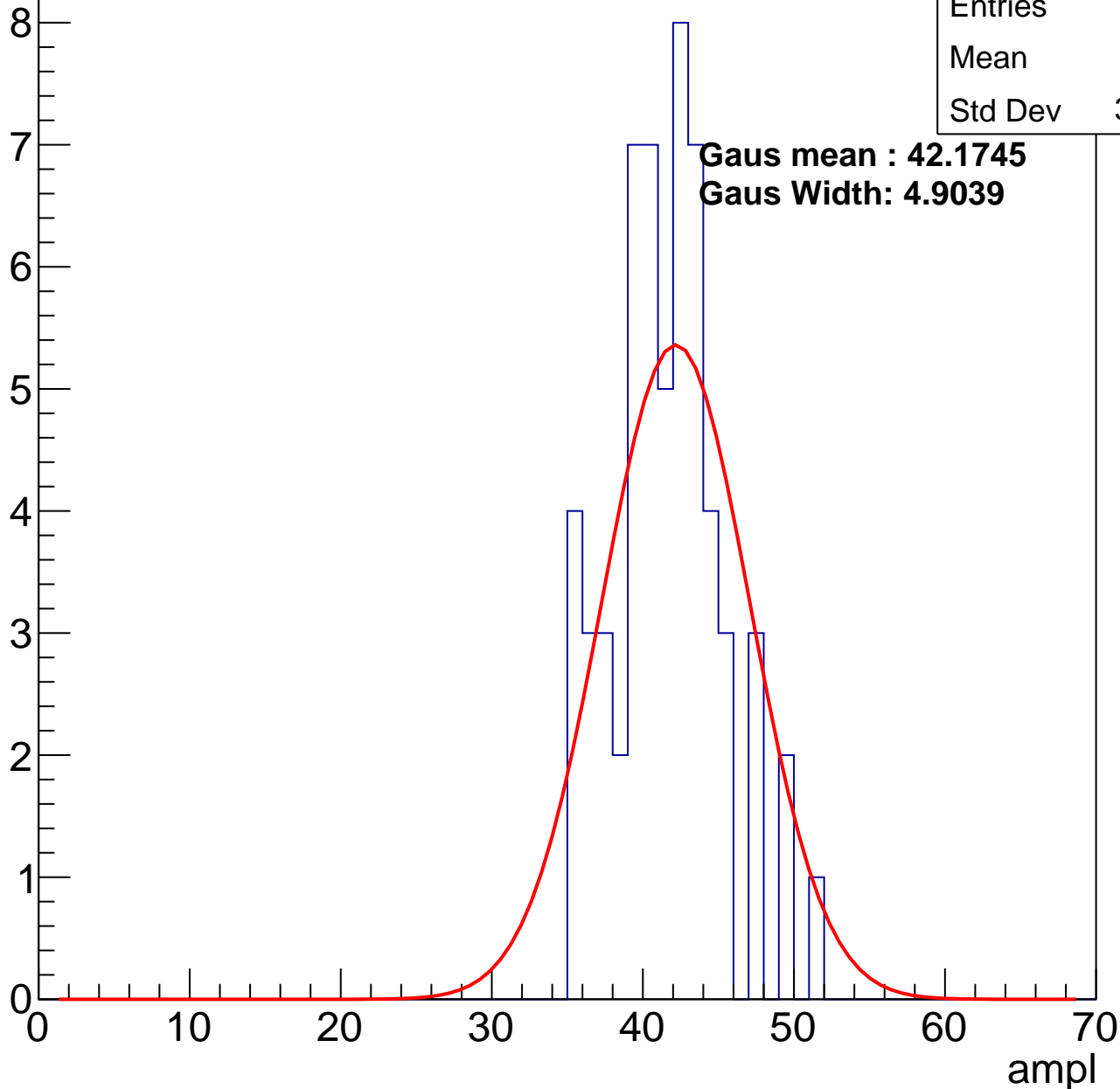
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.2
Std Dev	3.621

**Gaus mean : 42.1745**

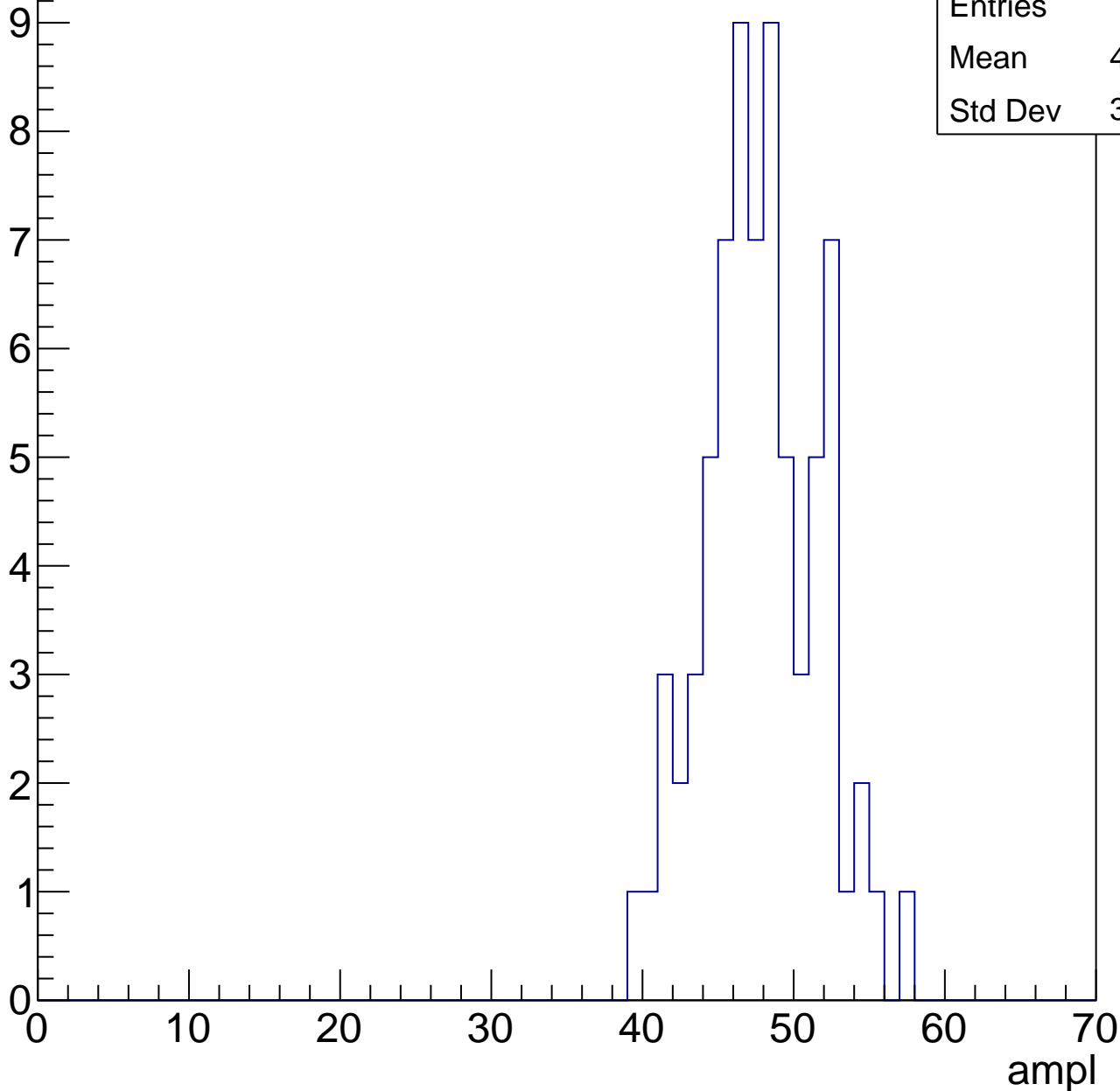
**Gaus Width: 4.9039**



# B1L102S, U24-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



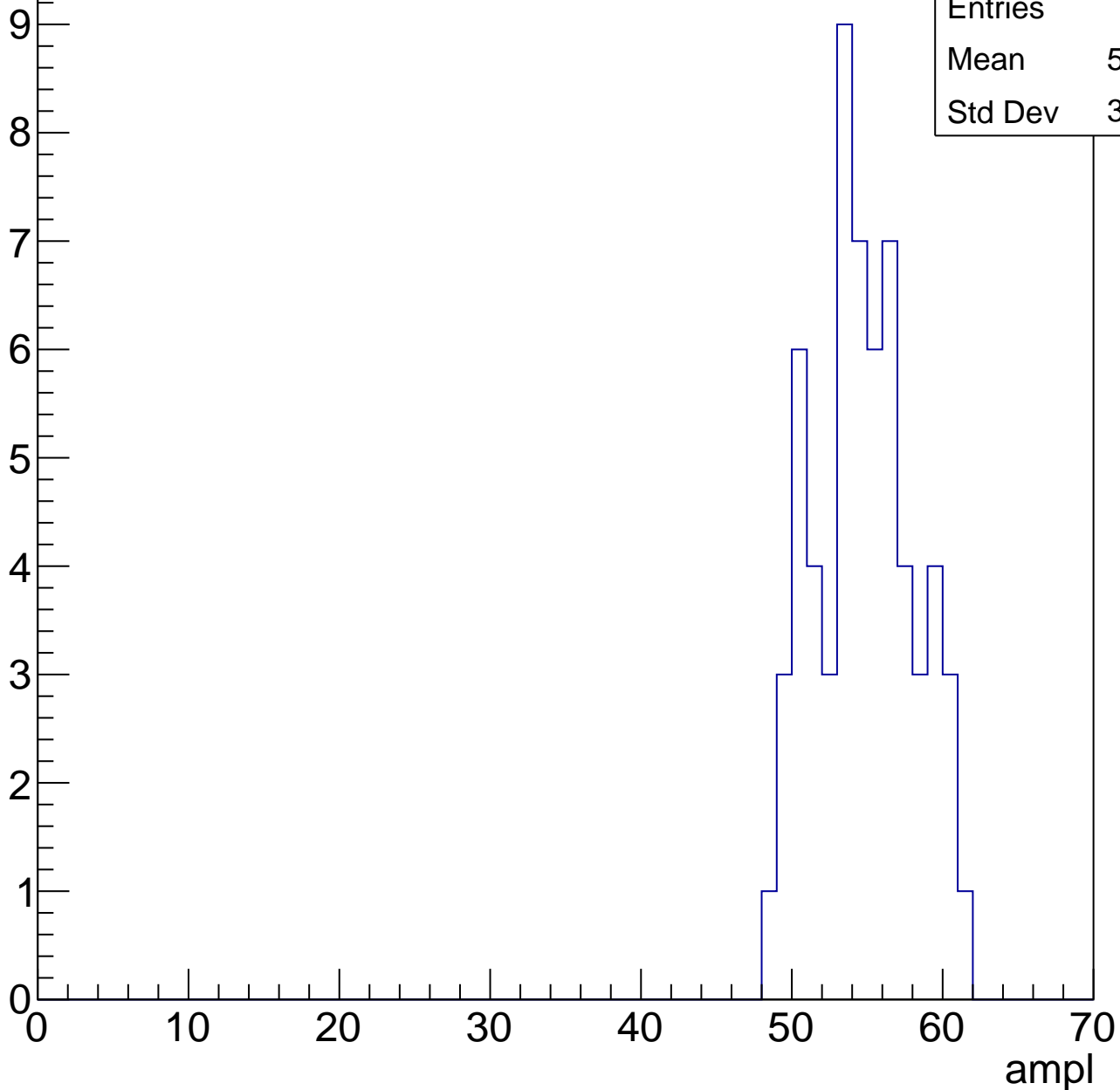
Entries	72
Mean	47.39
Std Dev	3.725

# B1L102S, U24-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.28
Std Dev	3.225

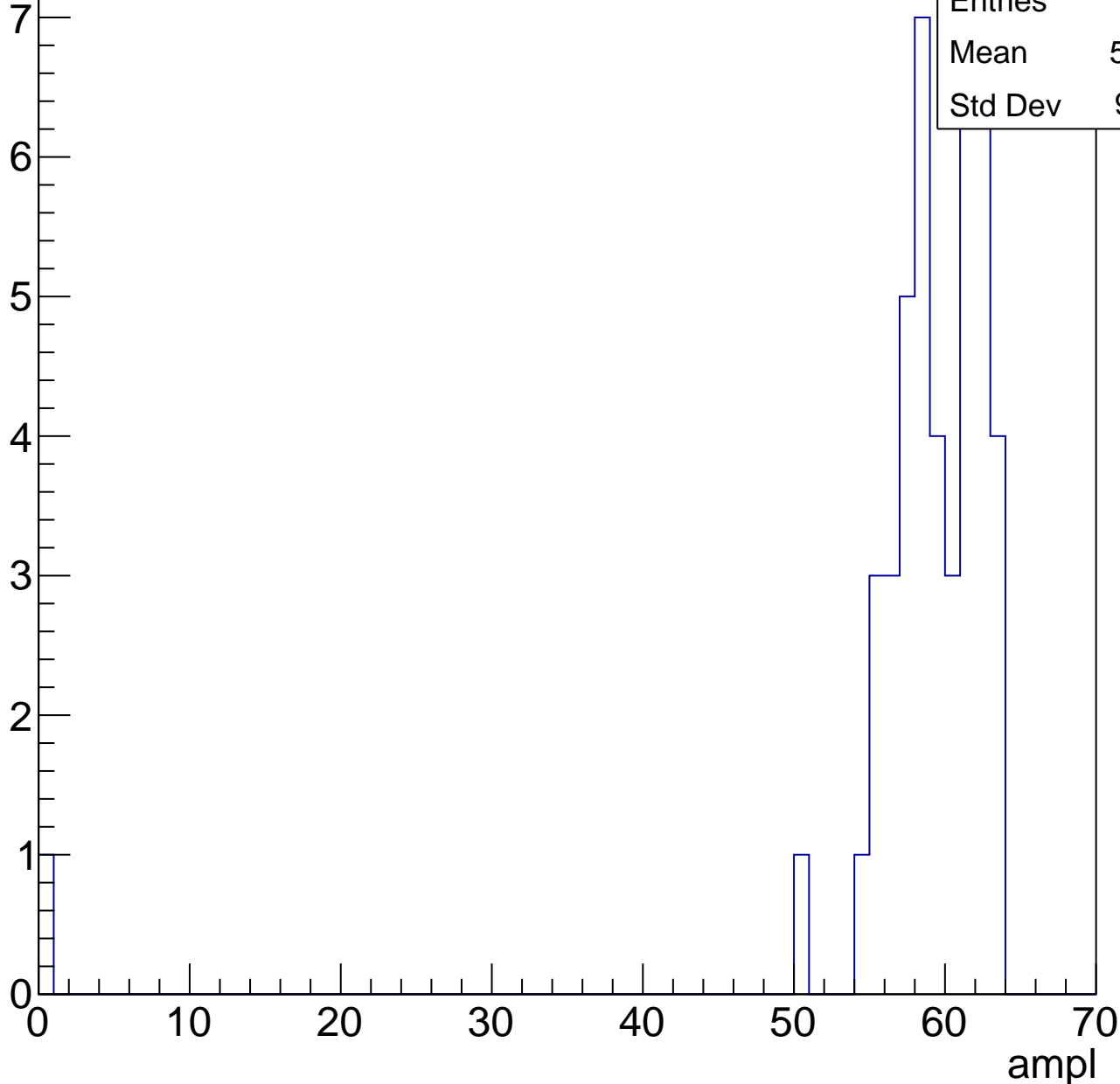


# B1L102S, U24-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	57.76
Std Dev	9.061

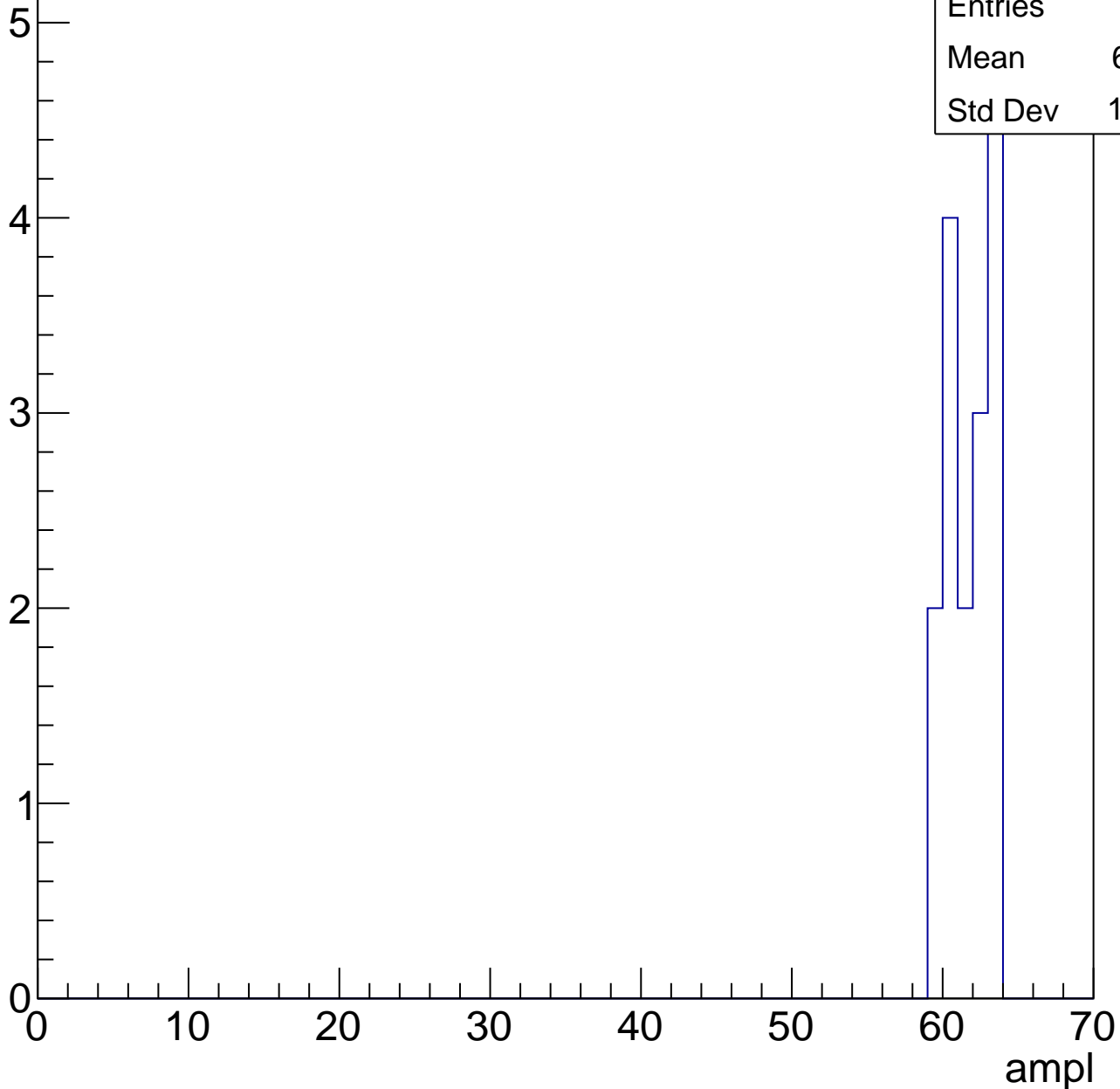


# B1L102S, U24-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.31
Std Dev	1.446

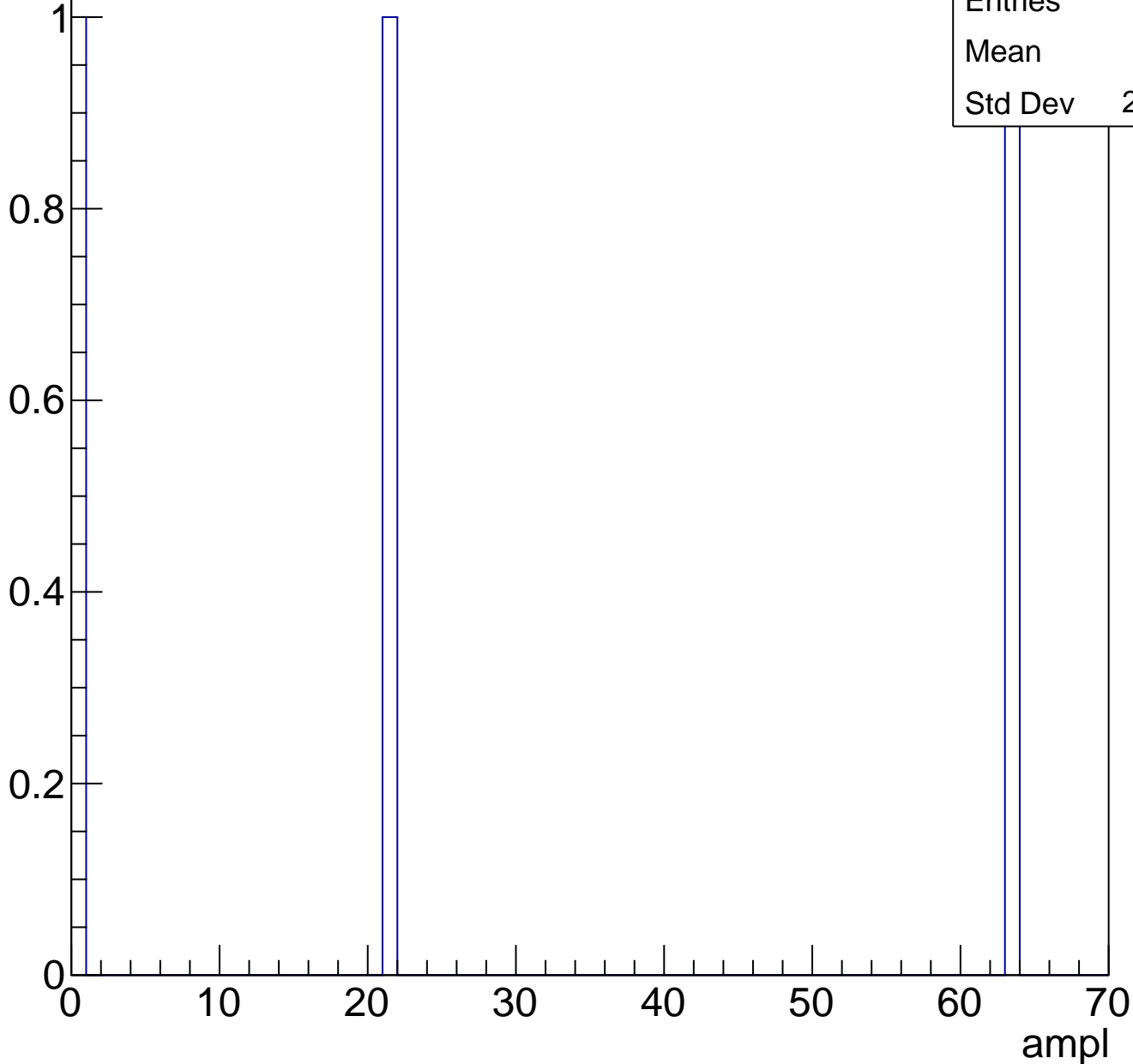




# B1L102S, U24-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L102S, U24-ch94, adc0

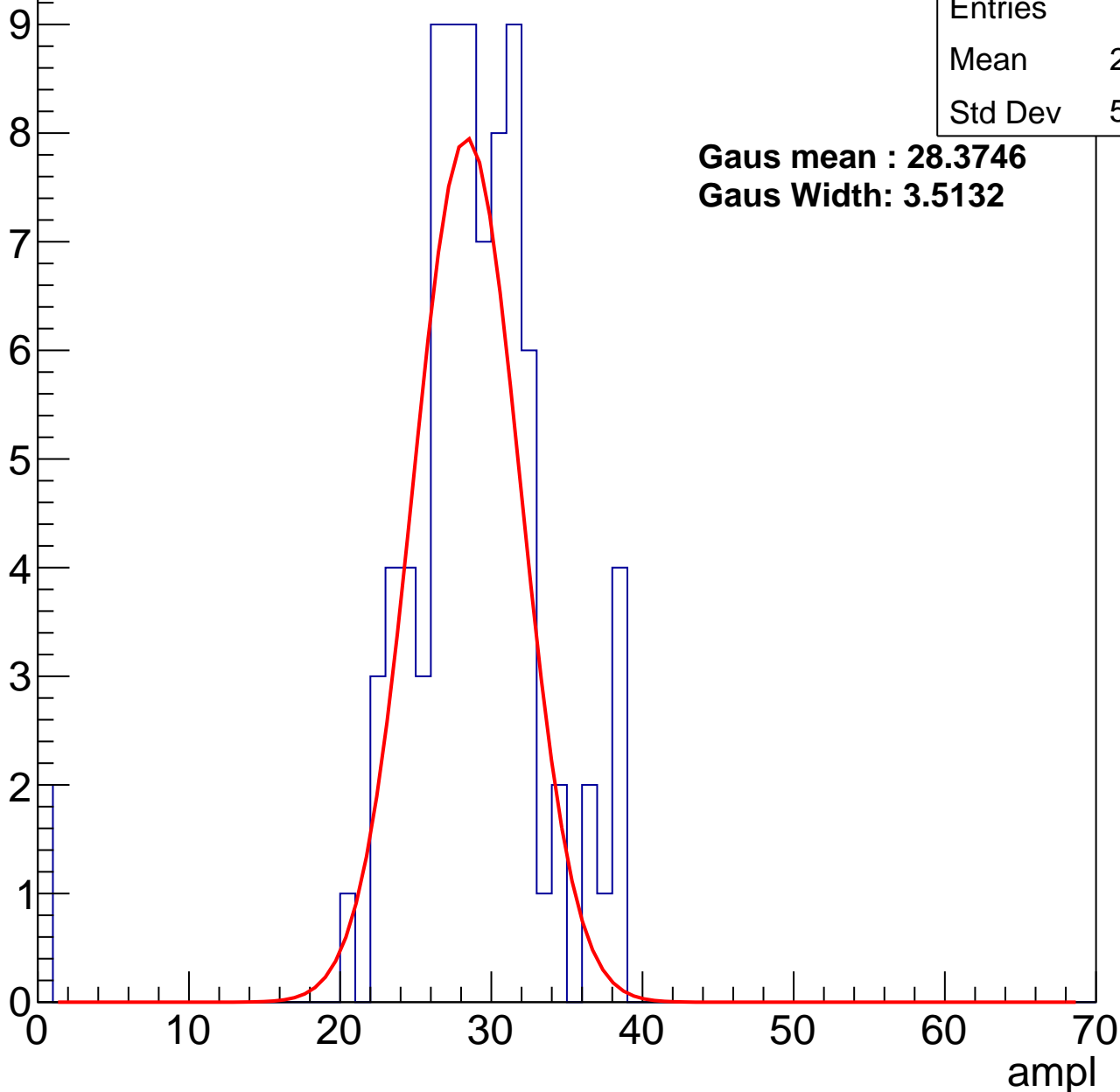
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.02
Std Dev	5.874

**Gaus mean : 28.3746**

**Gaus Width: 3.5132**



# B1L102S, U24-ch94, adc1

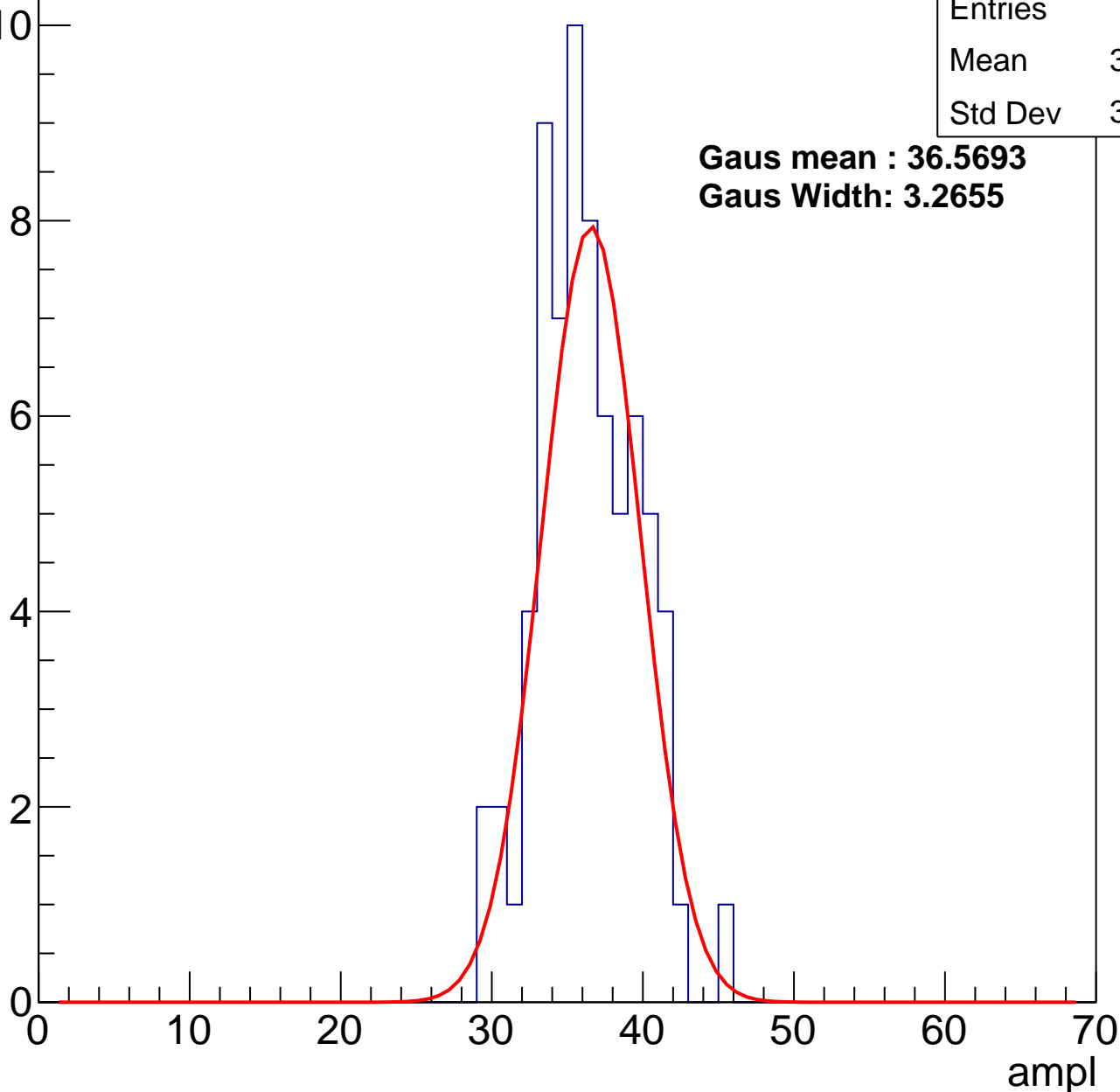
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.87
Std Dev	3.258

**Gaus mean : 36.5693**

**Gaus Width: 3.2655**

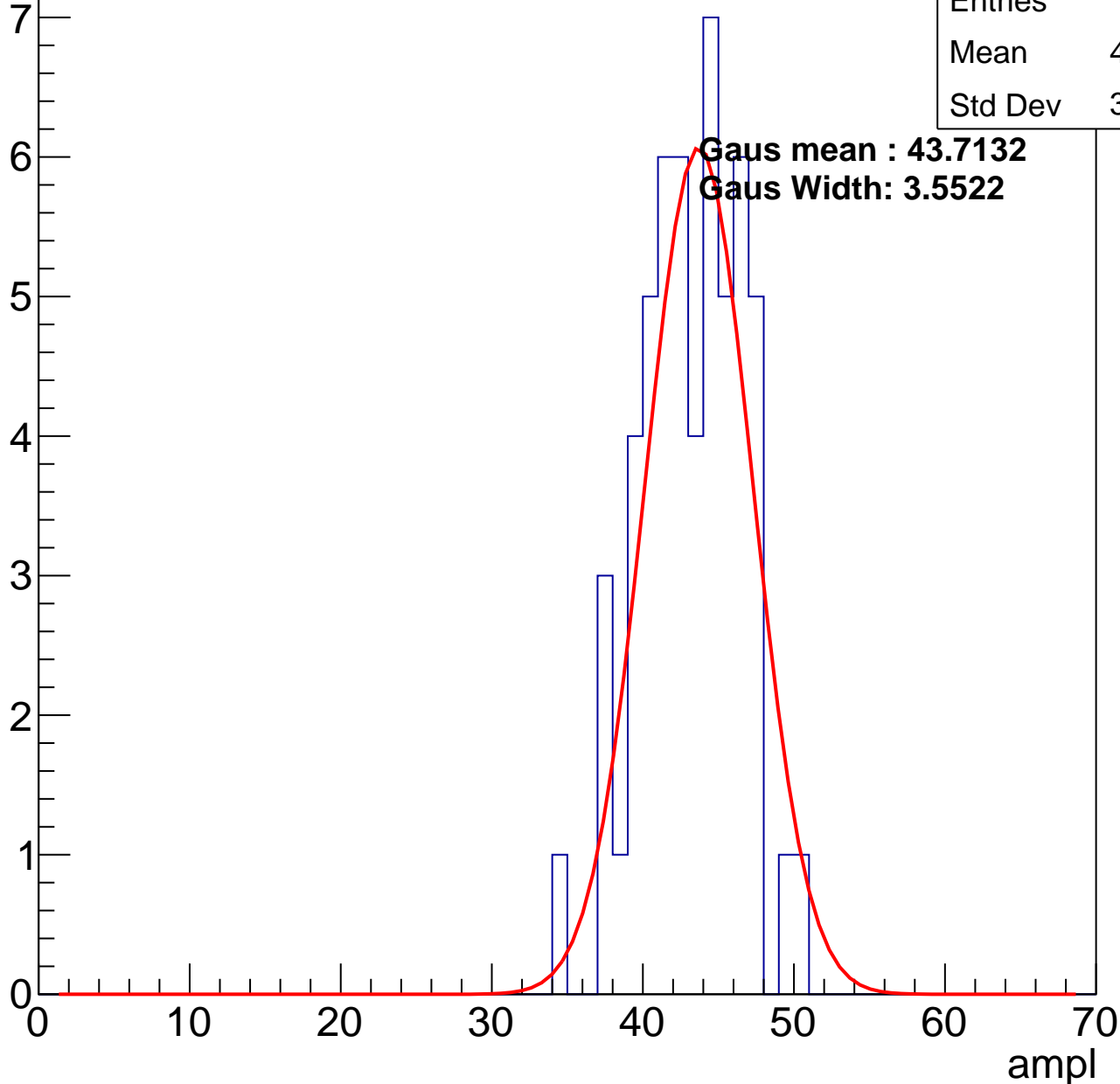


# B1L102S, U24-ch94, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.76
Std Dev	3.292

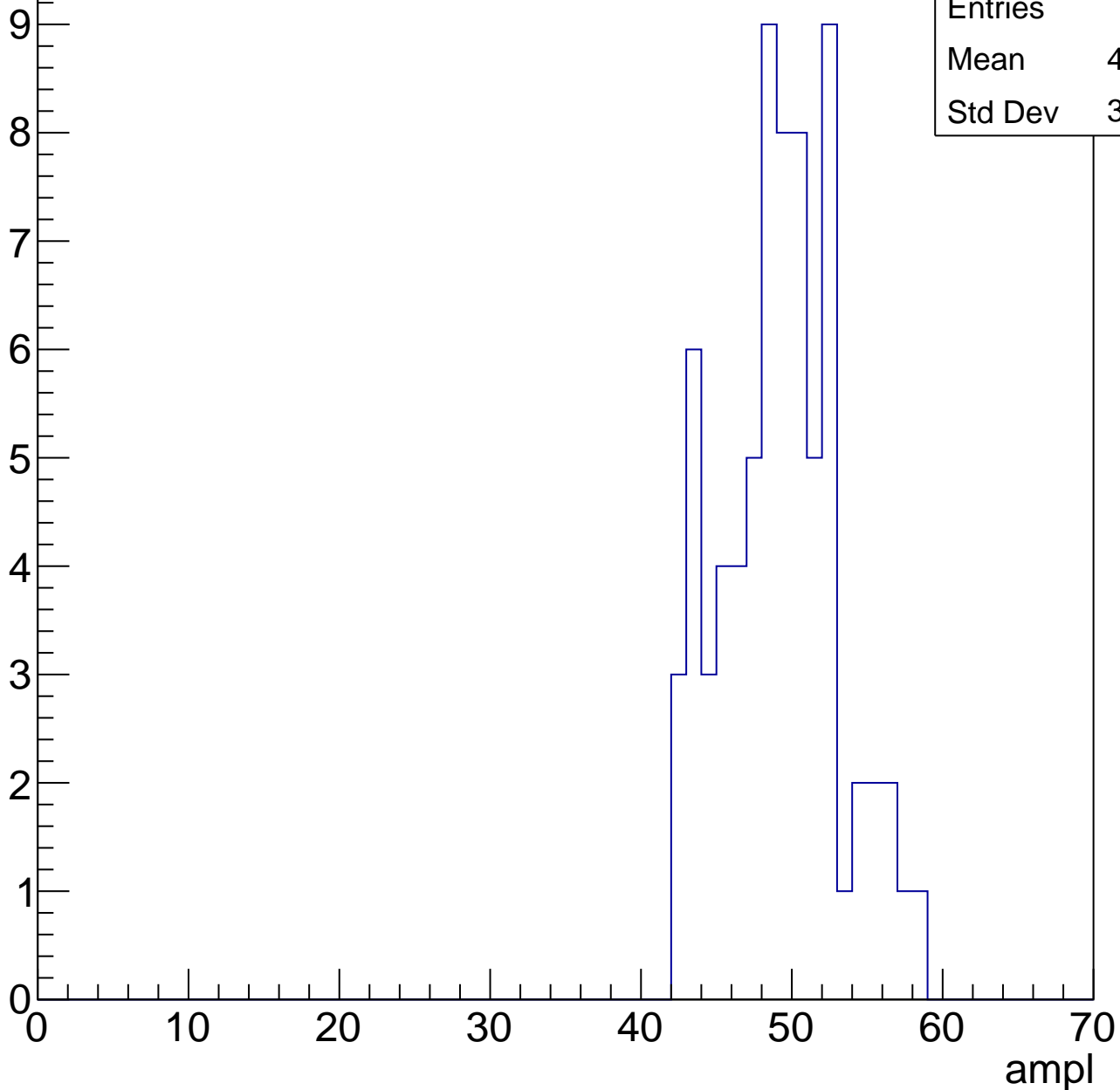


# B1L102S, U24-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.77
Std Dev	3.798

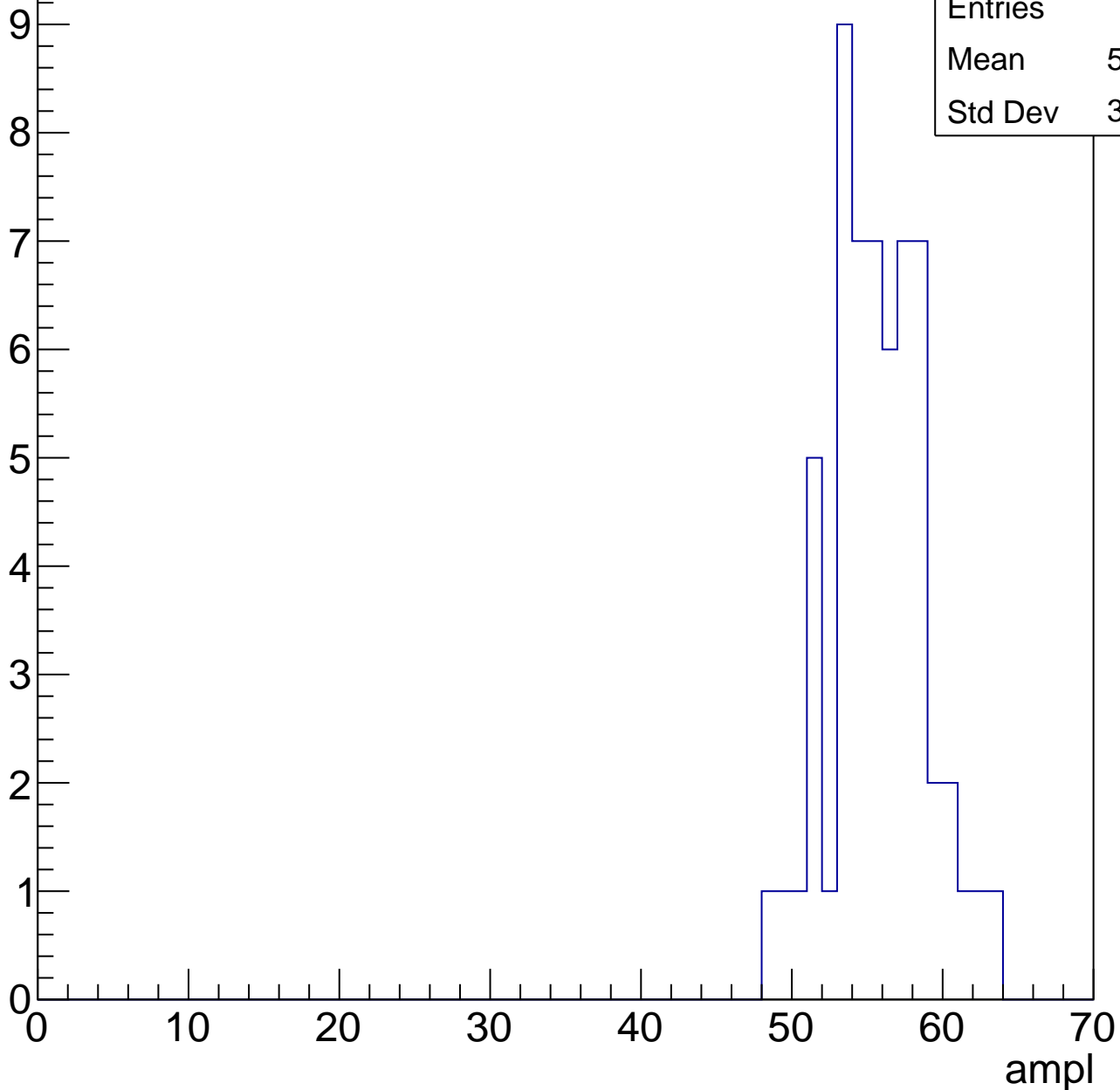


# B1L102S, U24-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.24
Std Dev	3.099



# B1L102S, U24-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	39
Mean	58.38
Std Dev	9.734

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

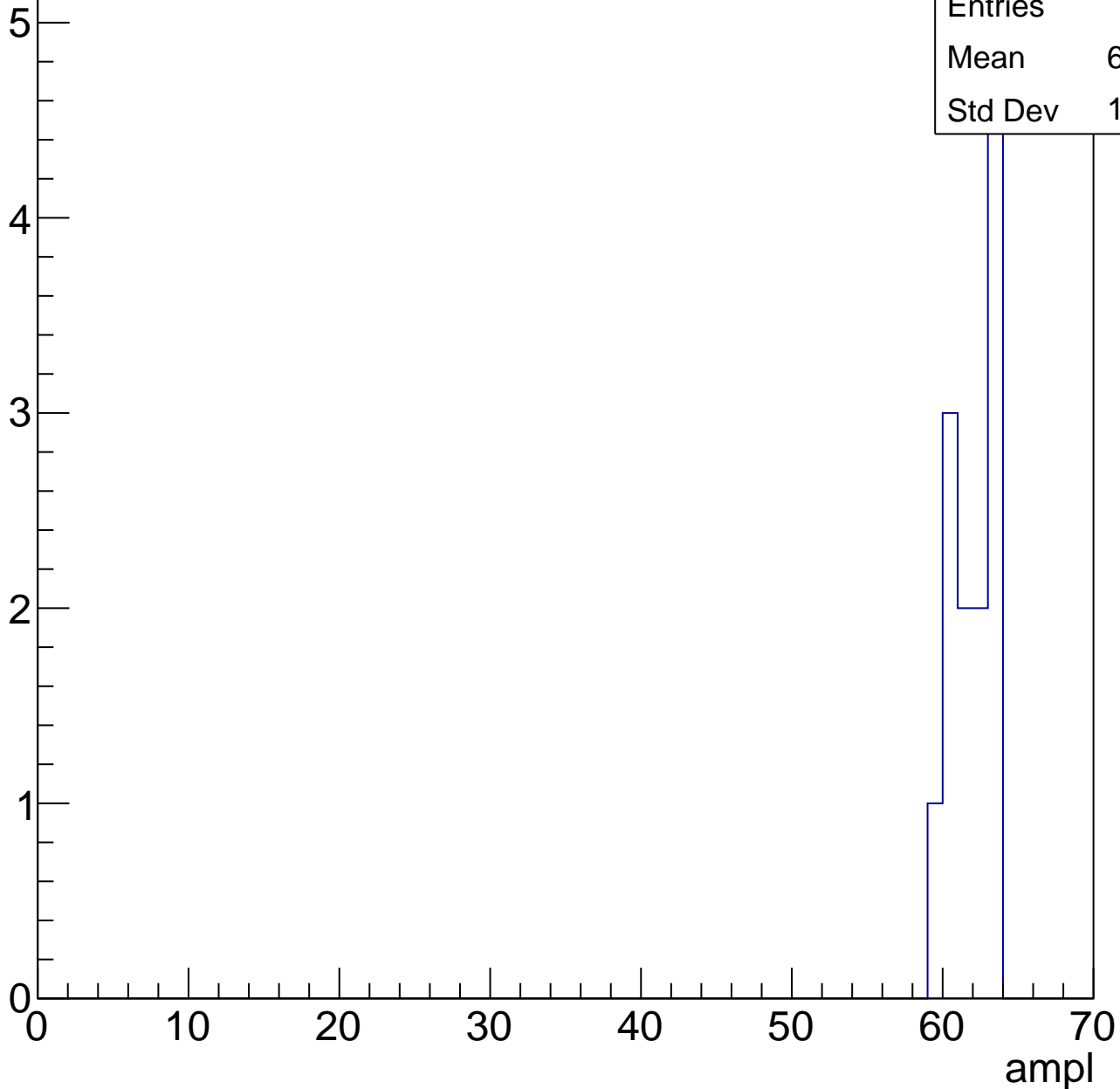
7

# B1L102S, U24-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.54
Std Dev	1.393





# B1L102S, U24-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	15
Std Dev	0

ampl

# B1L102S, U24-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	30.1
Std Dev	3.683

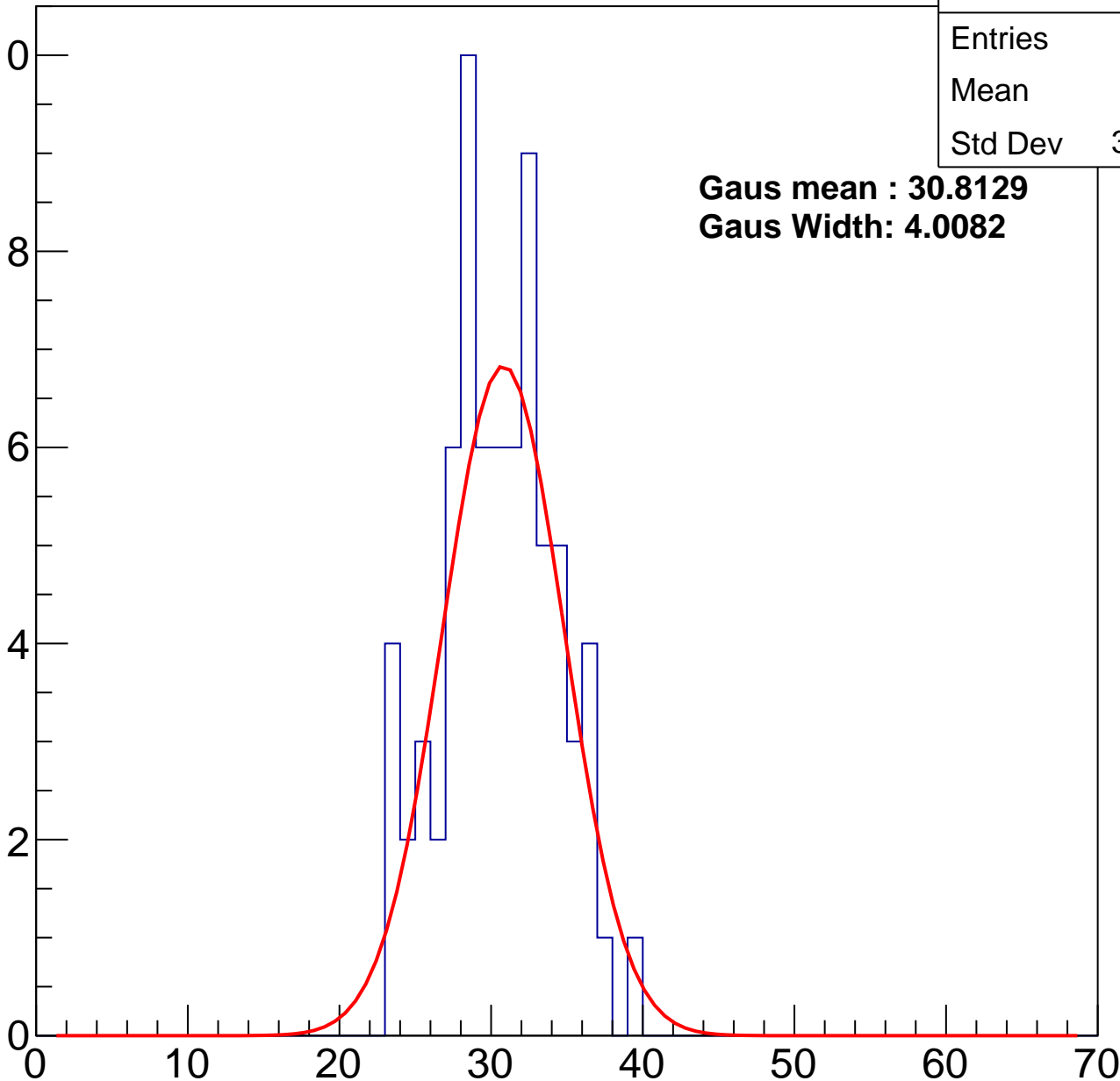
**Gaus mean : 30.8129**

**Gaus Width: 4.0082**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L102S, U24-ch95, adc1

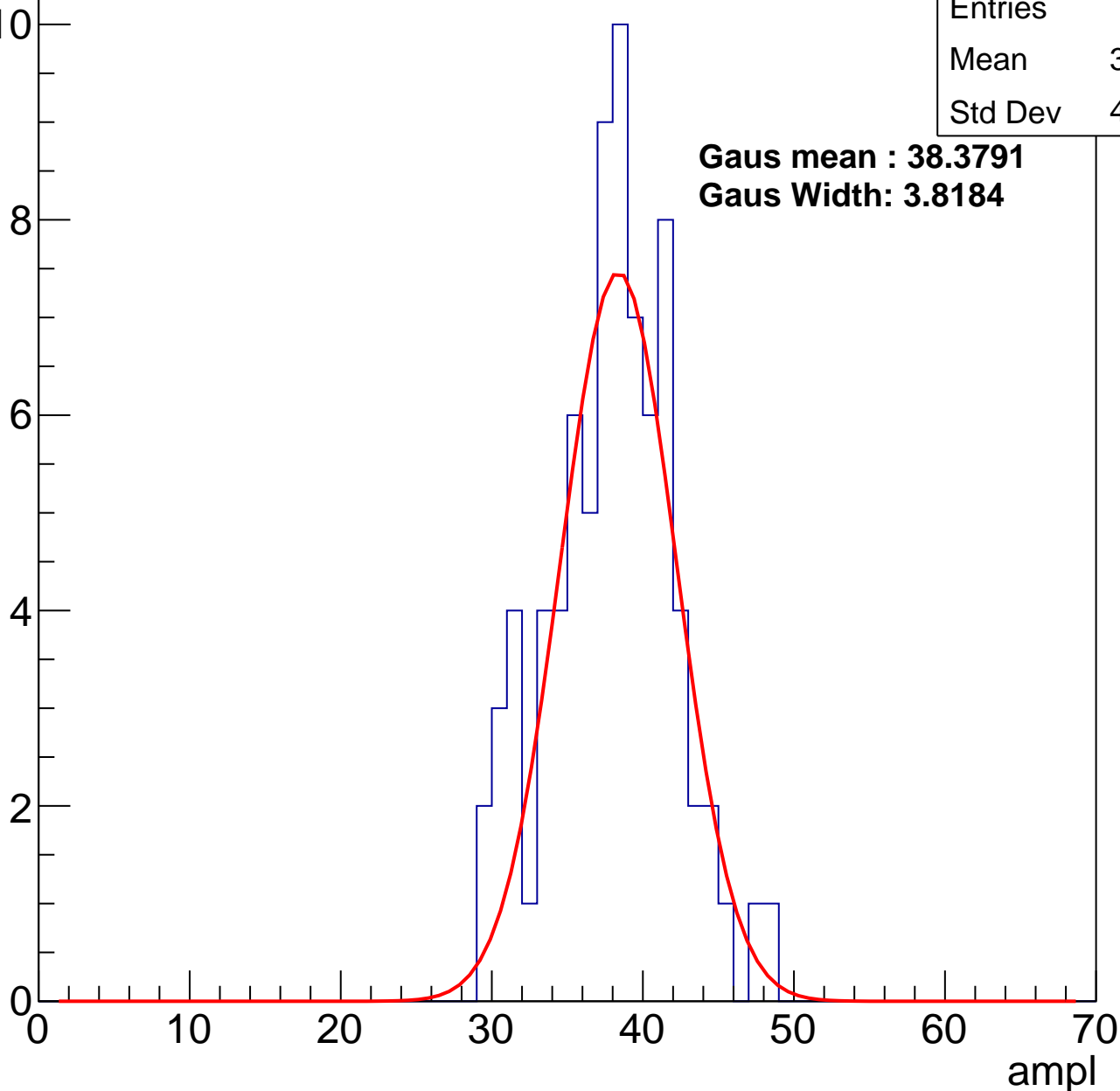
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	37.48
Std Dev	4.077

**Gaus mean : 38.3791**

**Gaus Width: 3.8184**



# B1L102S, U24-ch95, adc2

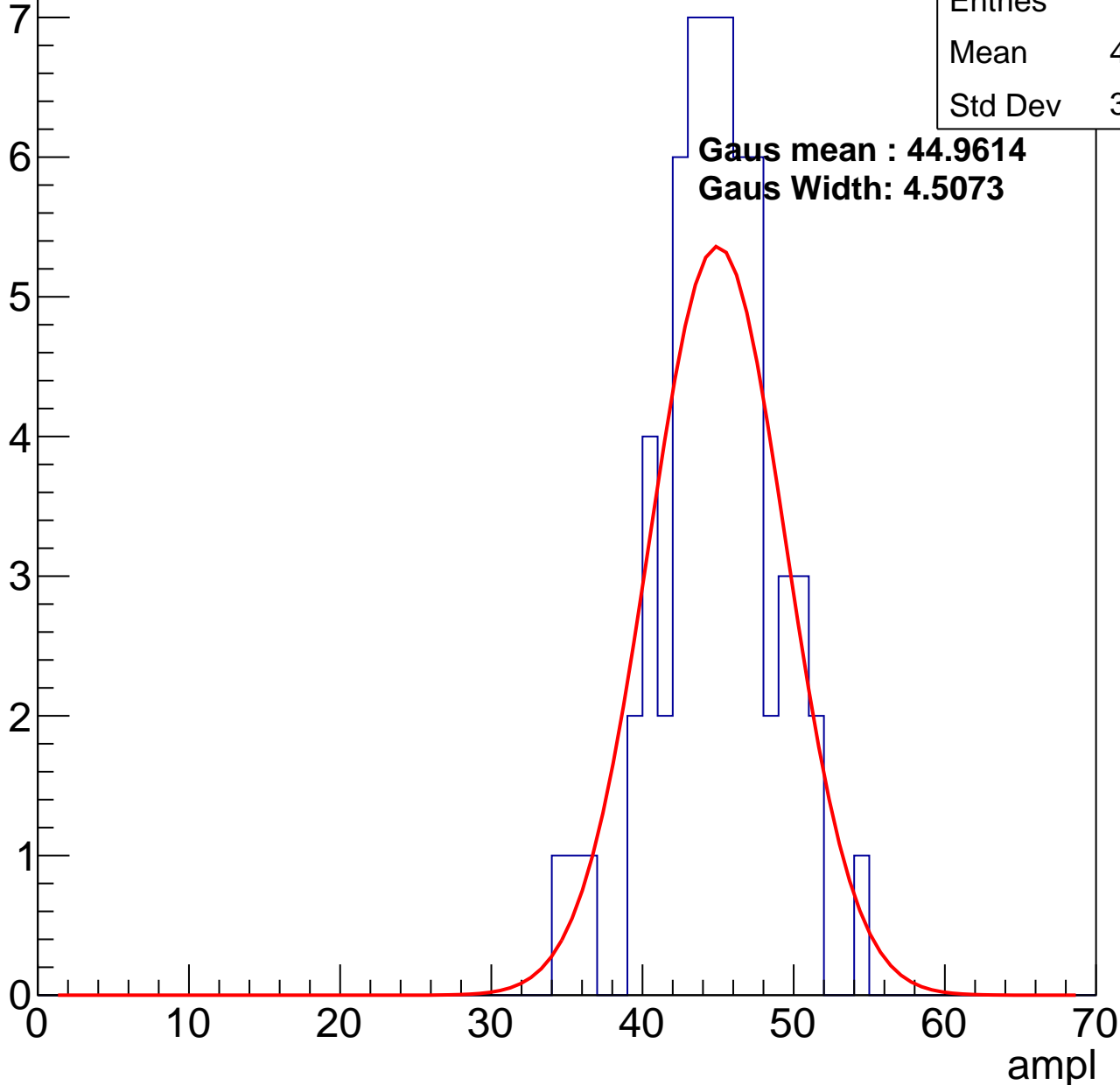
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	44.39
Std Dev	3.834

**Gaus mean : 44.9614**

**Gaus Width: 4.5073**

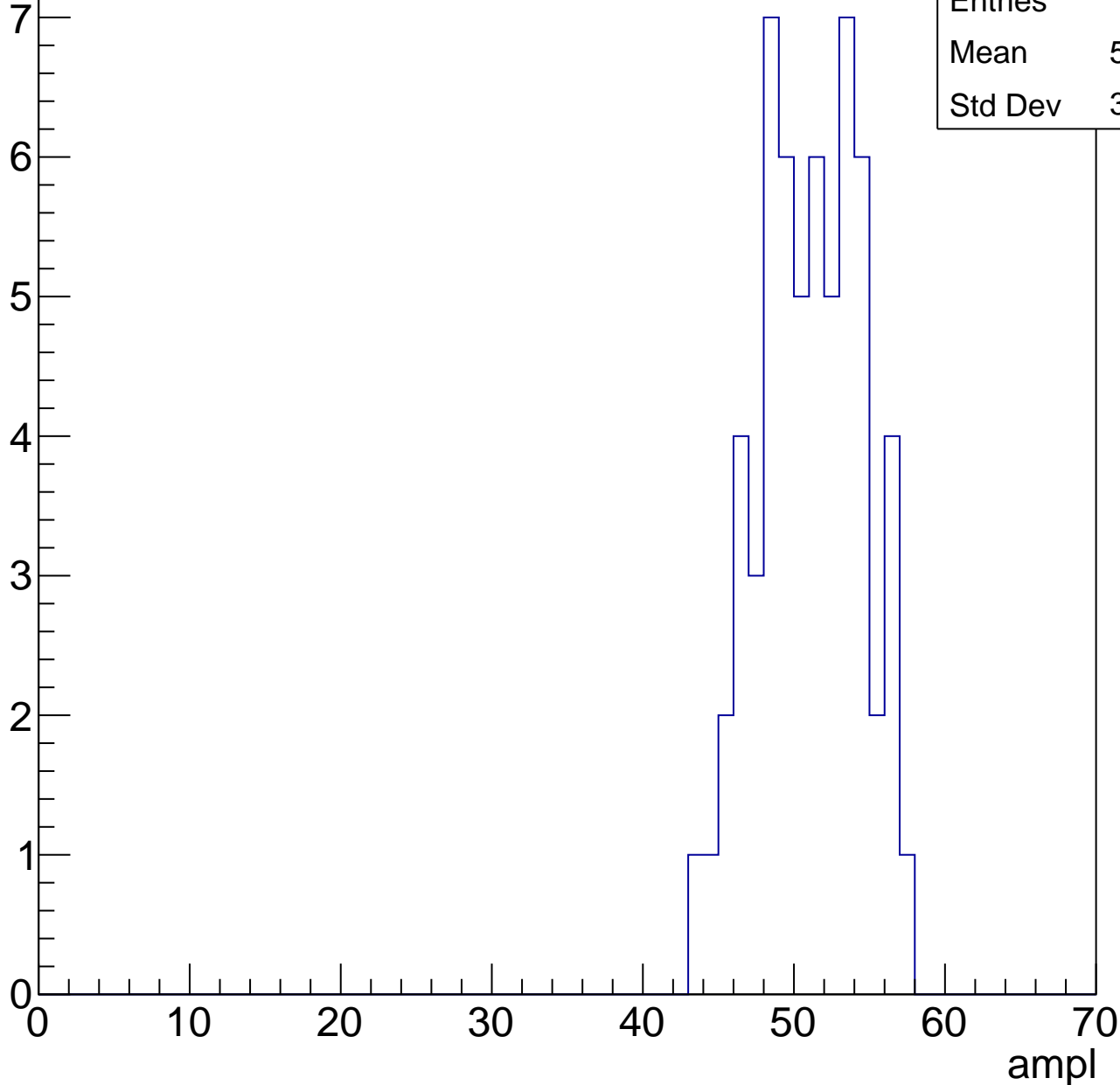


# B1L102S, U24-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.57
Std Dev	3.338



# B1L102S, U24-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	57.27
Std Dev	3.468

Entry

10

8

6

4

2

0

0

10

20

30

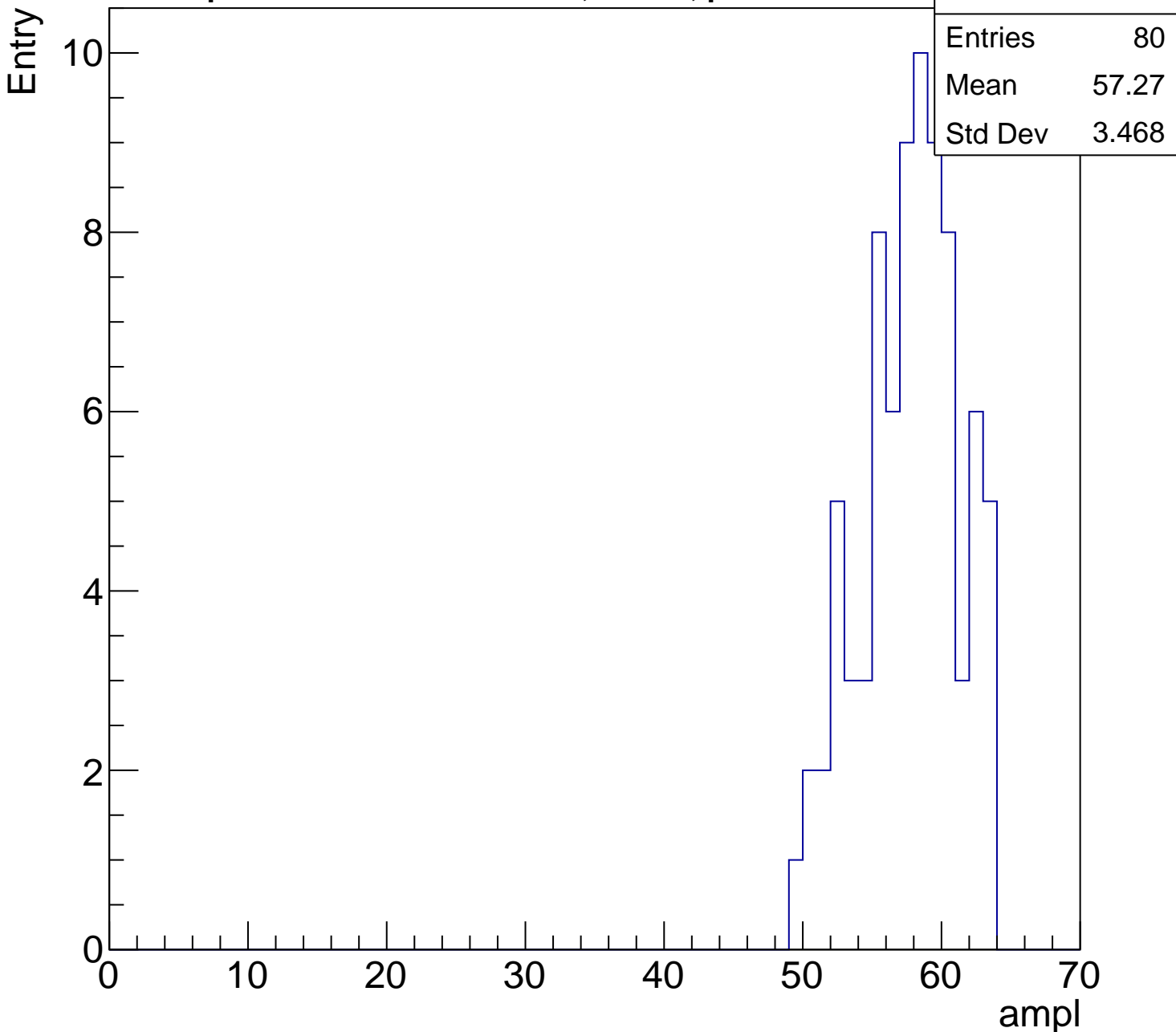
40

50

60

ampl

70

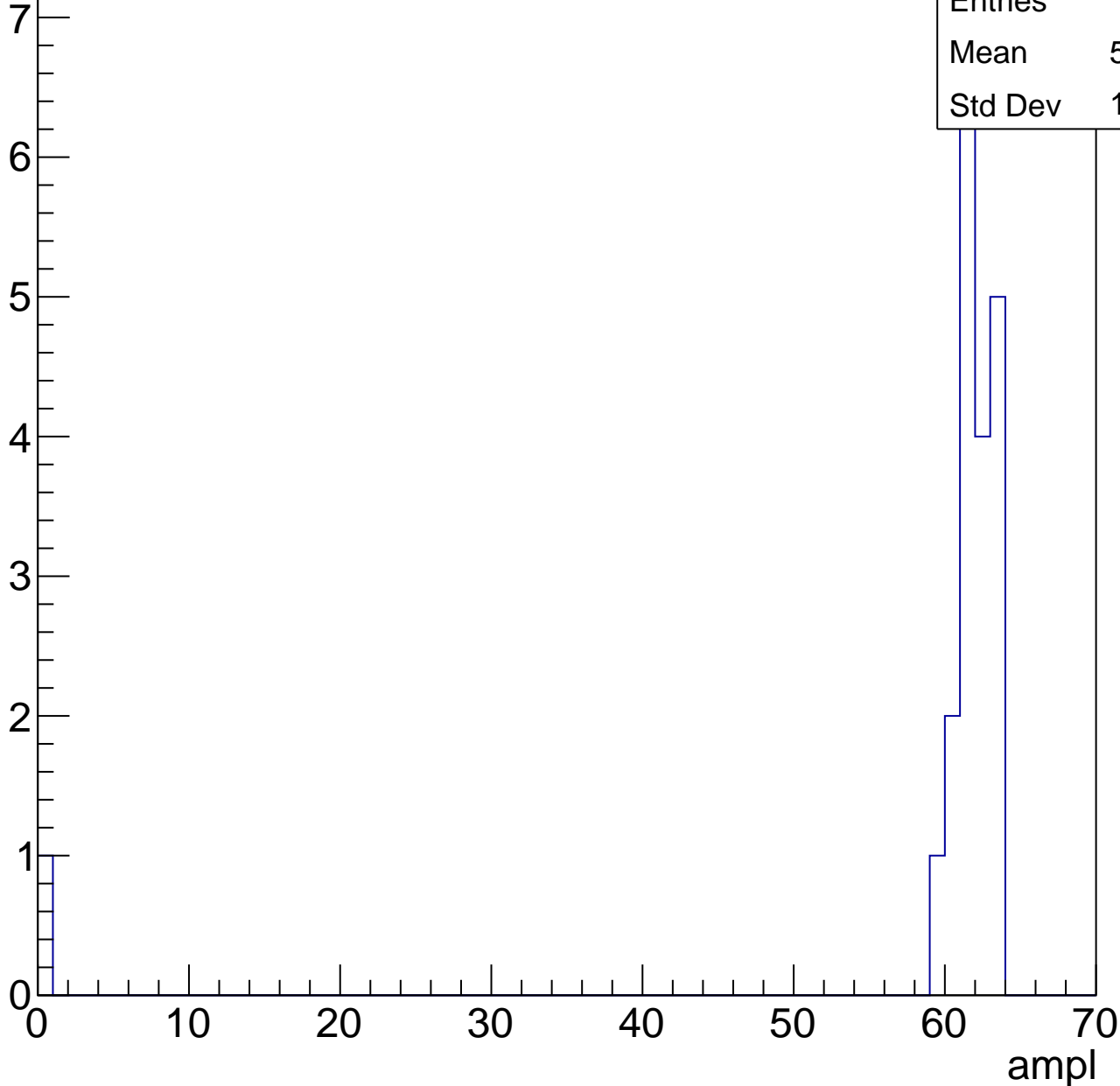


# B1L102S, U24-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	20
Mean	58.45
Std Dev	13.46



# B1L102S, U24-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0



# B1L102S, U24-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch96, adc0

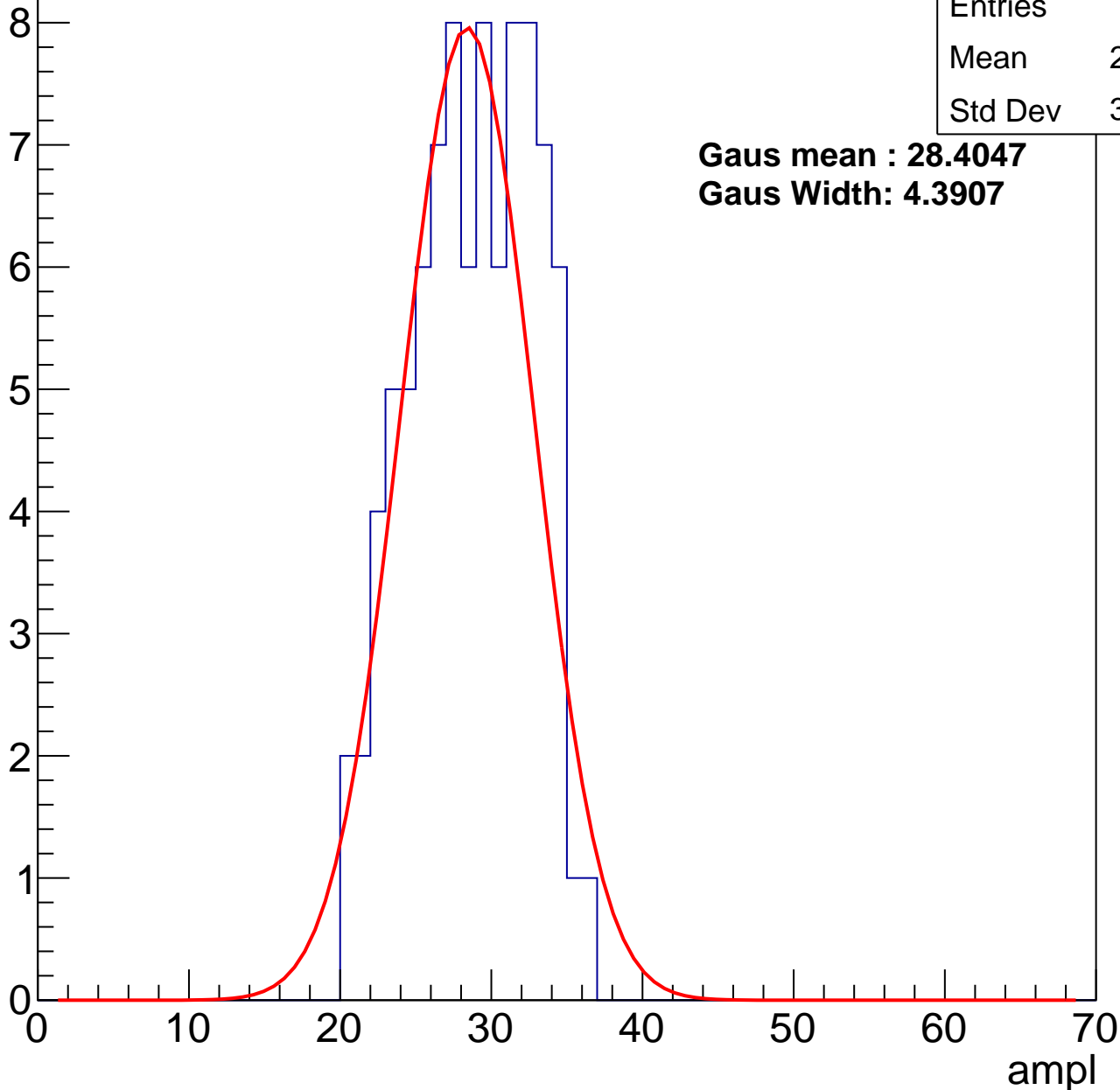
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	90
Mean	28.26
Std Dev	3.937

**Gaus mean : 28.4047**

**Gaus Width: 4.3907**



# B1L102S, U24-ch96, adc1

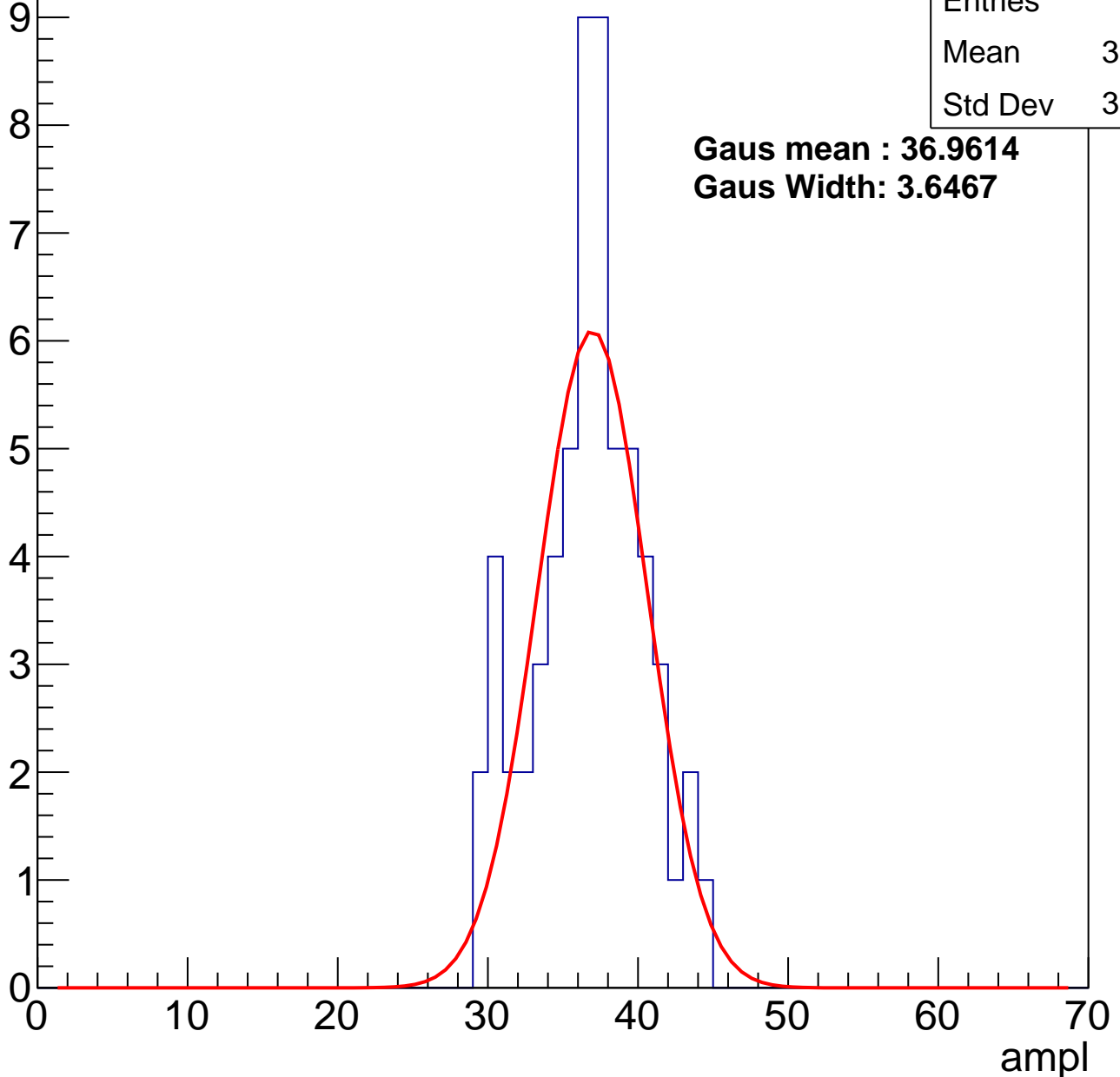
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.25
Std Dev	3.574

**Gaus mean : 36.9614**

**Gaus Width: 3.6467**



# B1L102S, U24-ch96, adc2

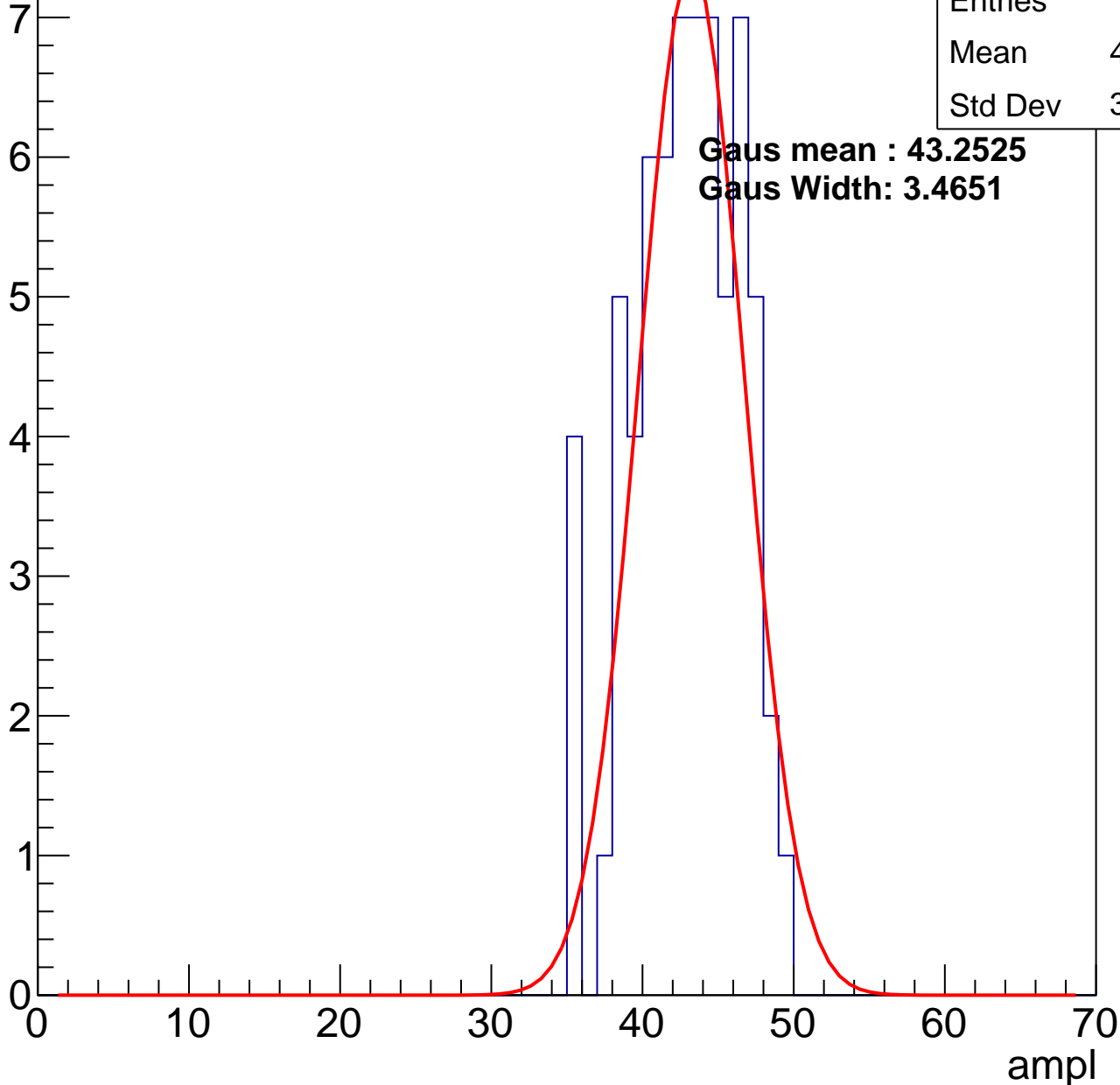
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.37
Std Dev	3.446

**Gaus mean : 43.2525**

**Gaus Width: 3.4651**

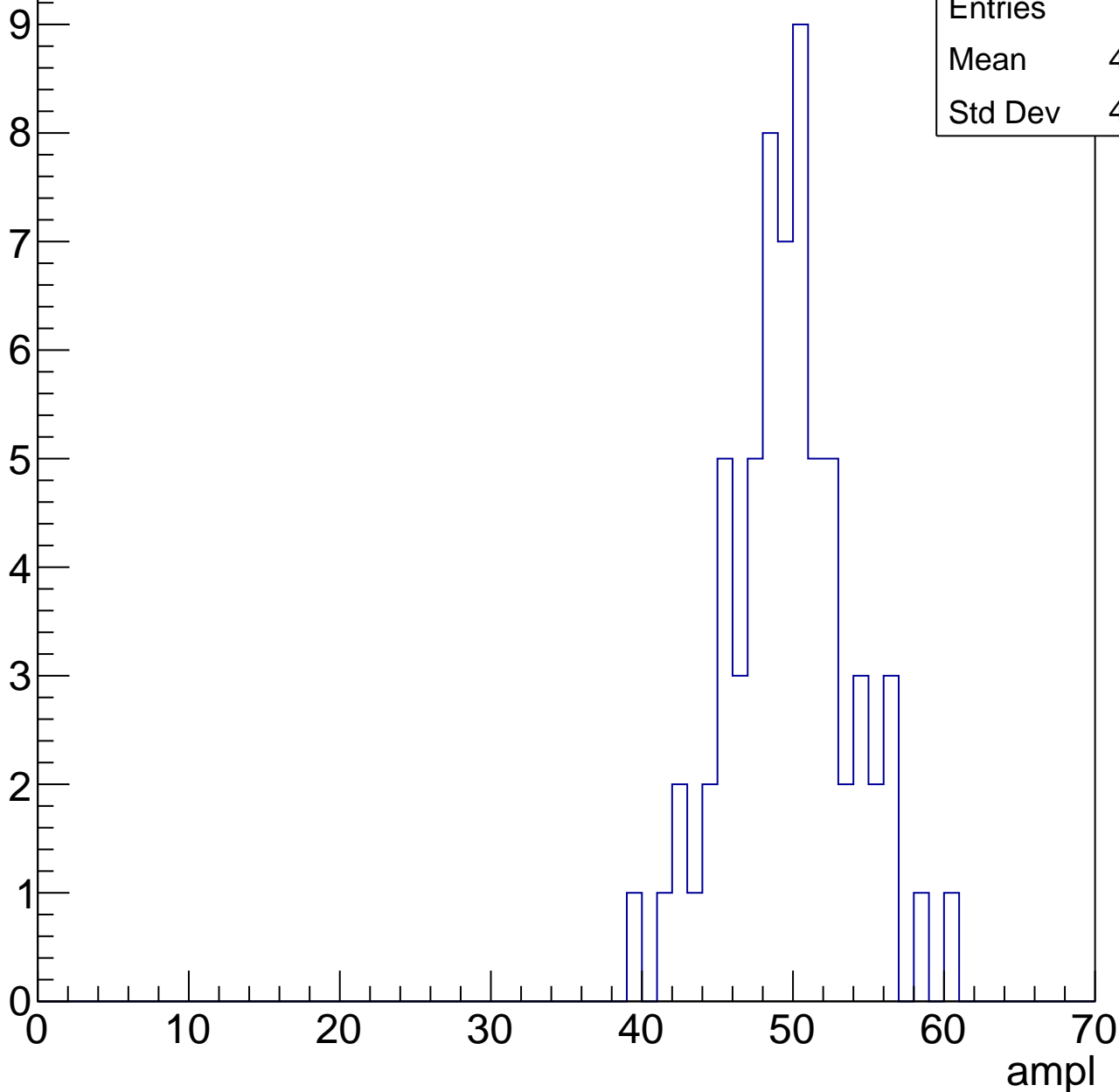


# B1L102S, U24-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.23
Std Dev	4.048

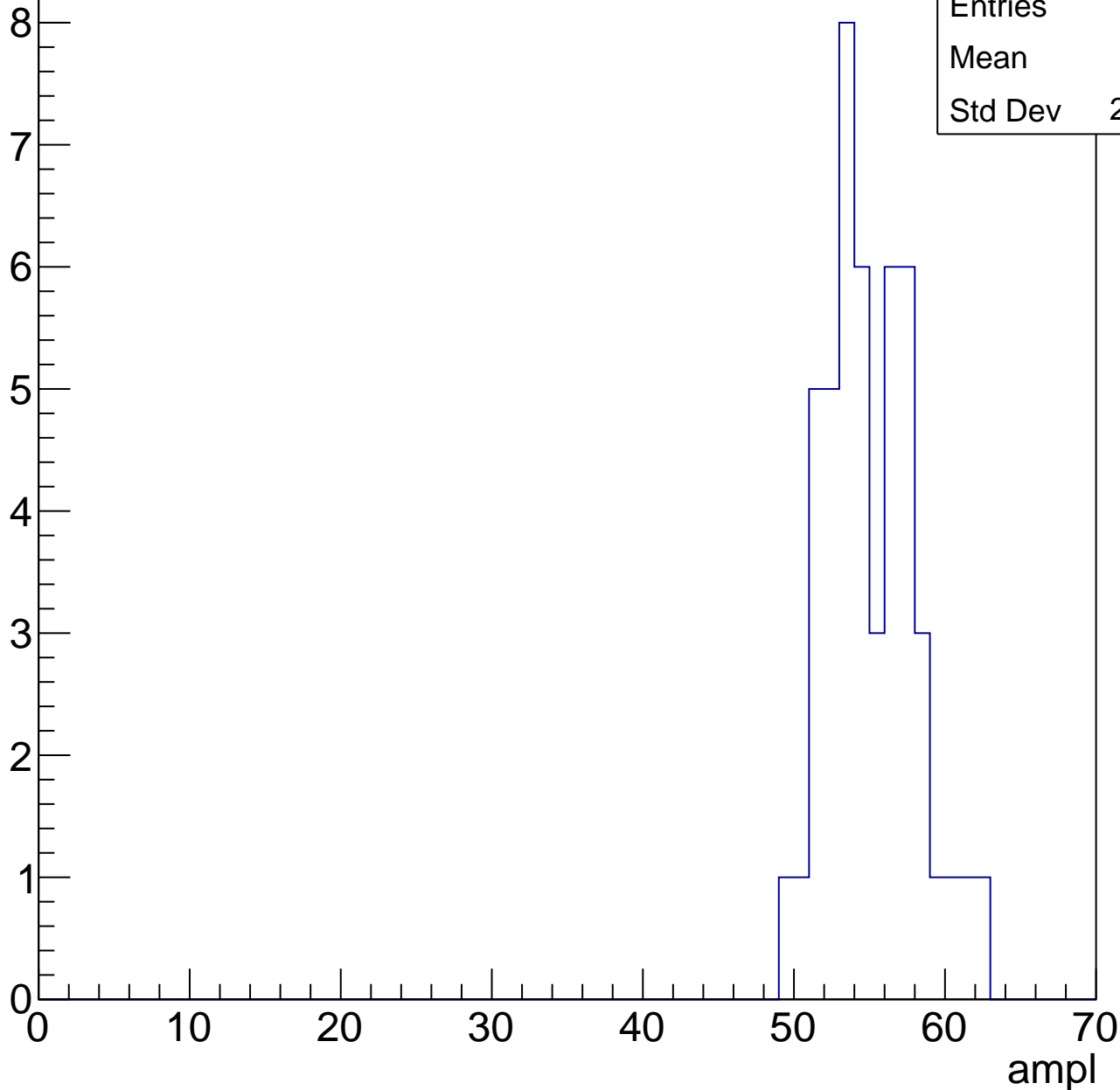


# B1L102S, U24-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

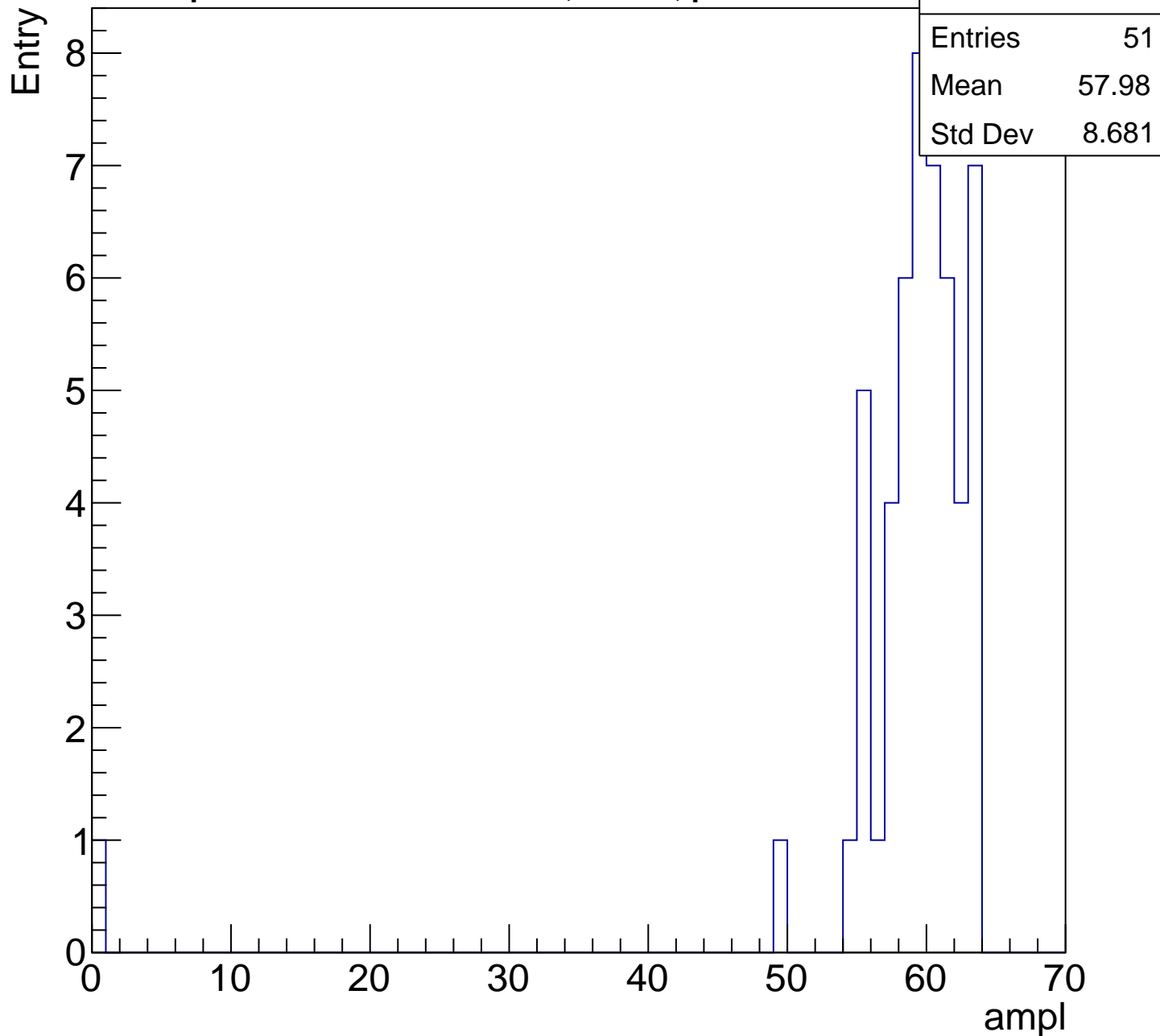
Entry

Entries	48
Mean	54.6
Std Dev	2.885



# B1L102S, U24-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

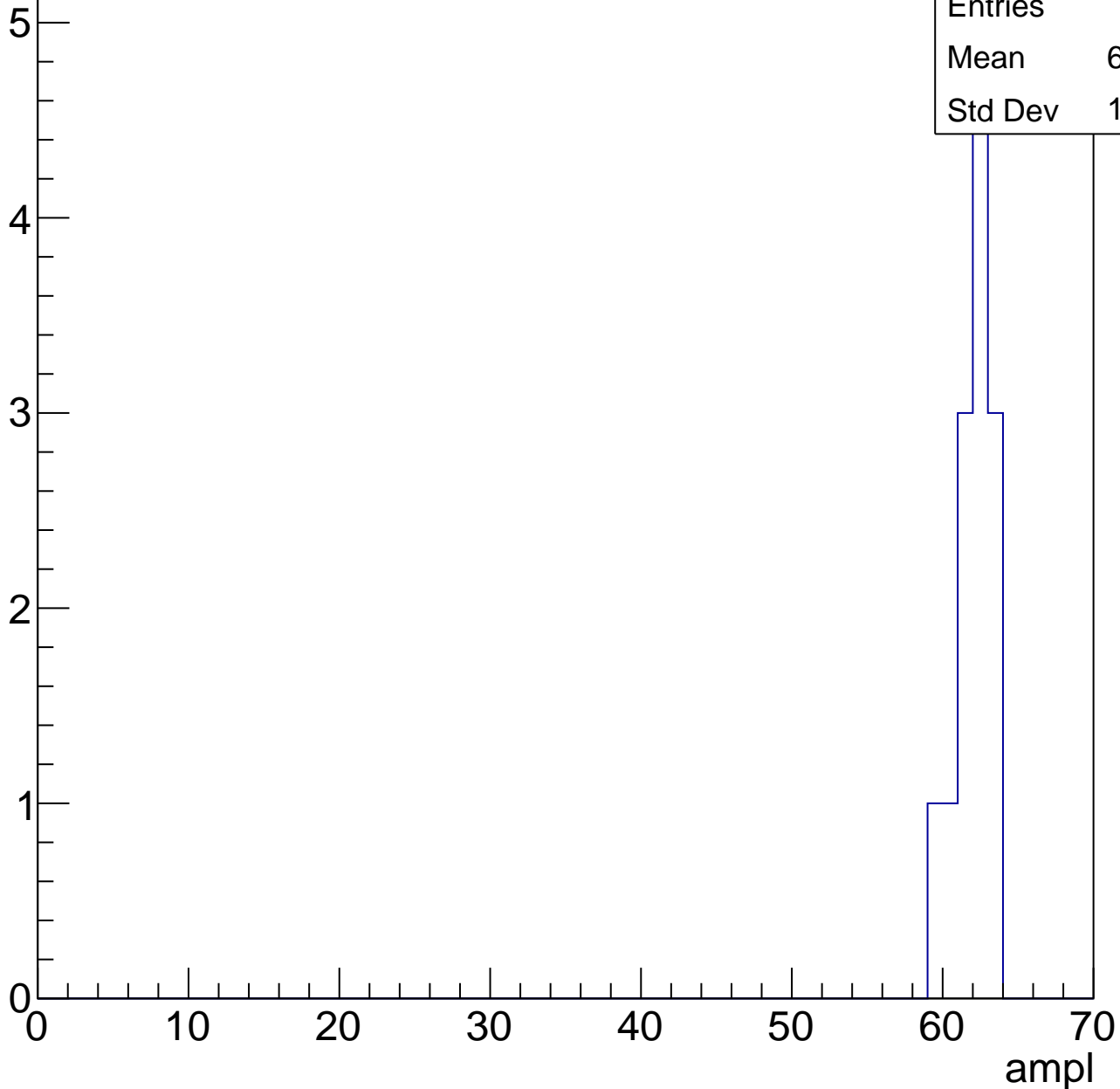


# B1L102S, U24-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.62
Std Dev	1.146





# B1L102S, U24-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch97, adc0

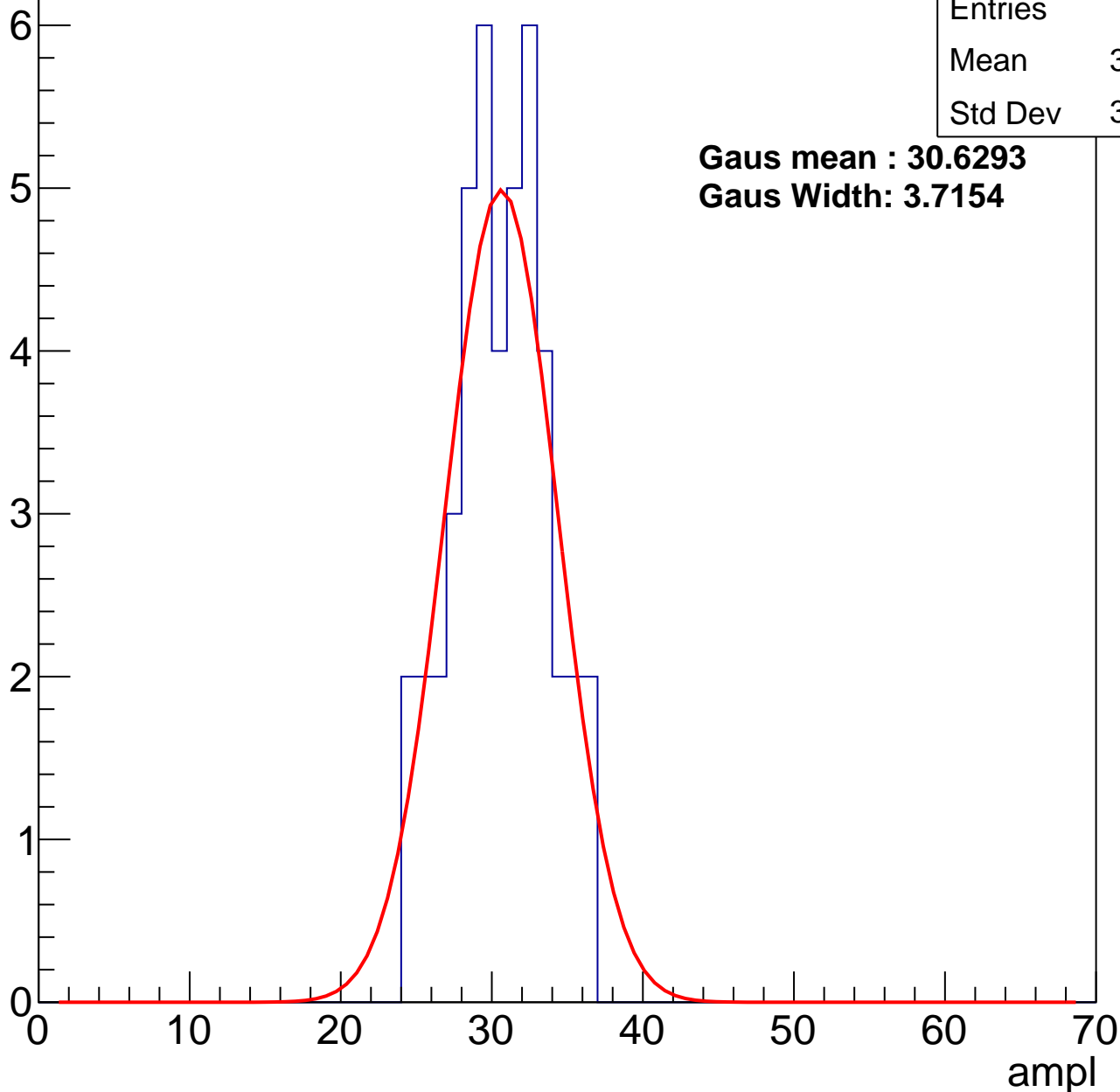
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	30.09
Std Dev	3.076

**Gaus mean : 30.6293**

**Gaus Width: 3.7154**



# B1L102S, U24-ch97, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	82
Mean	36.61
Std Dev	3.41

**Gaus mean : 36.8047**

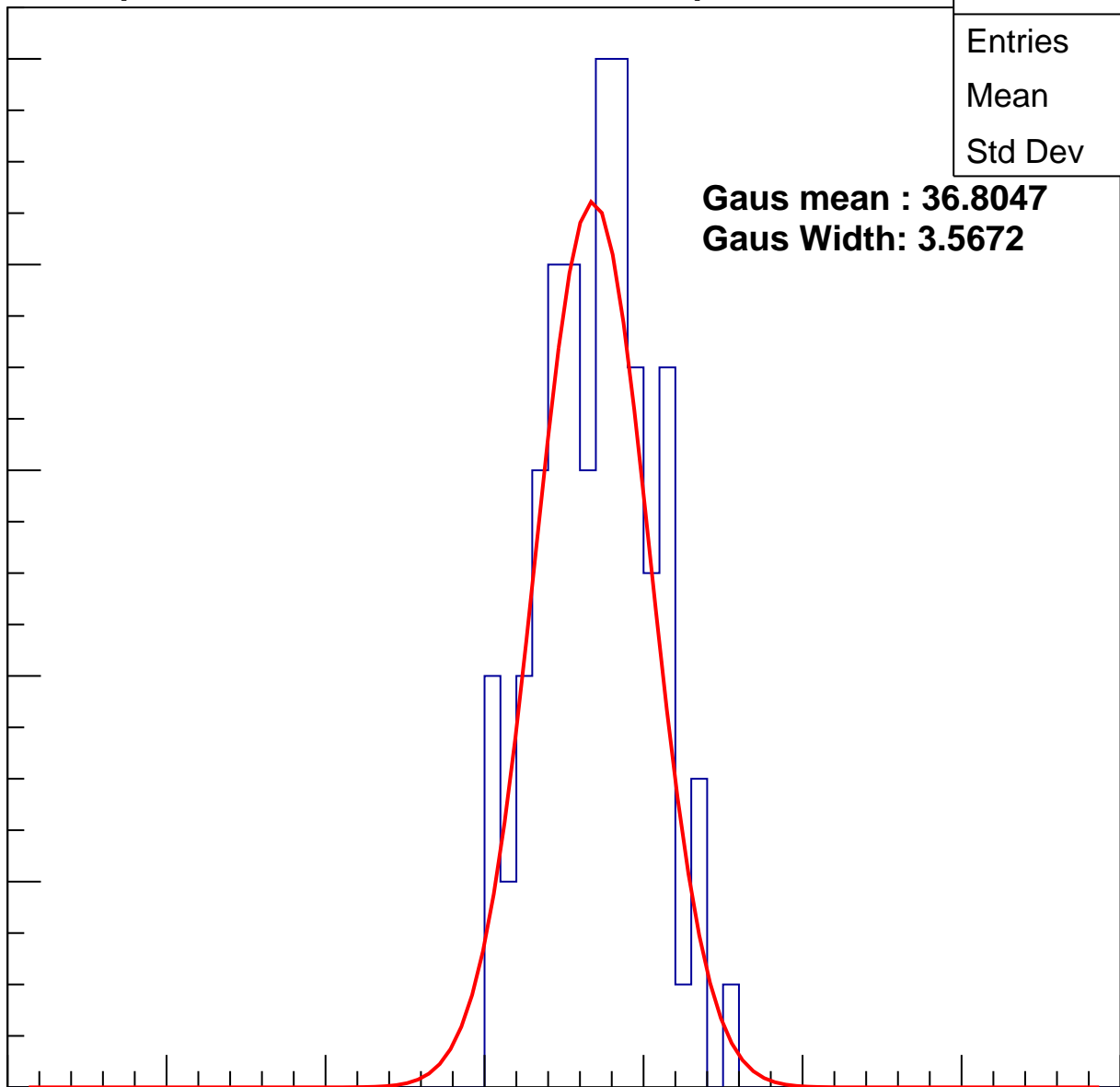
**Gaus Width: 3.5672**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch97, adc2

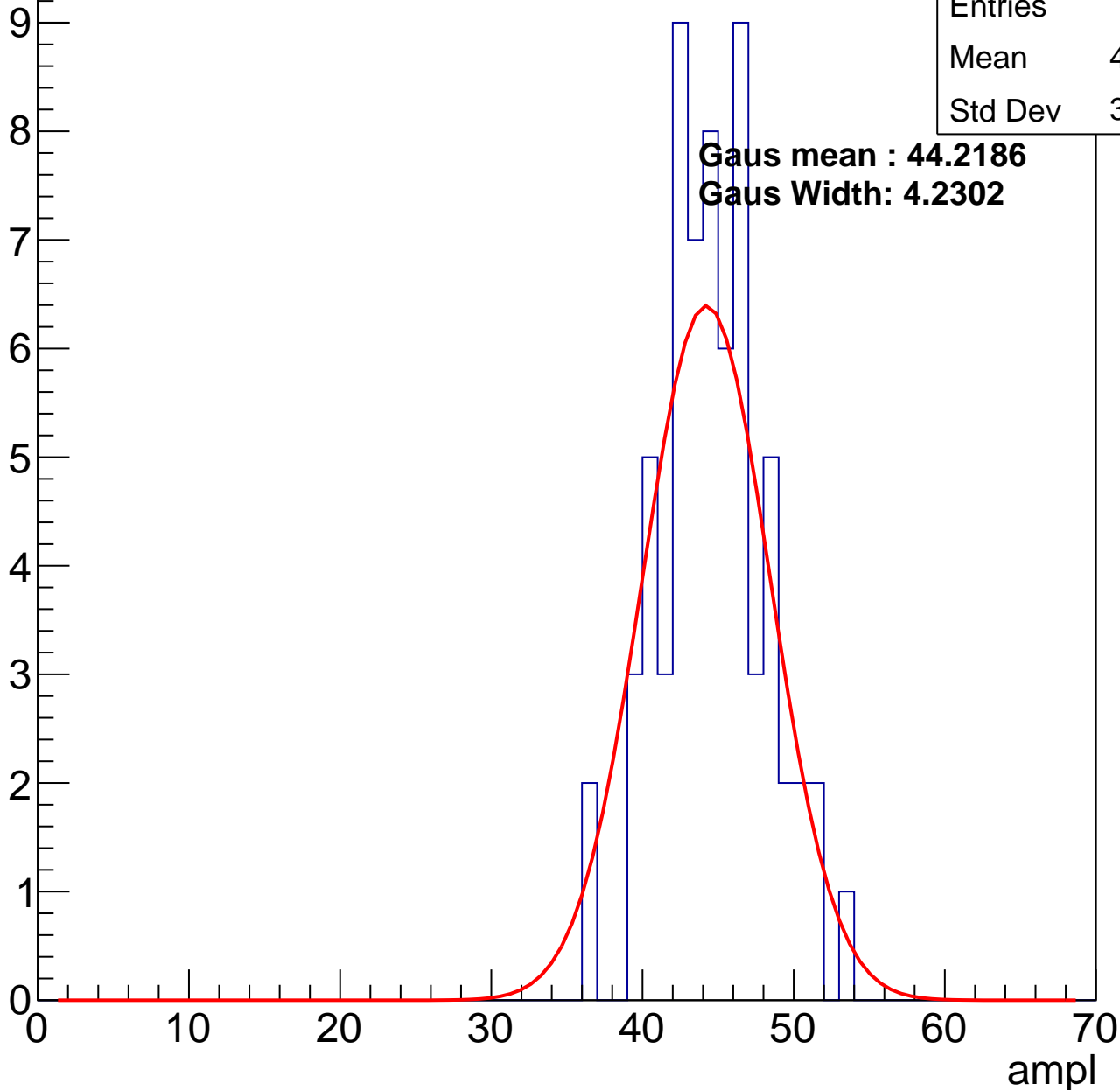
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.19
Std Dev	3.457

**Gaus mean : 44.2186**

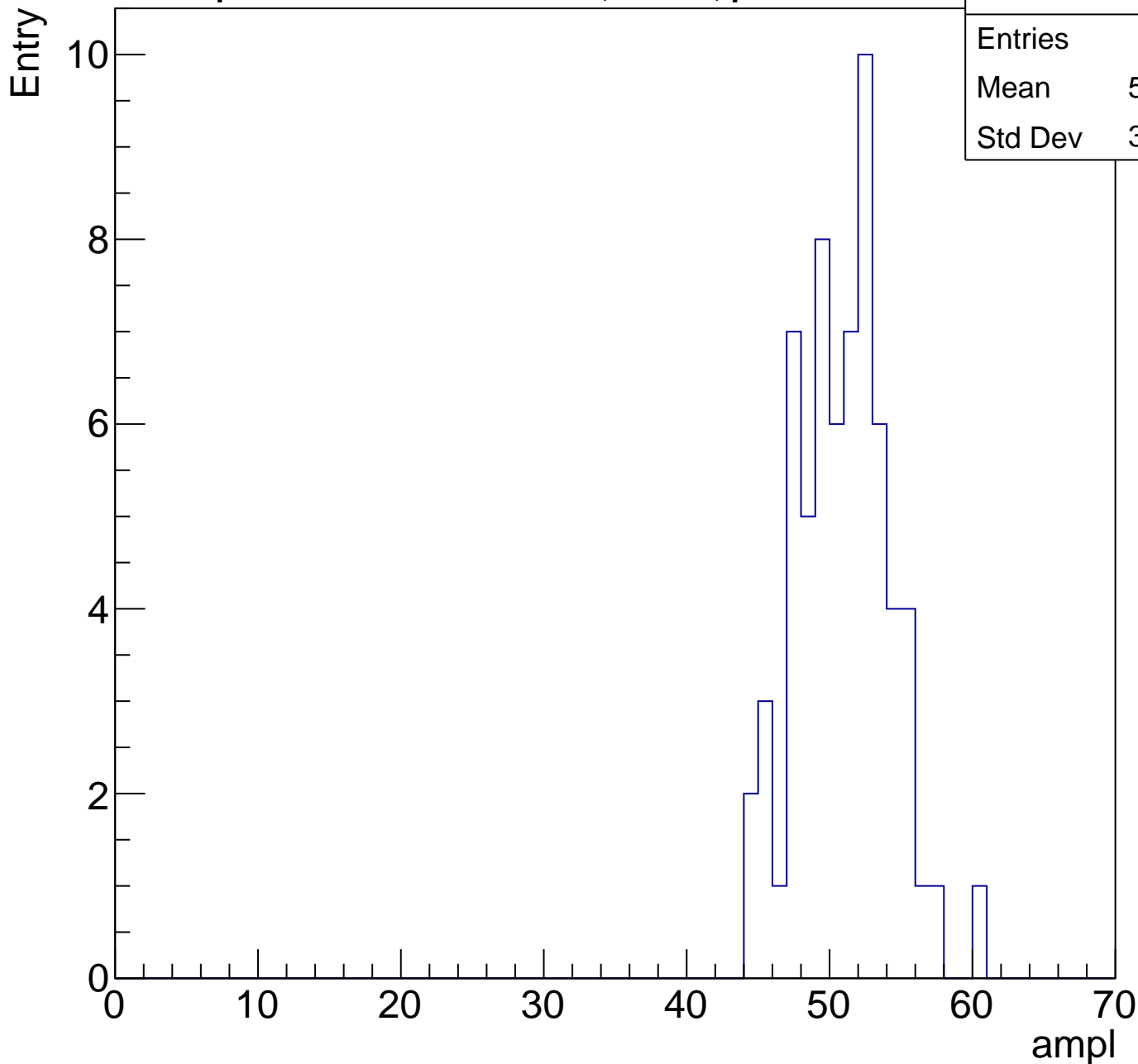
**Gaus Width: 4.2302**



# B1L102S, U24-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	66
Mean	50.52
Std Dev	3.225

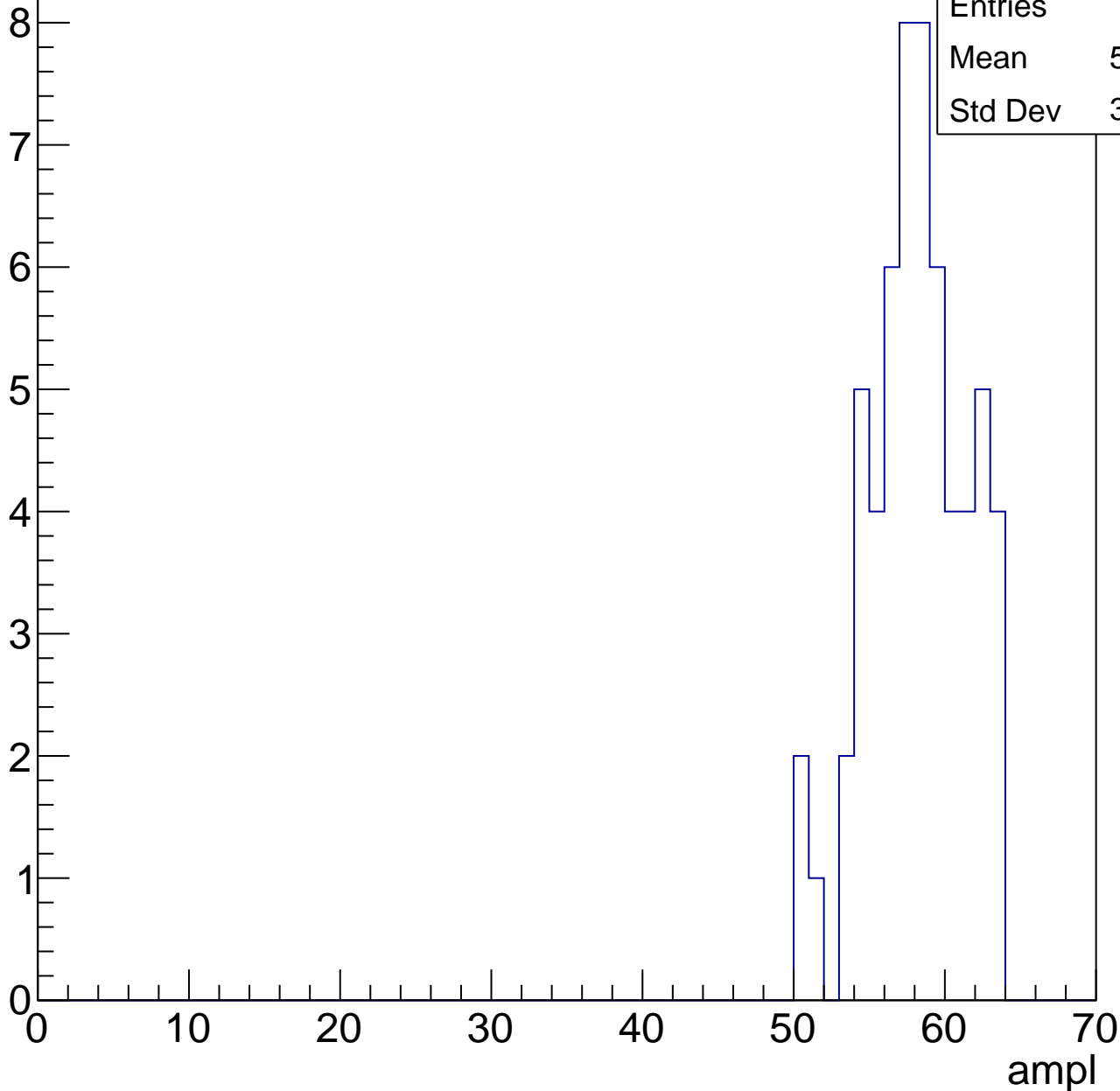


# B1L102S, U24-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.68
Std Dev	3.207

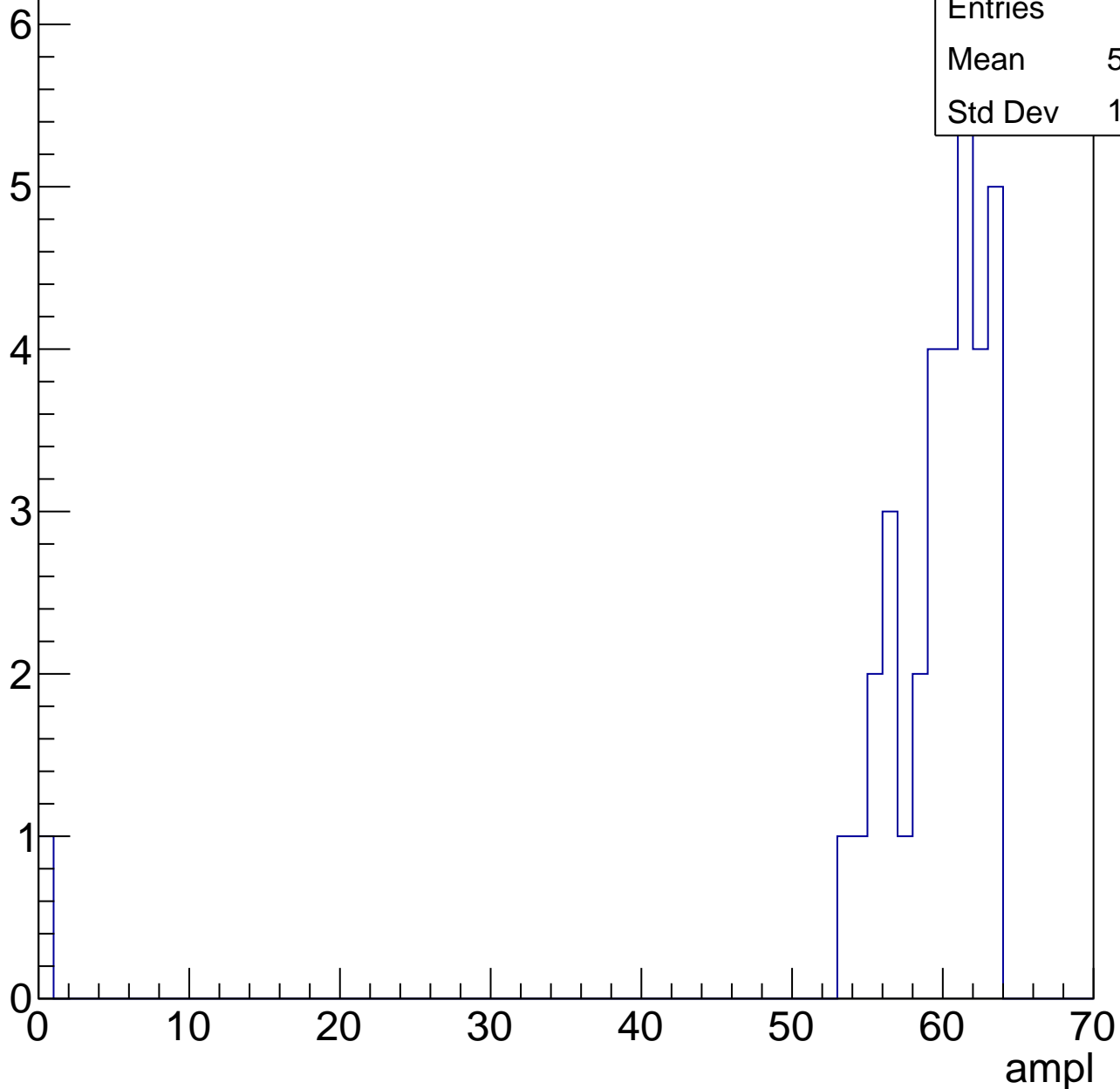


# B1L102S, U24-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	34
Mean	57.74
Std Dev	10.43



# B1L102S, U24-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch98, adc0

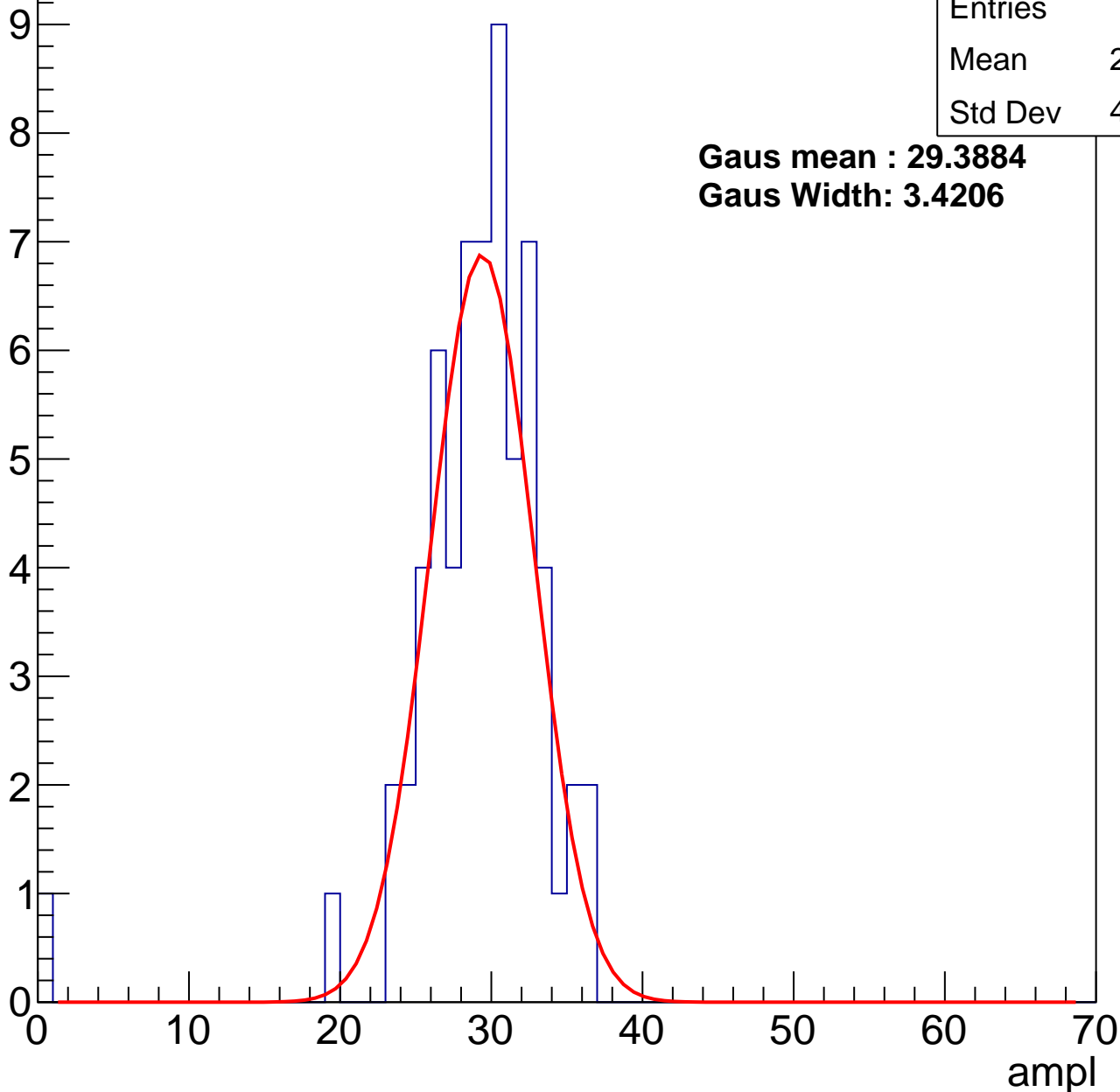
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.64
Std Dev	4.916

**Gaus mean : 29.3884**

**Gaus Width: 3.4206**



# B1L102S, U24-ch98, adc1

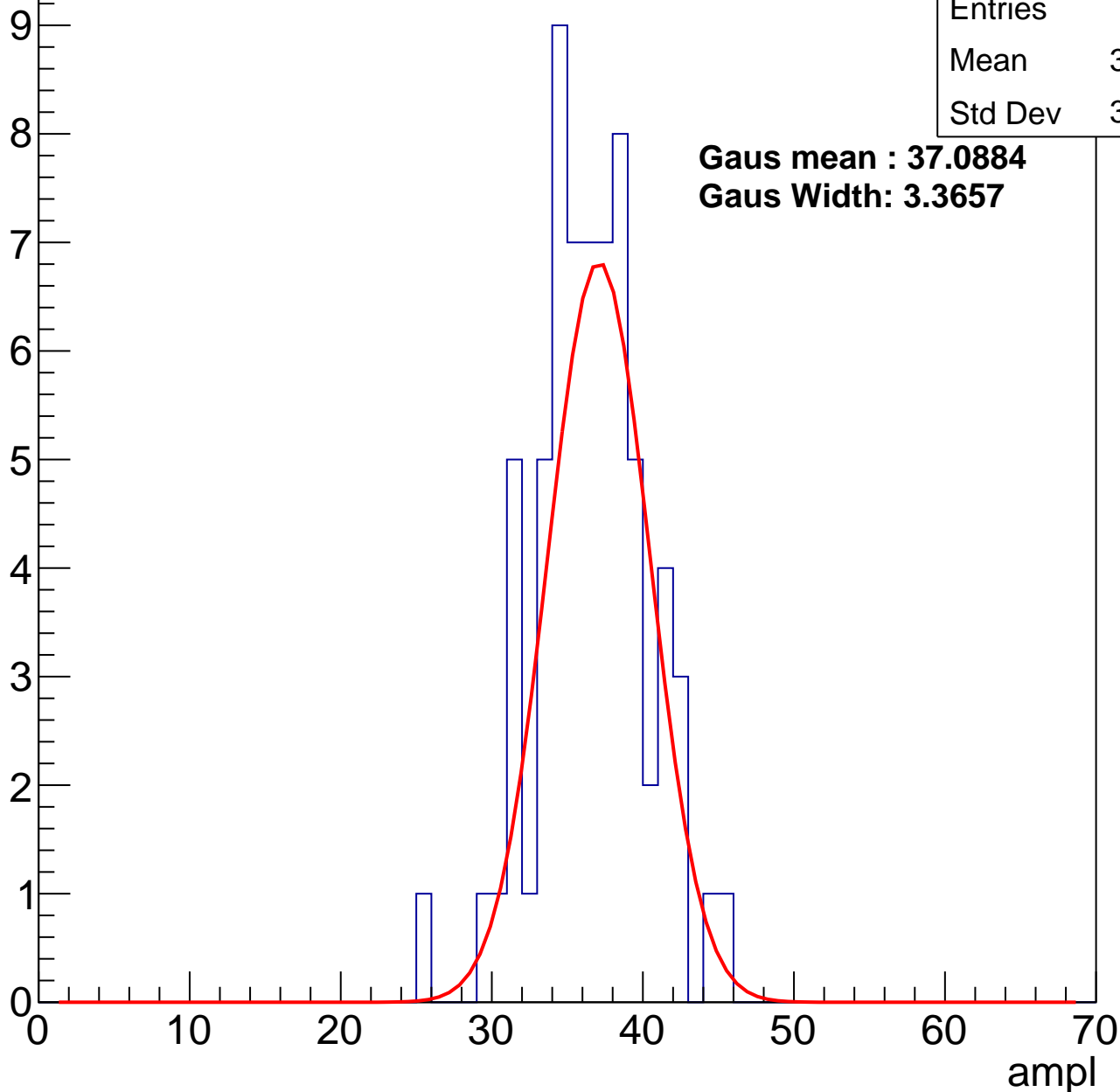
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.12
Std Dev	3.644

**Gaus mean : 37.0884**

**Gaus Width: 3.3657**



# B1L102S, U24-ch98, adc2

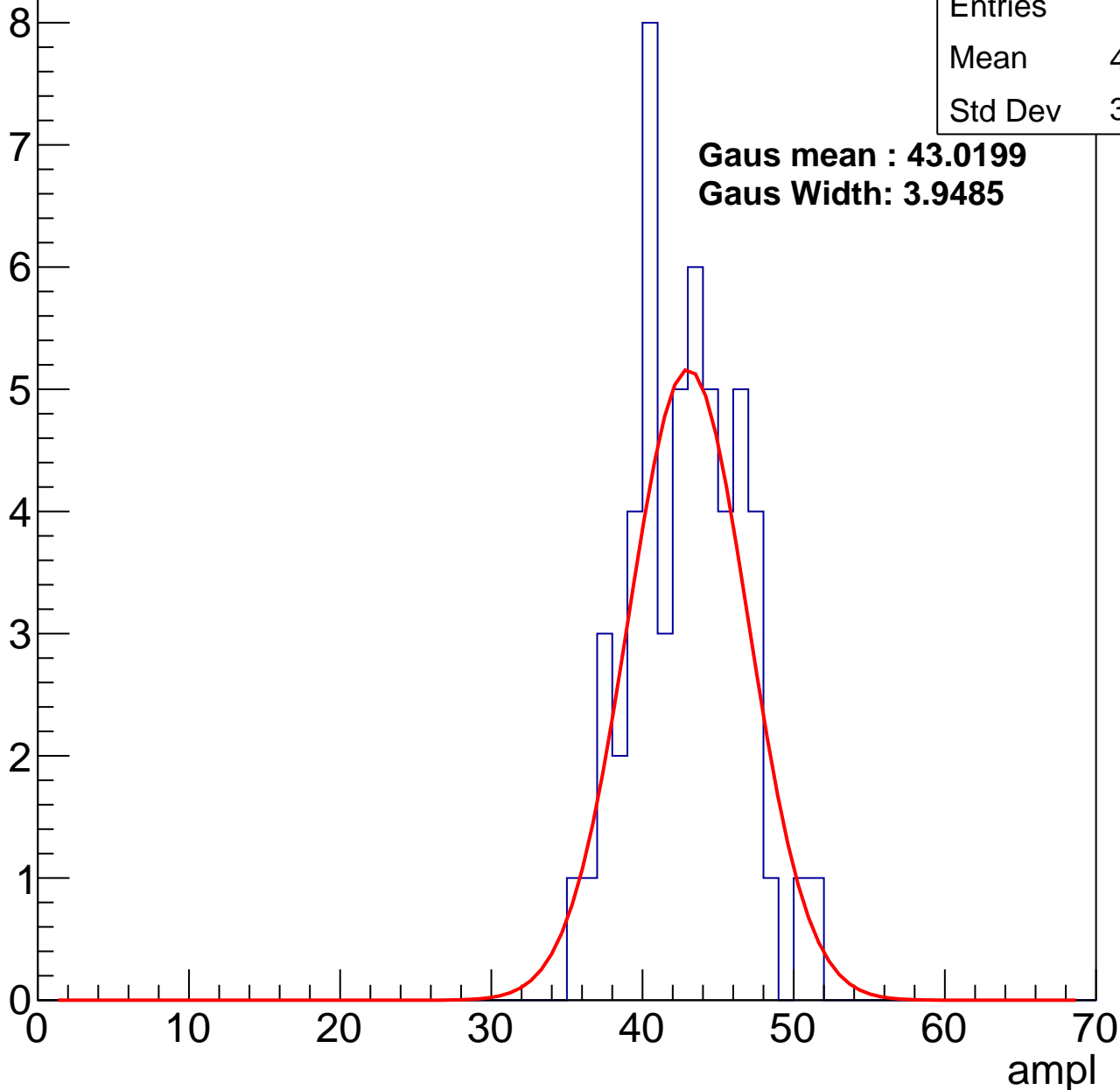
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.44
Std Dev	3.536

**Gaus mean : 43.0199**

**Gaus Width: 3.9485**

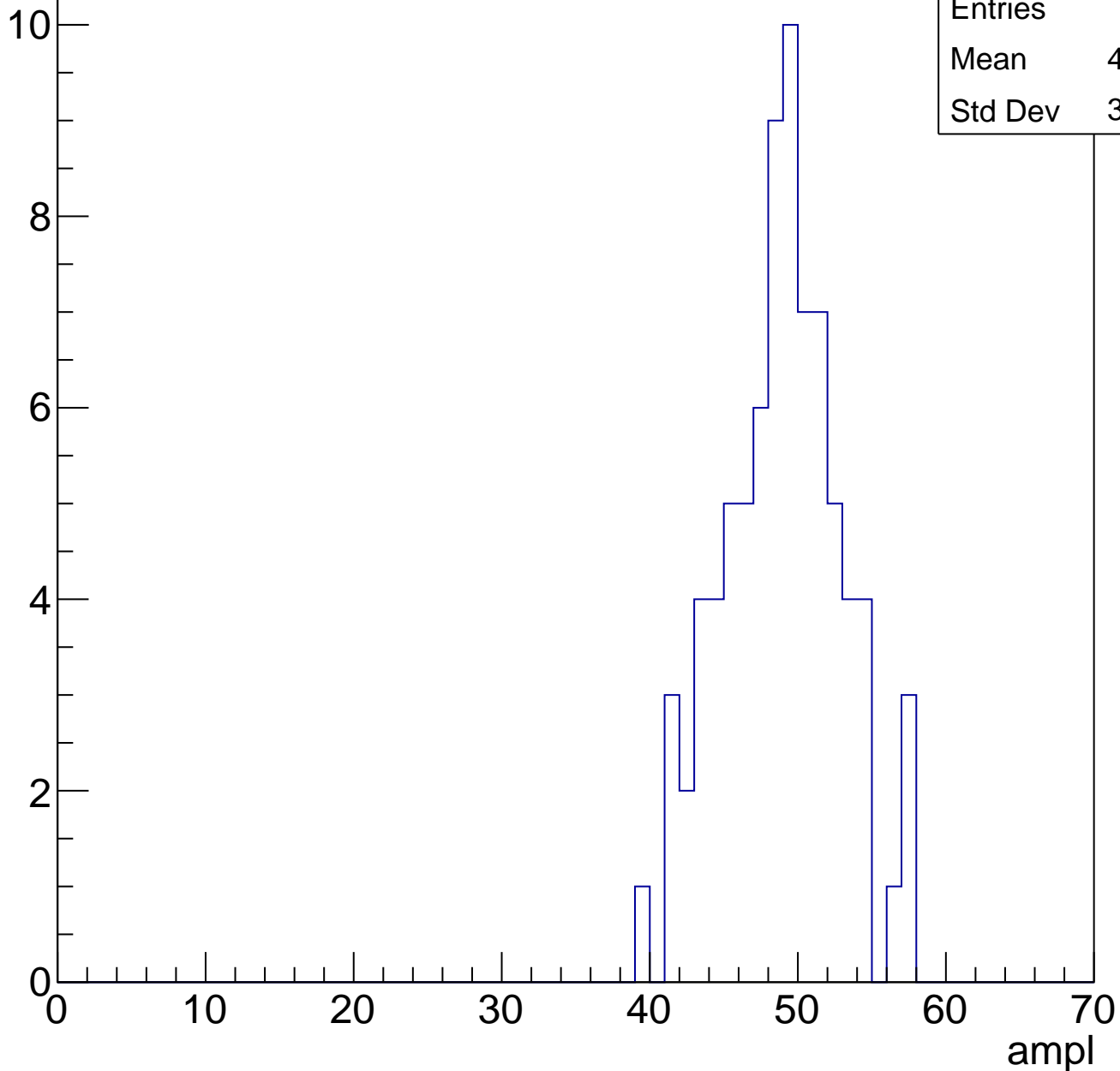


# B1L102S, U24-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	48.44
Std Dev	3.956

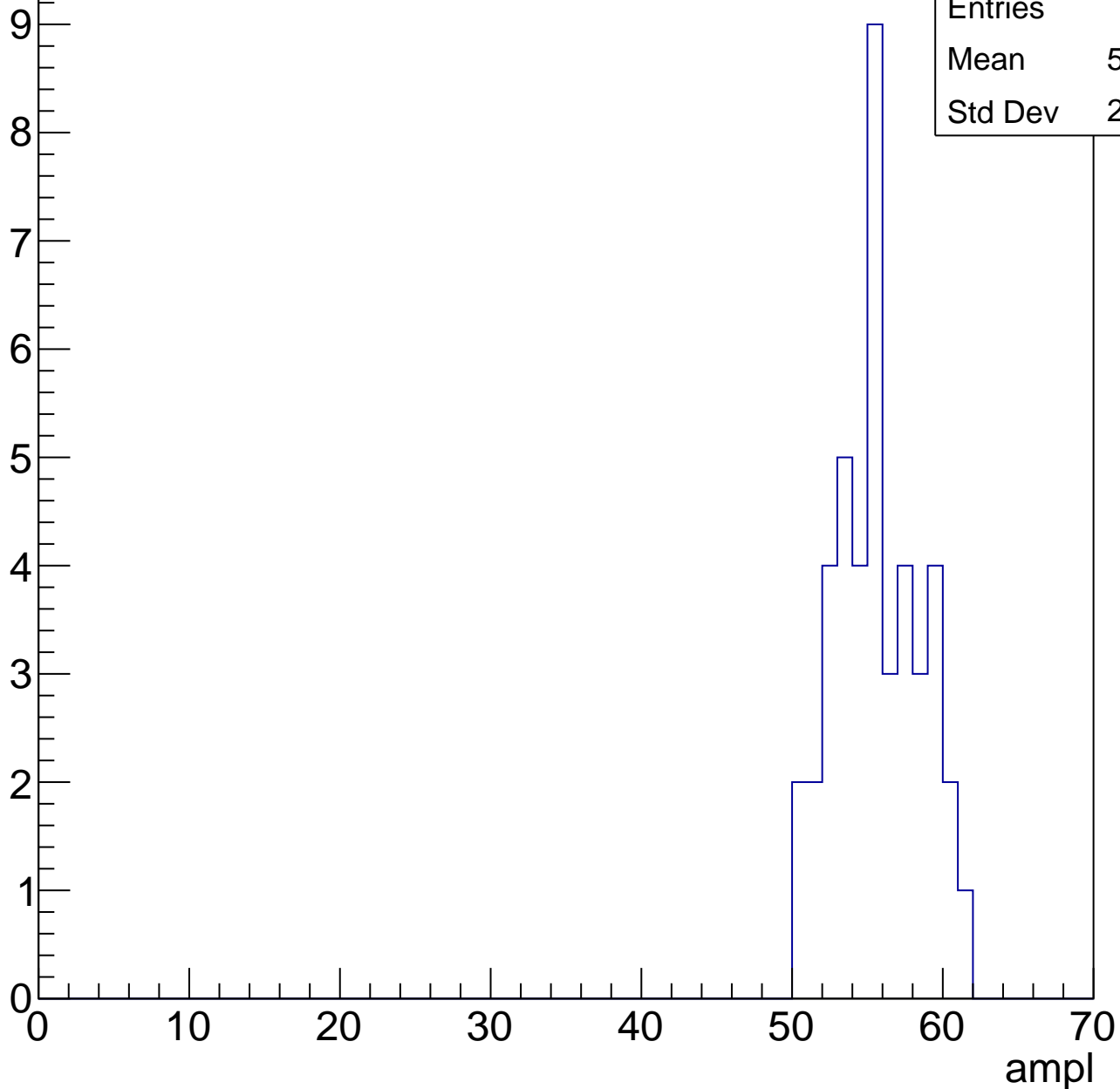
Entry



# B1L102S, U24-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

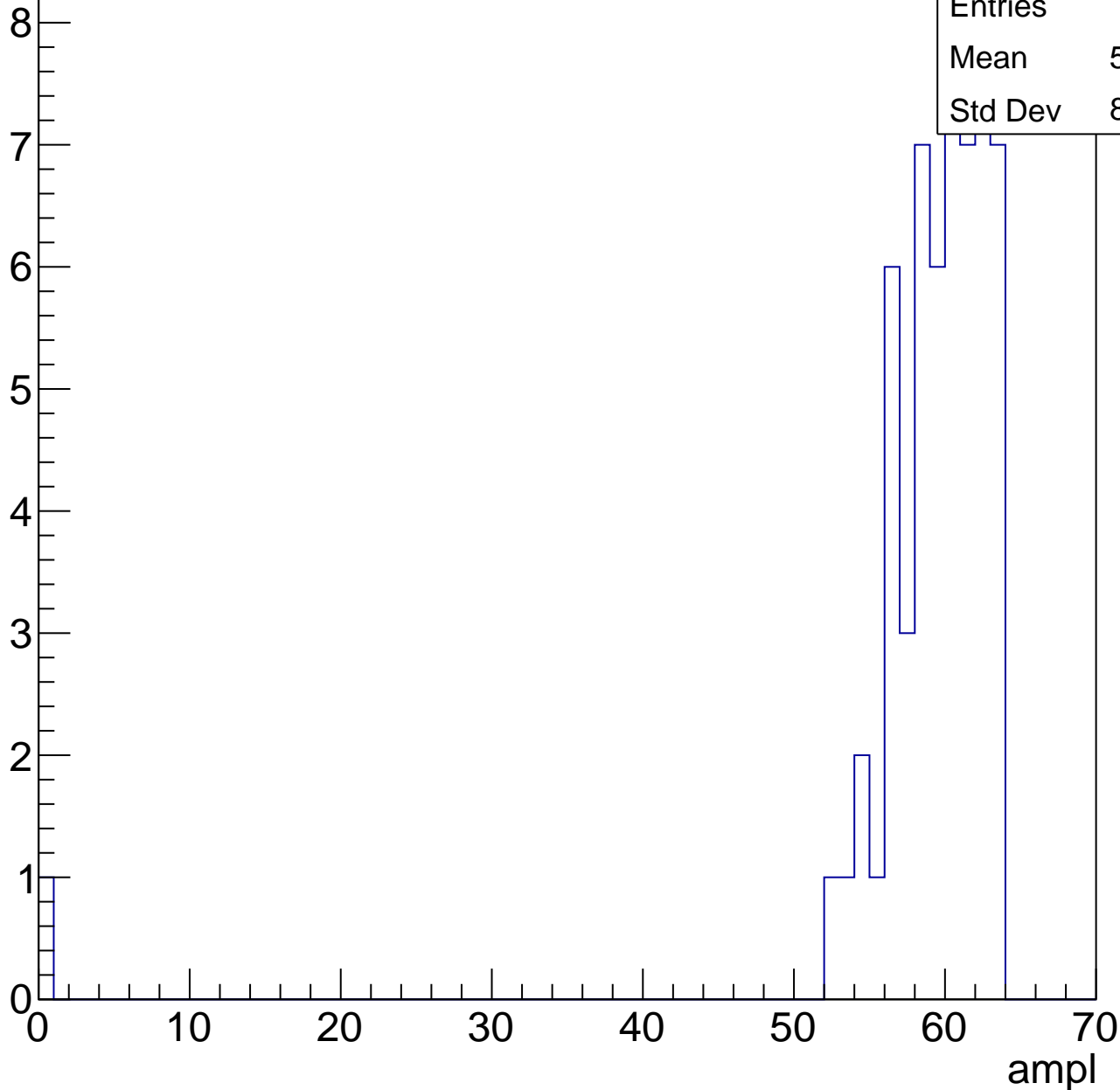


# B1L102S, U24-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.26
Std Dev	8.193



# B1L102S, U24-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch99, adc0

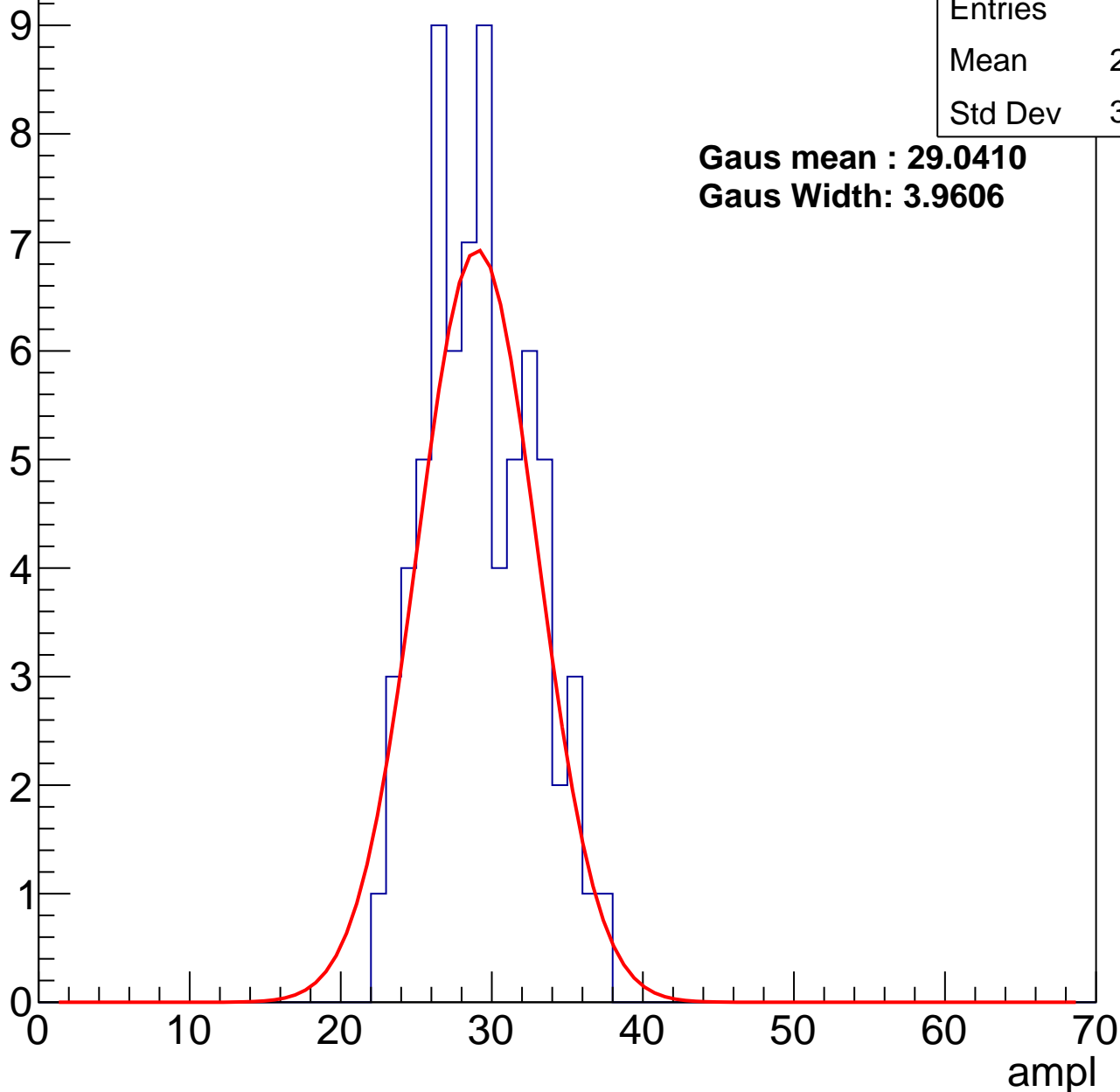
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.77
Std Dev	3.509

**Gaus mean : 29.0410**

**Gaus Width: 3.9606**



# B1L102S, U24-ch99, adc1

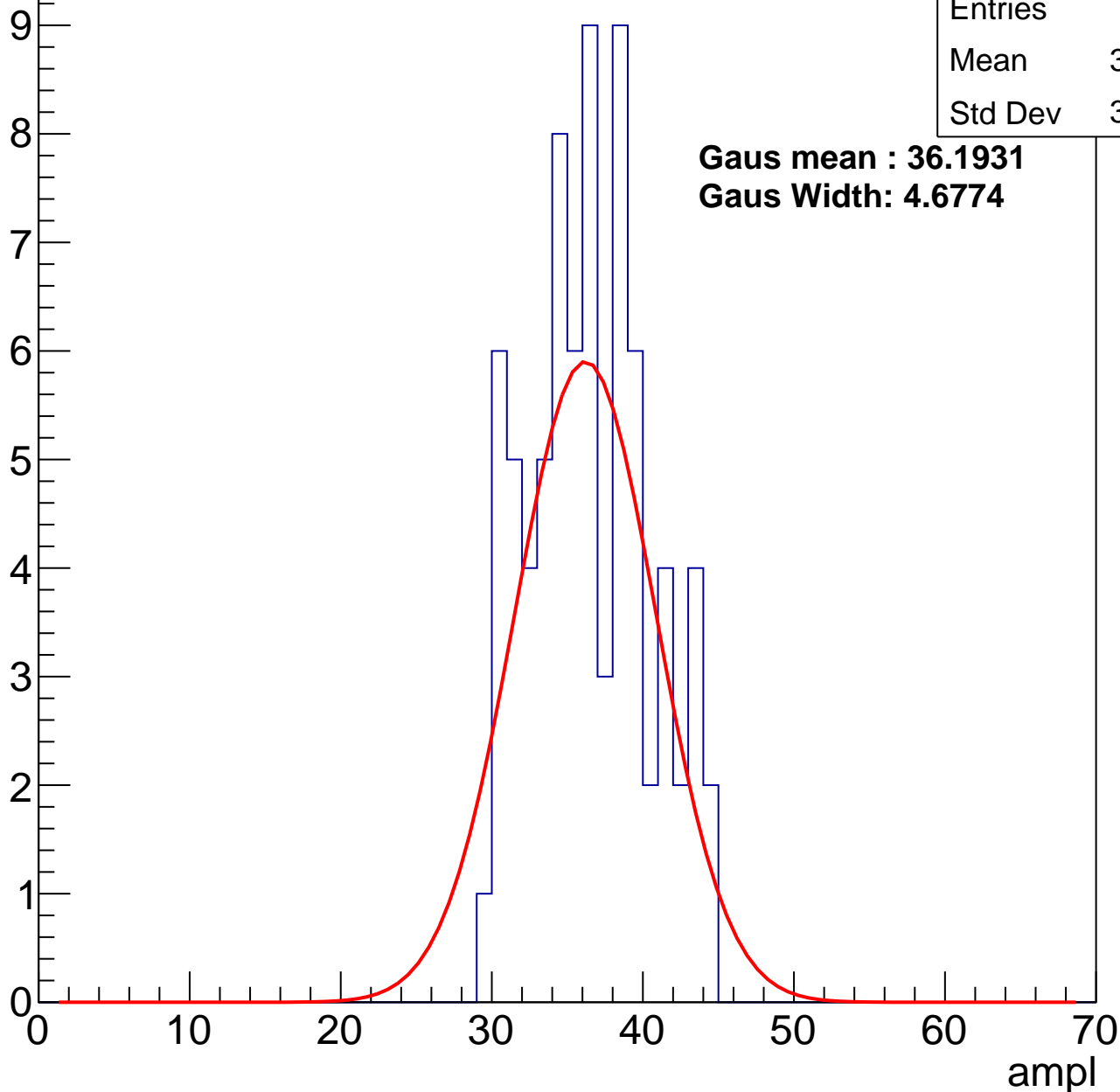
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.03
Std Dev	3.903

**Gaus mean : 36.1931**

**Gaus Width: 4.6774**



# B1L102S, U24-ch99, adc2

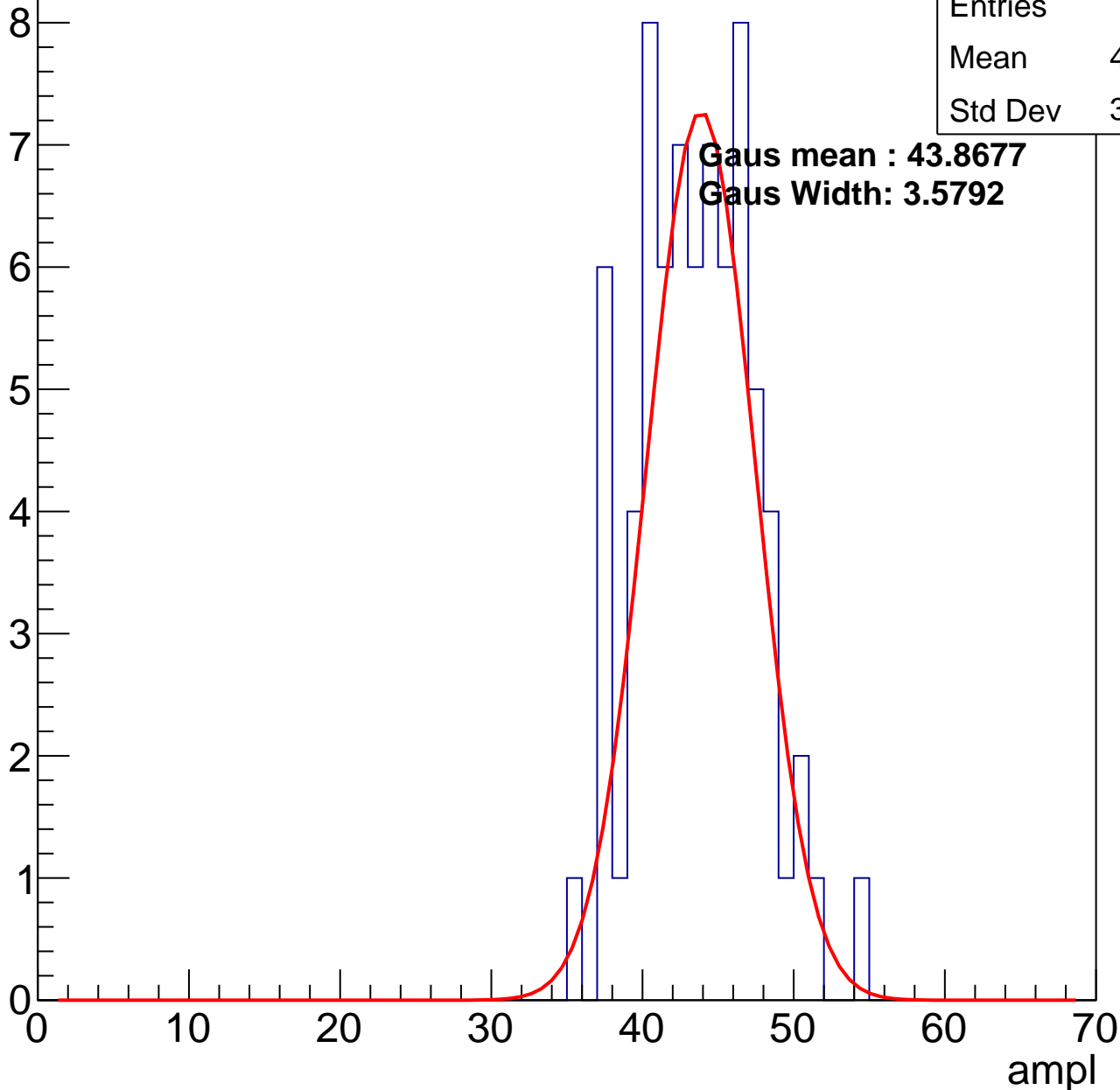
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.19
Std Dev	3.805

**Gaus mean : 43.8677**

**Gaus Width: 3.5792**

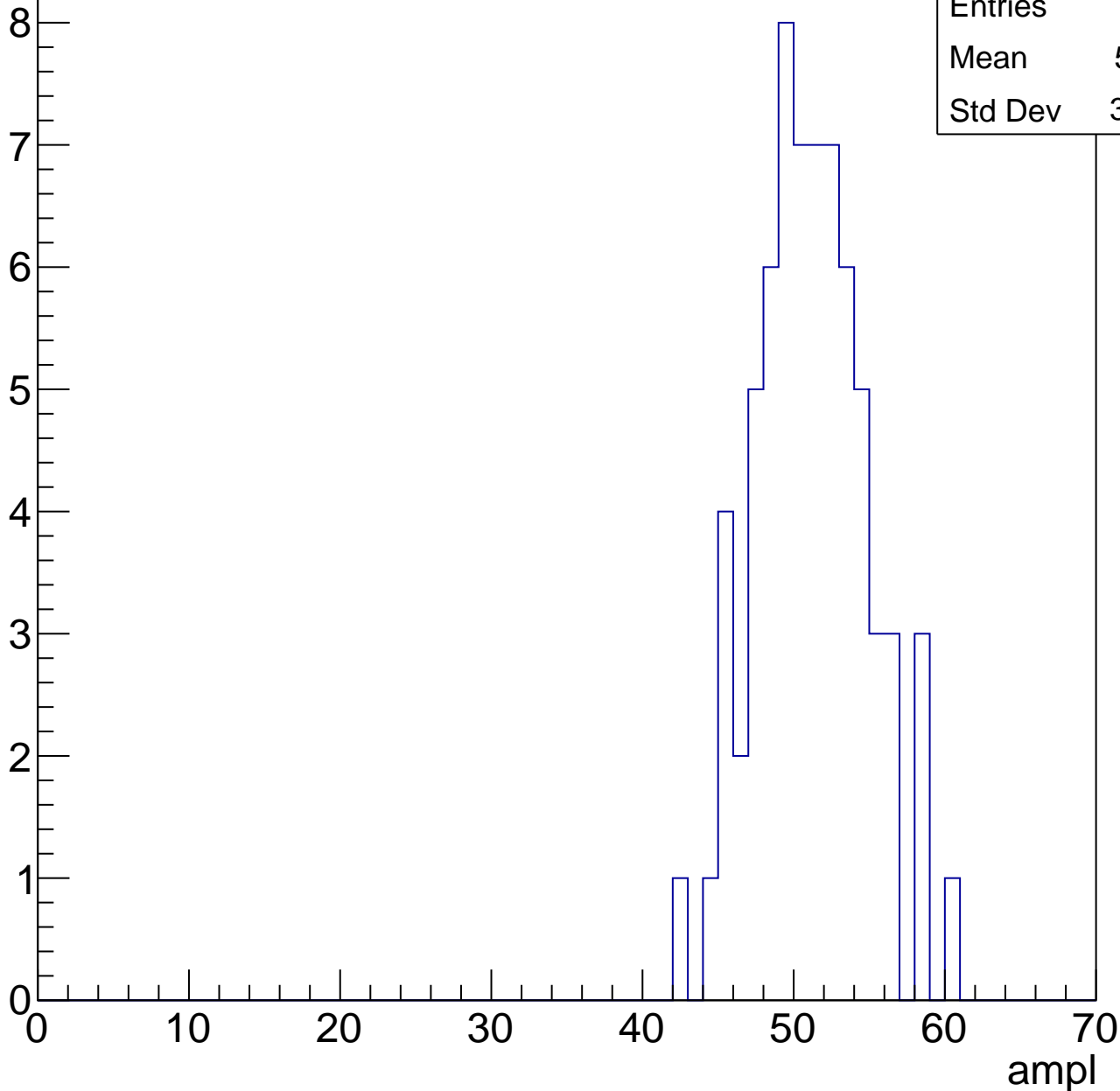


# B1L102S, U24-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.71
Std Dev	3.648

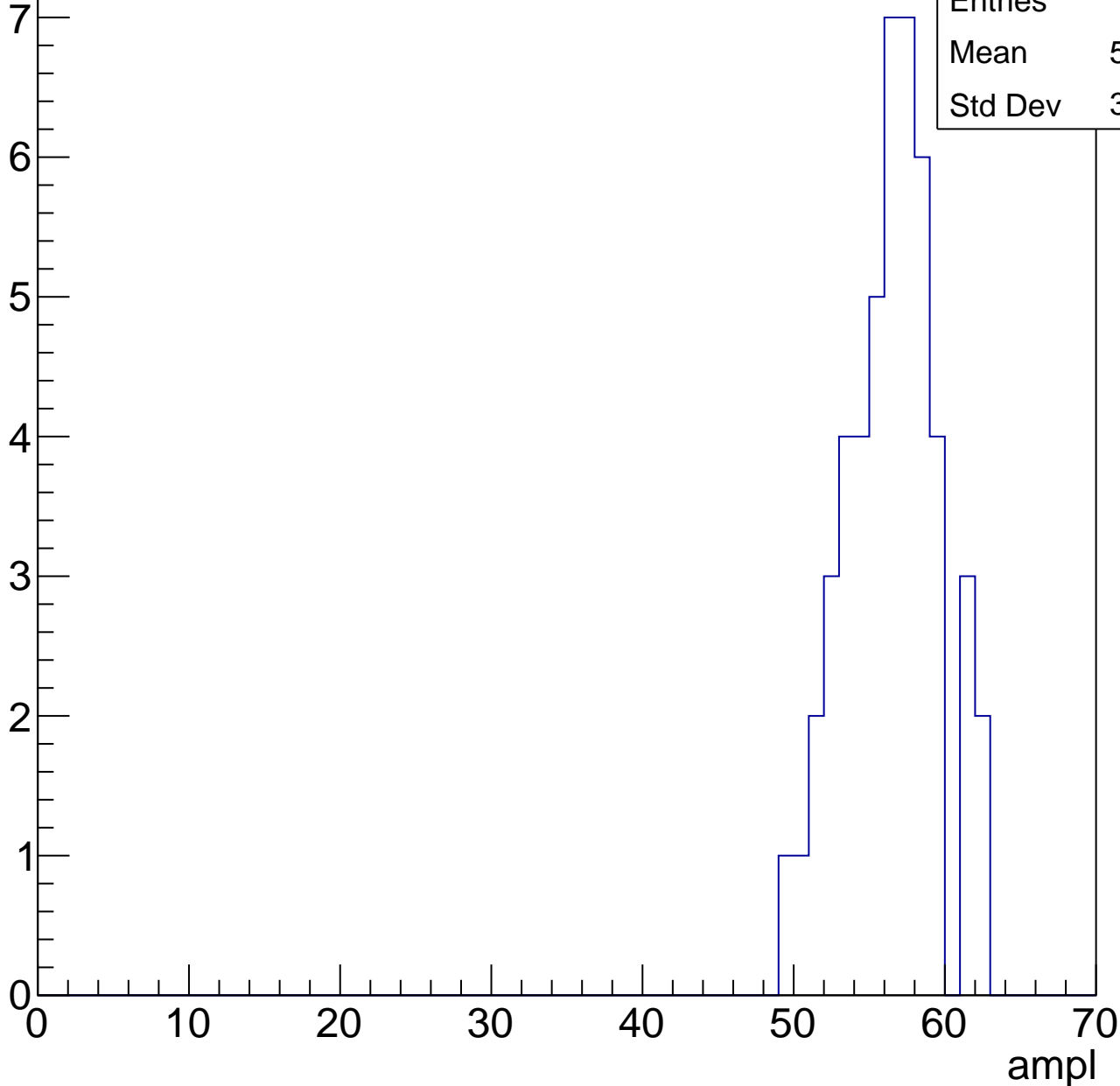


# B1L102S, U24-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.96
Std Dev	3.044



# B1L102S, U24-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	58.8
Std Dev	9.521

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

# B1L102S, U24-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

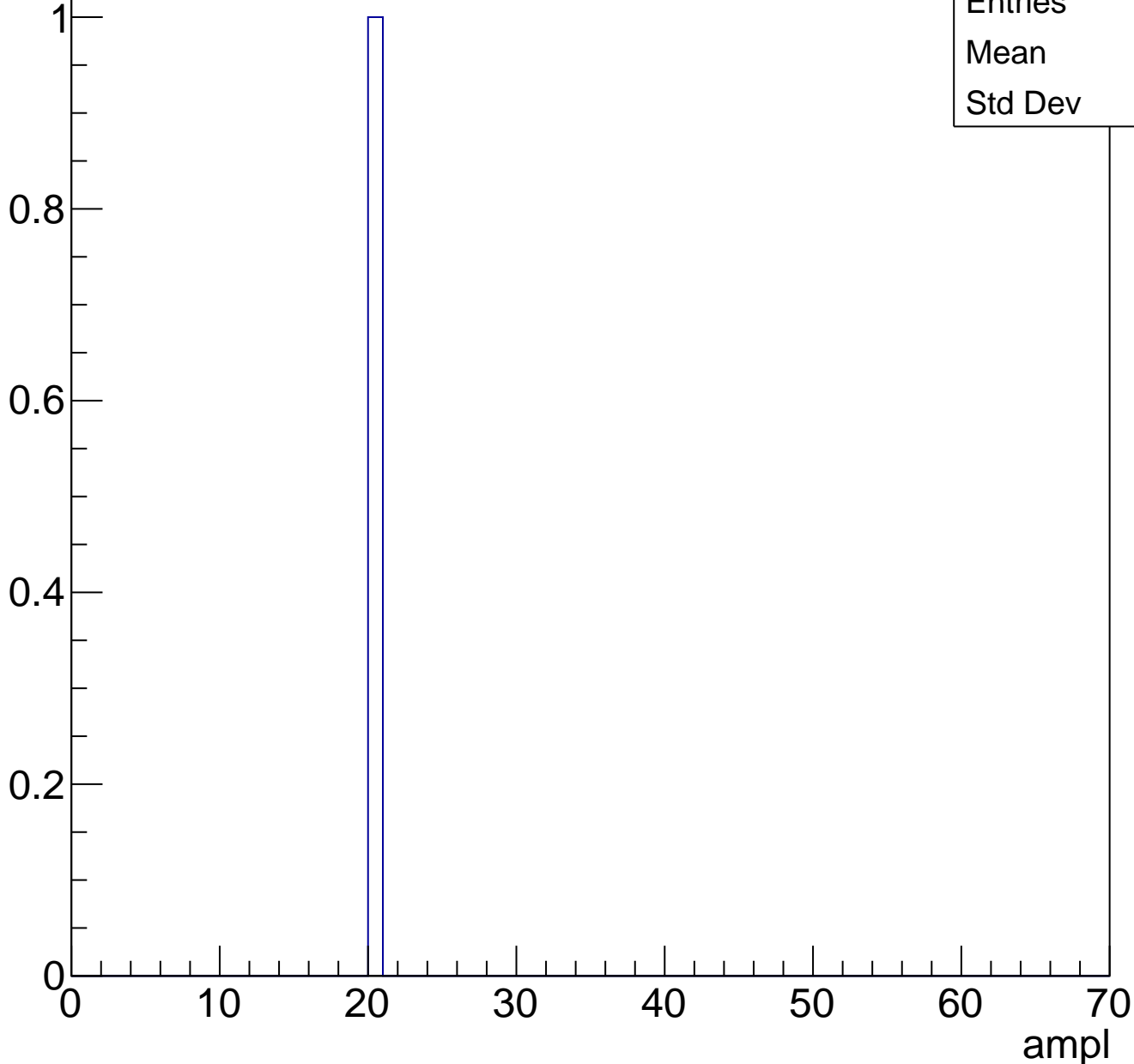




# B1L102S, U24-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch100, adc0

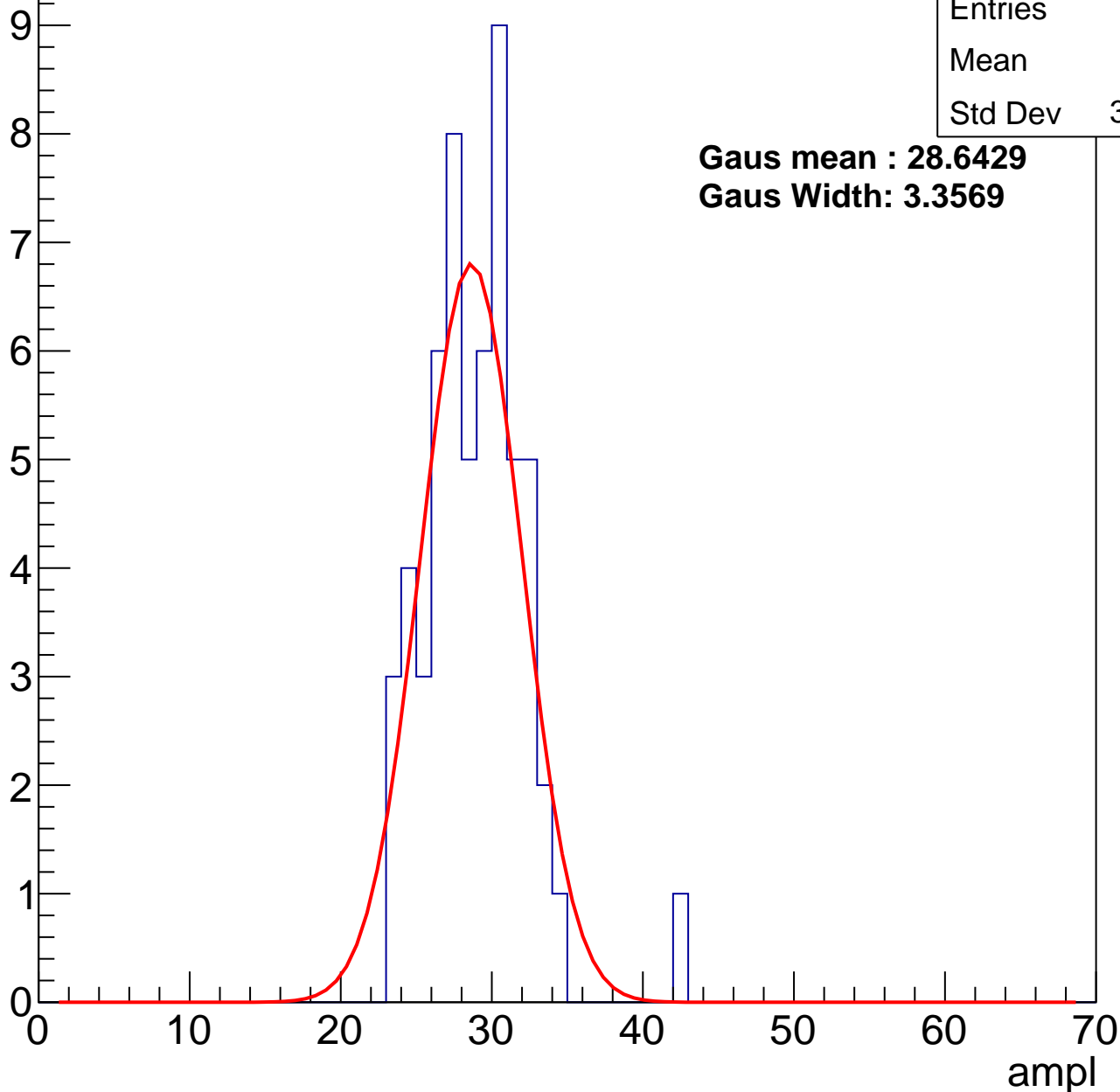
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.5
Std Dev	3.302

**Gaus mean : 28.6429**

**Gaus Width: 3.3569**



# B1L102S, U24-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	78
Mean	35.62
Std Dev	5.16

**Gaus mean : 36.1471**

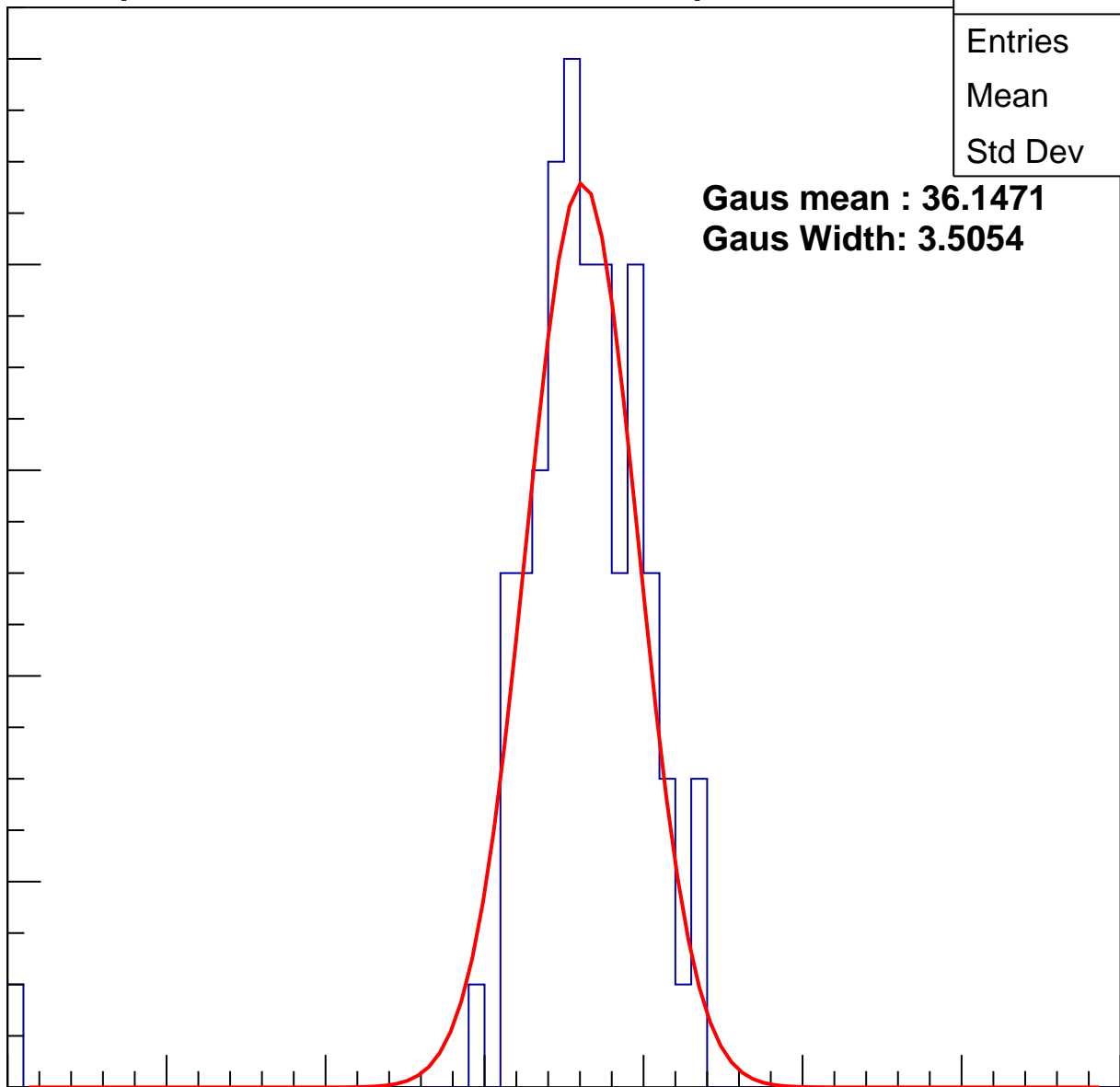
**Gaus Width: 3.5054**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch100, adc2

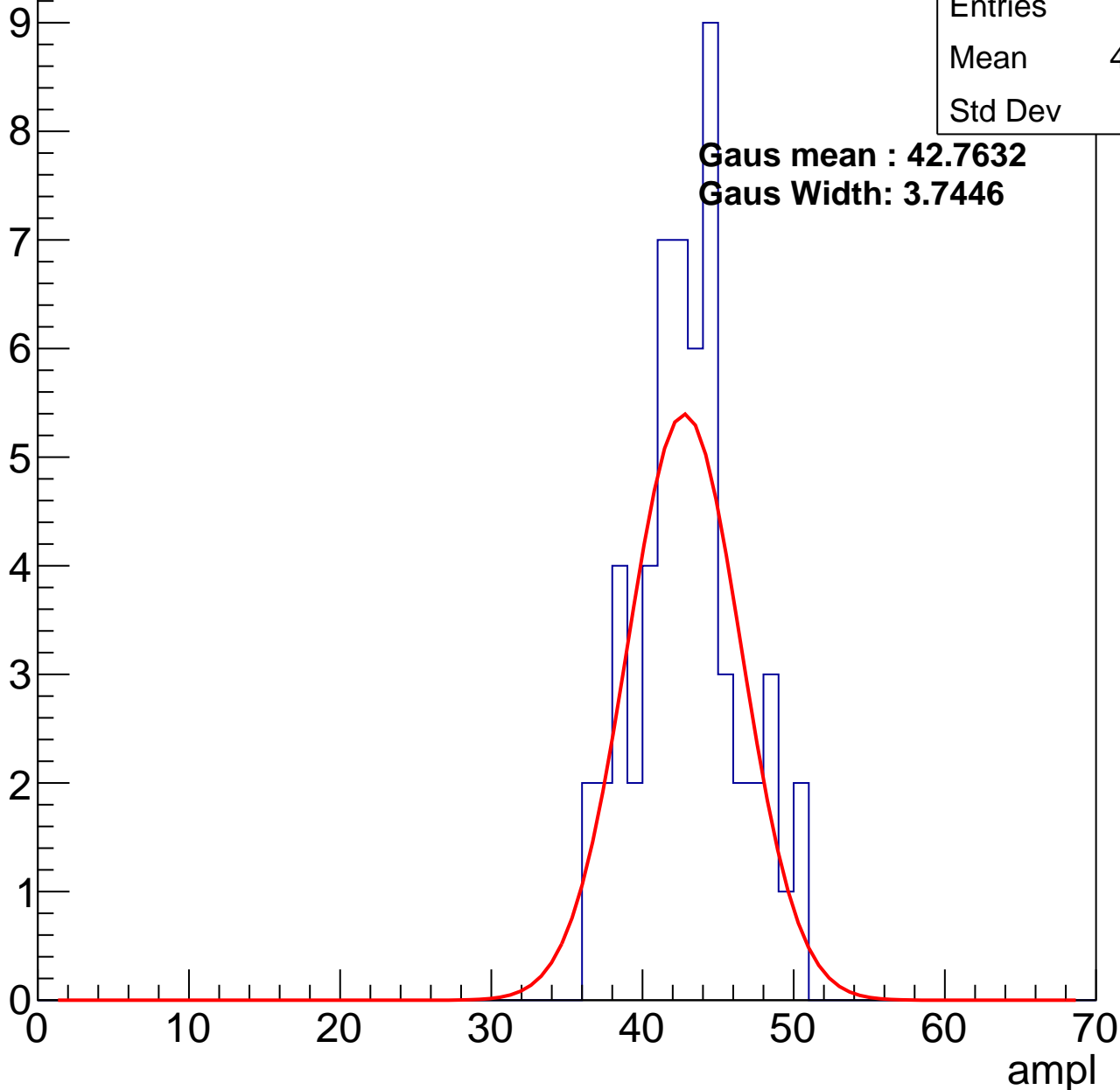
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.59
Std Dev	3.39

**Gaus mean : 42.7632**

**Gaus Width: 3.7446**

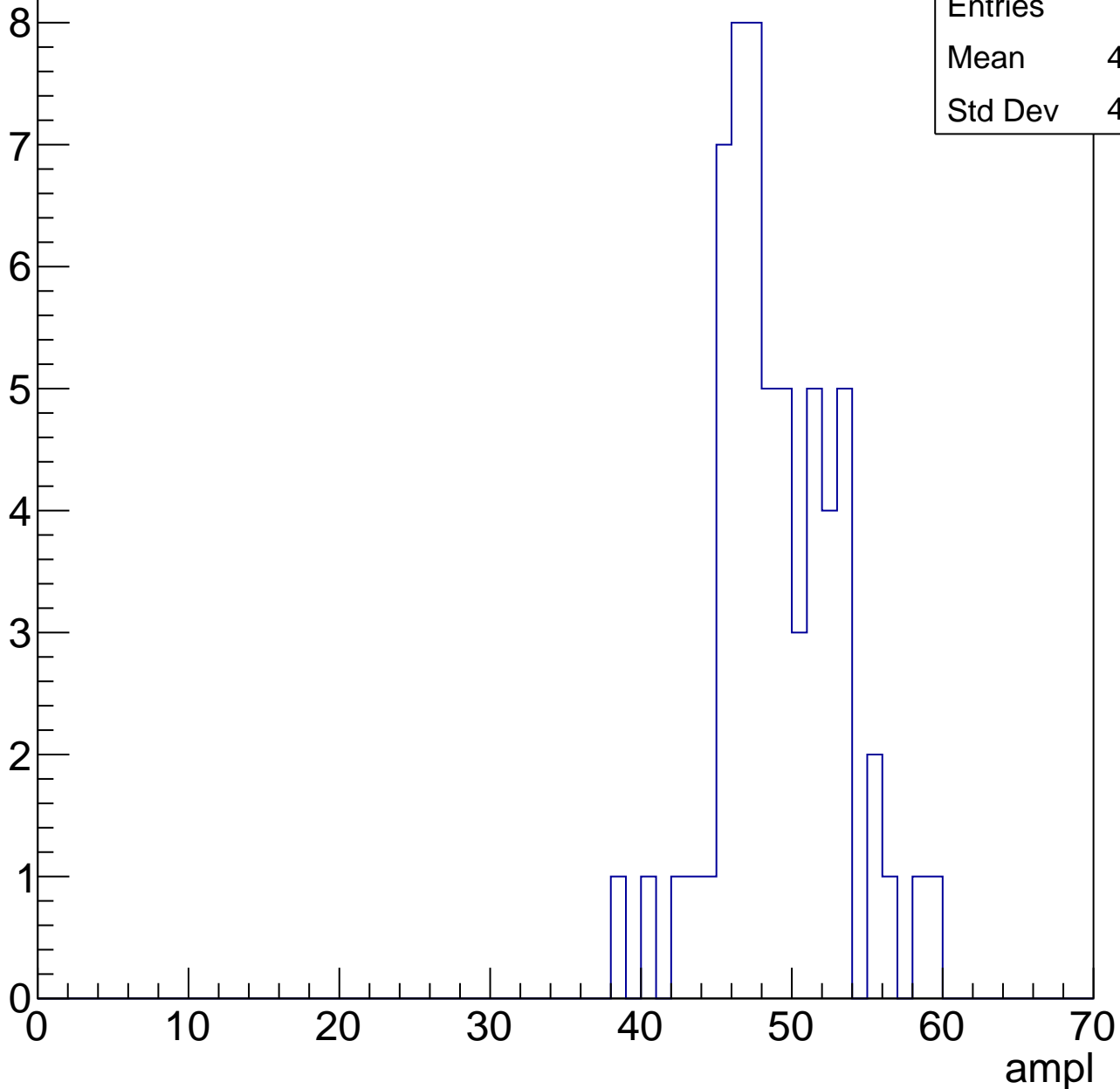


# B1L102S, U24-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.53
Std Dev	4.002

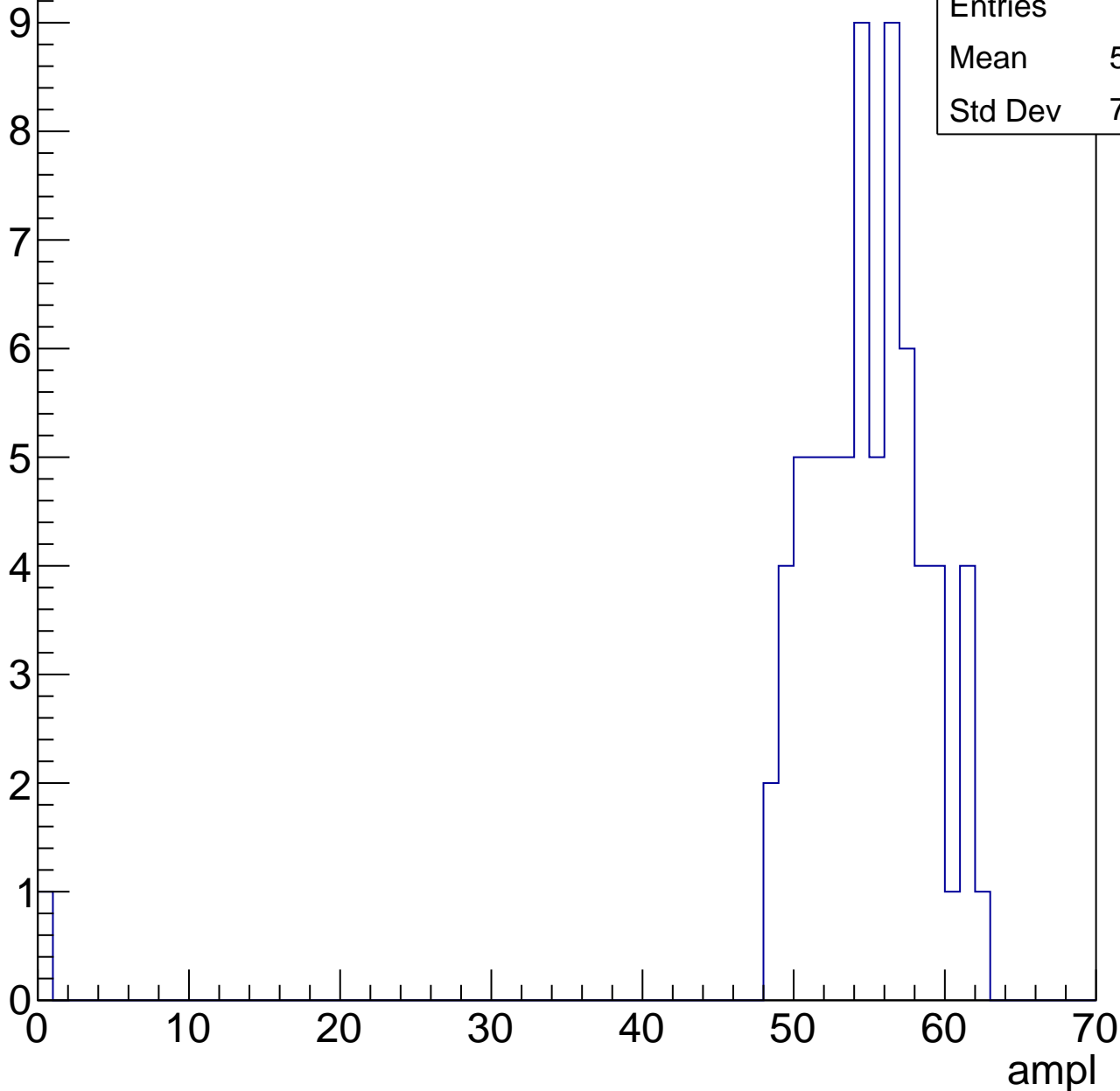


# B1L102S, U24-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	53.76
Std Dev	7.357

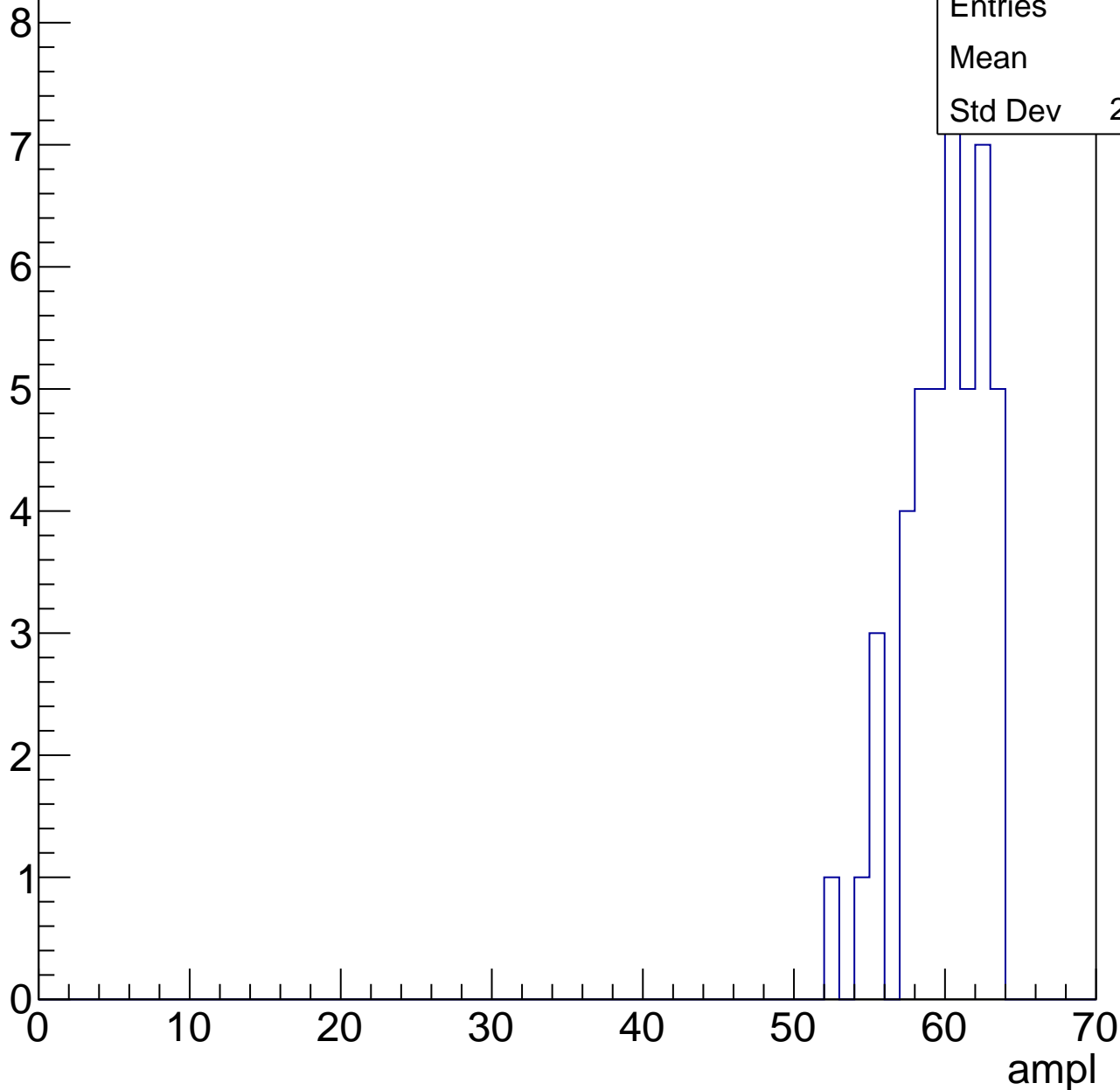


# B1L102S, U24-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.5
Std Dev	2.624

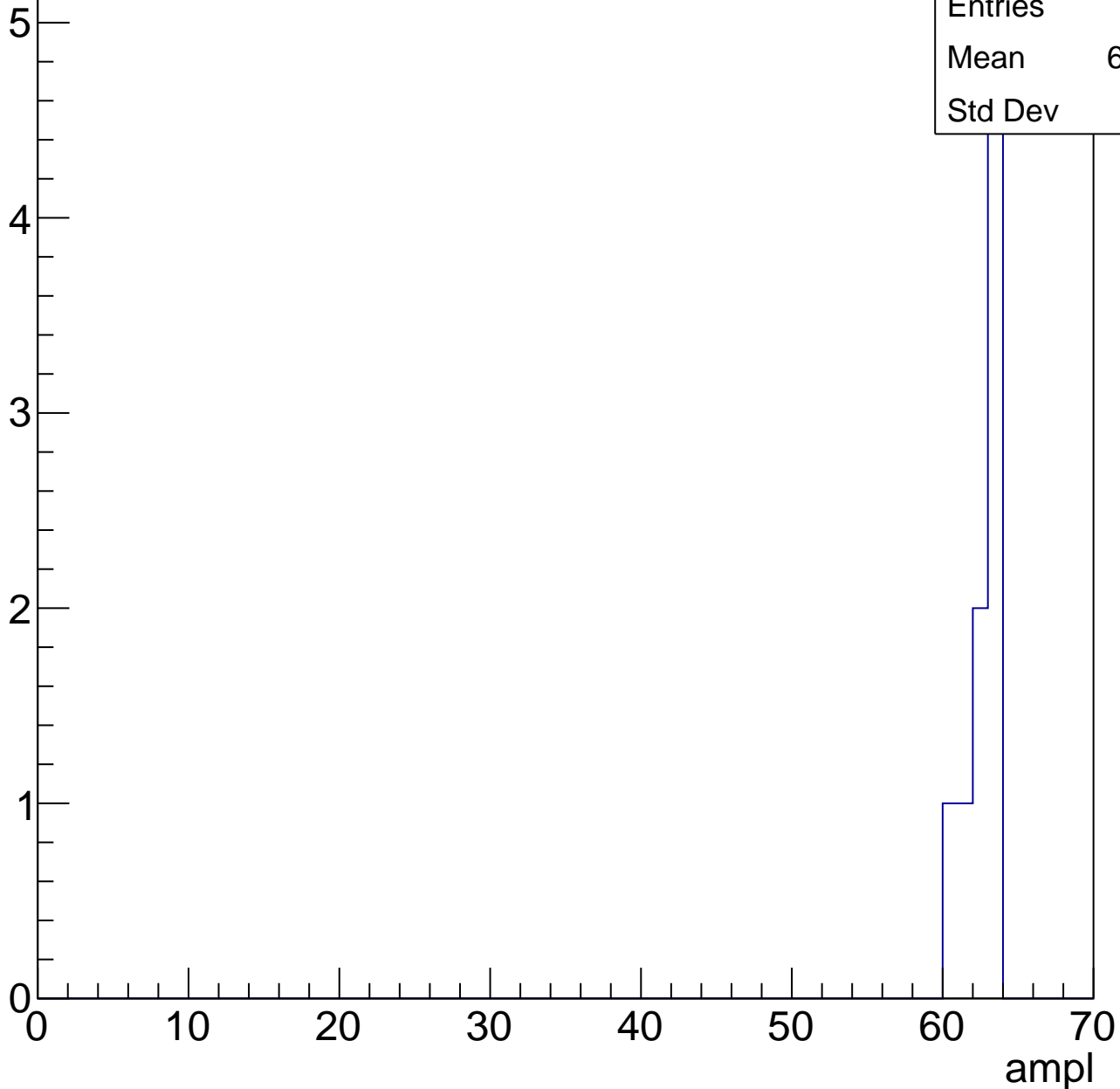


# B1L102S, U24-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.22
Std Dev	1.03





# B1L102S, U24-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch101, adc0

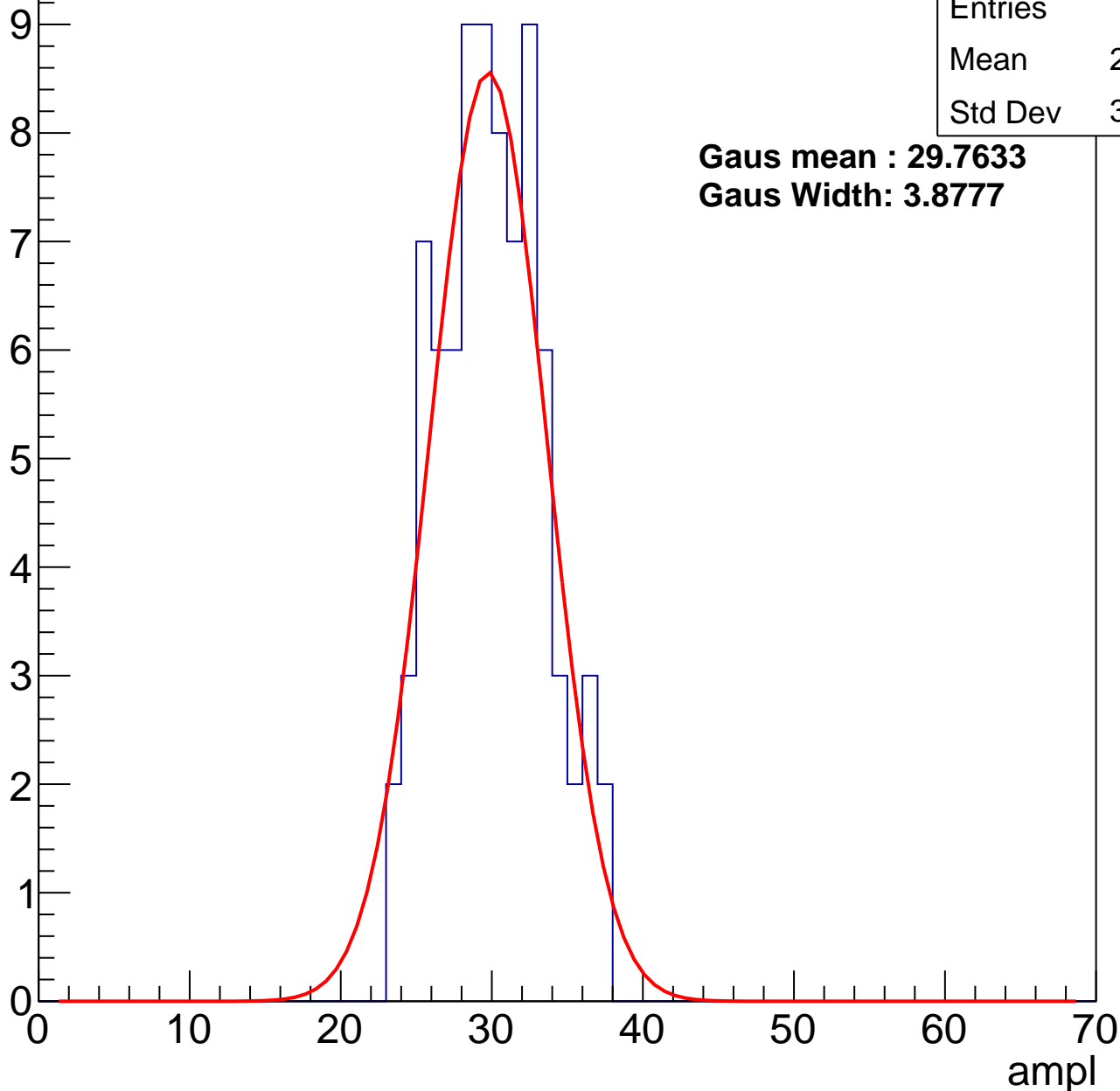
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	29.52
Std Dev	3.419

**Gaus mean : 29.7633**

**Gaus Width: 3.8777**



# B1L102S, U24-ch101, adc1

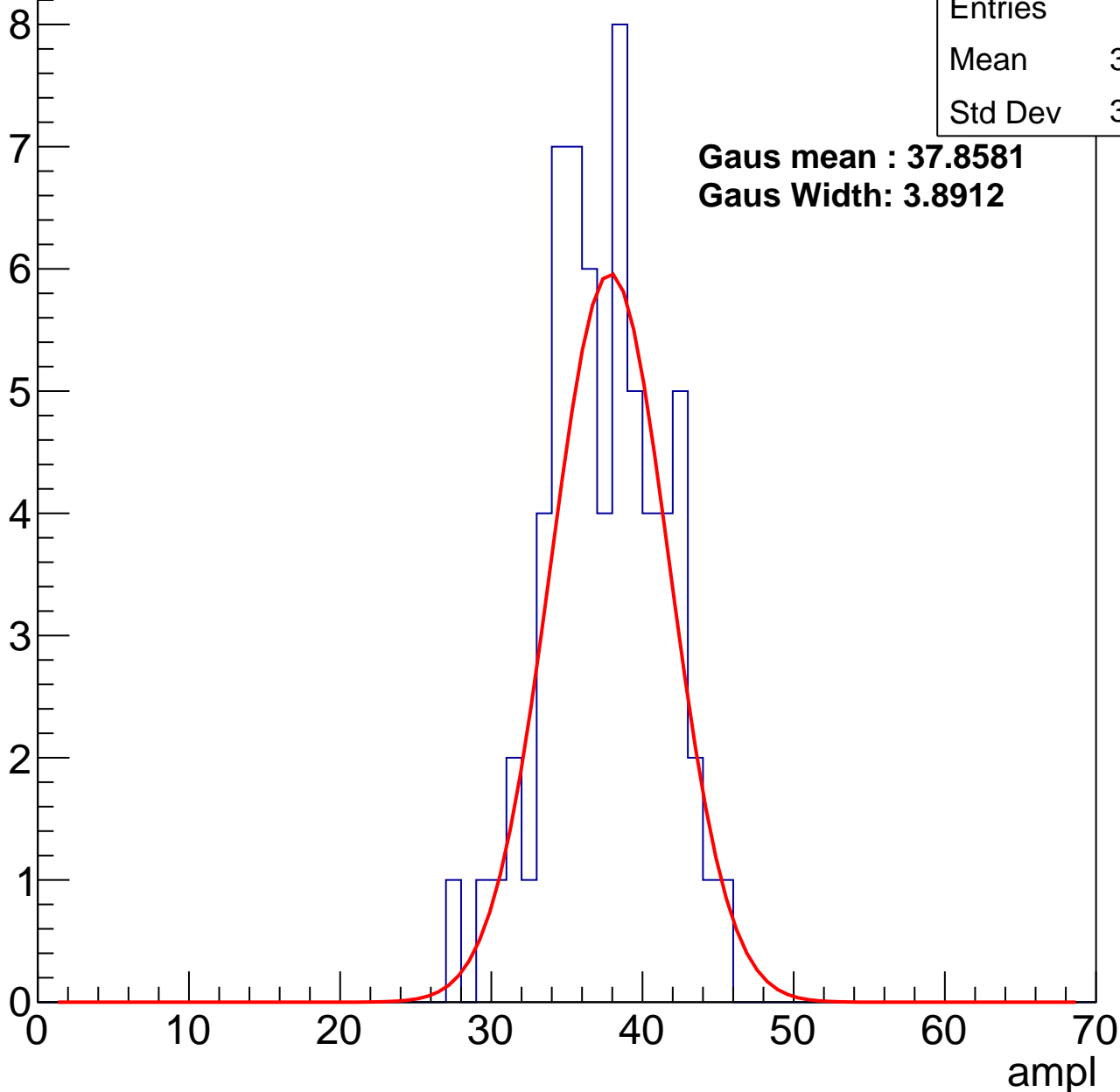
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.98
Std Dev	3.769

**Gaus mean : 37.8581**

**Gaus Width: 3.8912**



# B1L102S, U24-ch101, adc2

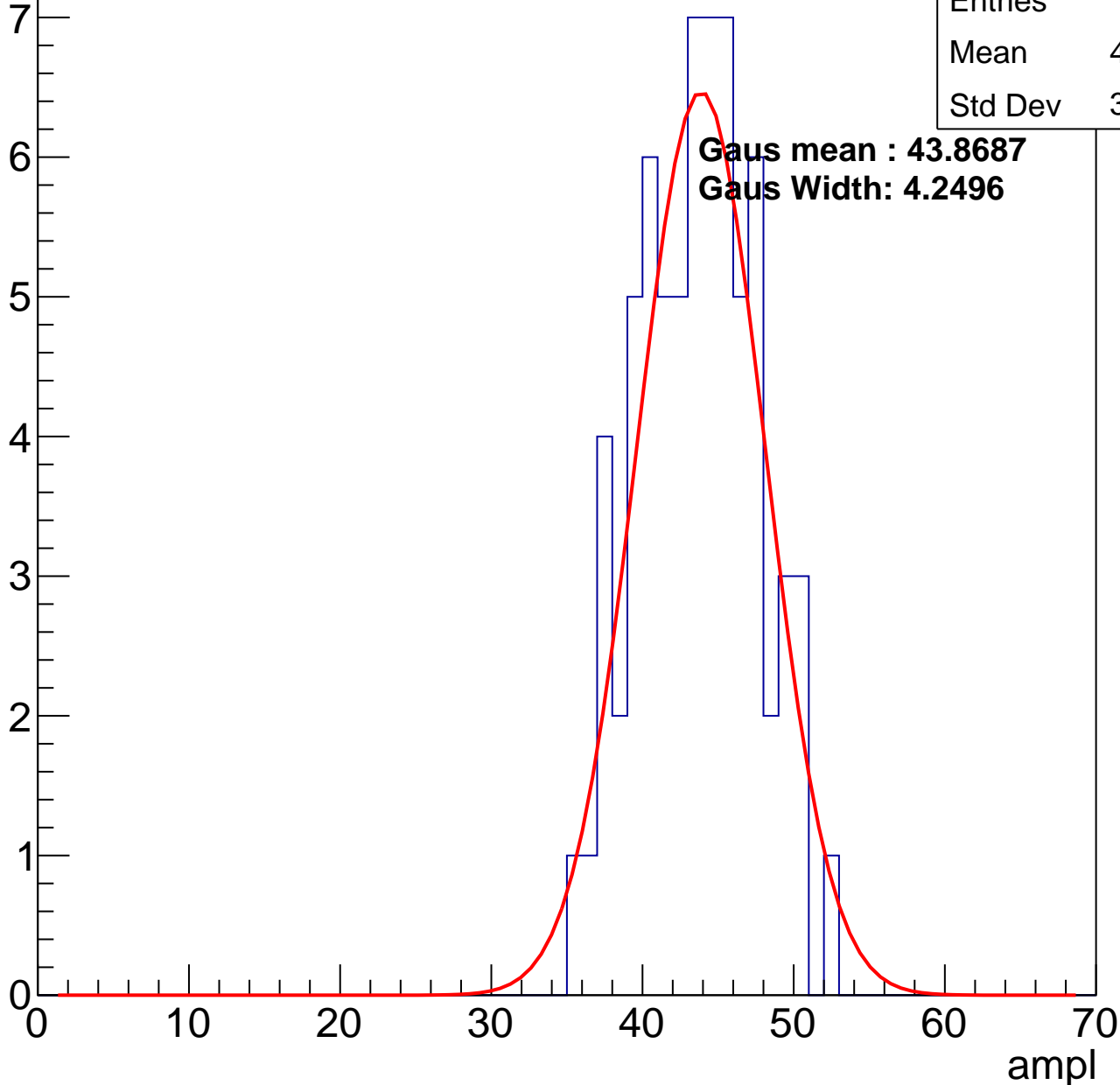
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.23
Std Dev	3.825

**Gaus mean : 43.8687**

**Gaus Width: 4.2496**

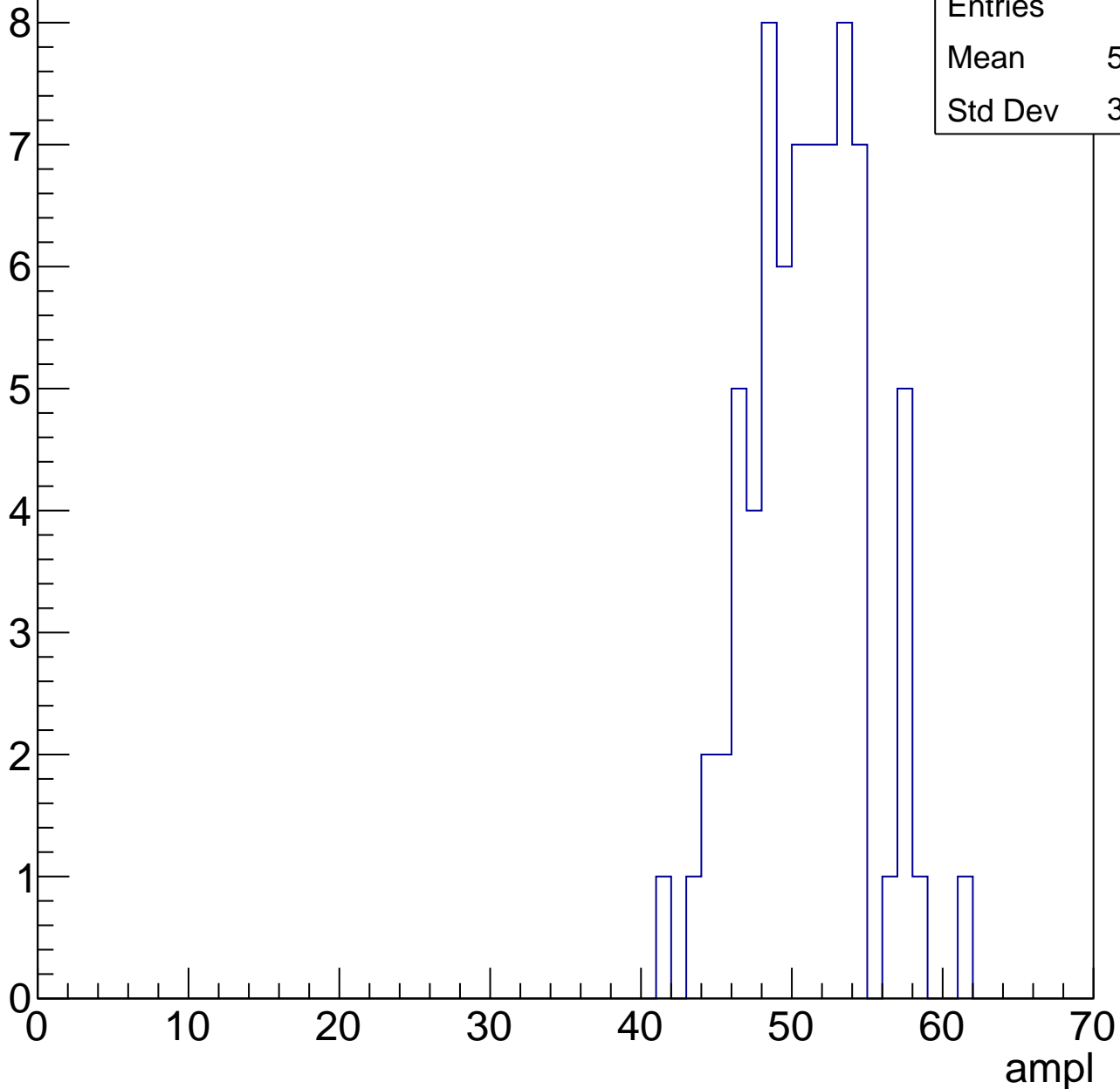


# B1L102S, U24-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.56
Std Dev	3.825

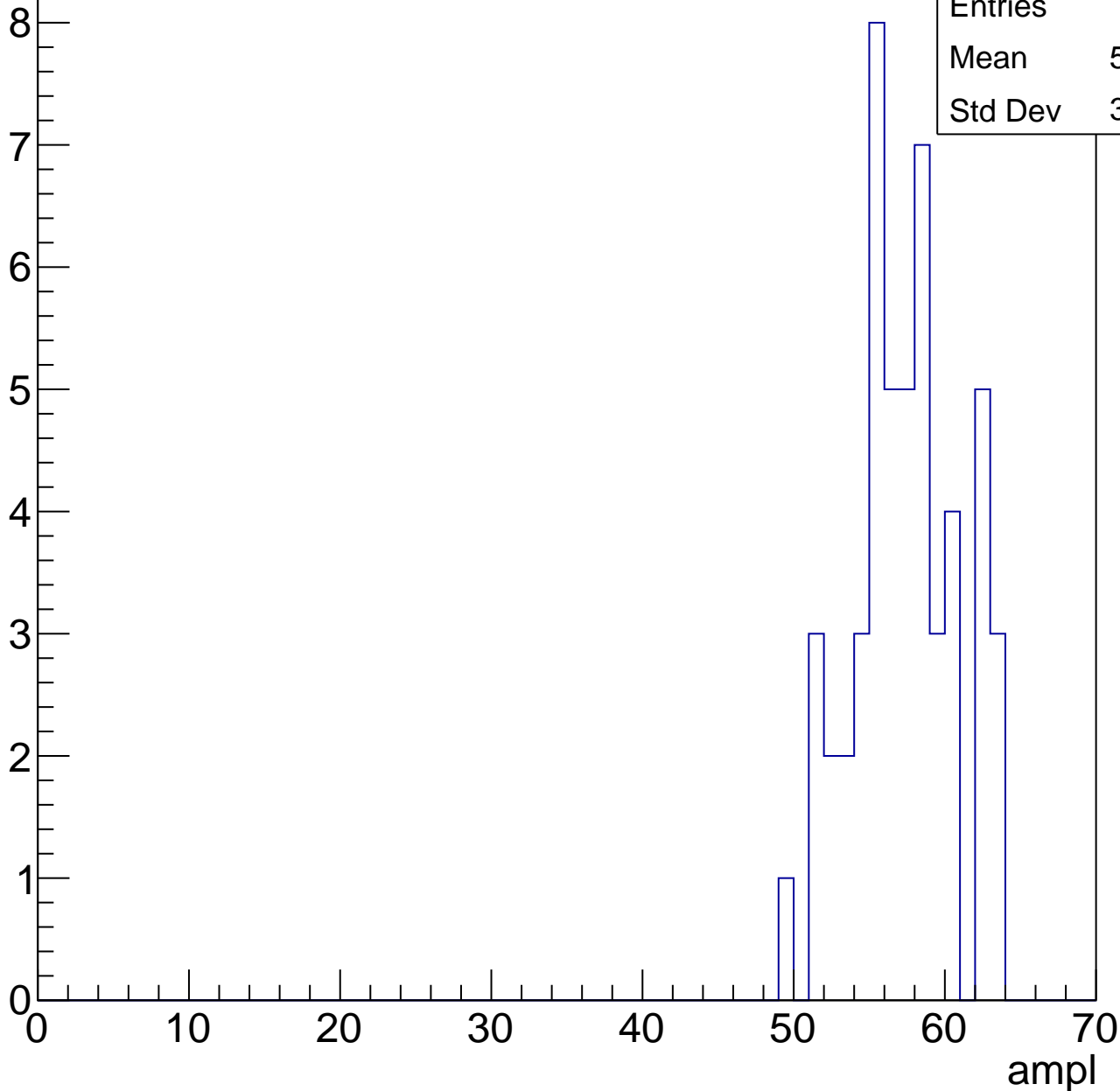


# B1L102S, U24-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.88
Std Dev	3.445

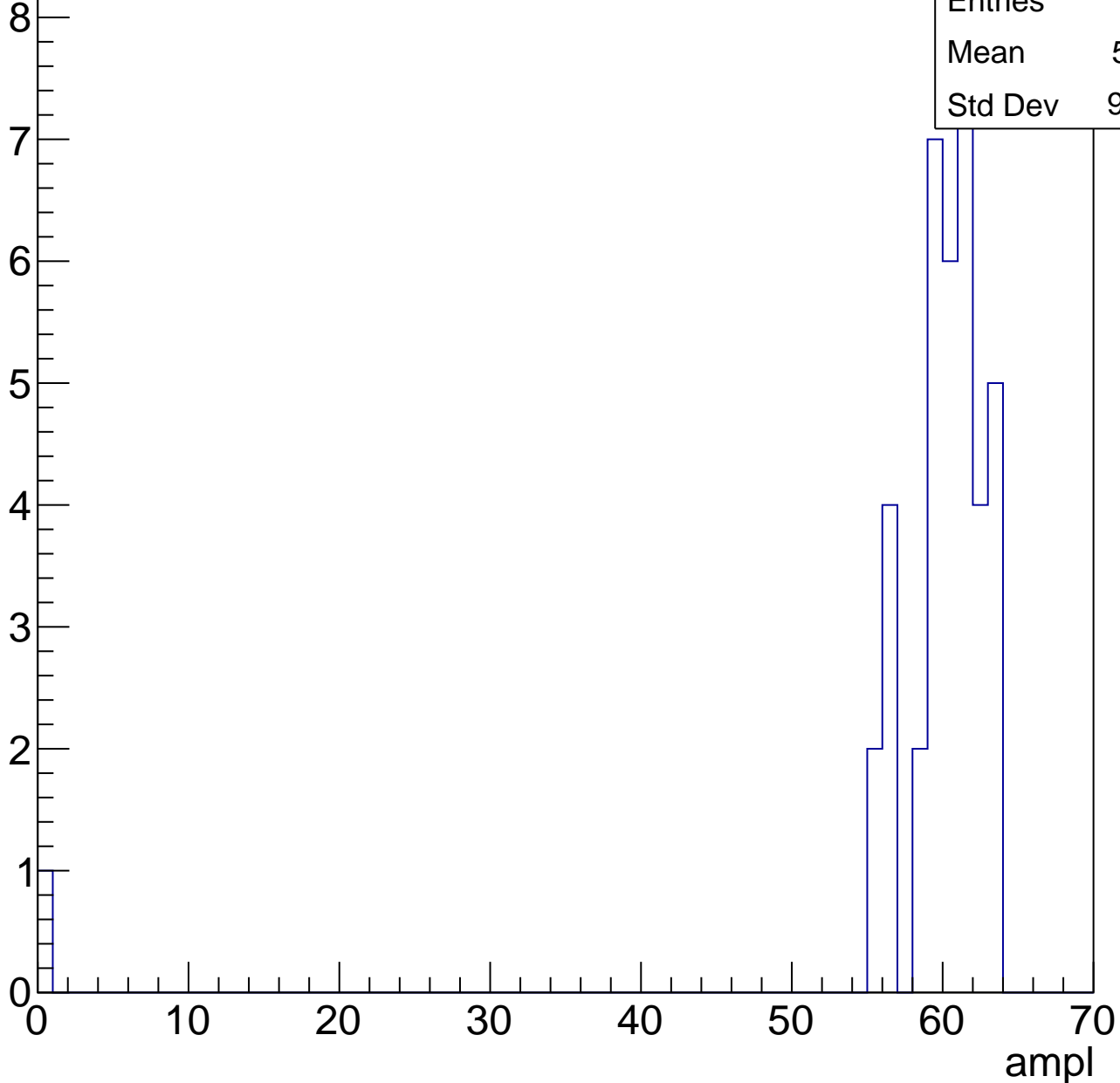


# B1L102S, U24-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

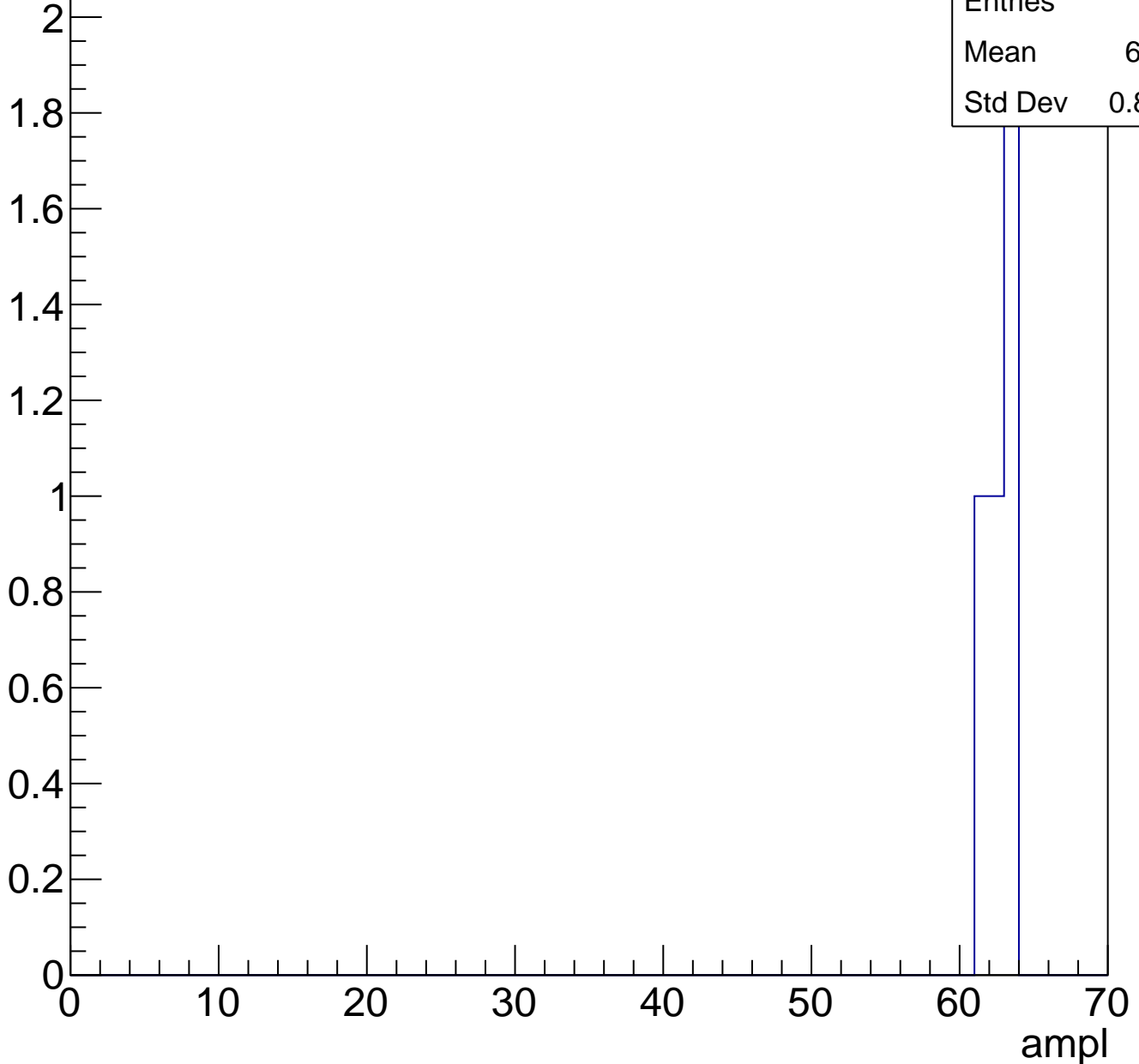
Entries	39
Mean	58.31
Std Dev	9.722



# B1L102S, U24-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch102, adc0

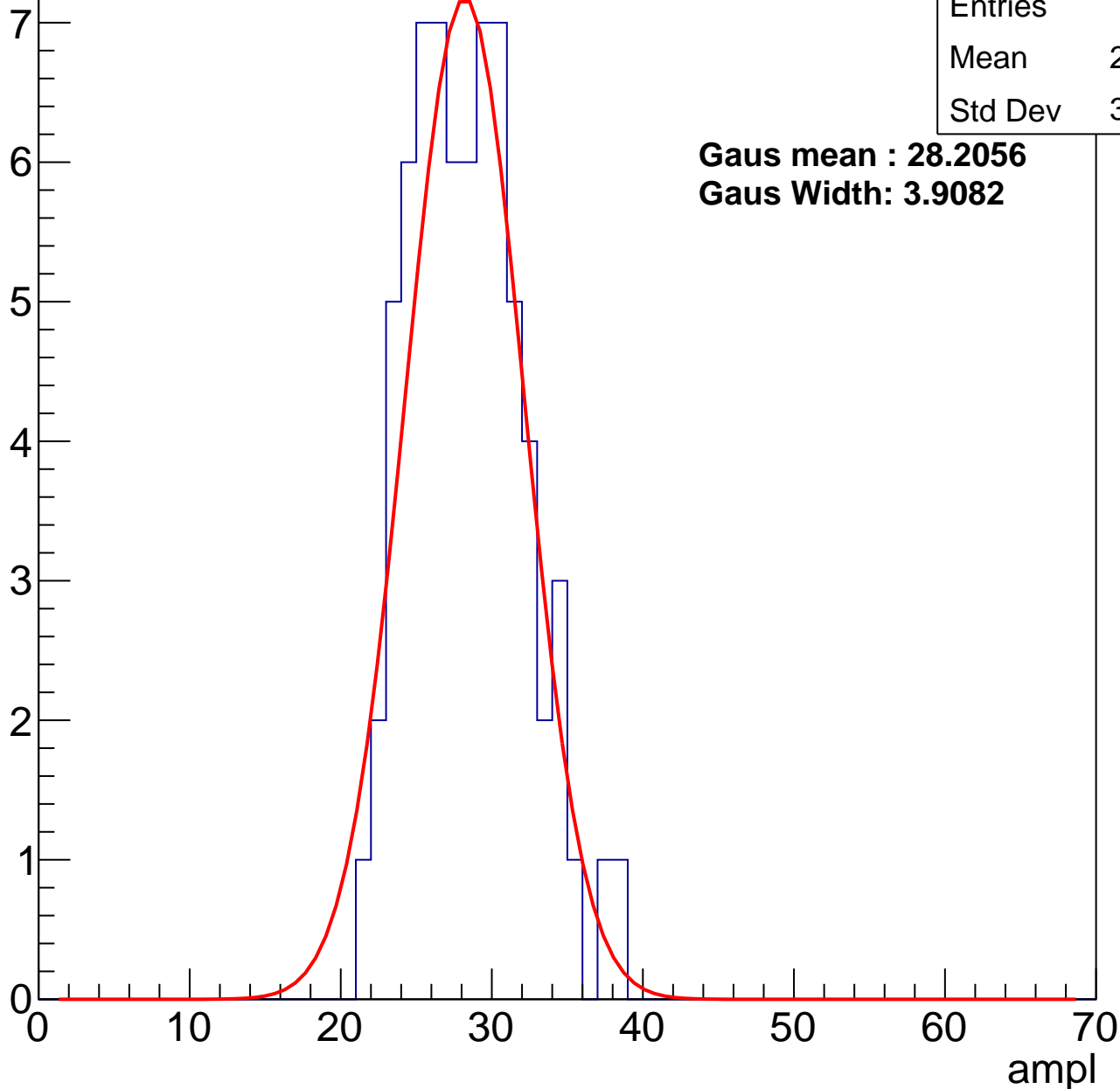
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.96
Std Dev	3.698

**Gaus mean : 28.2056**

**Gaus Width: 3.9082**



# B1L102S, U24-ch102, adc1

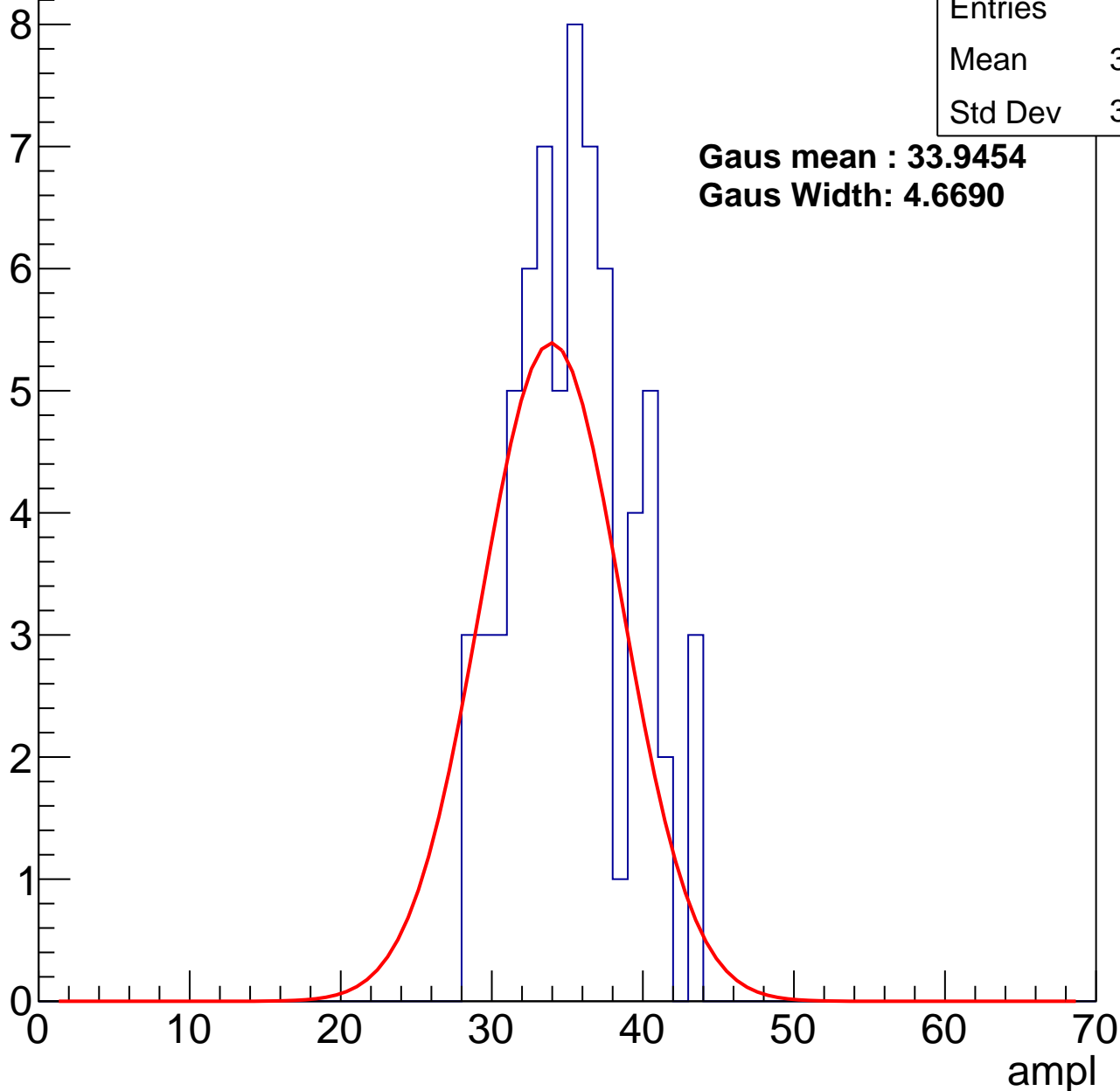
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.82
Std Dev	3.812

**Gaus mean : 33.9454**

**Gaus Width: 4.6690**



# B1L102S, U24-ch102, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.06
Std Dev	4.188

**Gaus mean : 42.6616**

**Gaus Width: 4.3970**

10

8

6

4

2

0

0

10

20

30

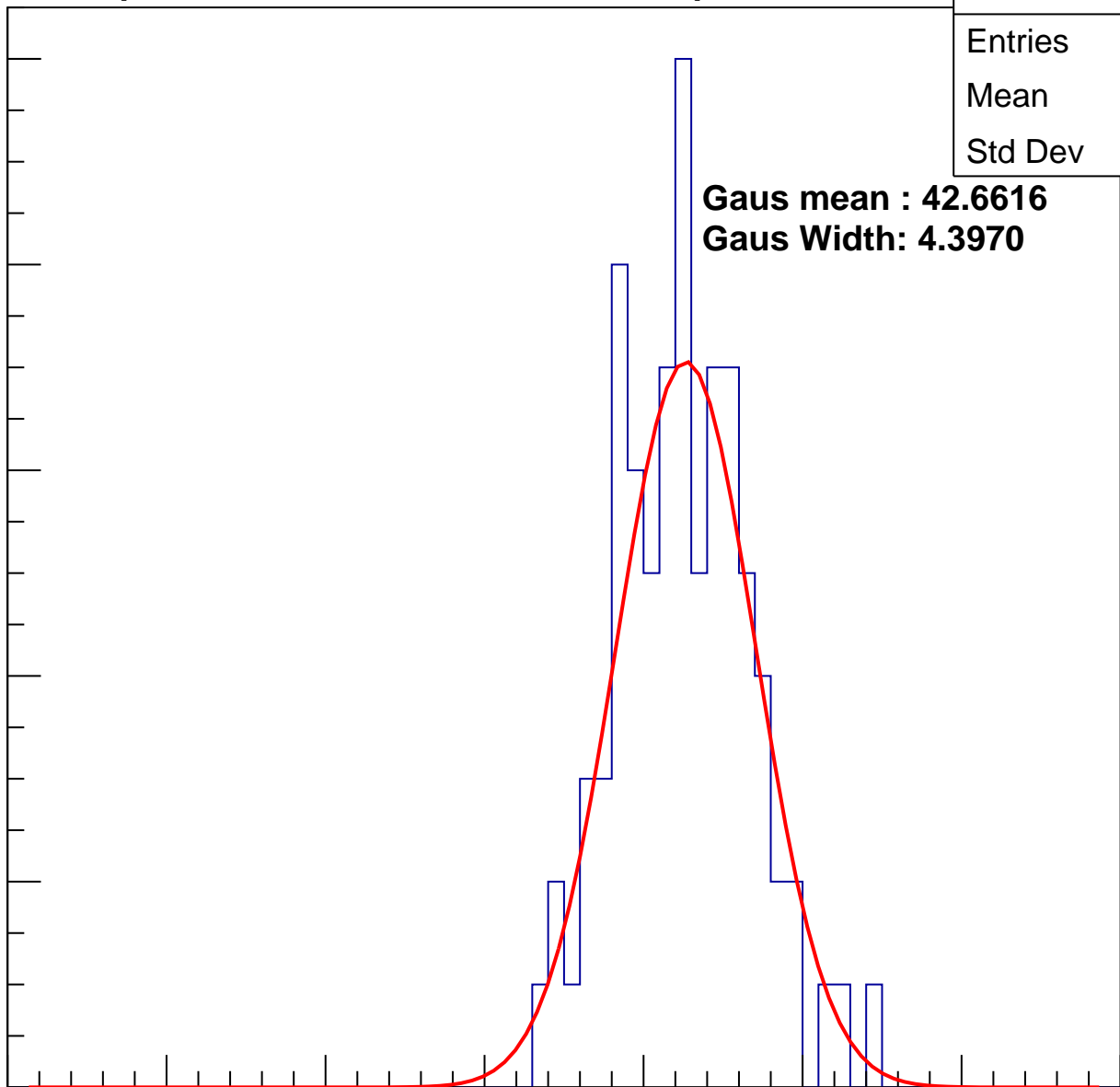
40

50

60

70

ampl

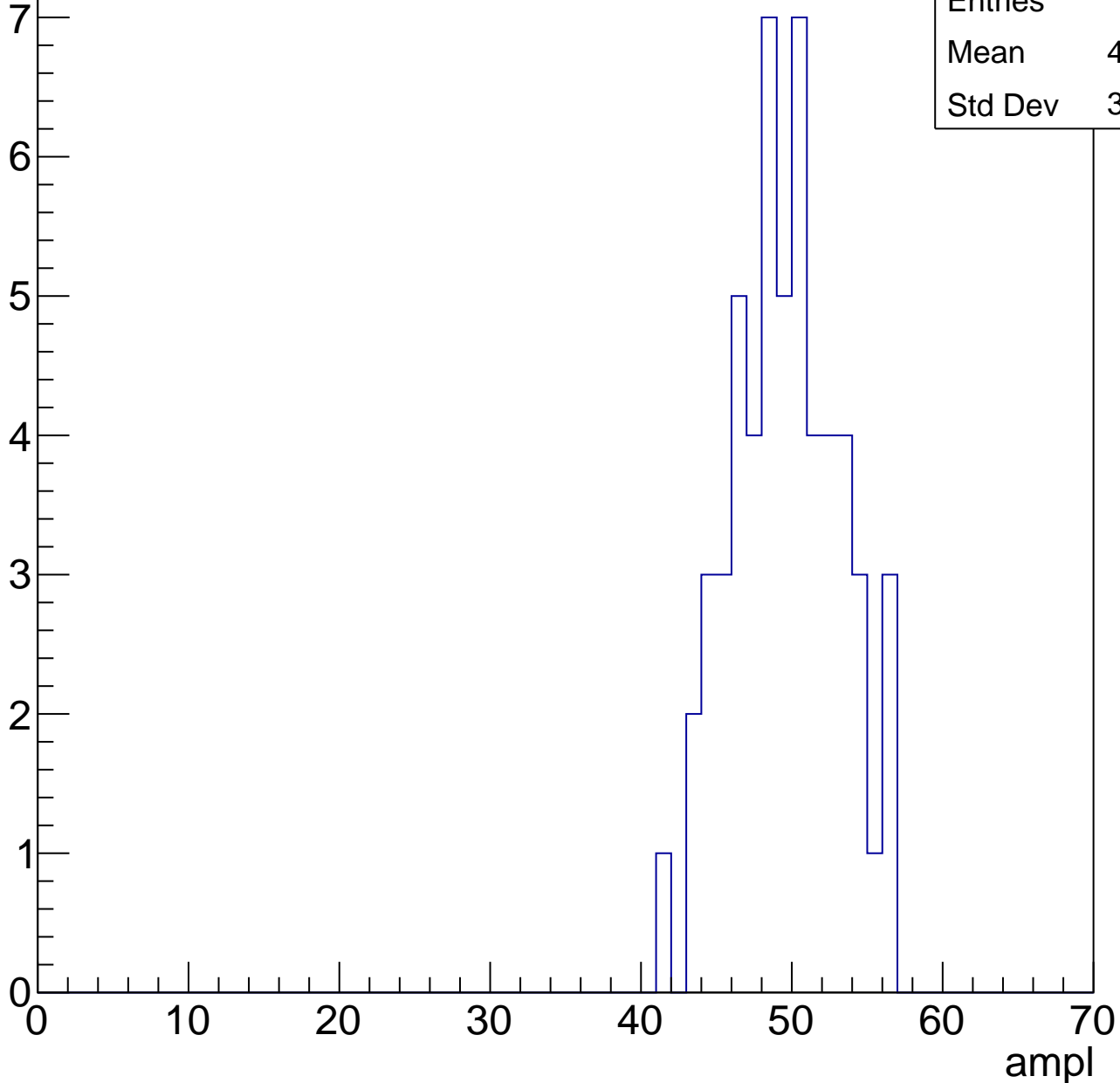


# B1L102S, U24-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.14
Std Dev	3.553

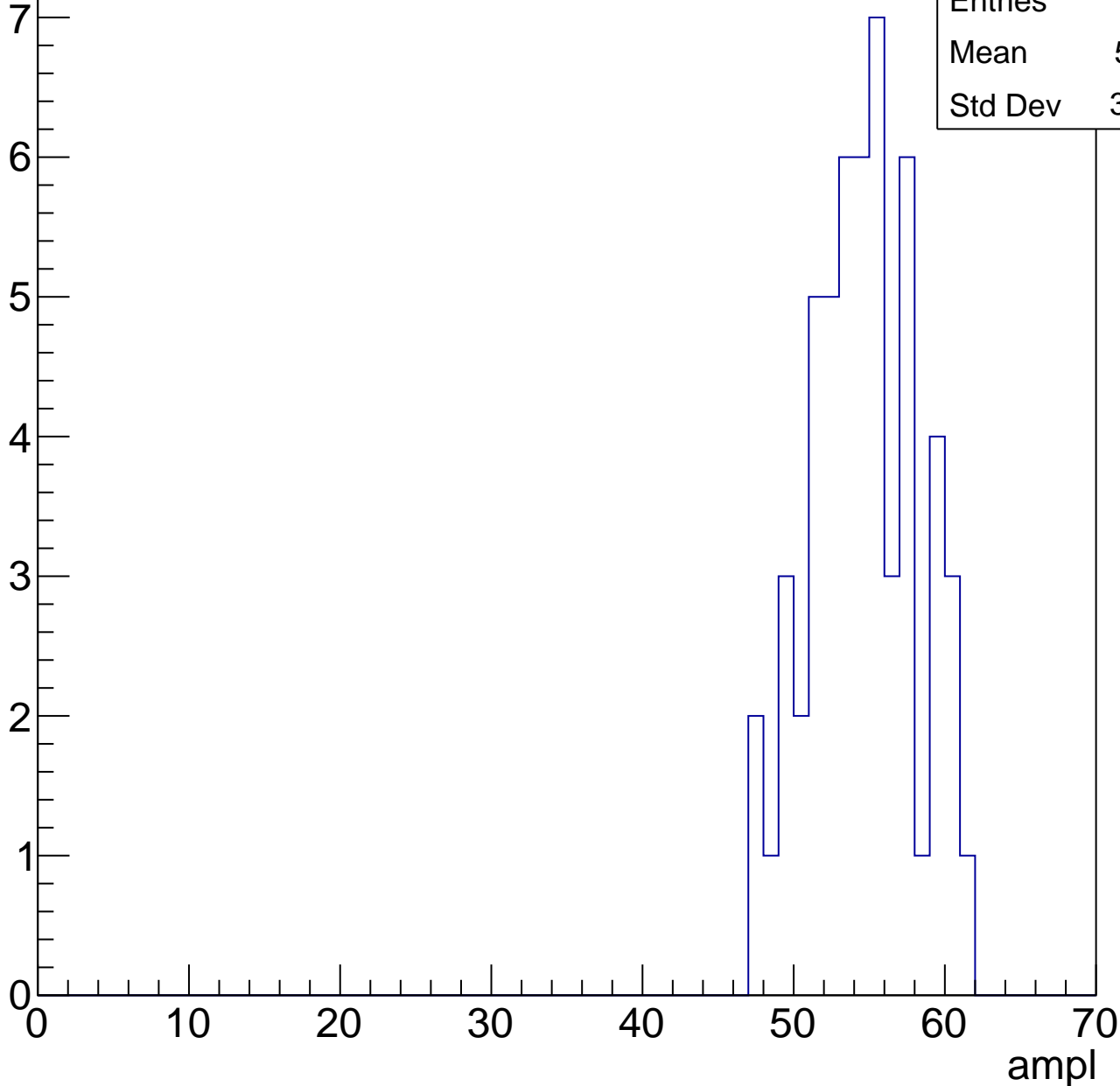


# B1L102S, U24-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.11
Std Dev	3.457

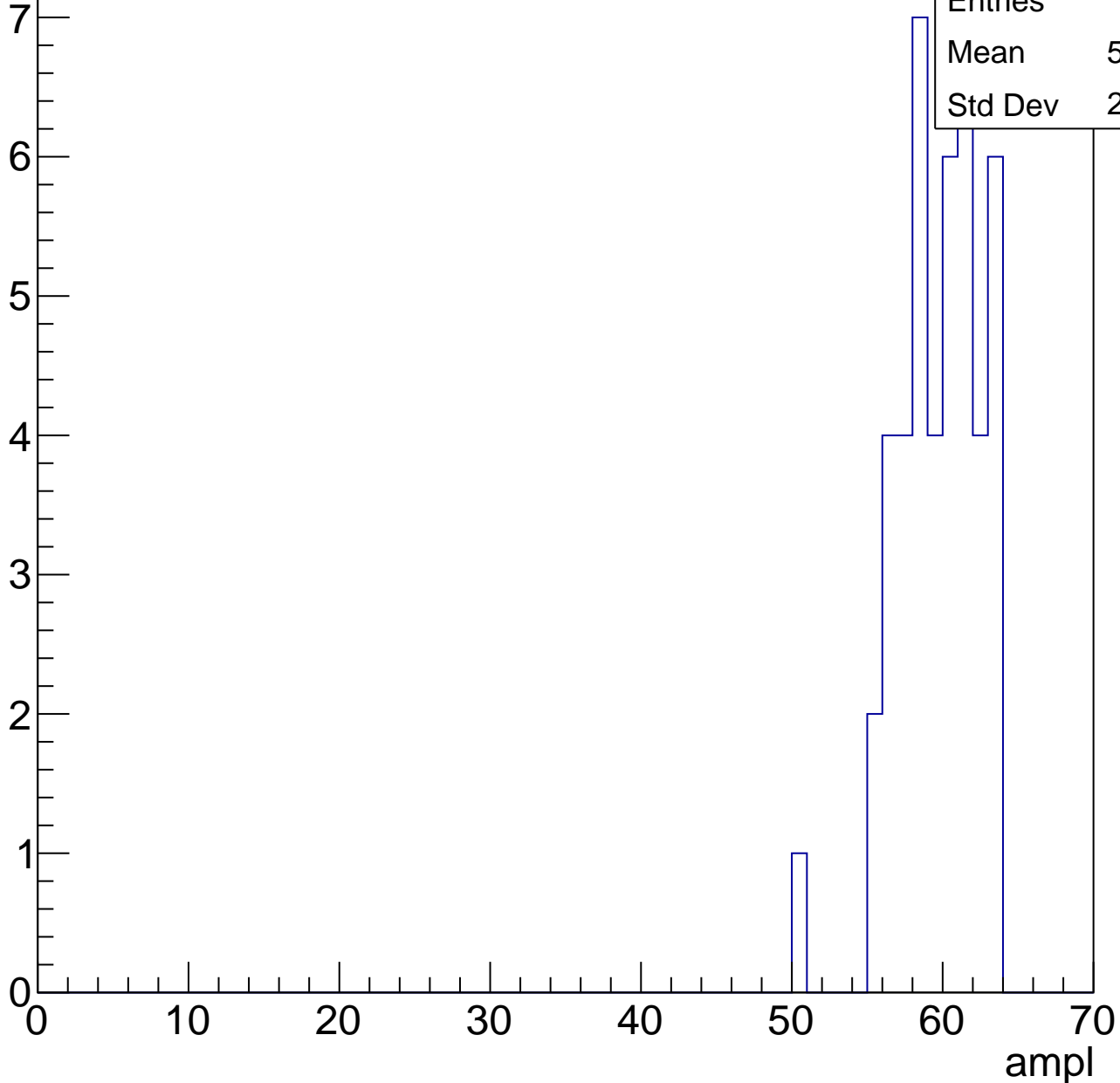


# B1L102S, U24-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.27
Std Dev	2.728

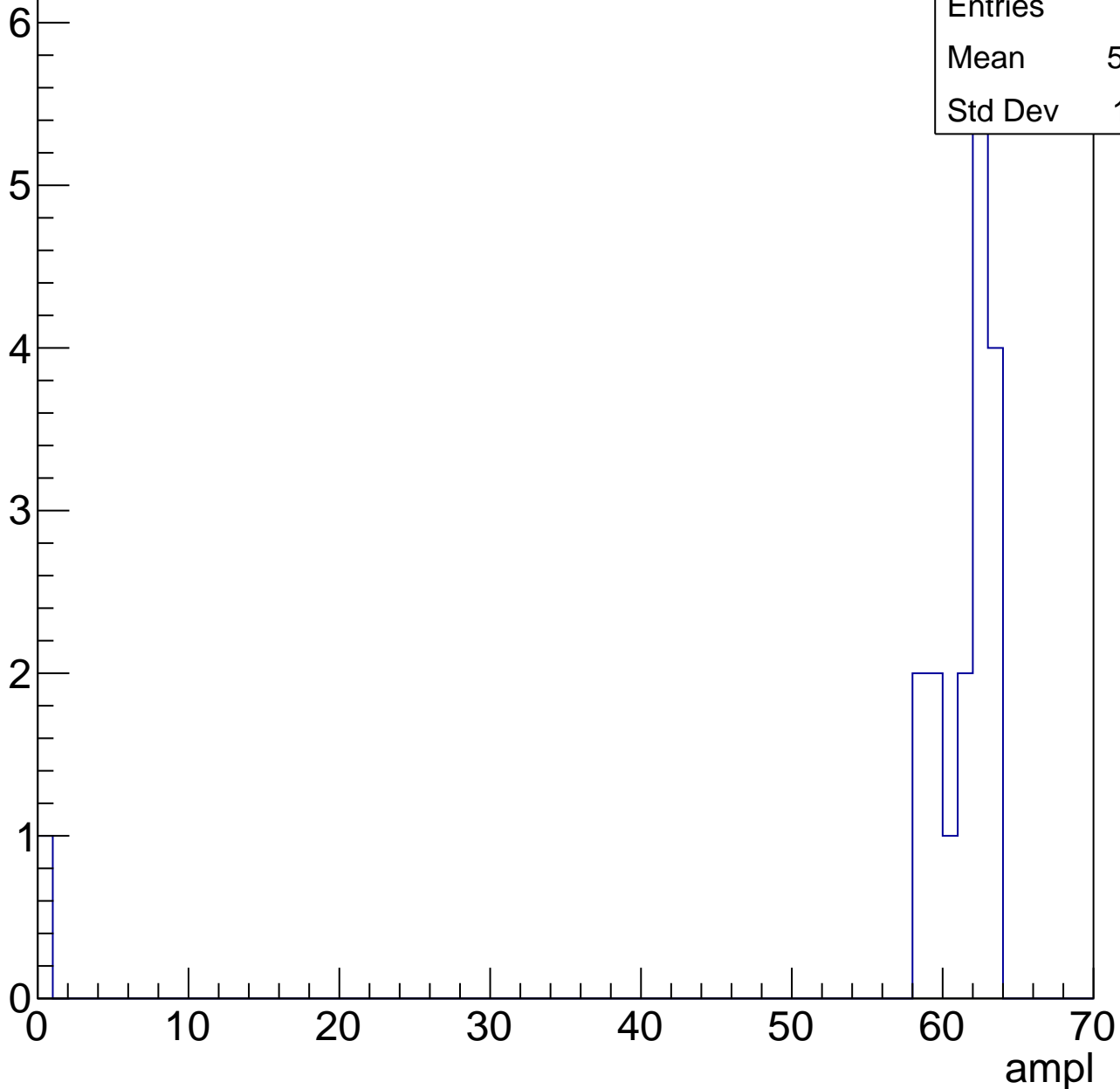


# B1L102S, U24-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	18
Mean	57.78
Std Dev	14.11





# B1L102S, U24-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

# B1L102S, U24-ch103, adc0

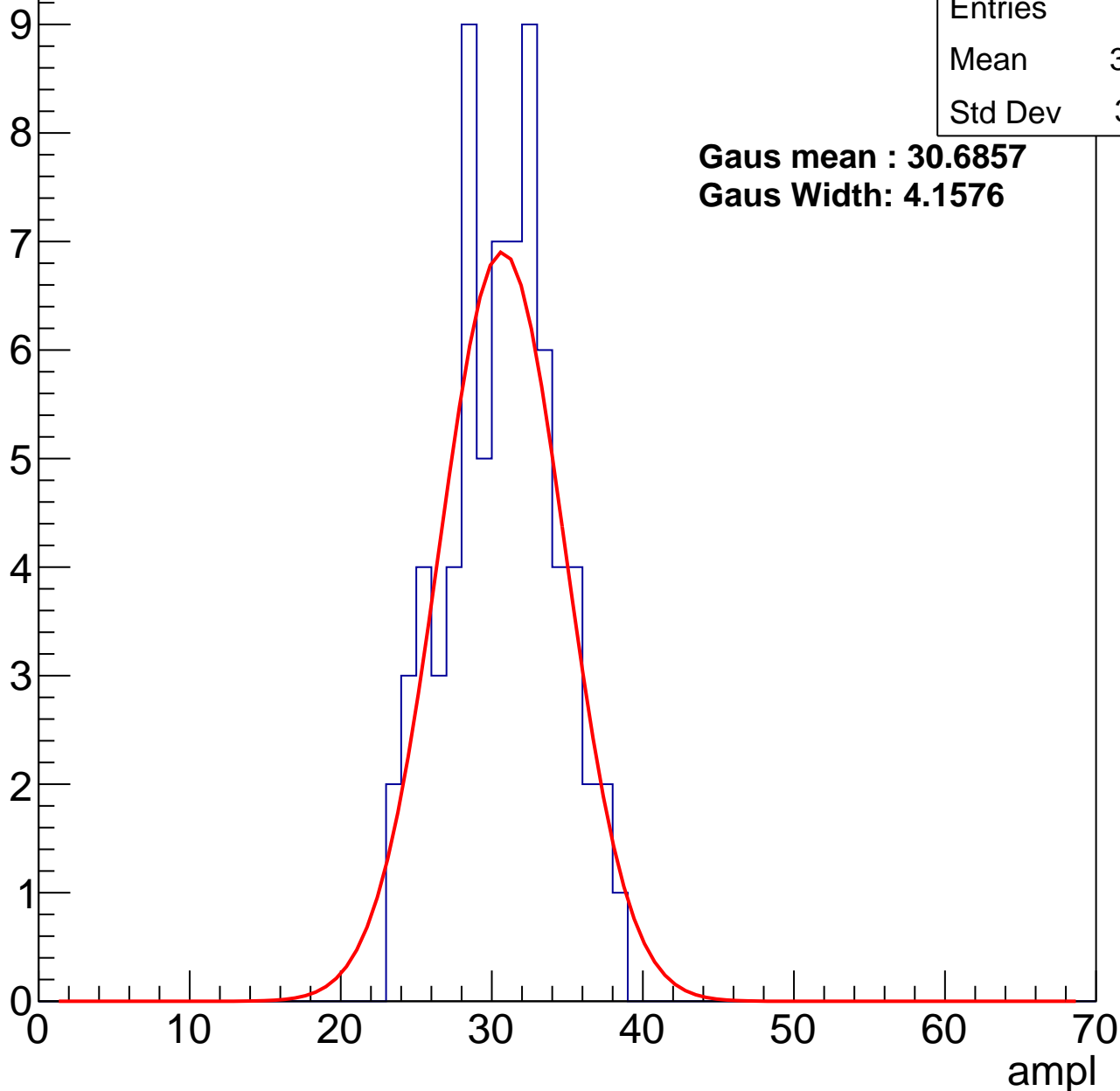
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	30.19
Std Dev	3.581

**Gaus mean : 30.6857**

**Gaus Width: 4.1576**



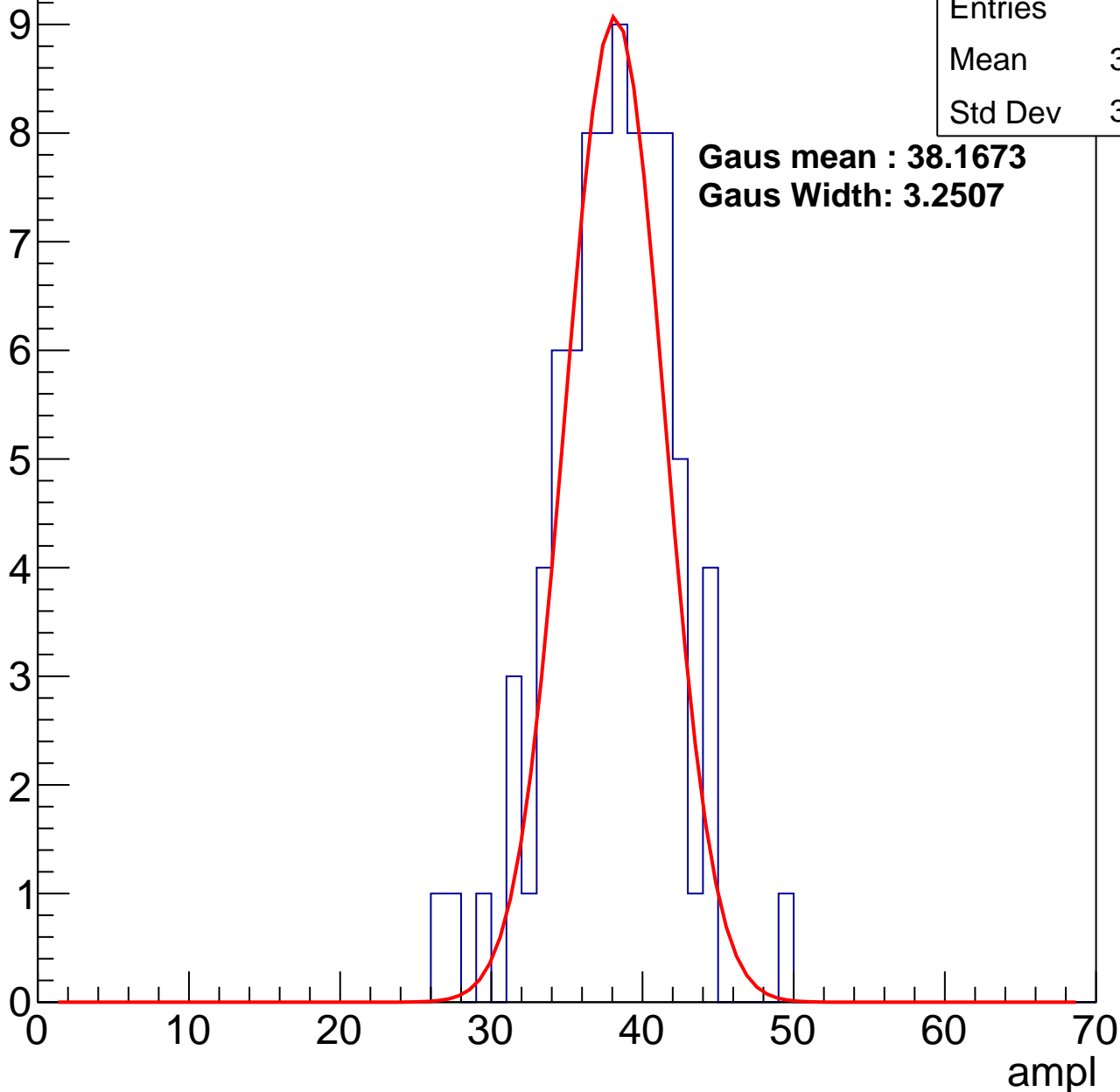
# B1L102S, U24-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	37.55
Std Dev	3.934

**Gaus mean : 38.1673**  
**Gaus Width: 3.2507**



# B1L102S, U24-ch103, adc2

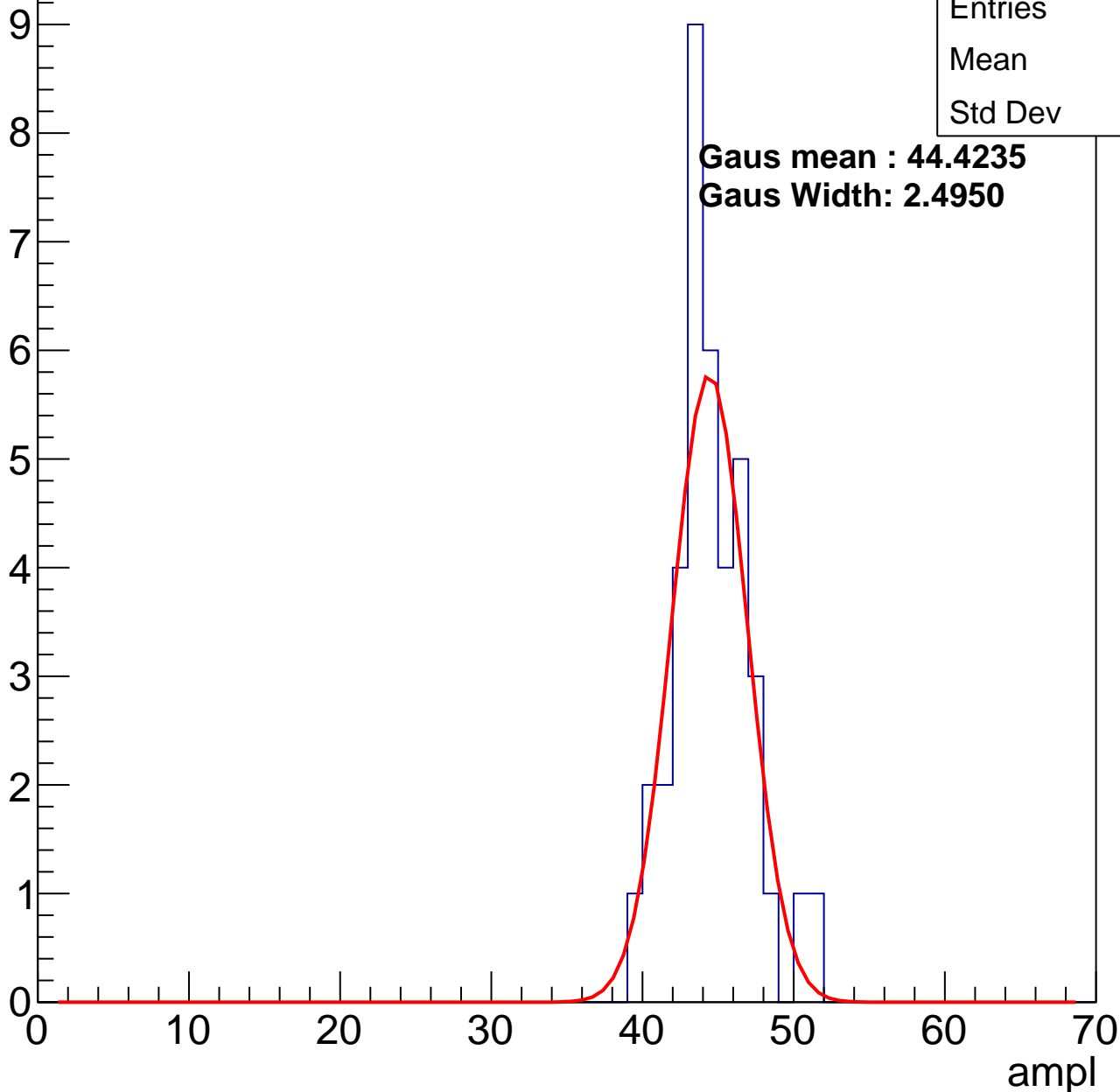
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	39
Mean	44.1
Std Dev	2.54

**Gaus mean : 44.4235**

**Gaus Width: 2.4950**

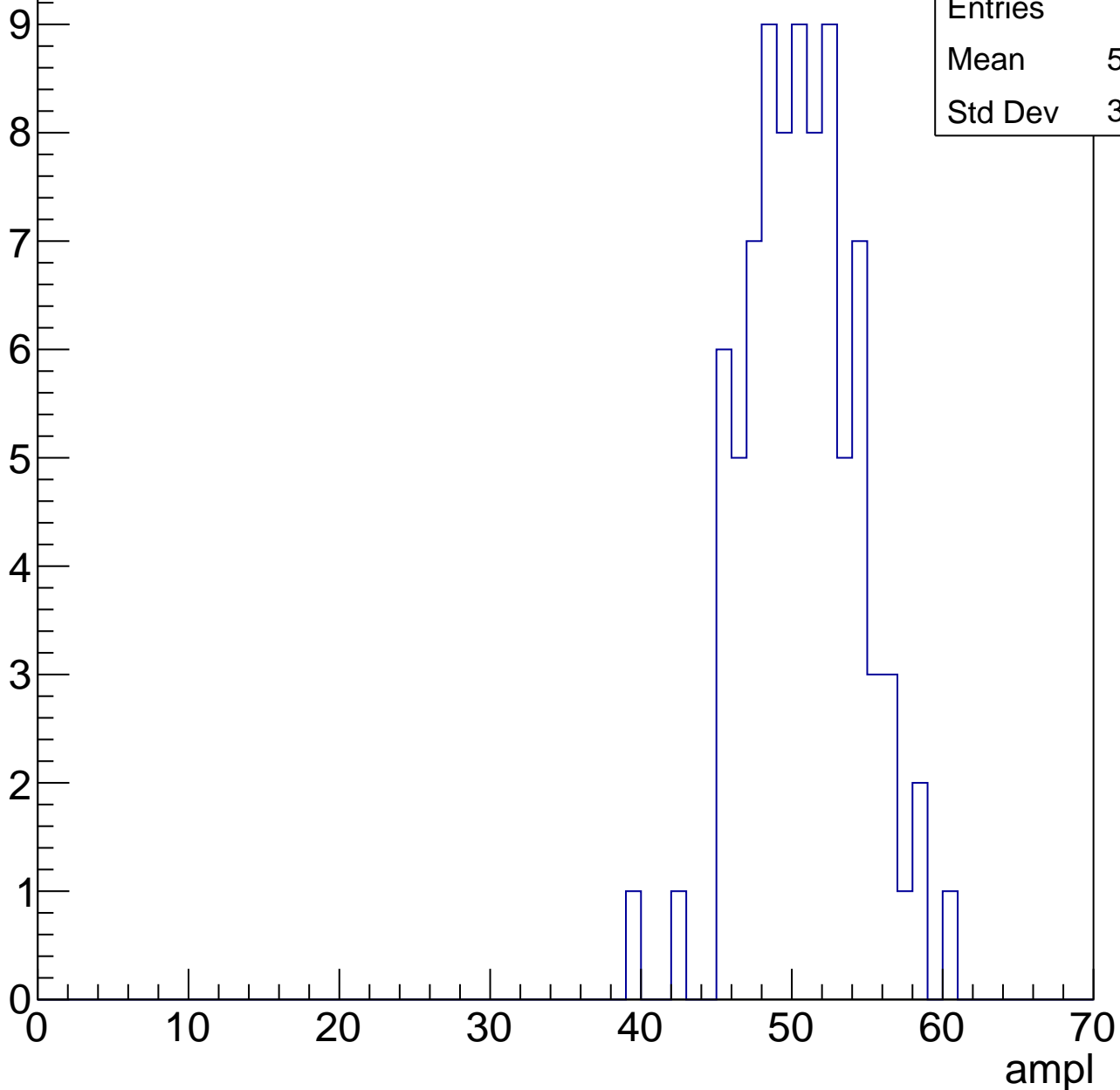


# B1L102S, U24-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	50.22
Std Dev	3.724

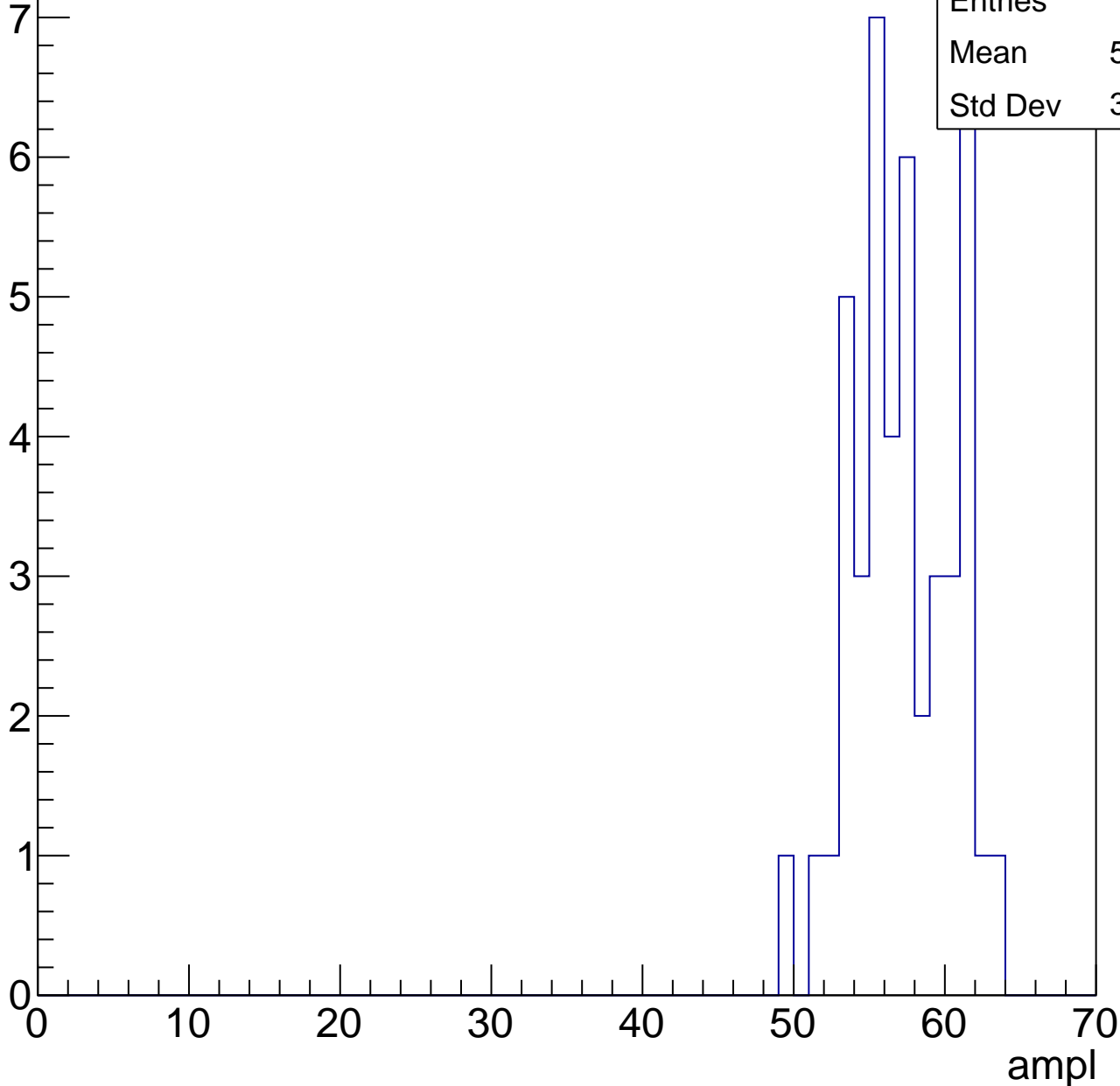


# B1L102S, U24-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	56.78
Std Dev	3.252

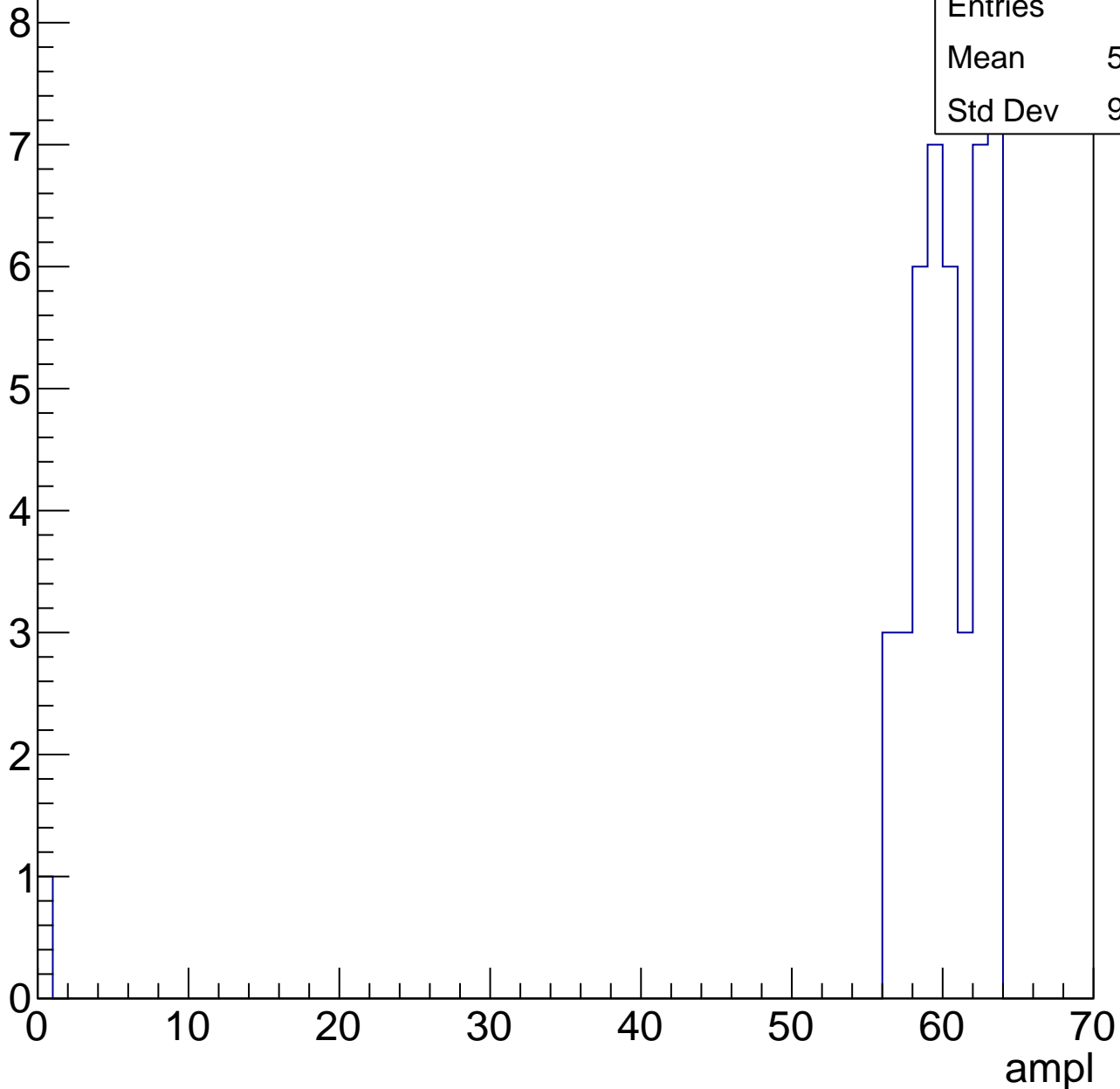


# B1L102S, U24-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

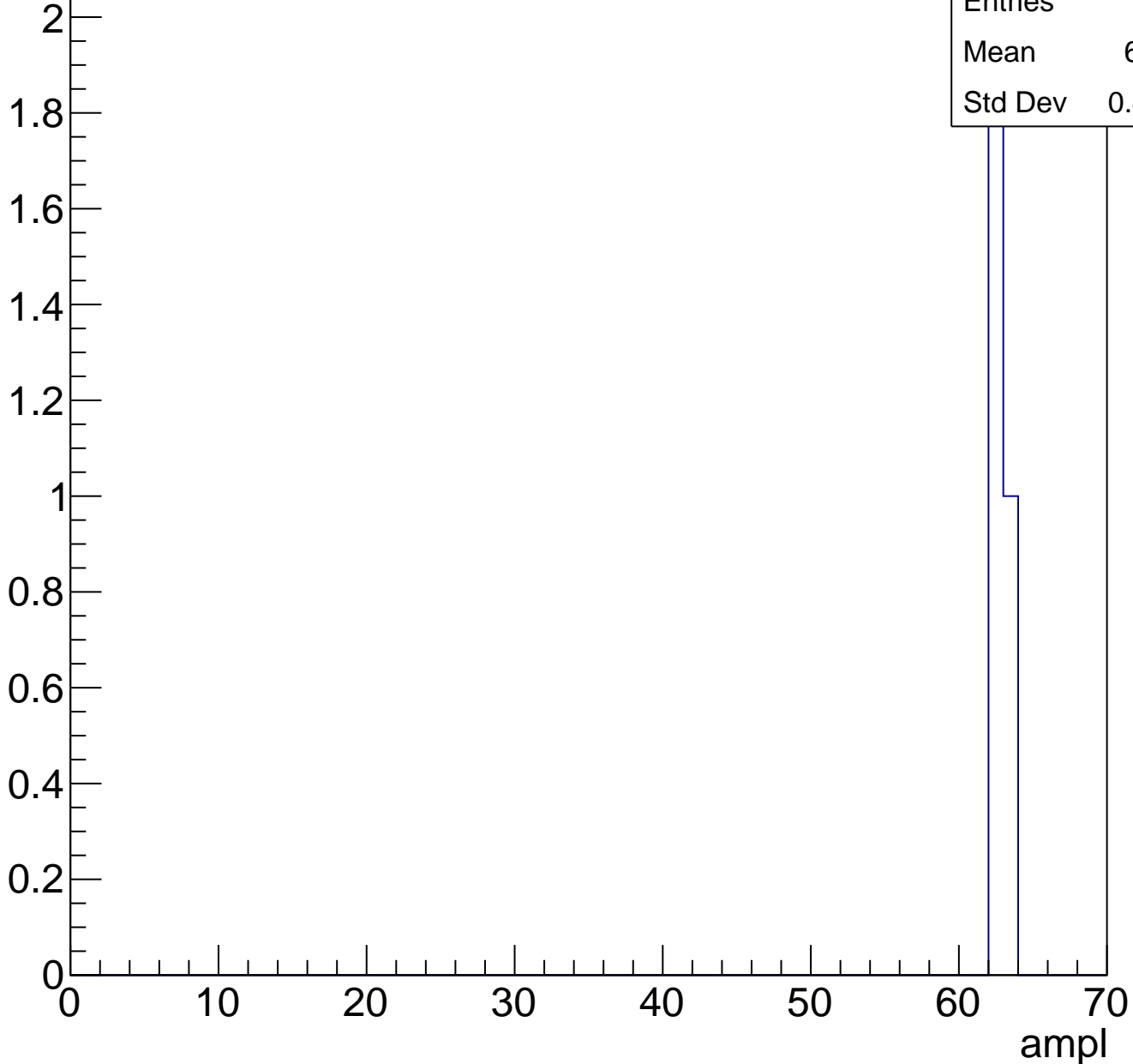
Entries	44
Mean	58.66
Std Dev	9.207



# B1L102S, U24-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch104, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	88
Mean	29.7
Std Dev	4.834

**Gaus mean : 30.1237**

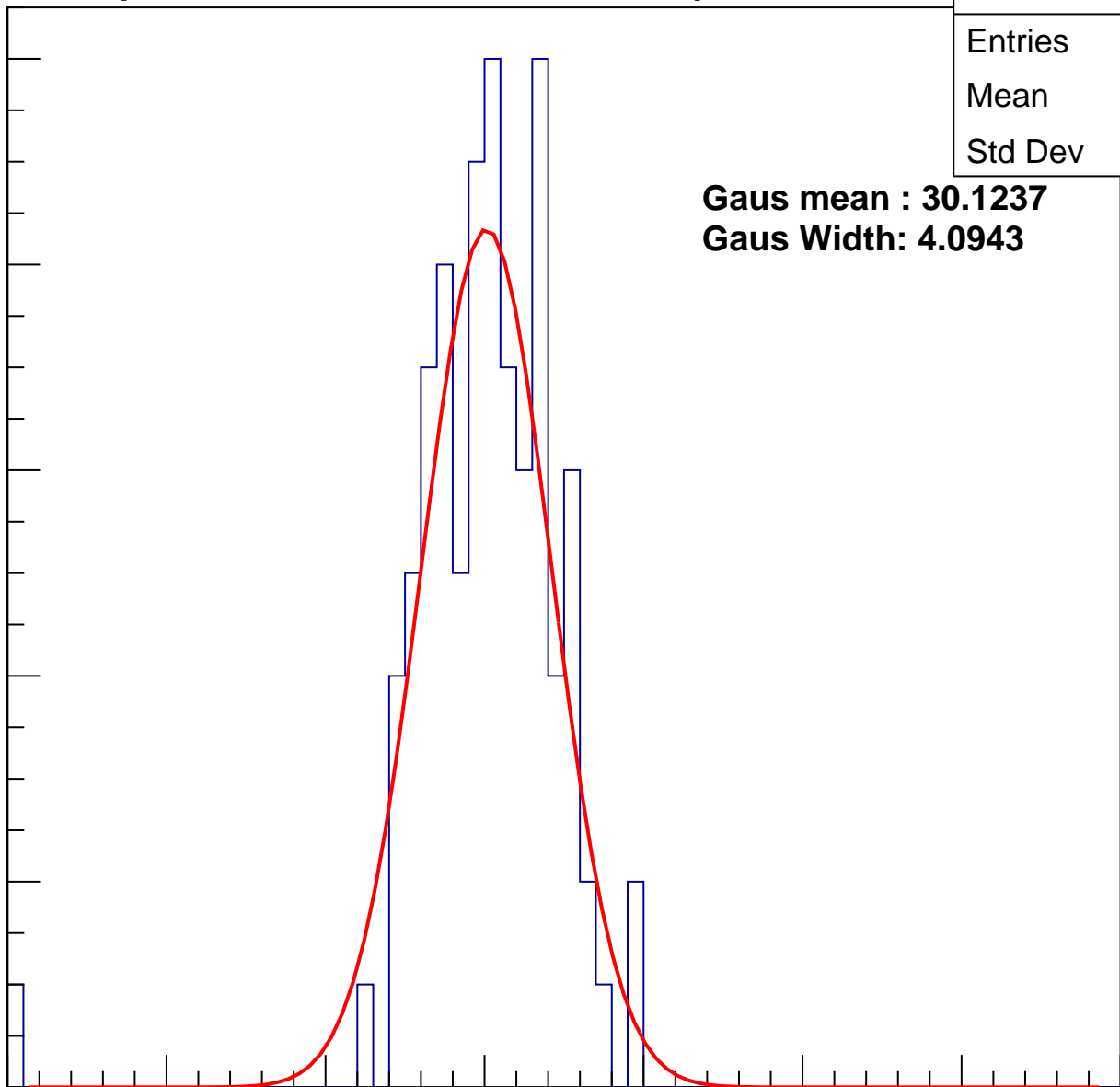
**Gaus Width: 4.0943**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch104, adc1

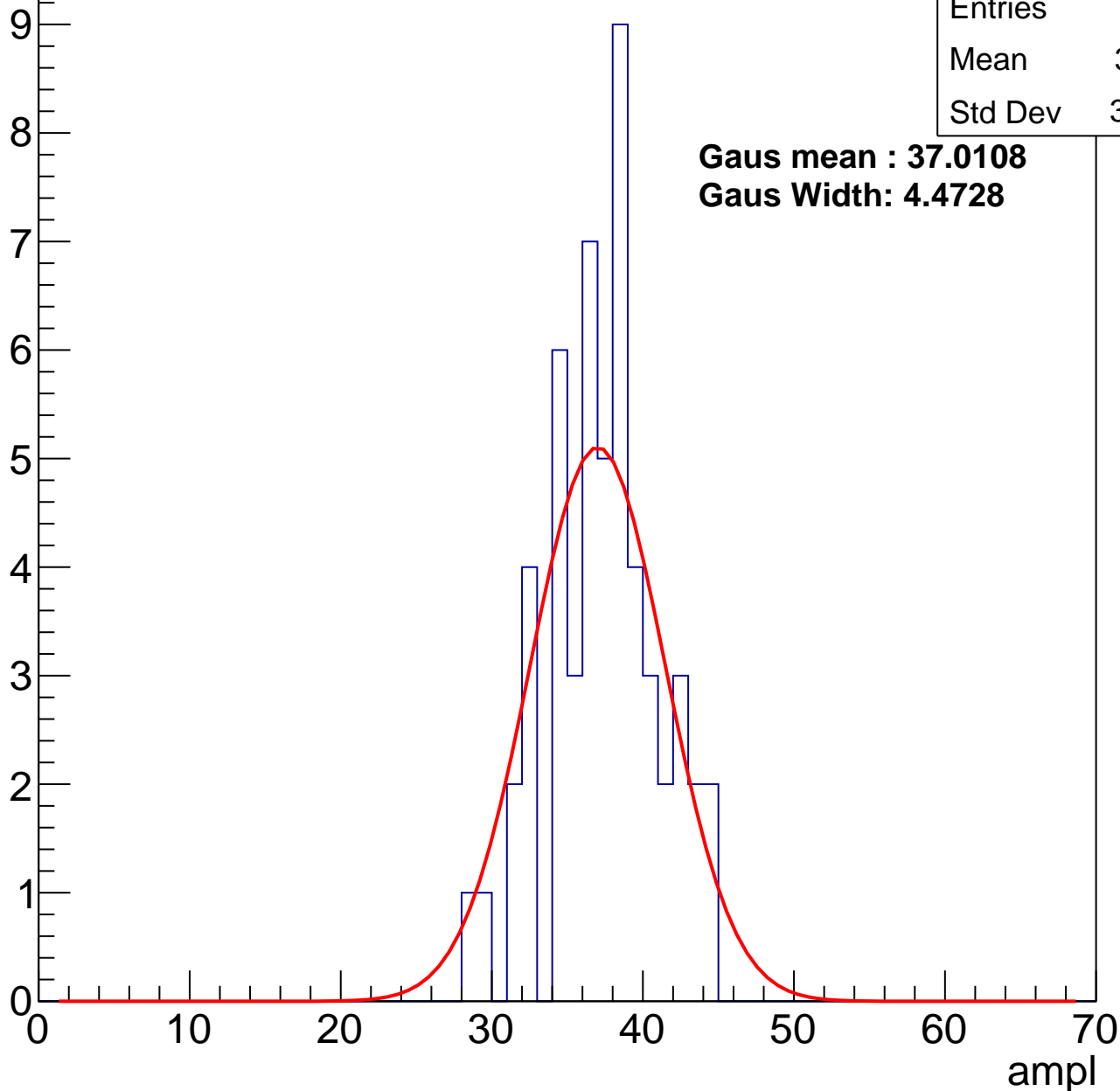
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	36.91
Std Dev	3.648

**Gaus mean : 37.0108**

**Gaus Width: 4.4728**



# B1L102S, U24-ch104, adc2

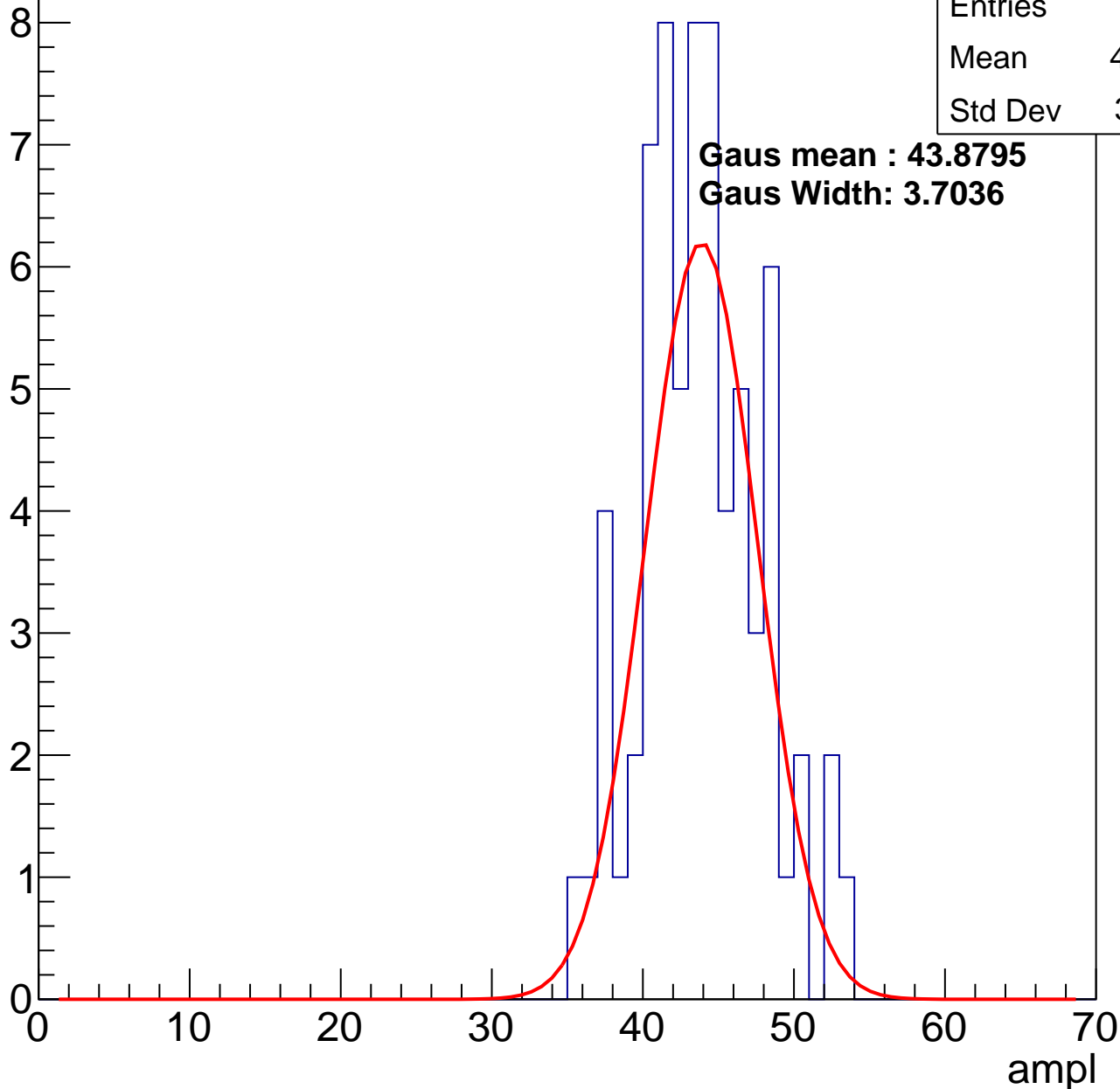
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.39
Std Dev	3.931

**Gaus mean : 43.8795**

**Gaus Width: 3.7036**

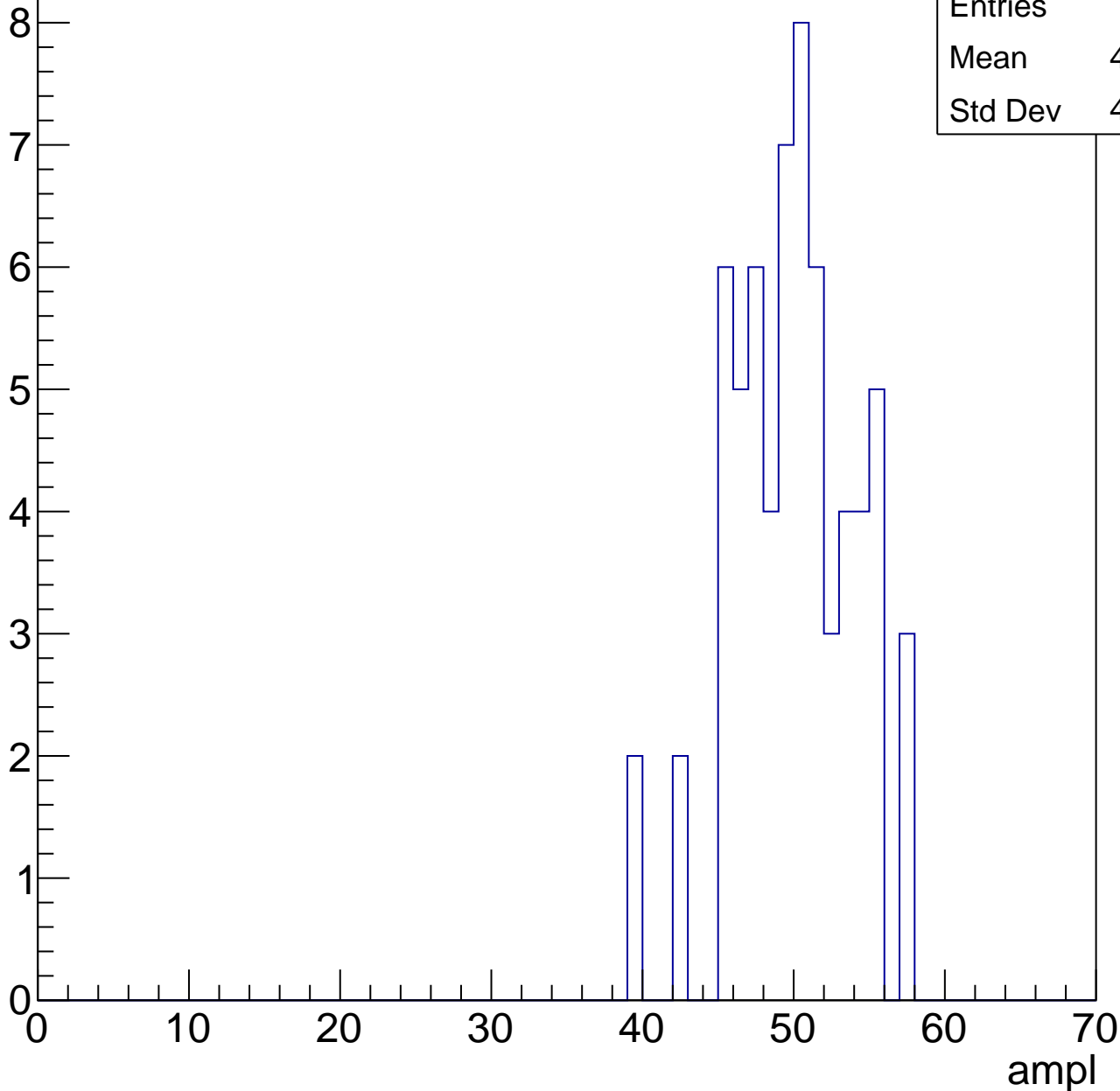


# B1L102S, U24-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.46
Std Dev	4.016

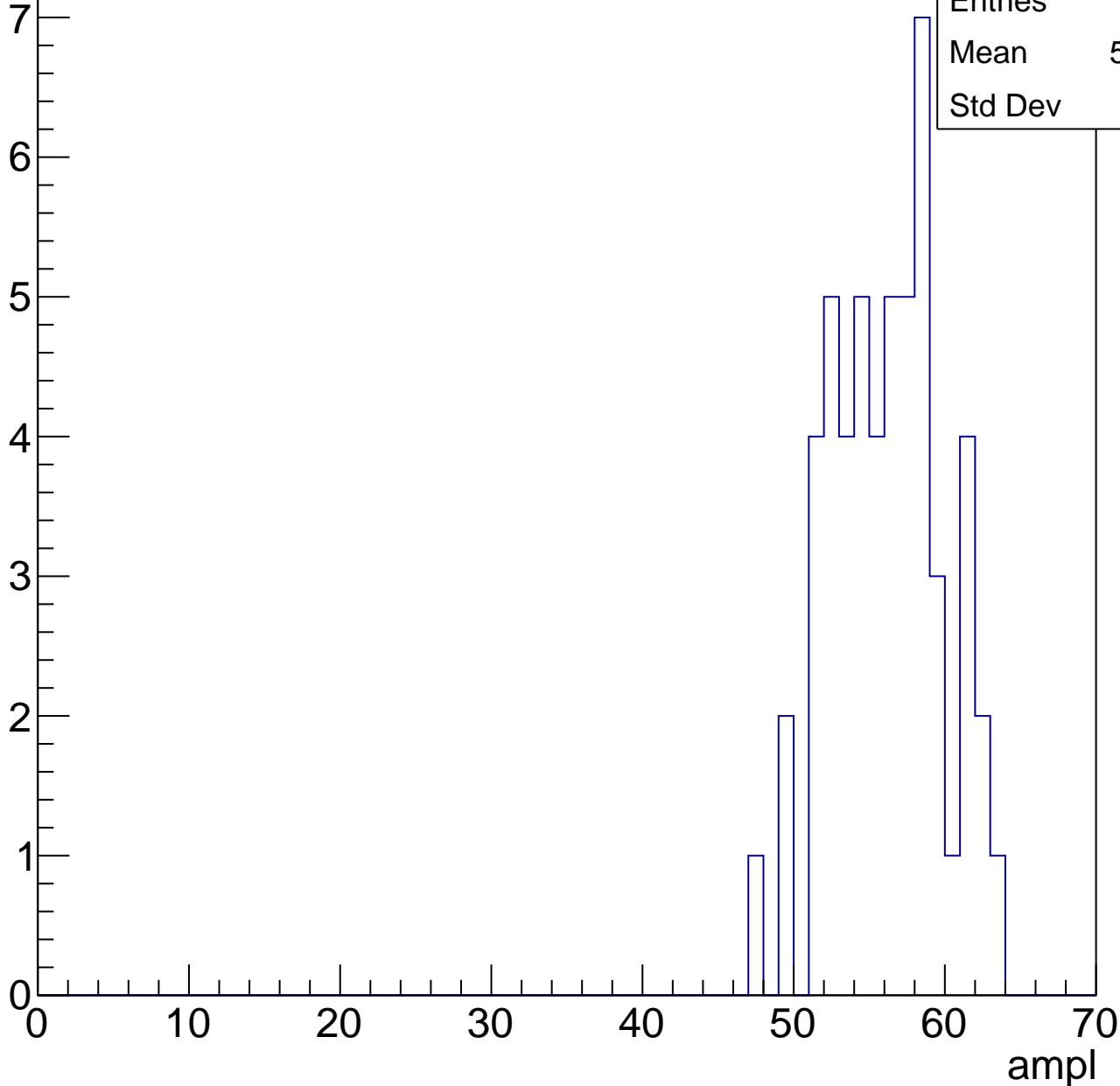


# B1L102S, U24-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.66
Std Dev	3.66

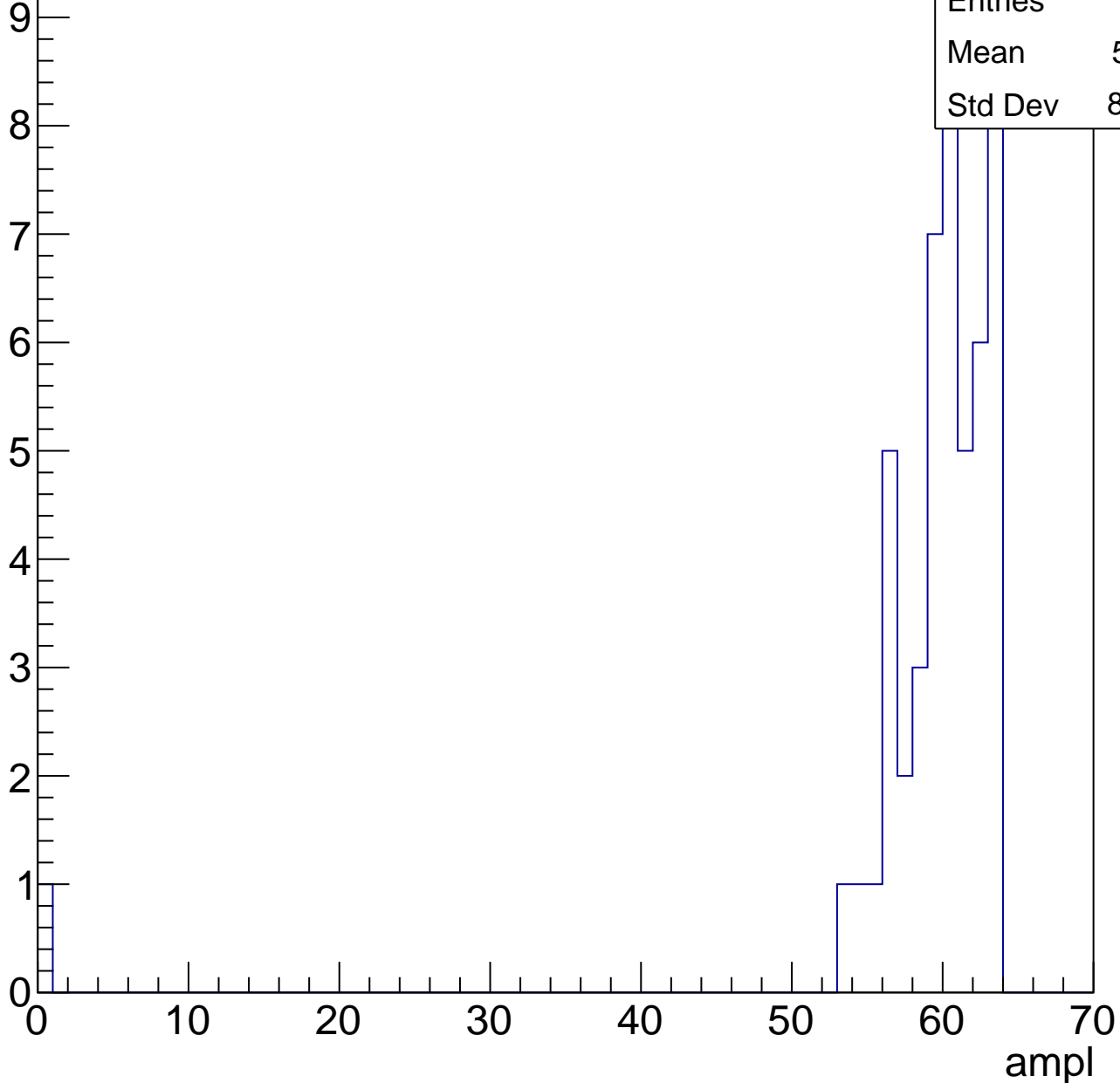


# B1L102S, U24-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

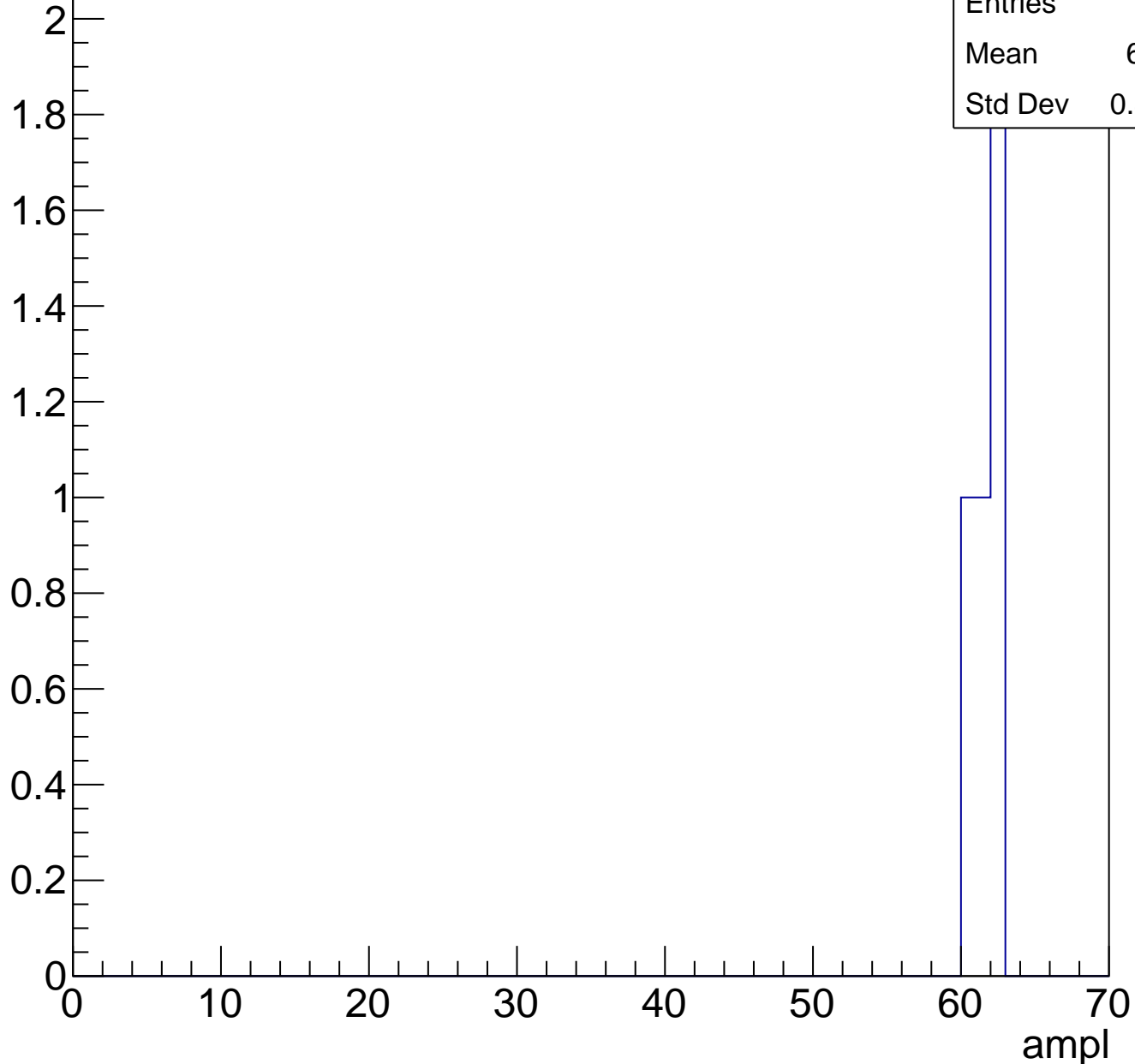
Entries	49
Mean	58.51
Std Dev	8.839



# B1L102S, U24-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



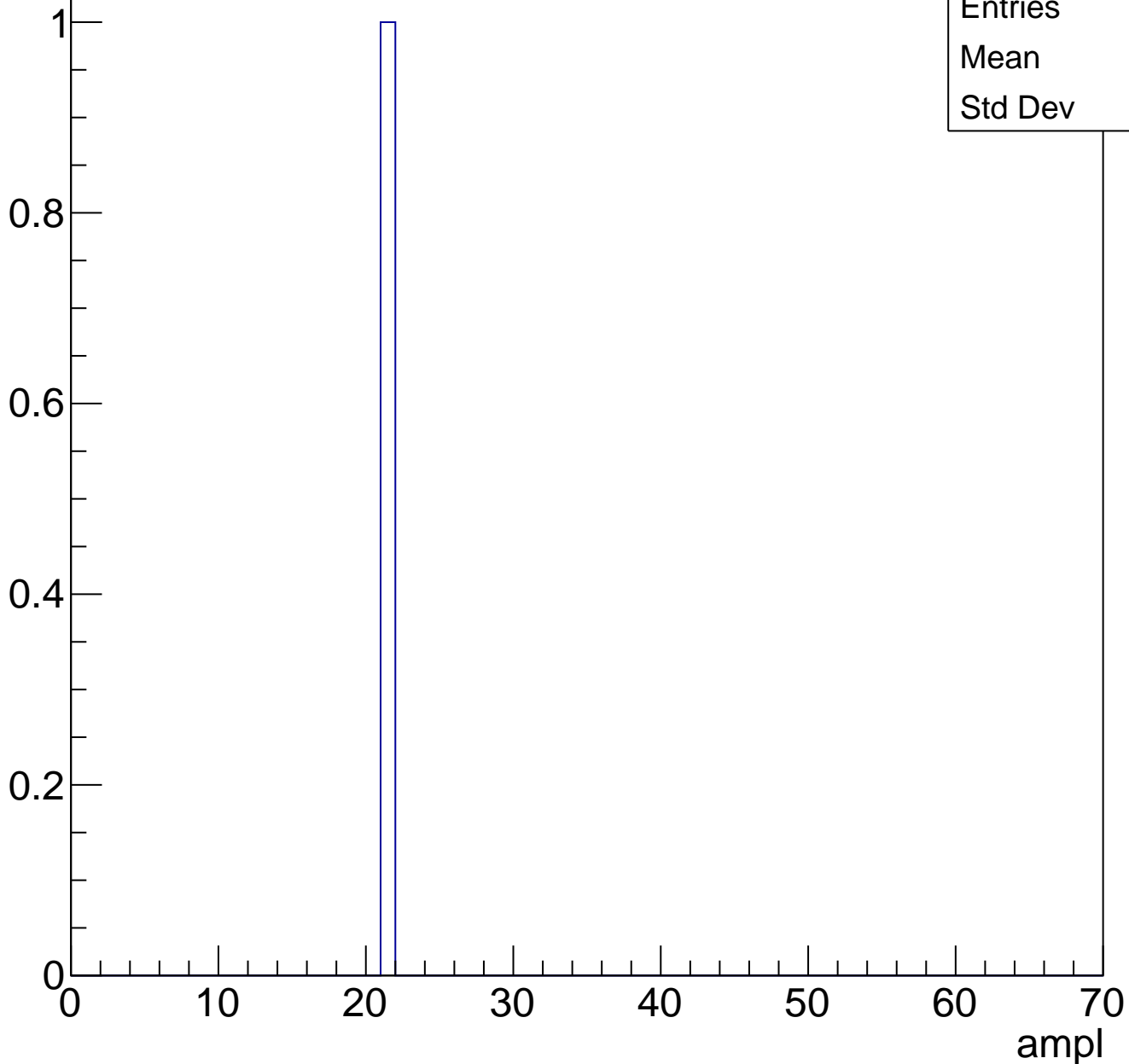
Entries	4
Mean	61.25
Std Dev	0.8292



# B1L102S, U24-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch105, adc0

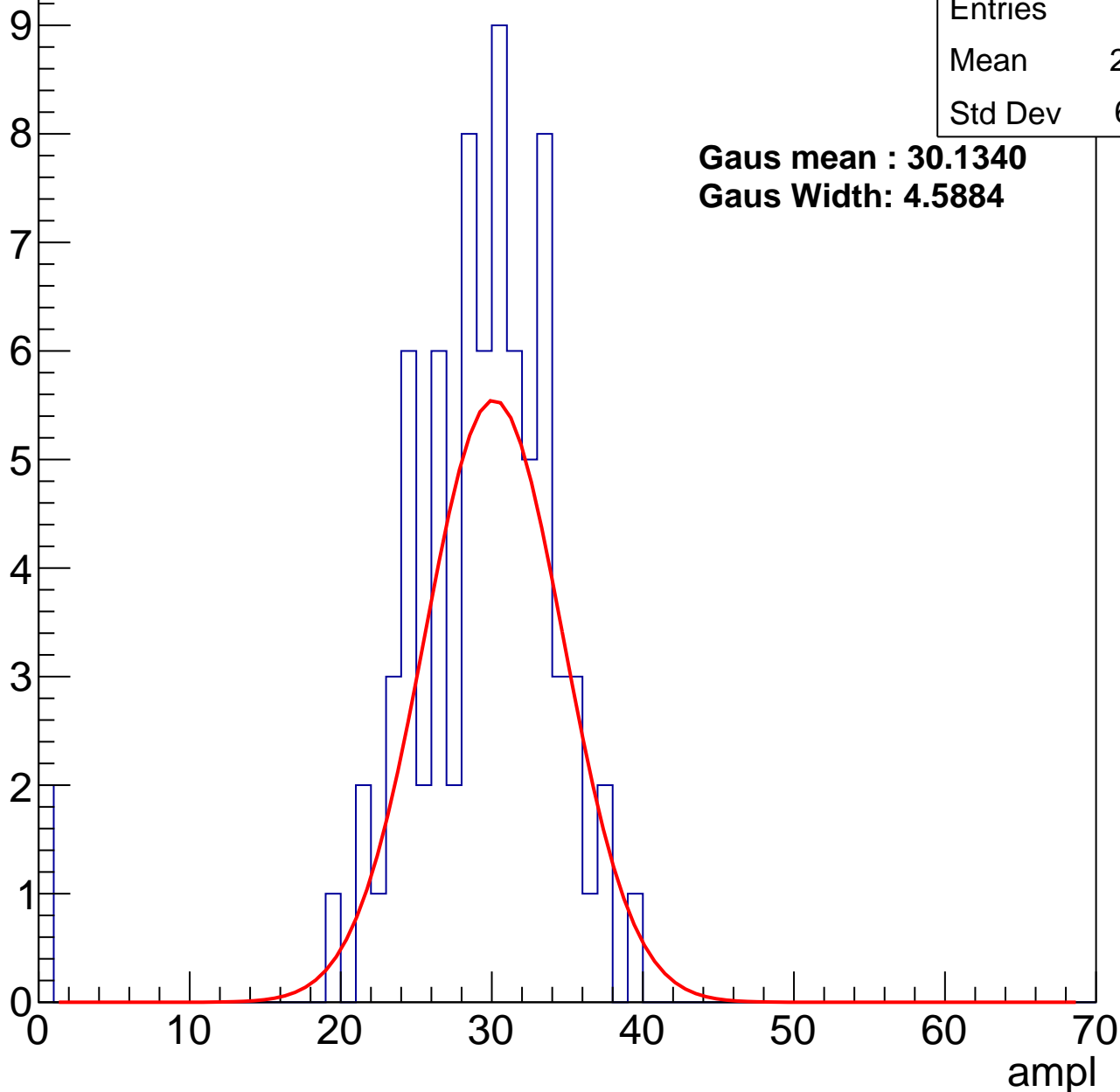
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.44
Std Dev	6.201

**Gaus mean : 30.1340**

**Gaus Width: 4.5884**



# B1L102S, U24-ch105, adc1

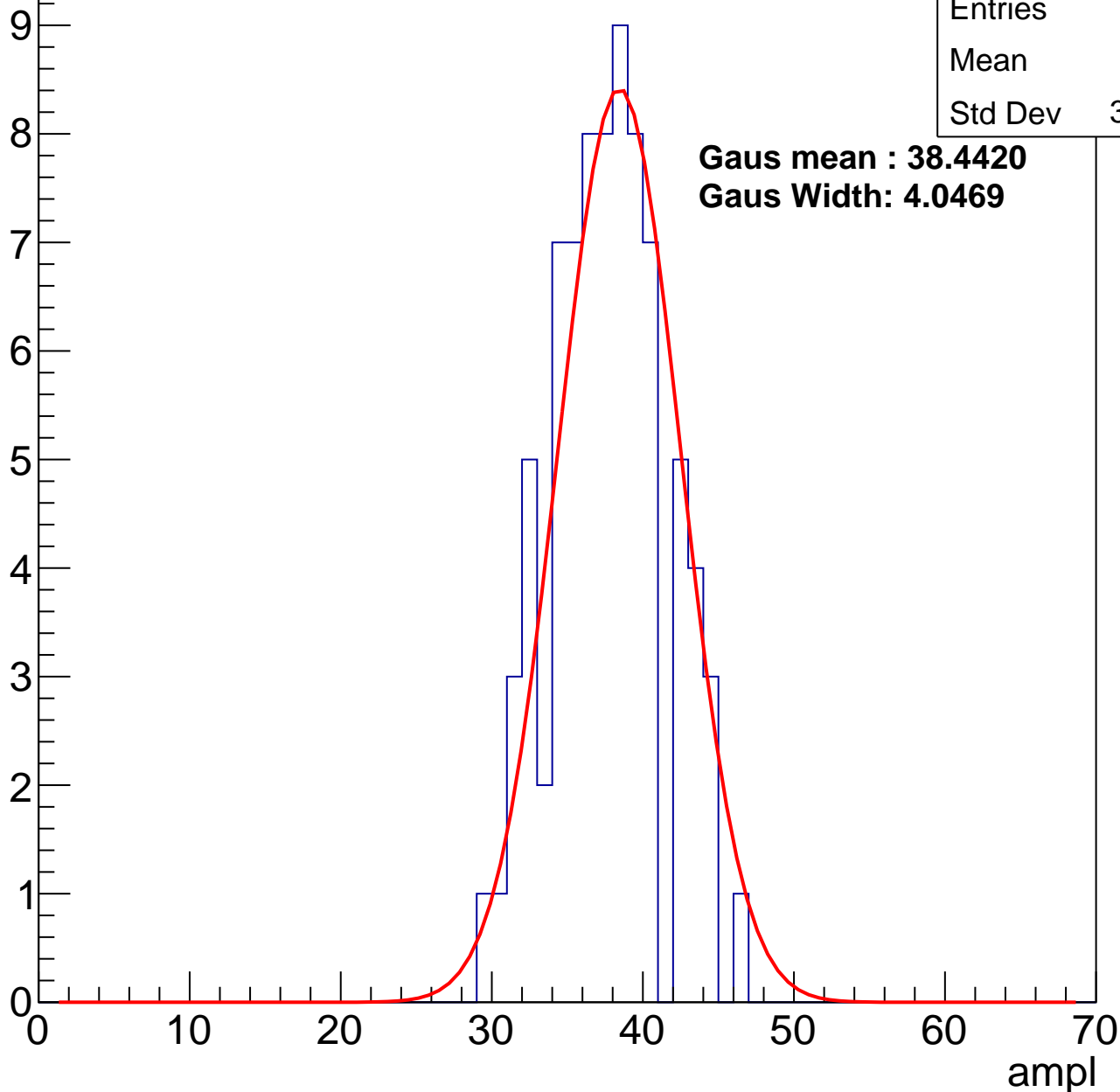
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	37.2
Std Dev	3.688

**Gaus mean : 38.4420**

**Gaus Width: 4.0469**



# B1L102S, U24-ch105, adc2

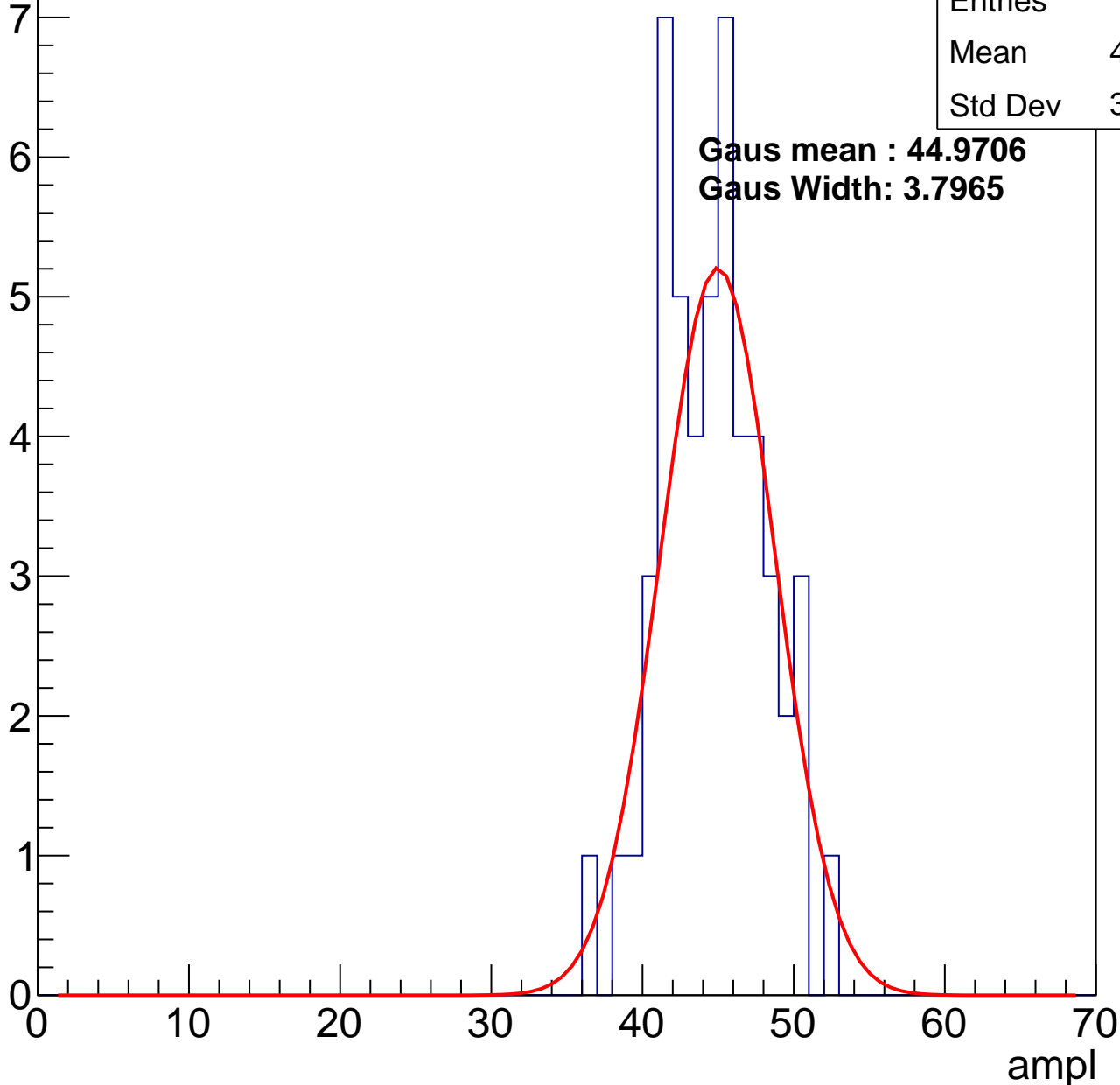
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	44.18
Std Dev	3.394

**Gaus mean : 44.9706**

**Gaus Width: 3.7965**

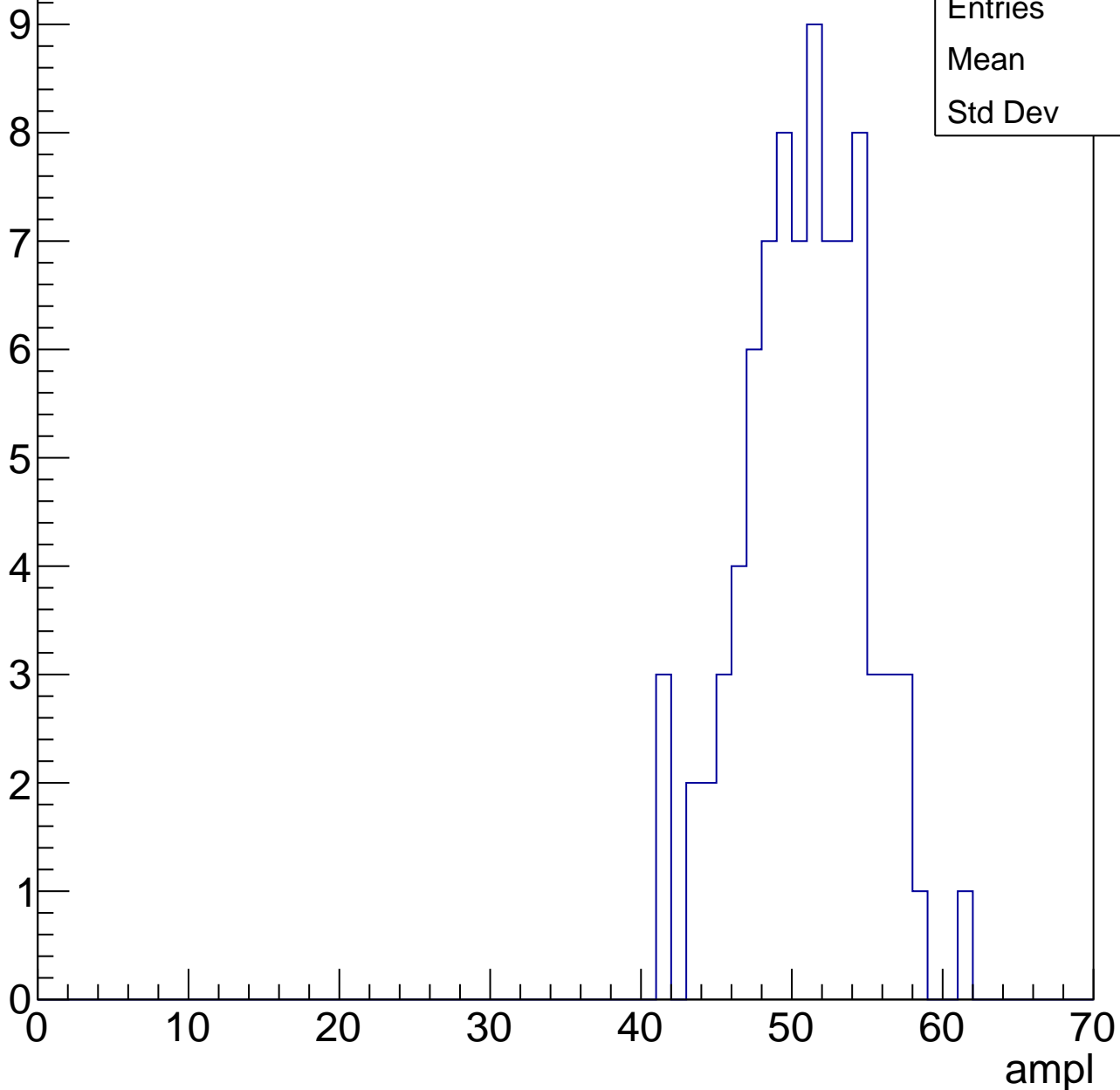


# B1L102S, U24-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	50.3
Std Dev	4.05

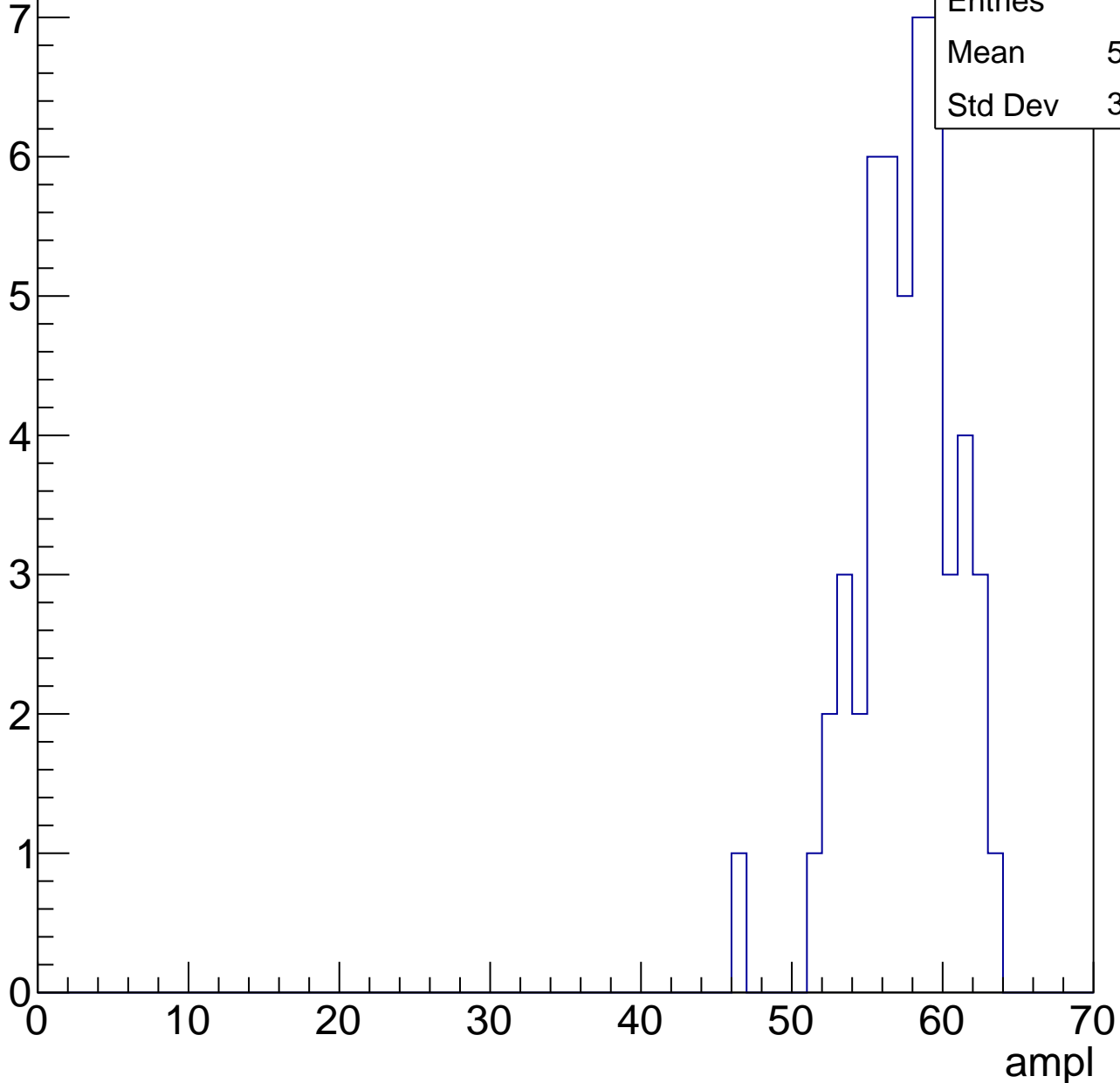


# B1L102S, U24-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.08
Std Dev	3.253

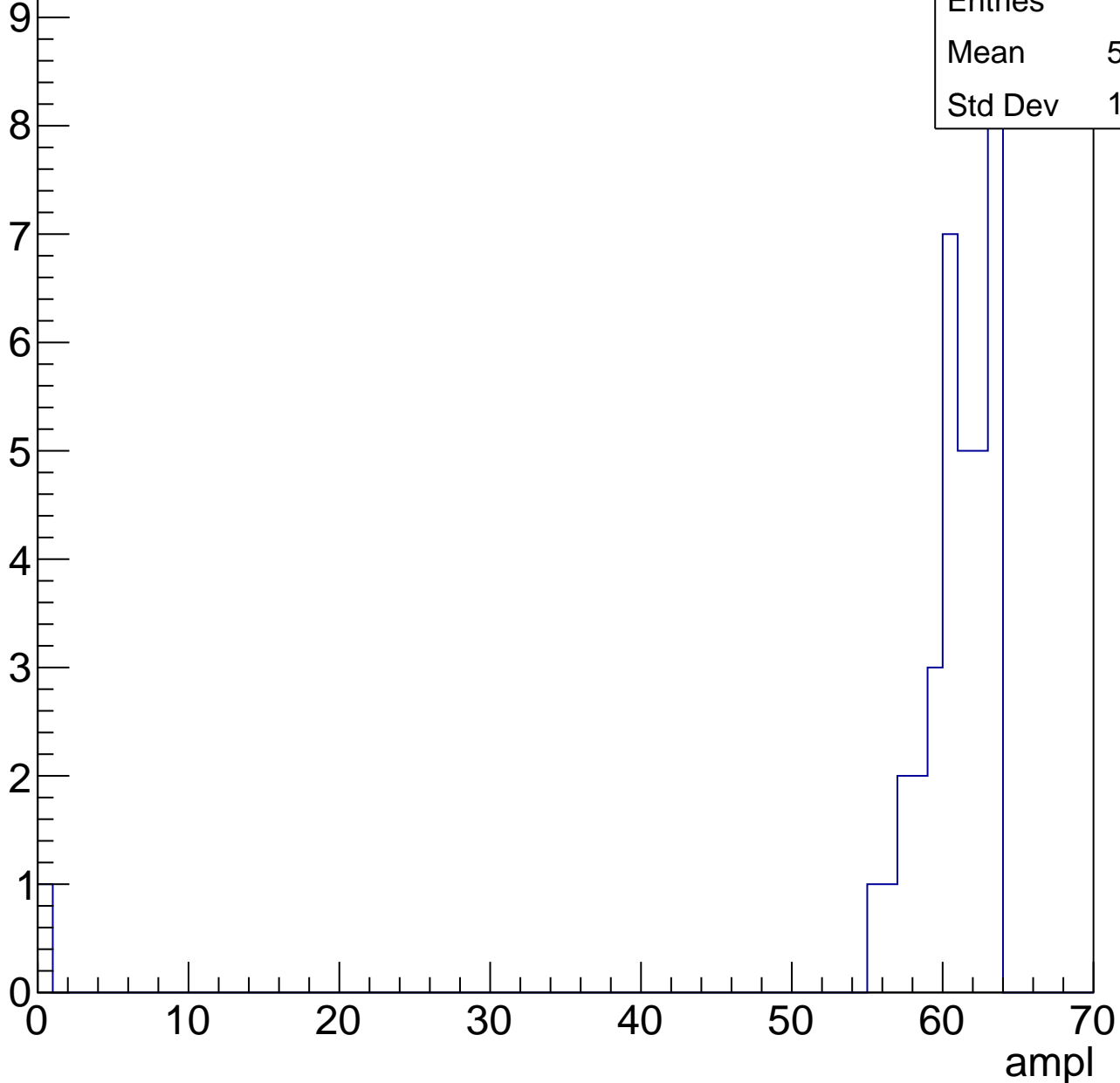


# B1L102S, U24-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.89
Std Dev	10.18



# B1L102S, U24-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	90
Mean	27.39
Std Dev	5.699

**Gaus mean : 28.7022**

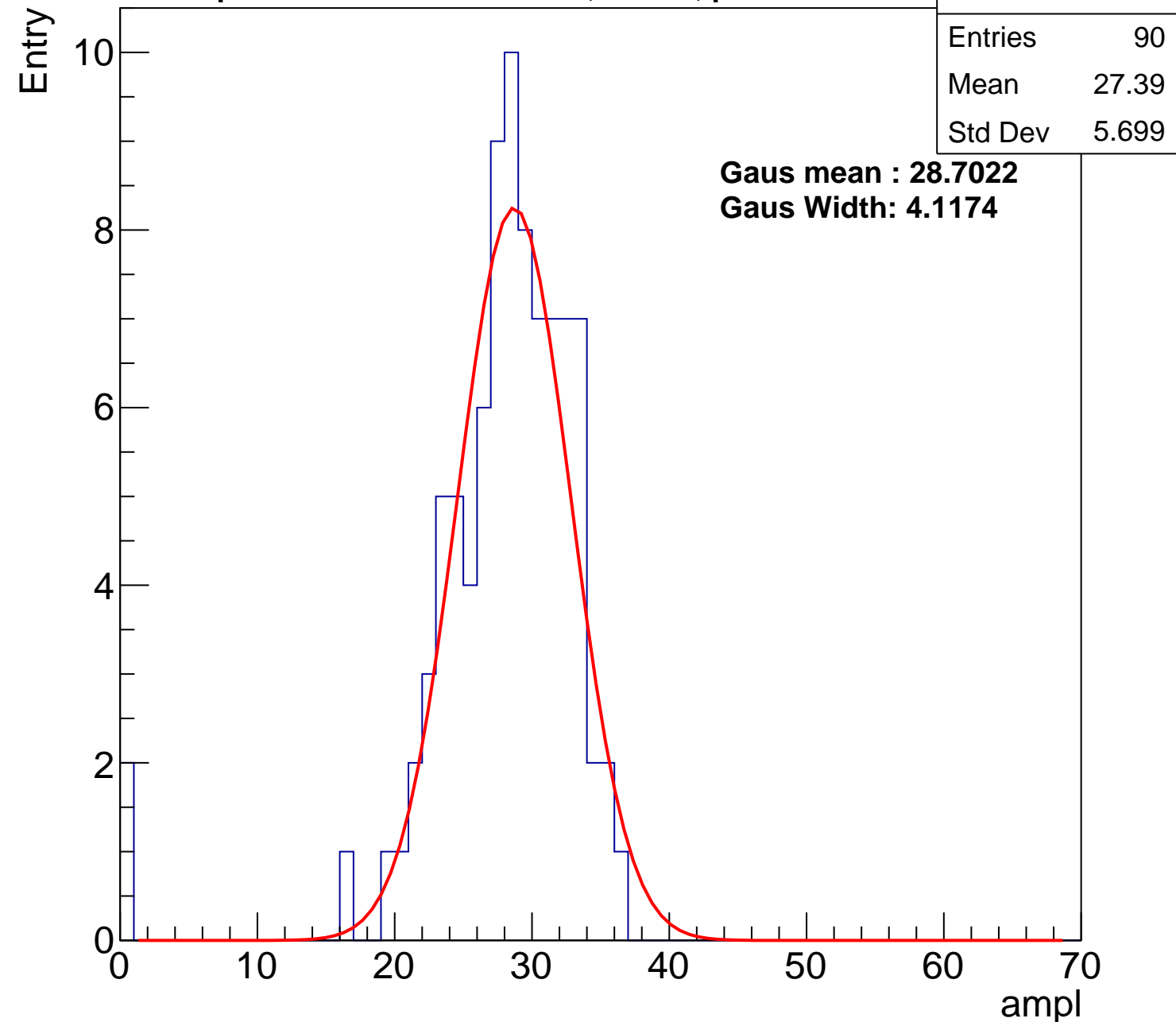
**Gaus Width: 4.1174**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U24-ch106, adc1

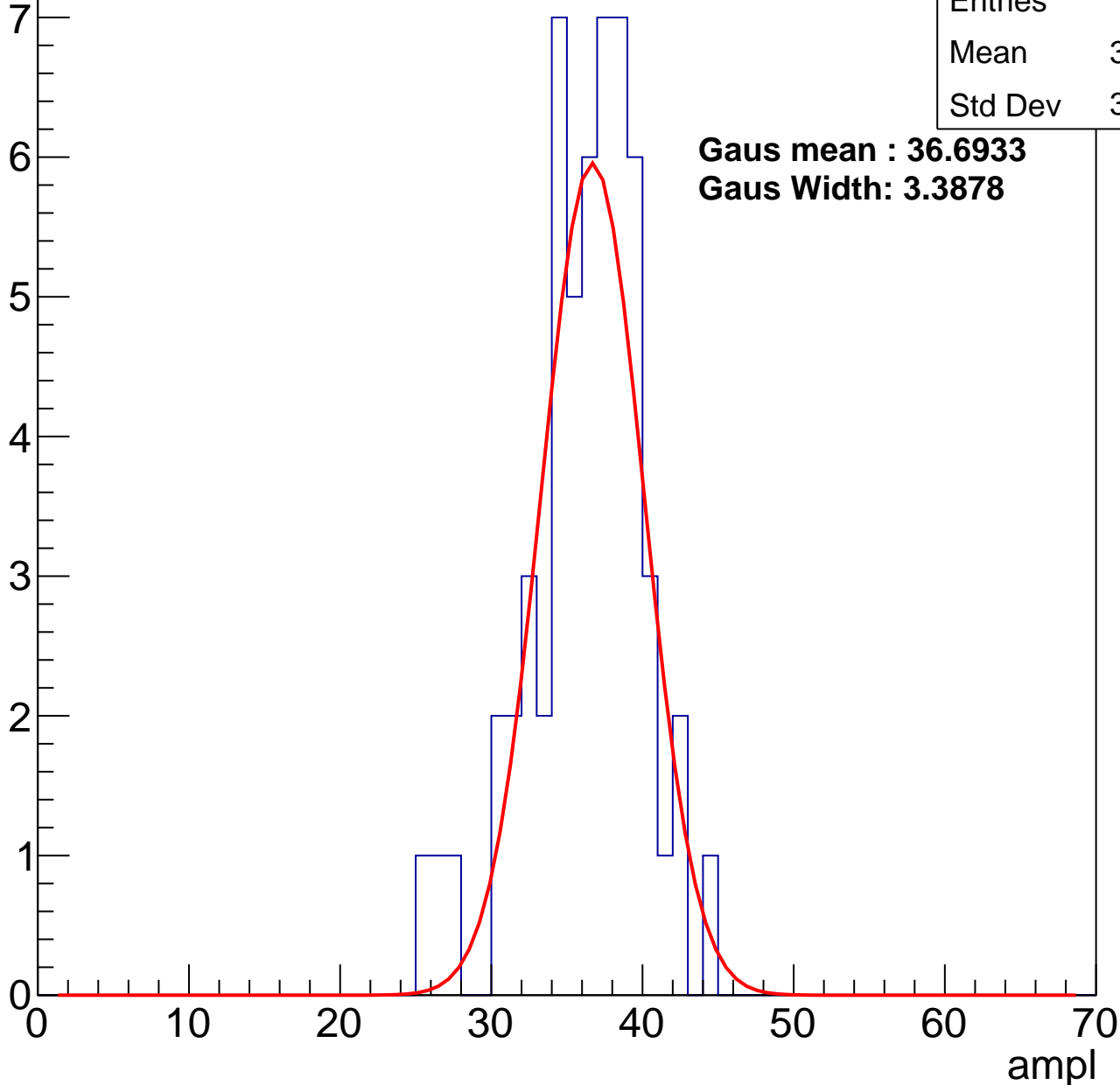
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.77
Std Dev	3.788

**Gaus mean : 36.6933**

**Gaus Width: 3.3878**



# B1L102S, U24-ch106, adc2

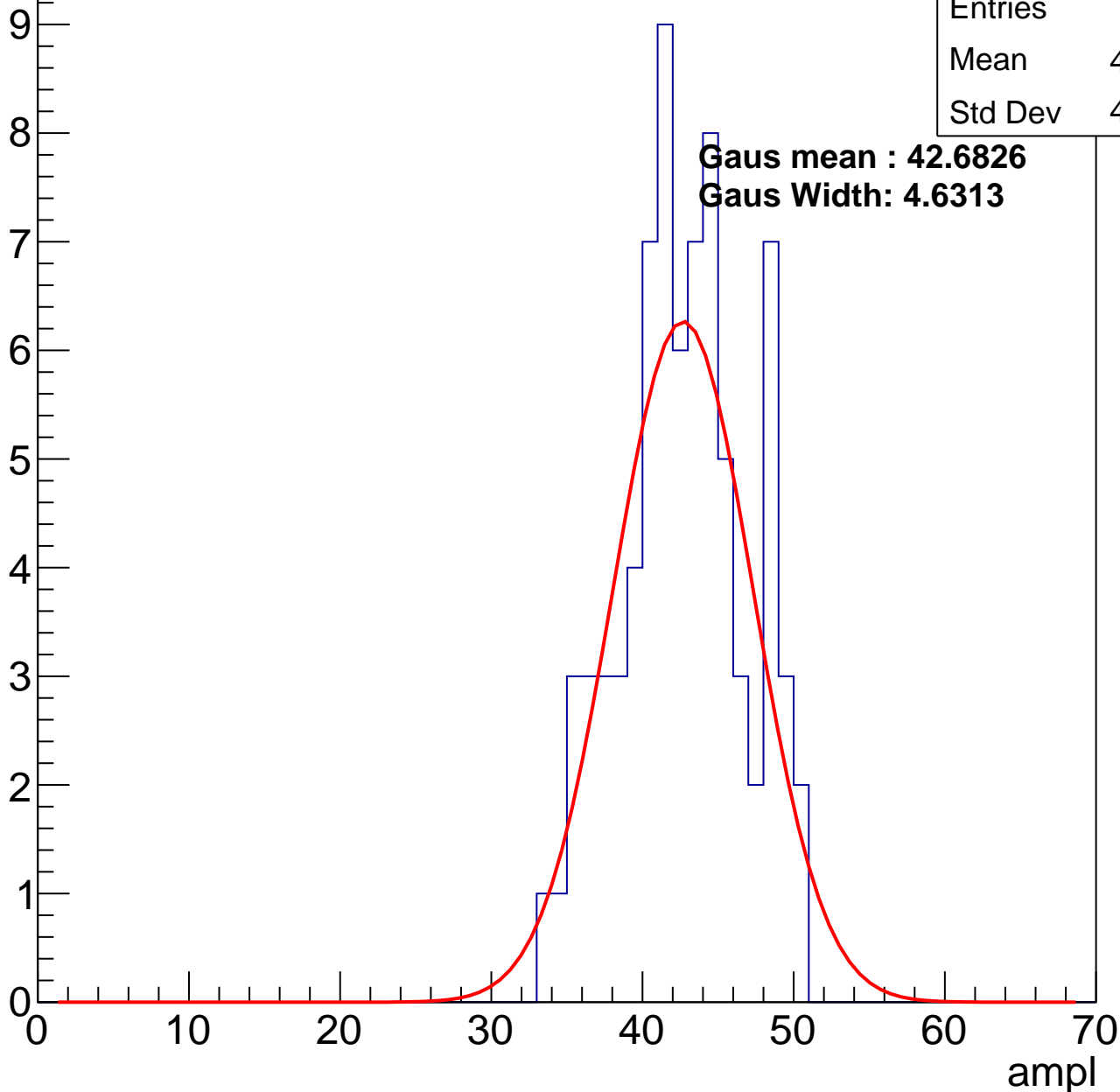
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	42.27
Std Dev	4.108

**Gaus mean : 42.6826**

**Gaus Width: 4.6313**

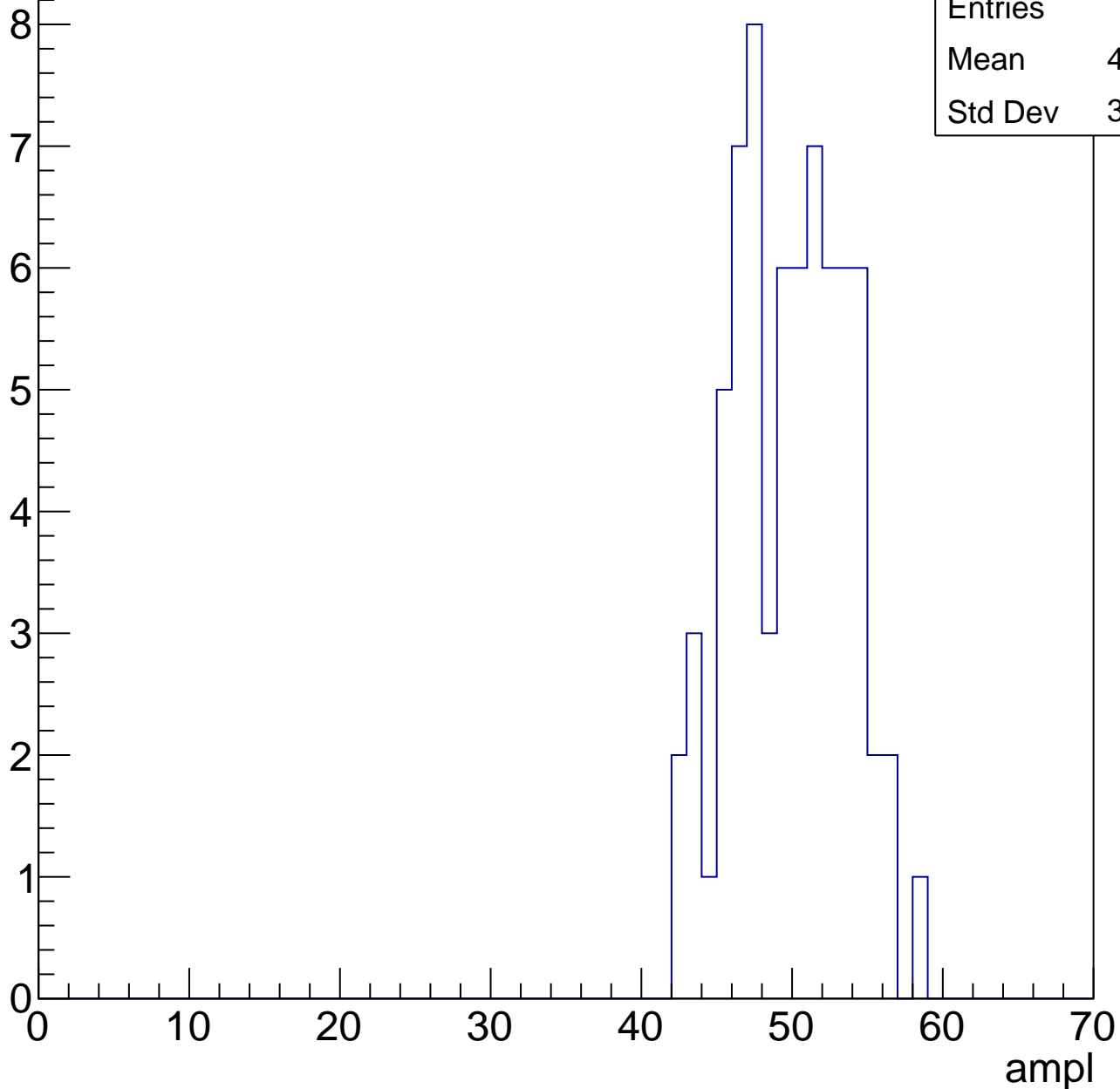


# B1L102S, U24-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.42
Std Dev	3.718

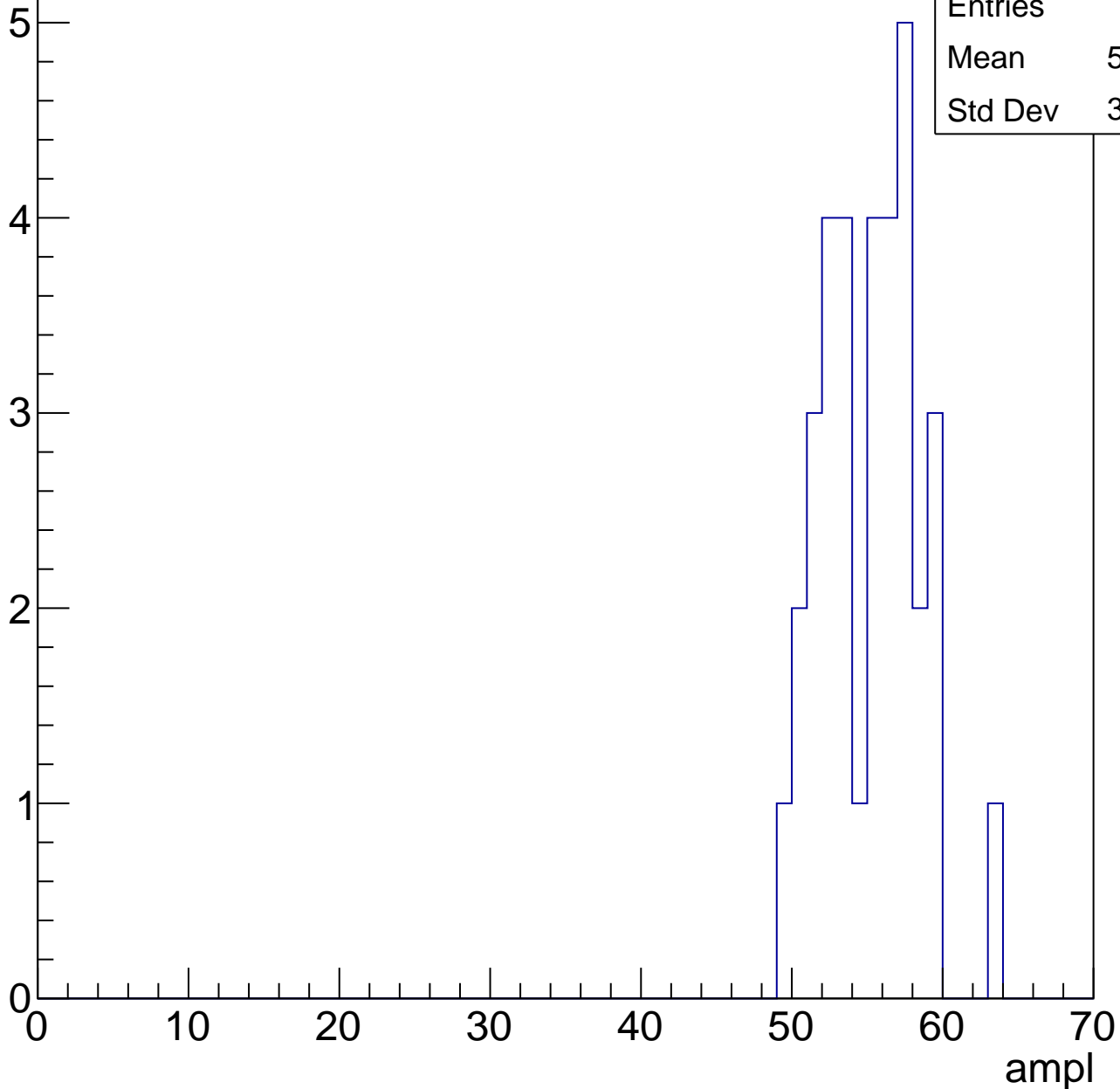


# B1L102S, U24-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	34
Mean	54.74
Std Dev	3.156

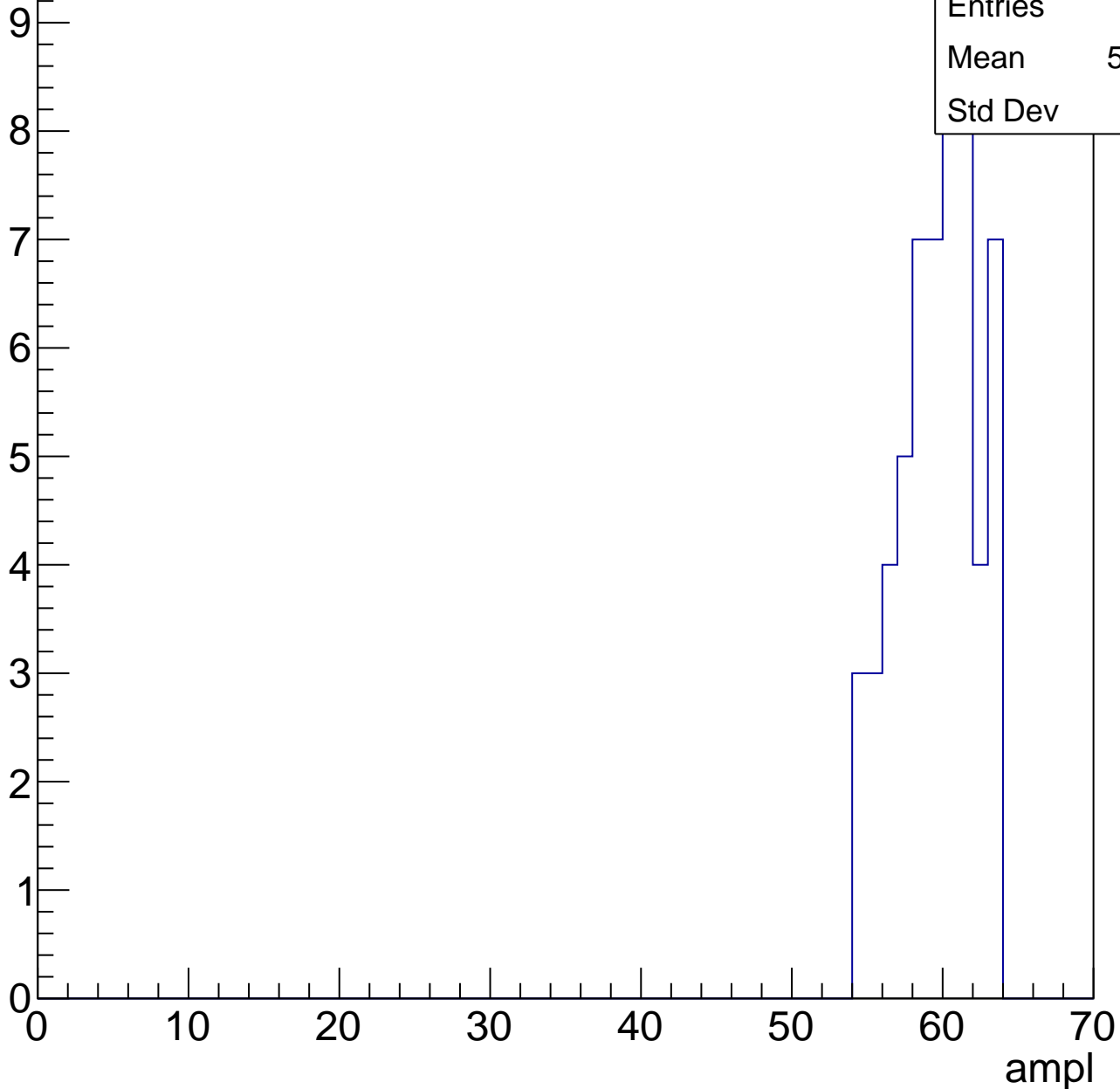


# B1L102S, U24-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	59.16
Std Dev	2.56

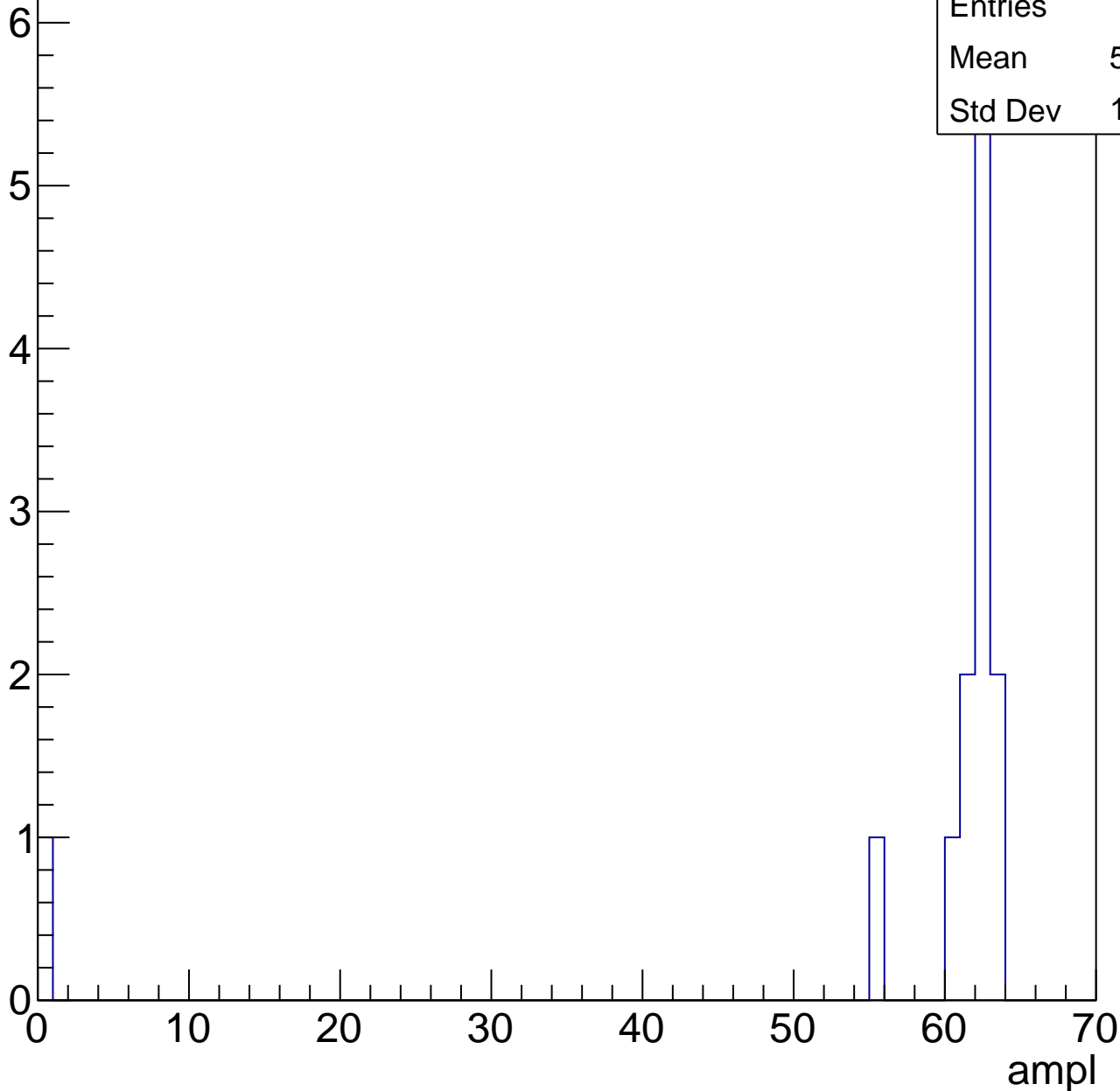


# B1L102S, U24-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.54
Std Dev	16.44





# B1L102S, U24-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch107, adc0

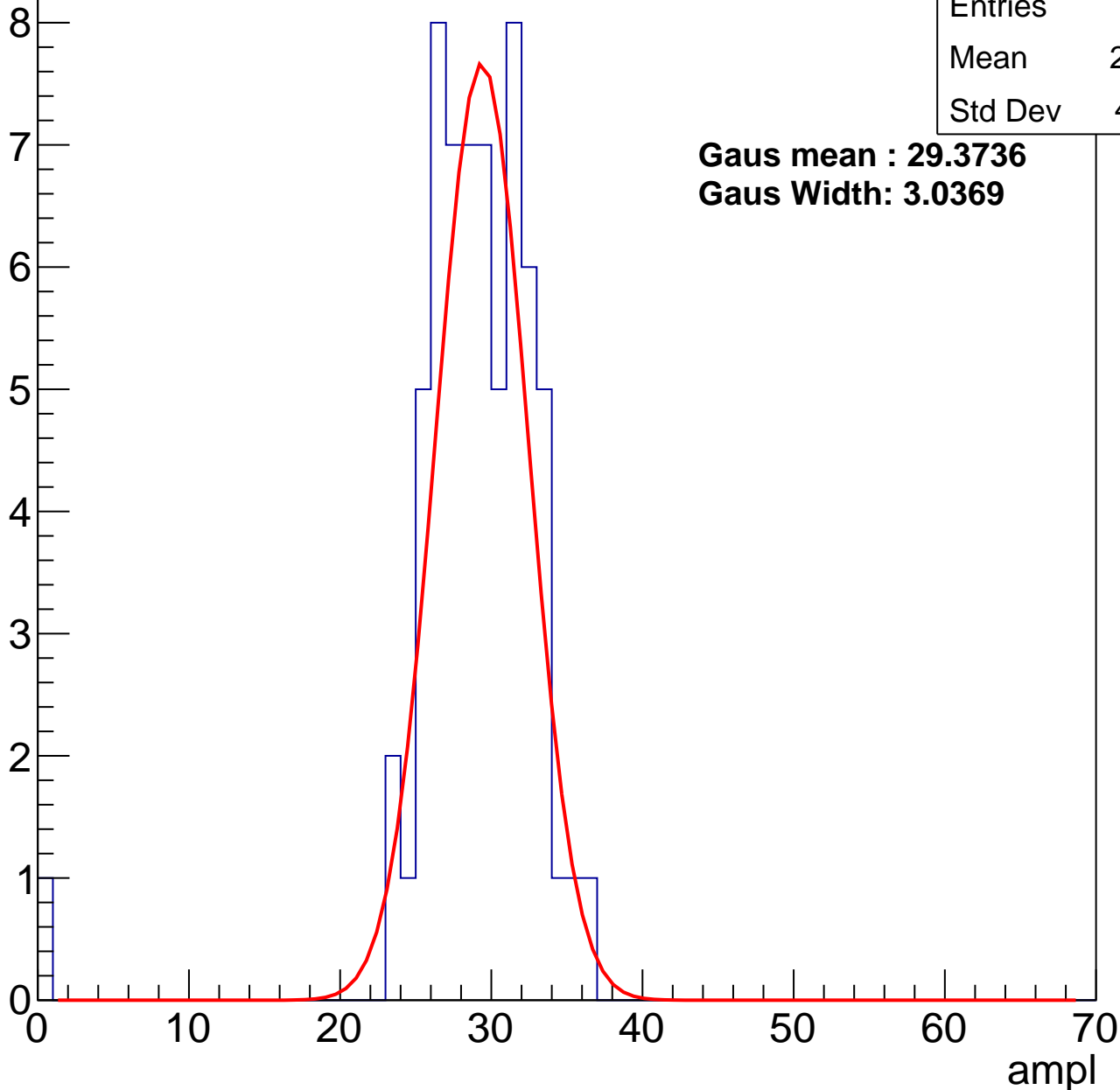
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.48
Std Dev	4.621

**Gaus mean : 29.3736**

**Gaus Width: 3.0369**



# B1L102S, U24-ch107, adc1

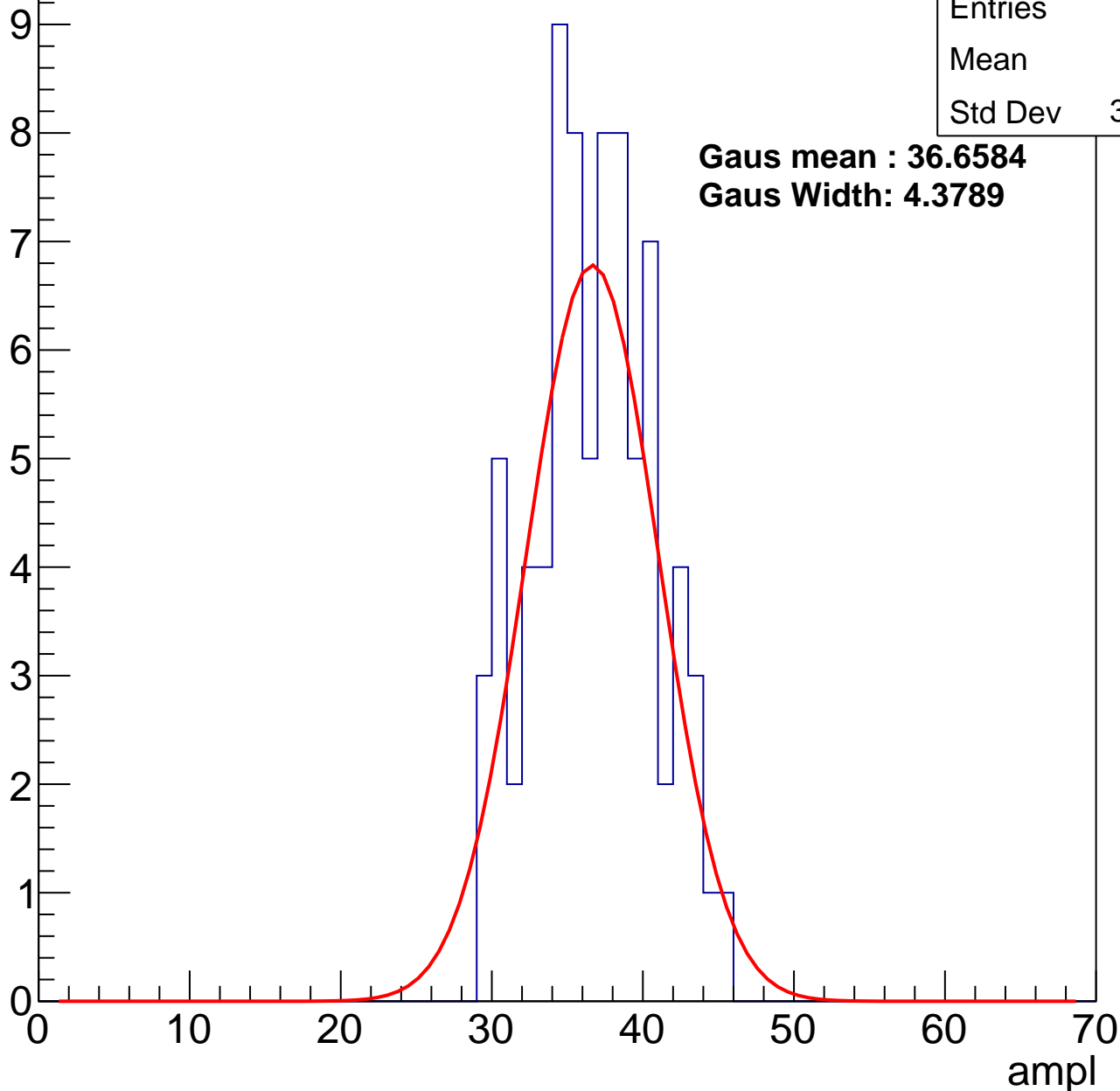
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.3
Std Dev	3.892

**Gaus mean : 36.6584**

**Gaus Width: 4.3789**



# B1L102S, U24-ch107, adc2

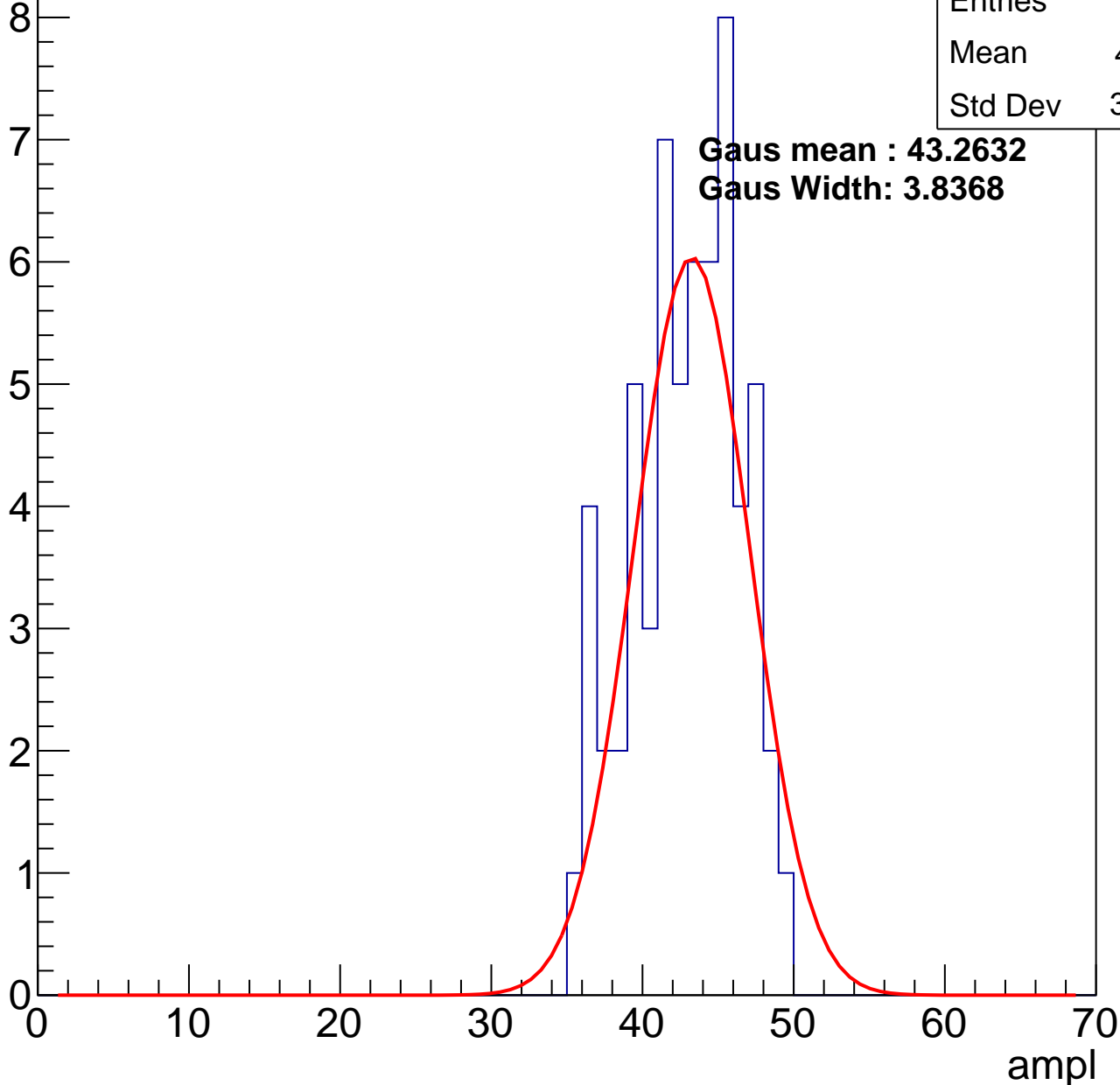
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.41
Std Dev	3.485

**Gaus mean : 43.2632**

**Gaus Width: 3.8368**

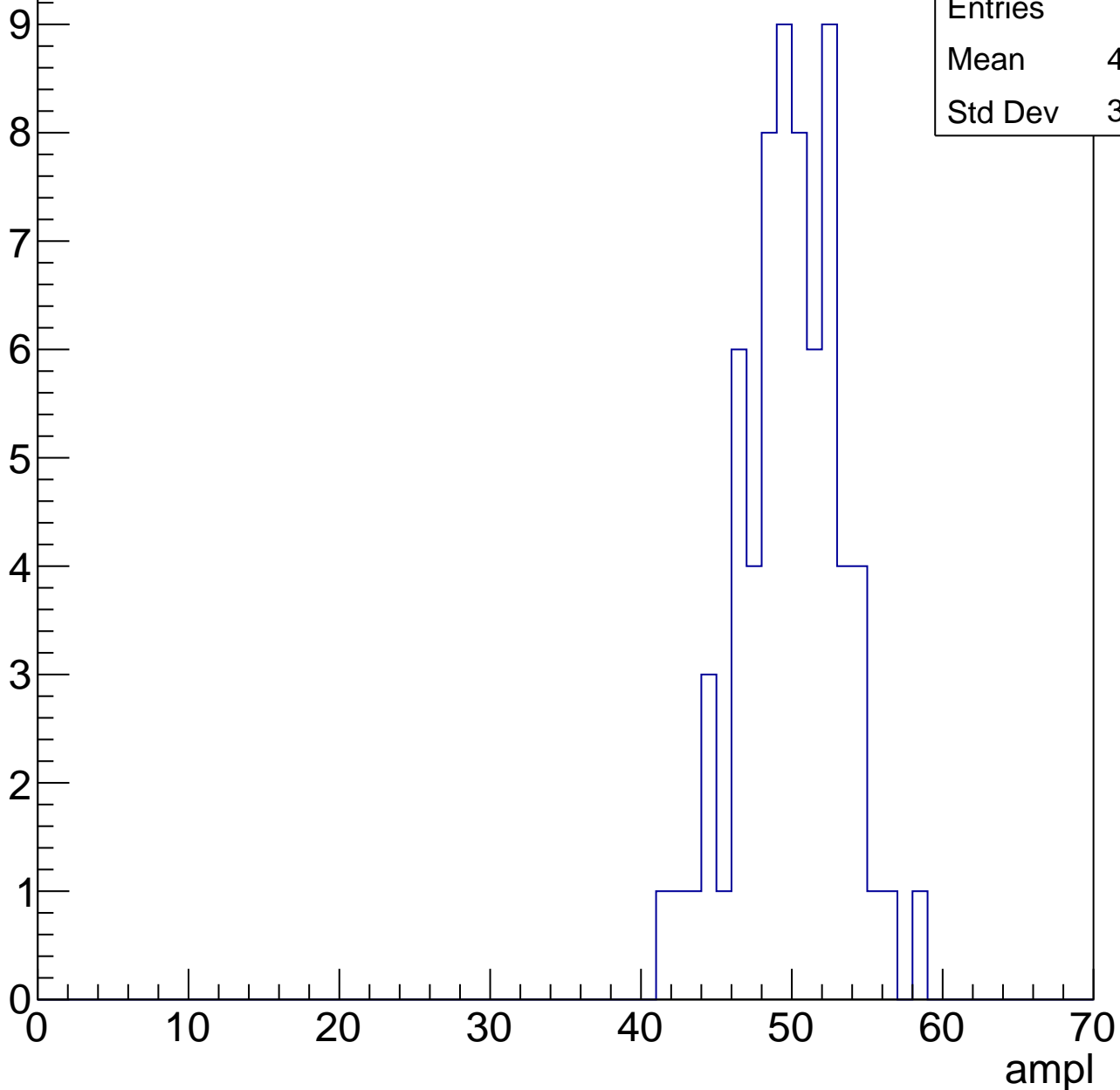


# B1L102S, U24-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.46
Std Dev	3.314

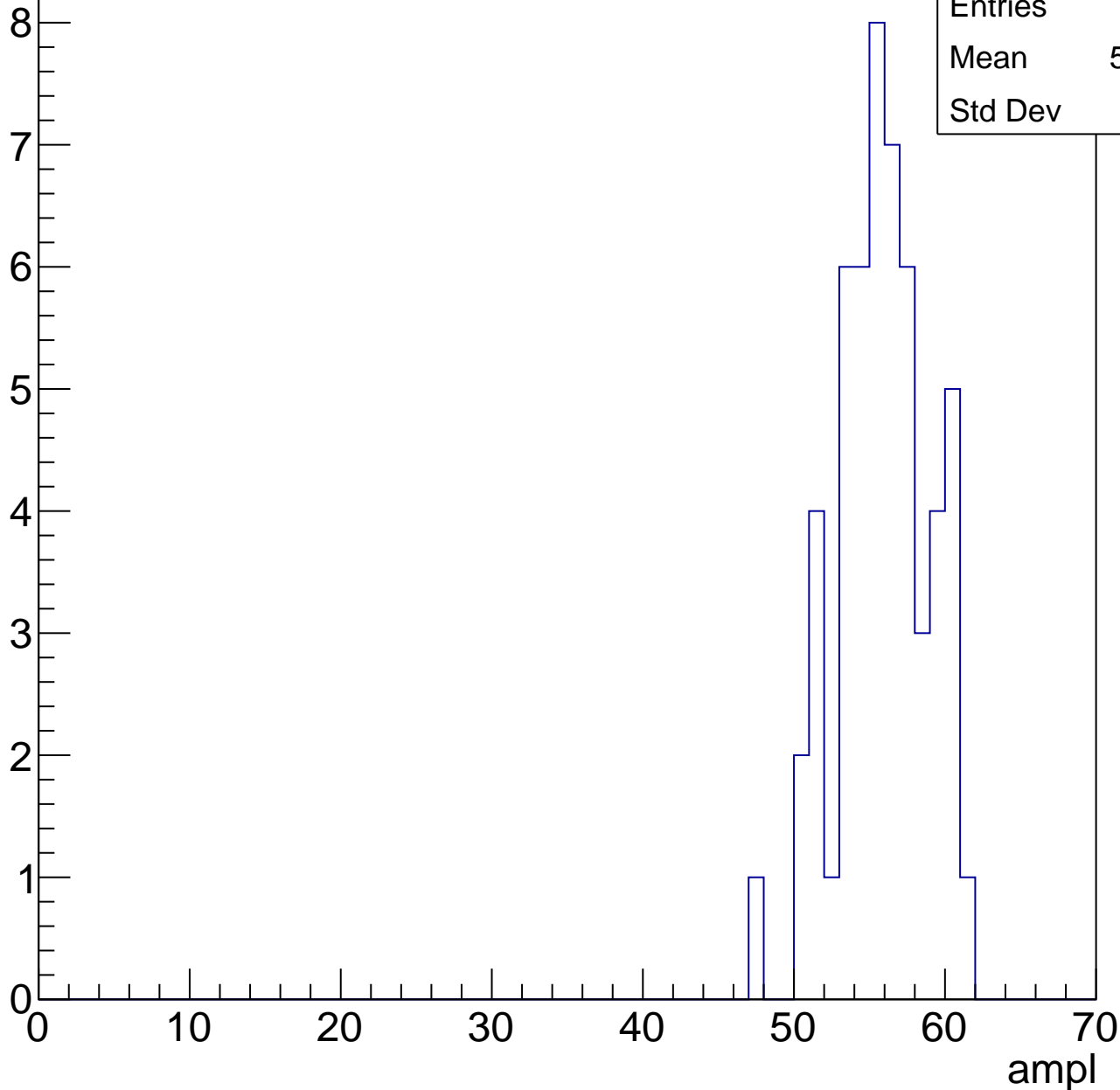


# B1L102S, U24-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.37
Std Dev	3.02

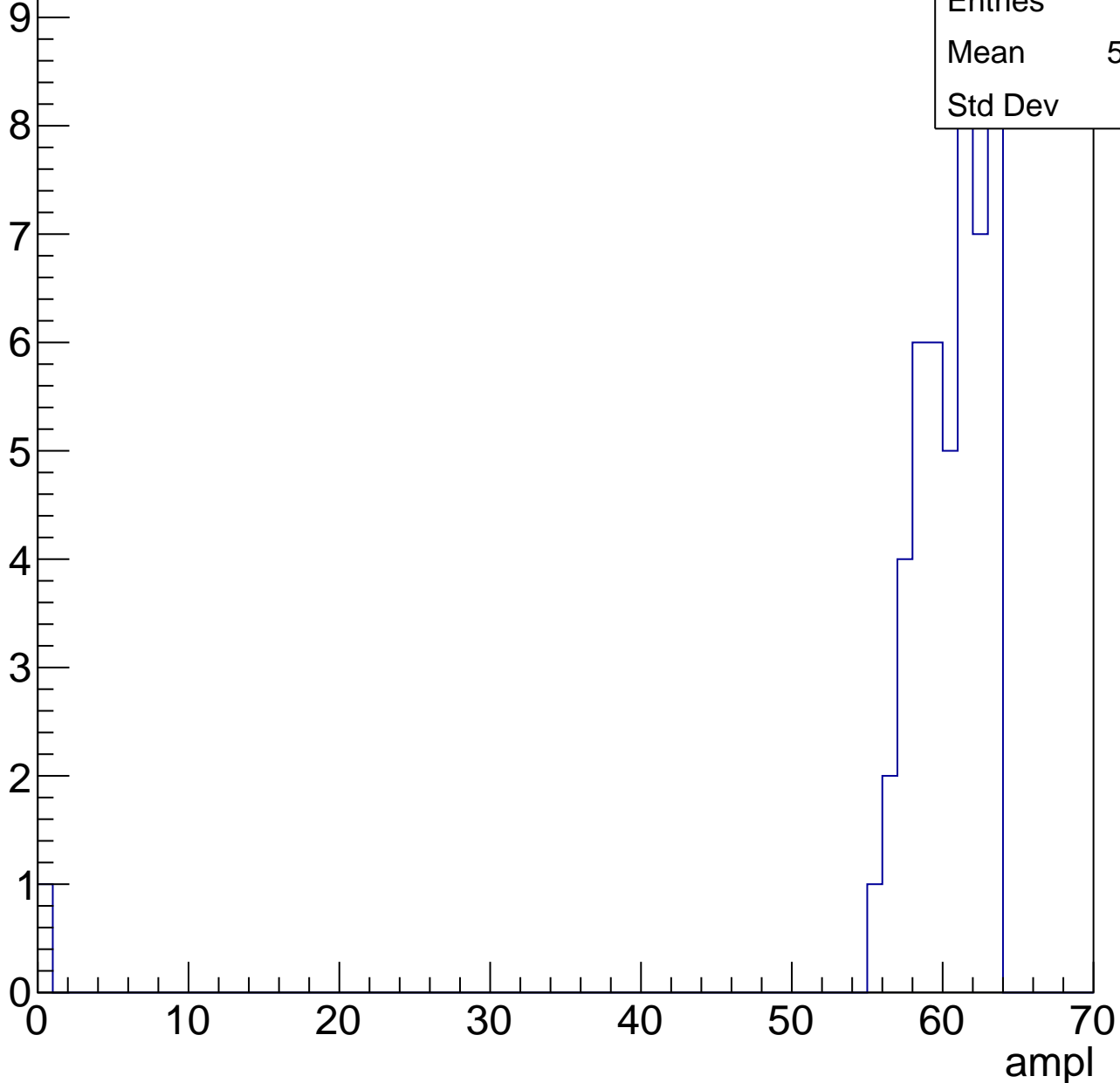


# B1L102S, U24-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.94
Std Dev	8.7



# B1L102S, U24-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch108, adc0

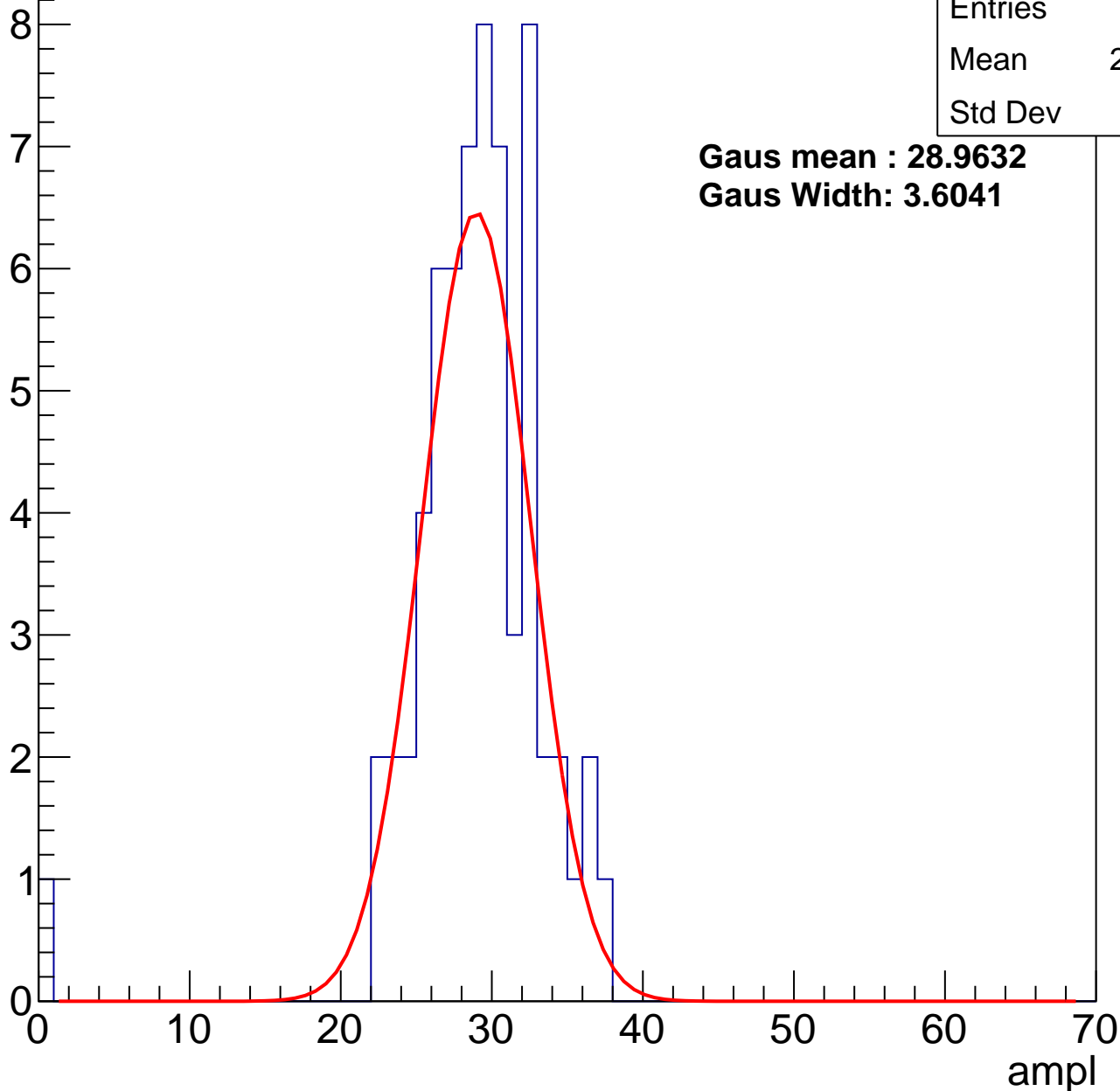
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.45
Std Dev	4.94

**Gaus mean : 28.9632**

**Gaus Width: 3.6041**



# B1L102S, U24-ch108, adc1

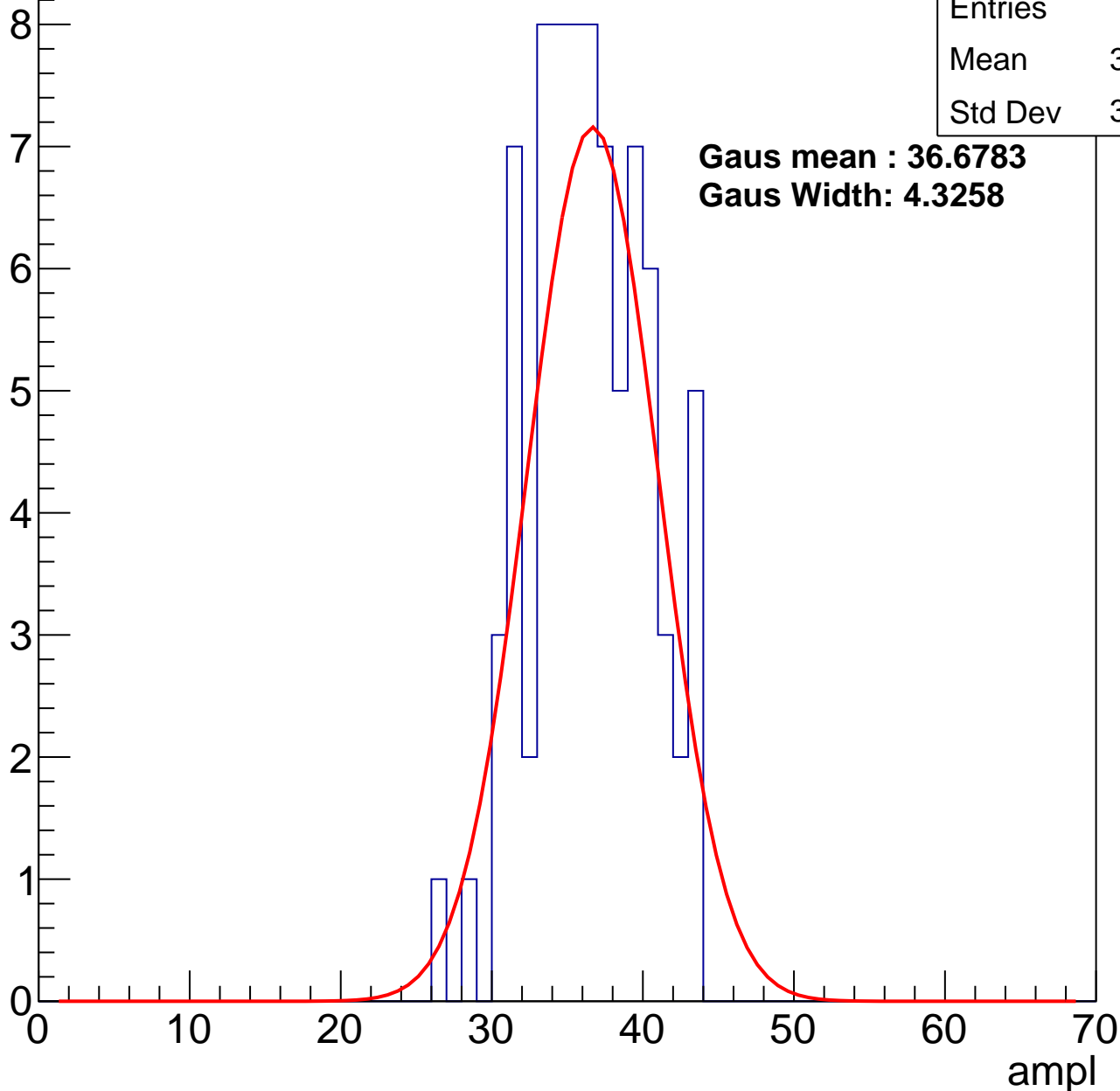
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.96
Std Dev	3.799

**Gaus mean : 36.6783**

**Gaus Width: 4.3258**



# B1L102S, U24-ch108, adc2

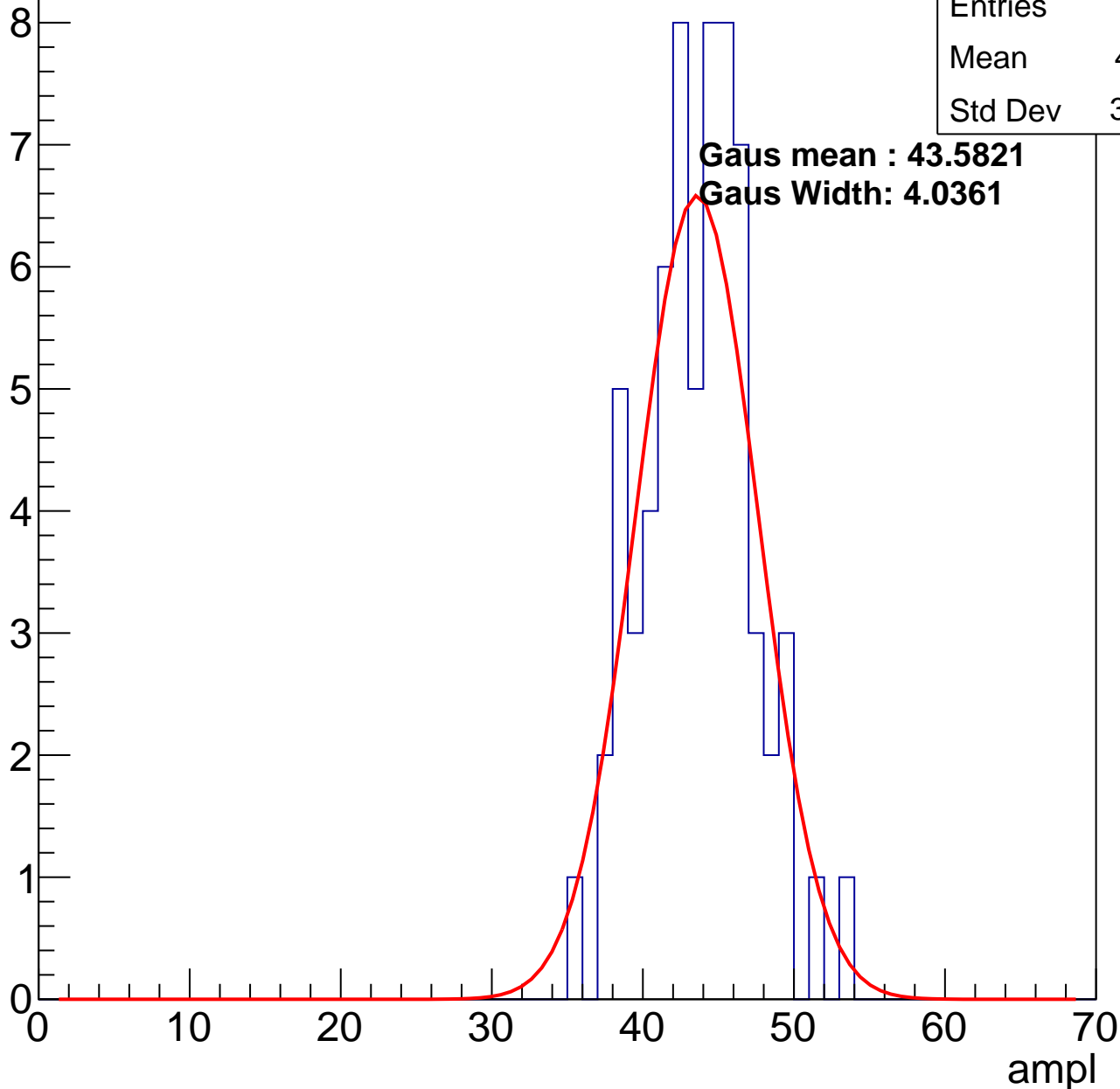
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.21
Std Dev	3.556

**Gaus mean : 43.5821**

**Gaus Width: 4.0361**

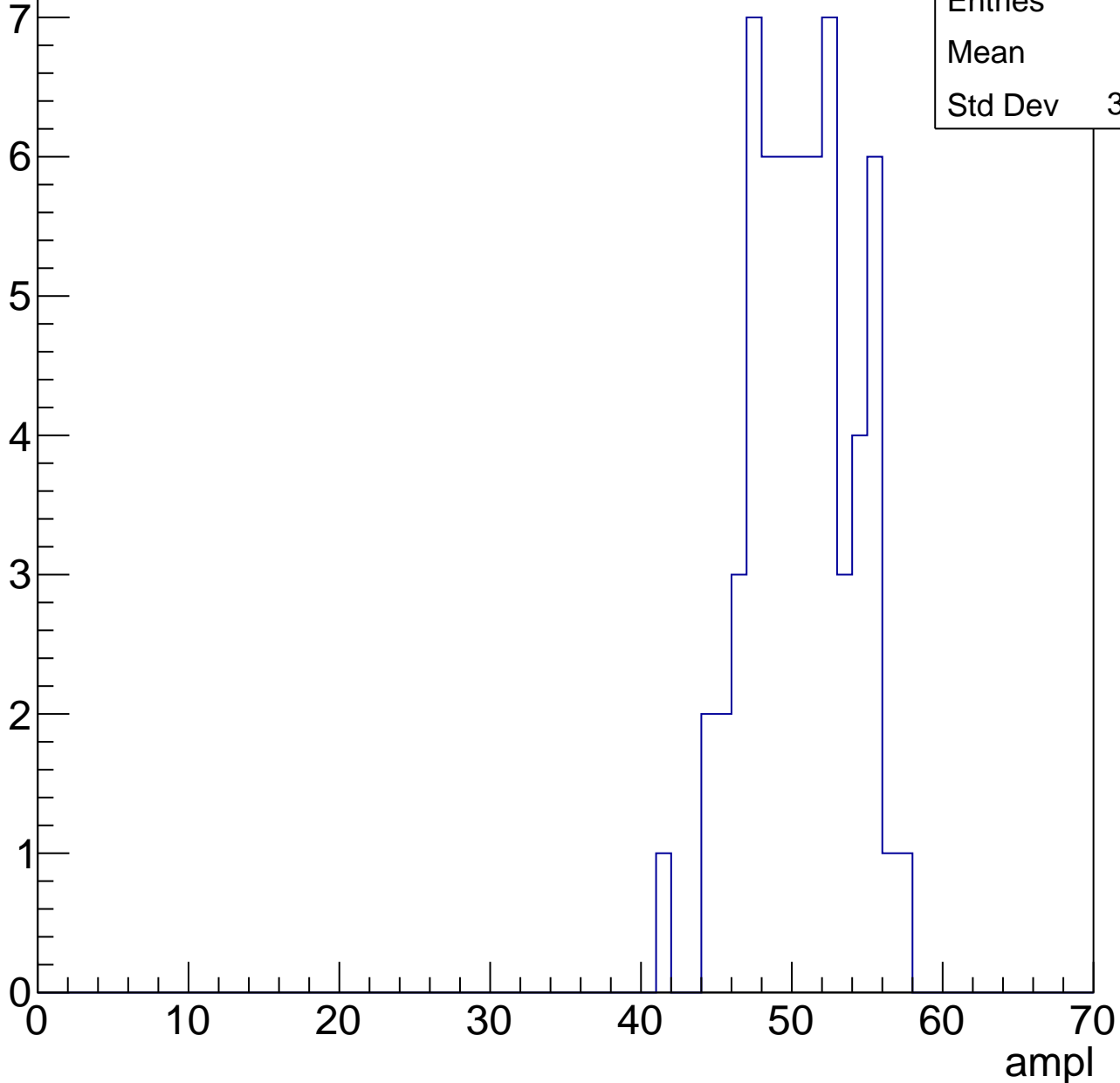


# B1L102S, U24-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.1
Std Dev	3.396

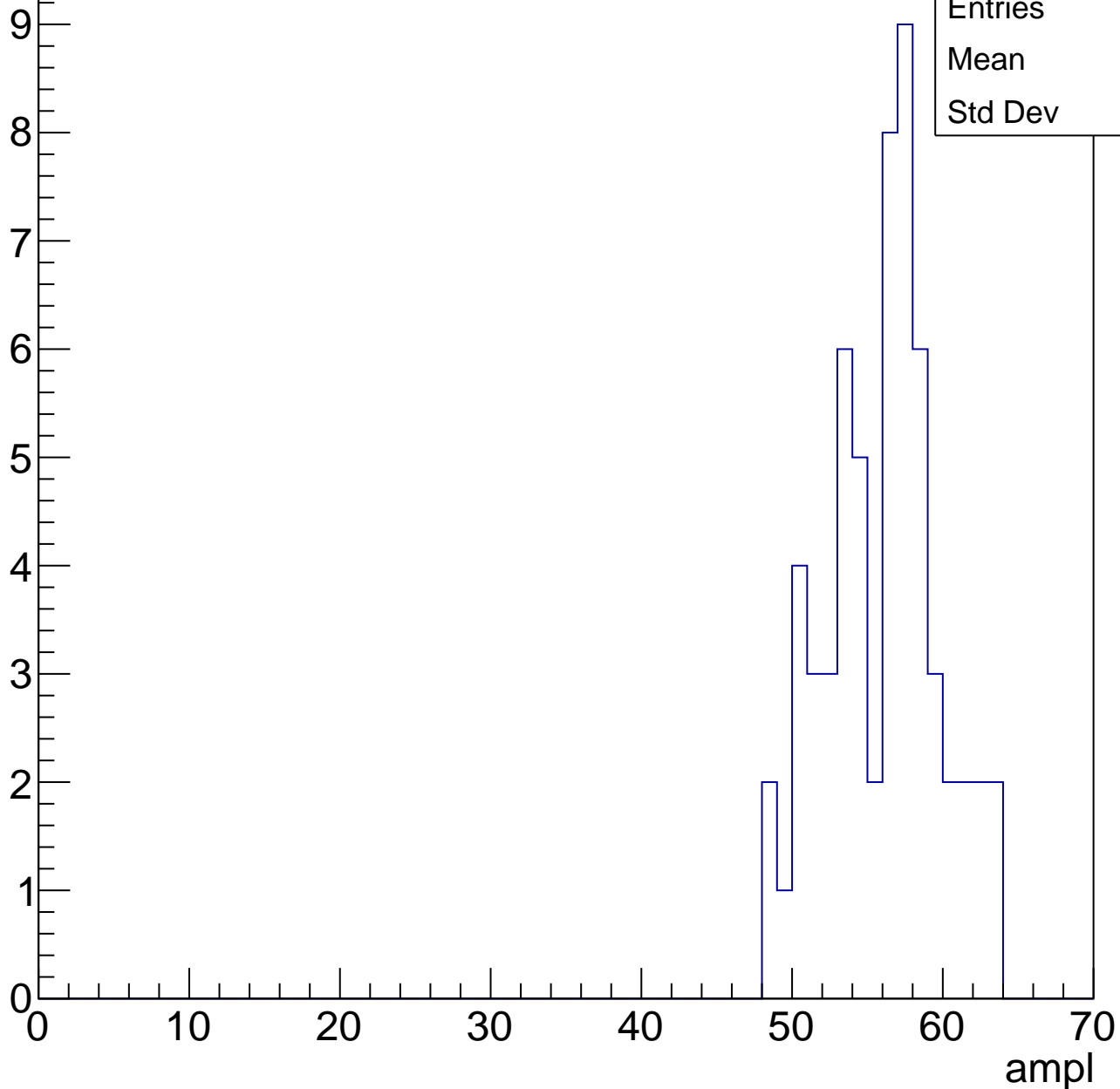


# B1L102S, U24-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.5
Std Dev	3.69

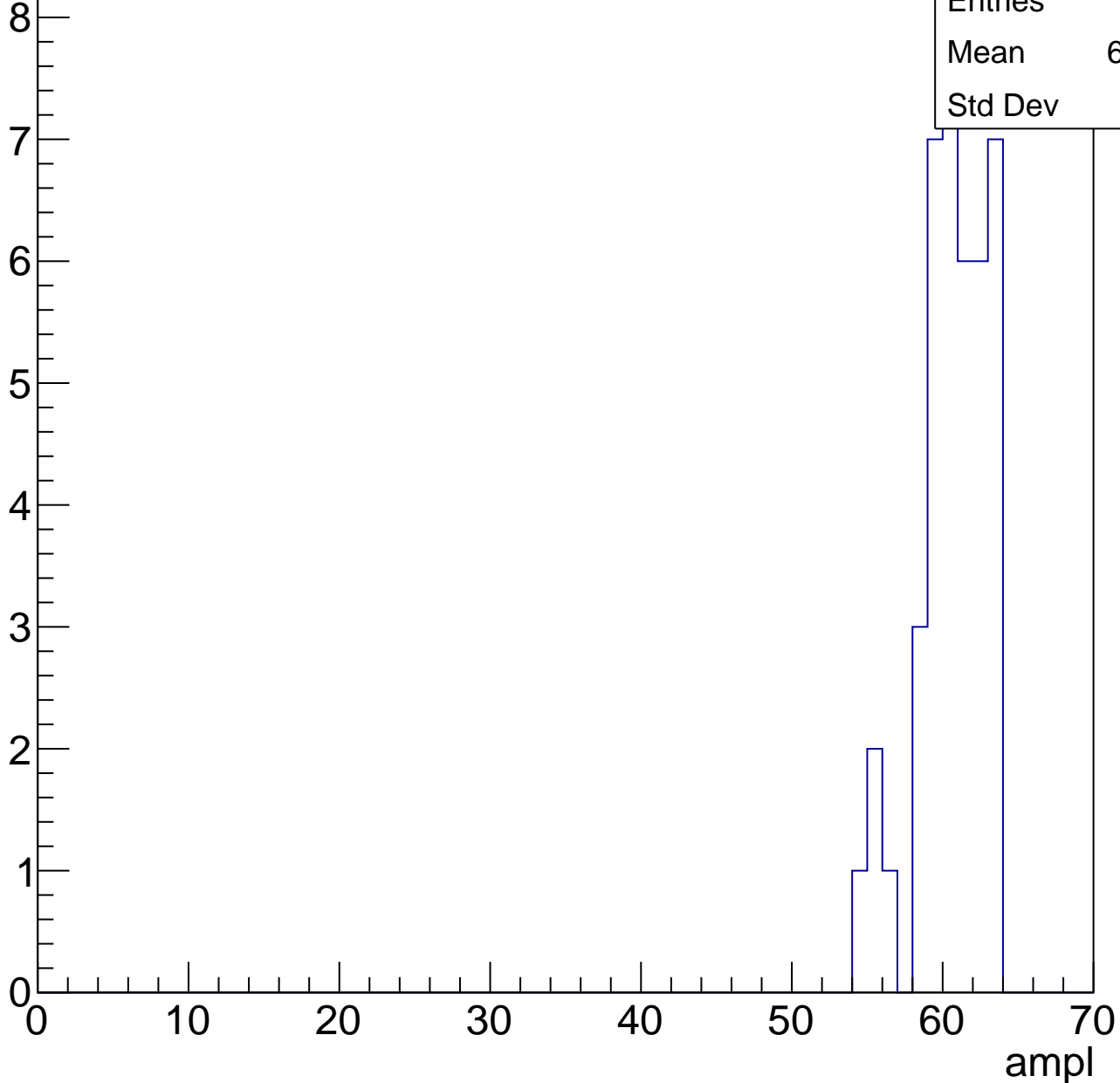


# B1L102S, U24-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	41
Mean	60.15
Std Dev	2.28



# B1L102S, U24-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

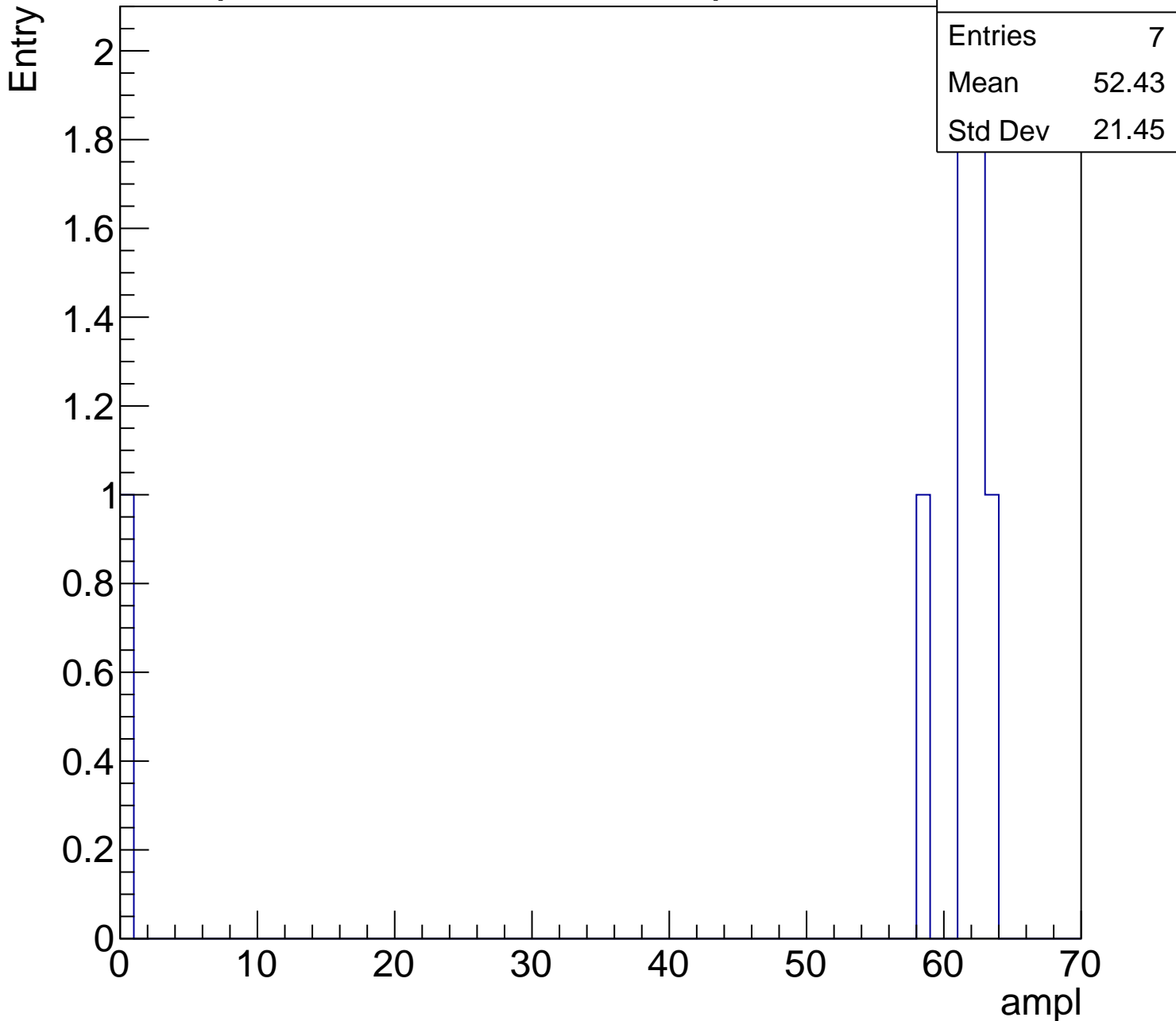
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.43
Std Dev	21.45

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch109, adc0

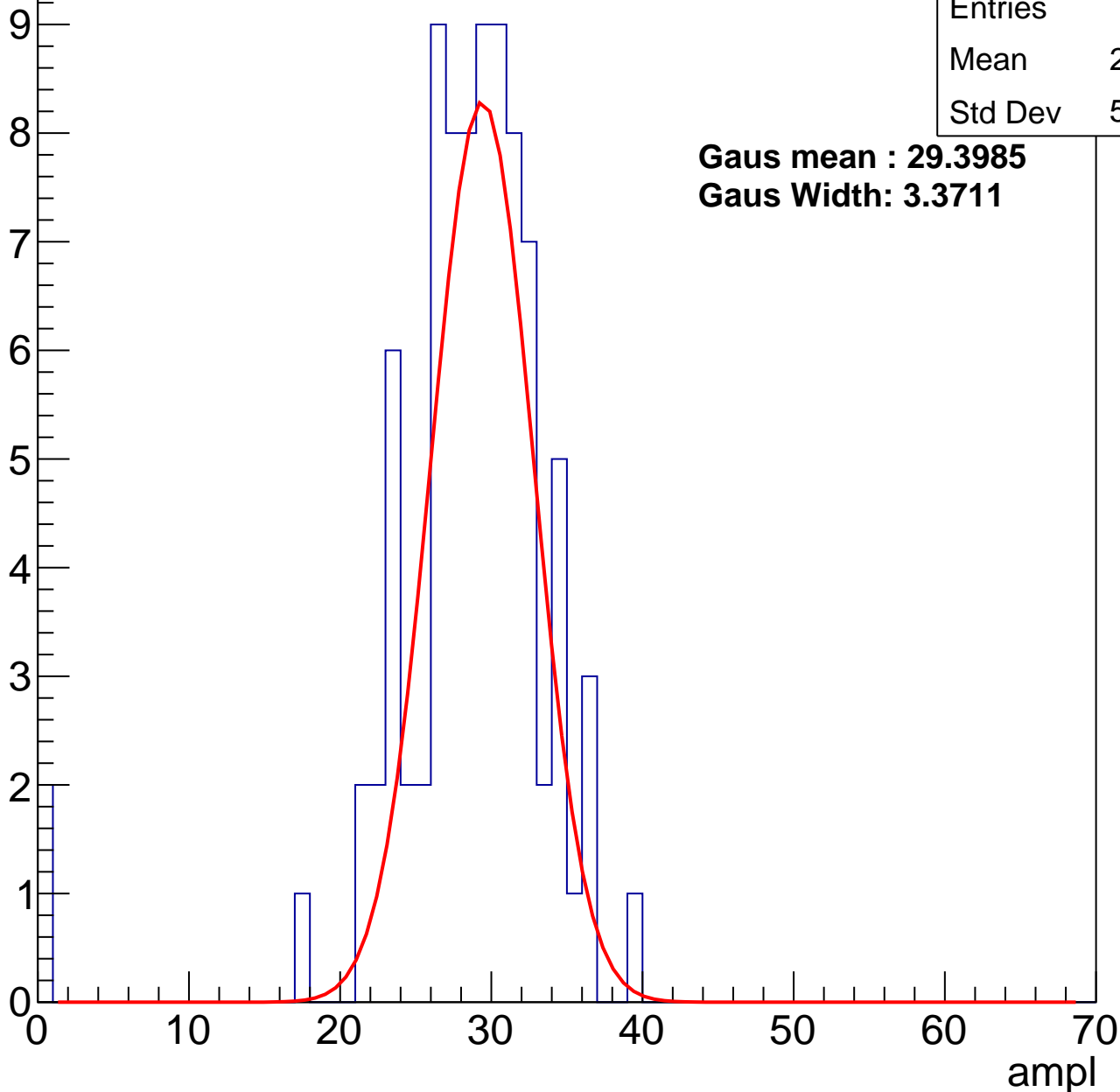
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	87
Mean	27.98
Std Dev	5.797

**Gaus mean : 29.3985**

**Gaus Width: 3.3711**



# B1L102S, U24-ch109, adc1

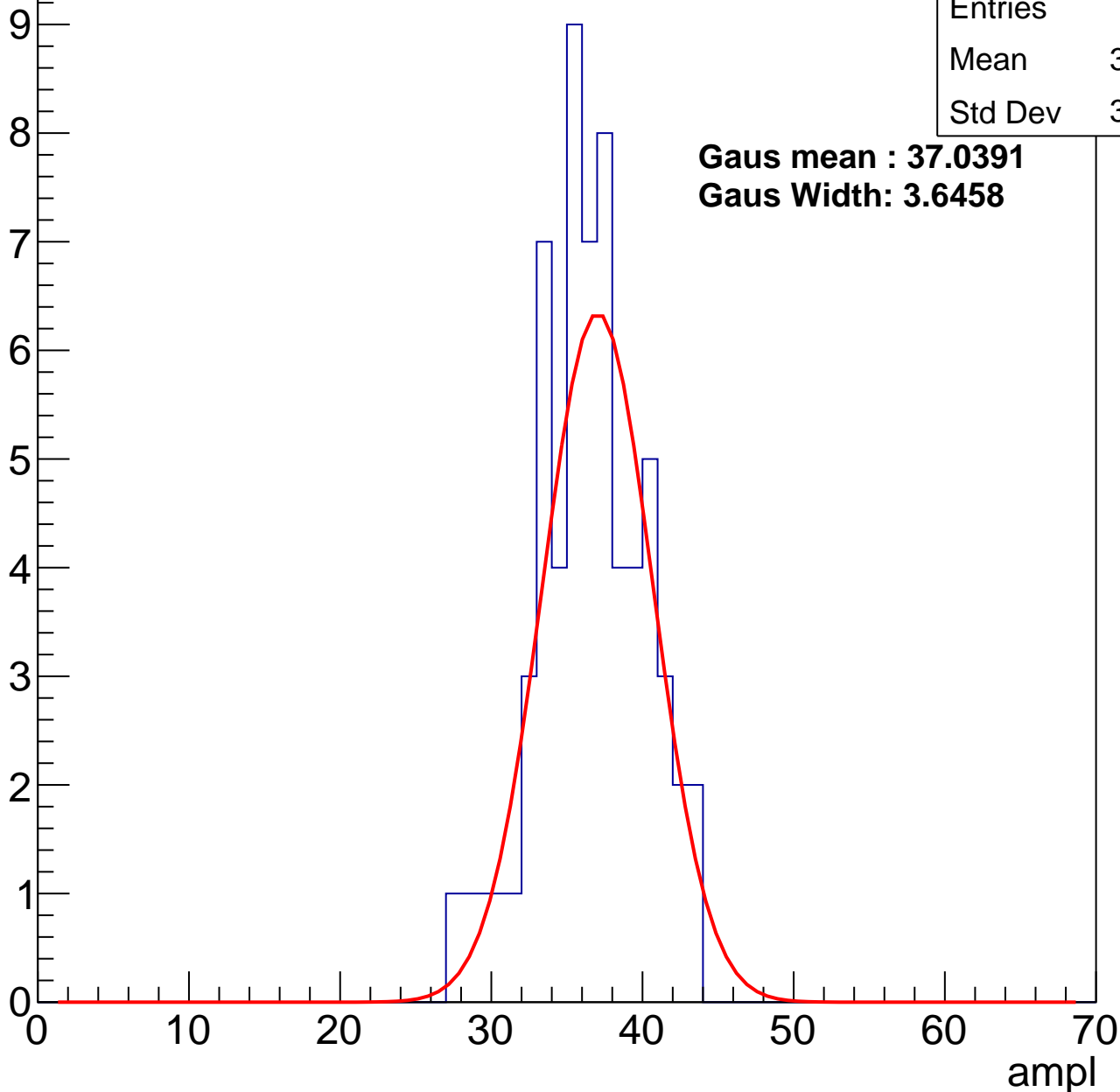
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.06
Std Dev	3.509

**Gaus mean : 37.0391**

**Gaus Width: 3.6458**



# B1L102S, U24-ch109, adc2

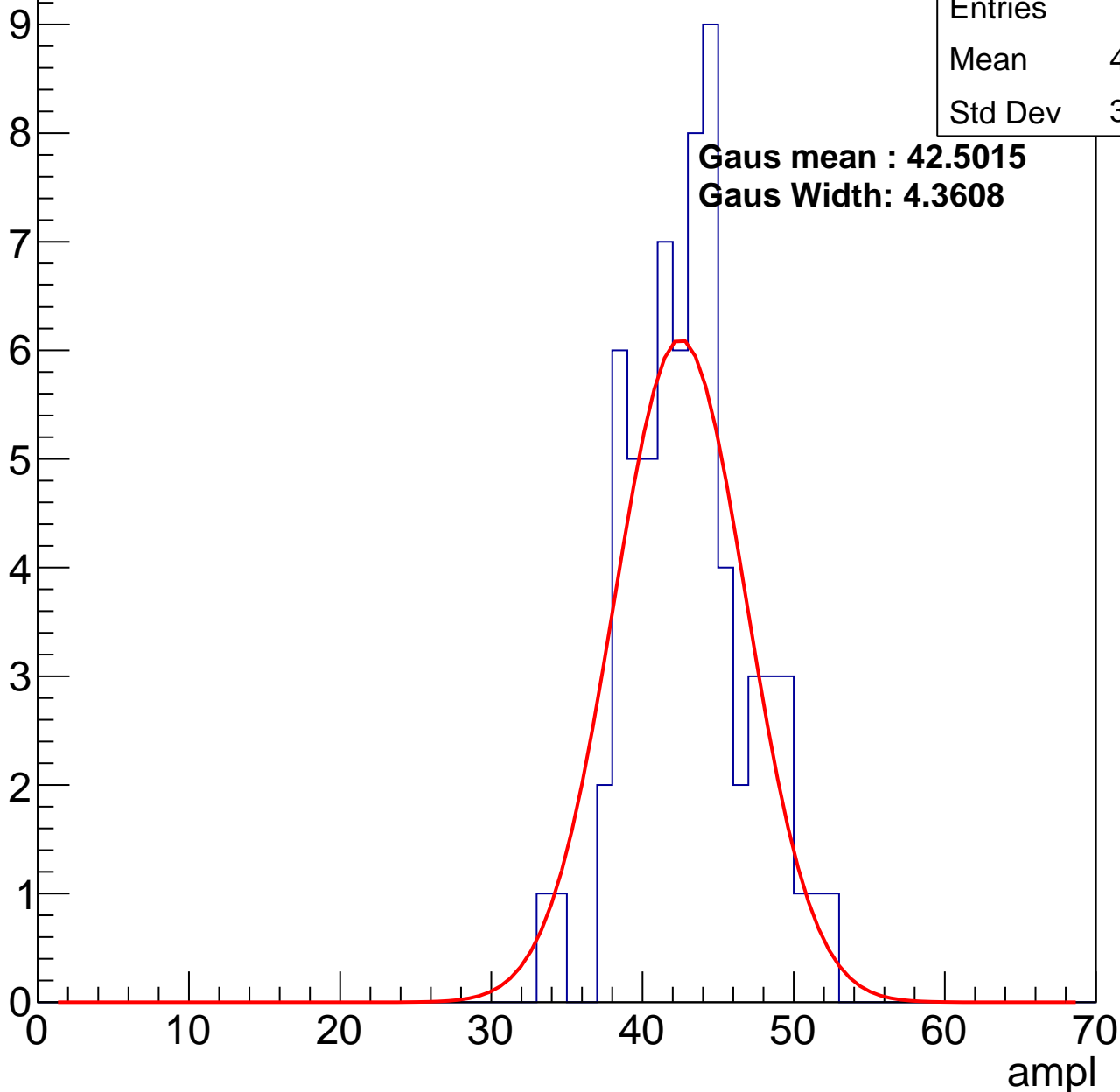
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.65
Std Dev	3.876

**Gaus mean : 42.5015**

**Gaus Width: 4.3608**

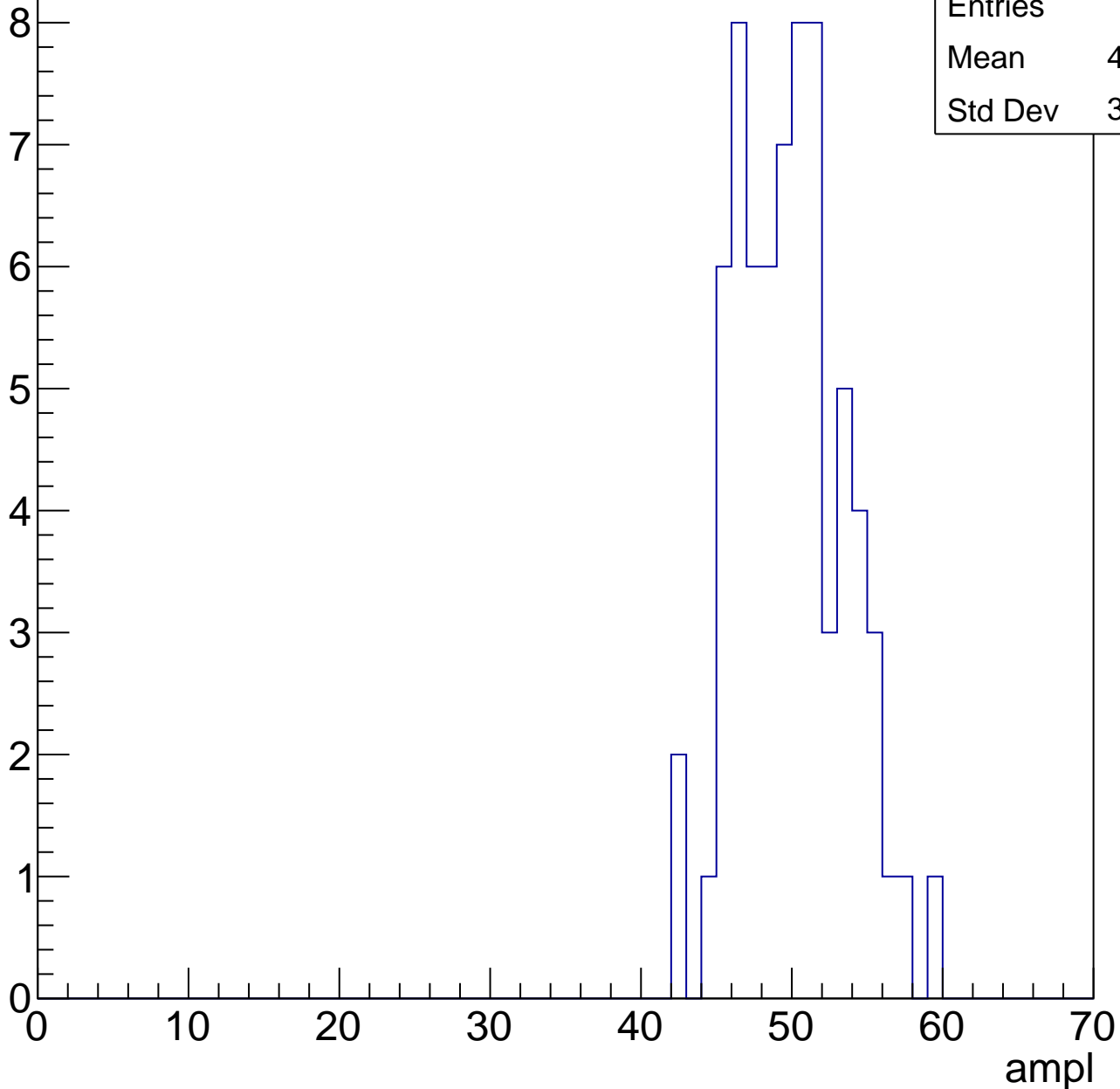


# B1L102S, U24-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.44
Std Dev	3.536

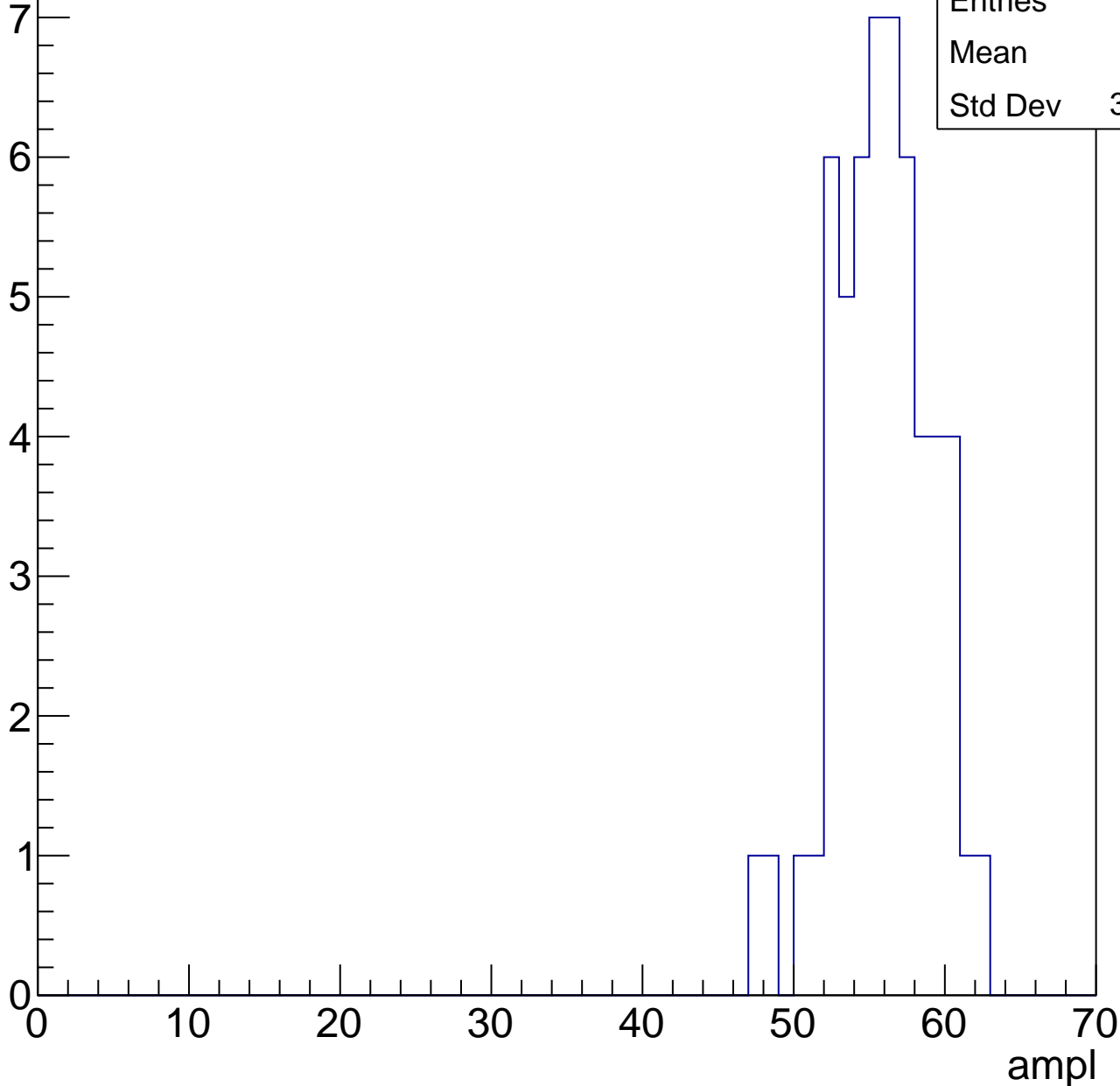


# B1L102S, U24-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.4
Std Dev	3.137

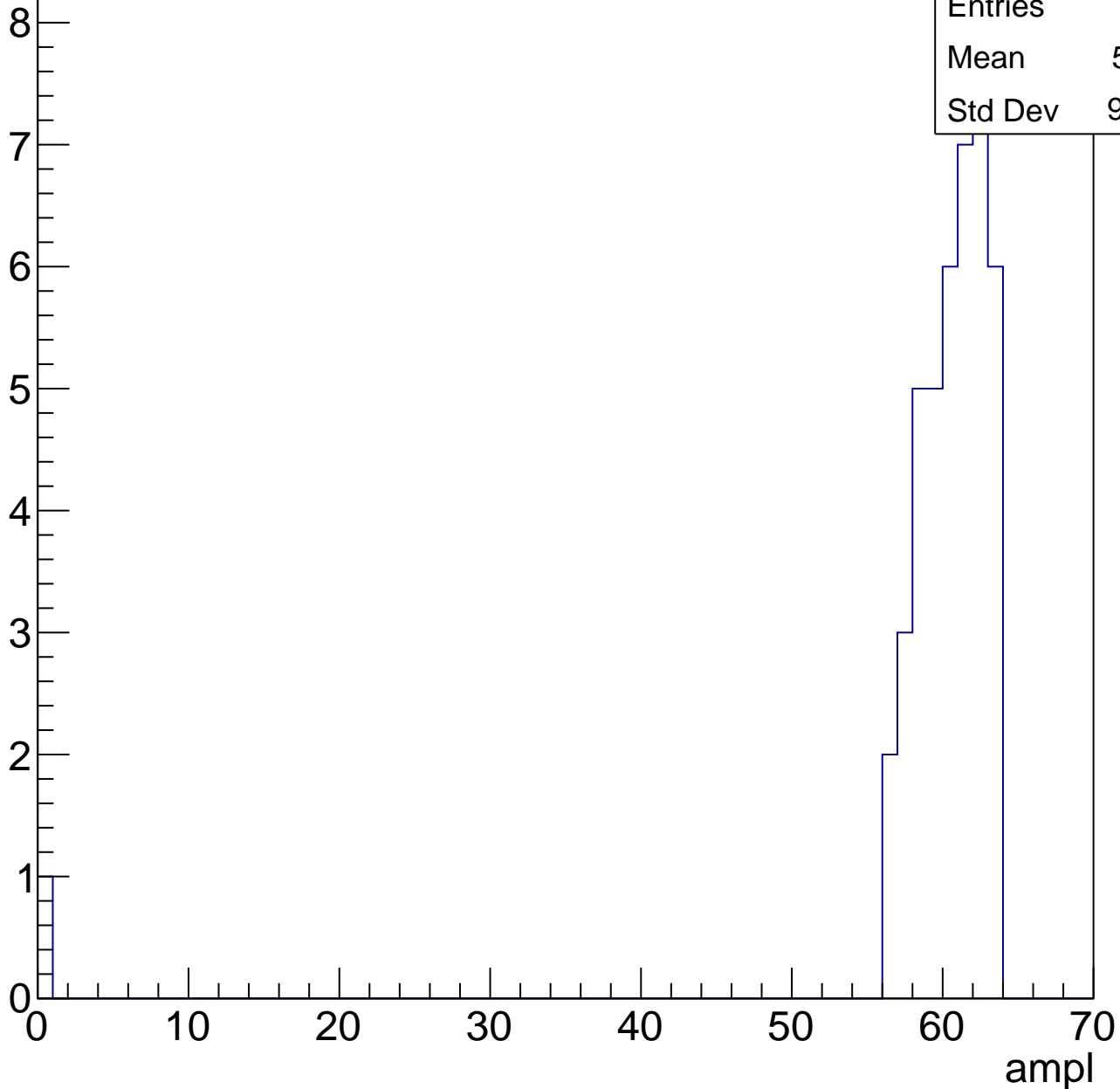


# B1L102S, U24-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

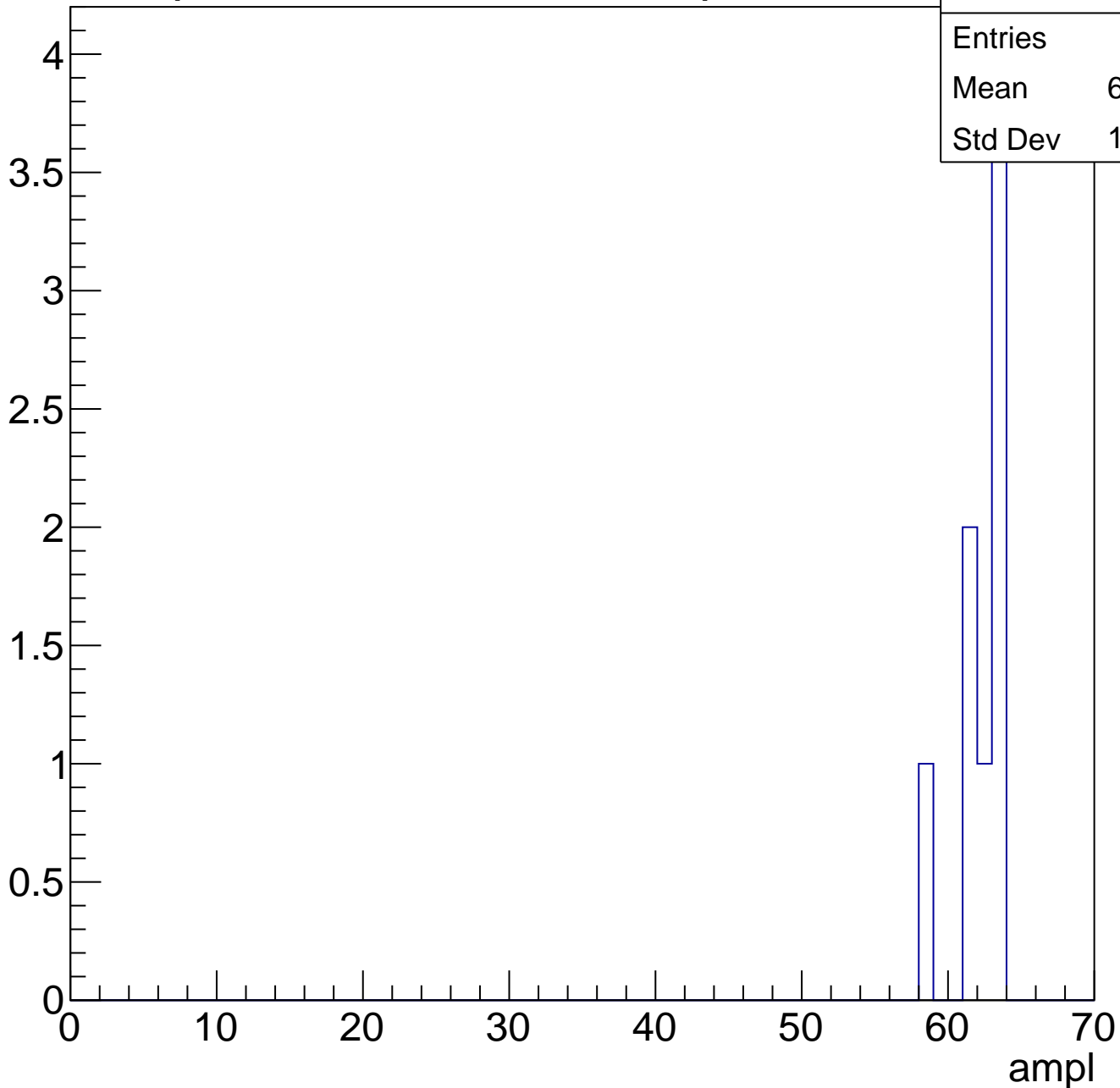
Entries	43
Mean	58.81
Std Dev	9.297



# B1L102S, U24-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	8
Mean	61.75
Std Dev	1.639



# B1L102S, U24-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch110, adc0

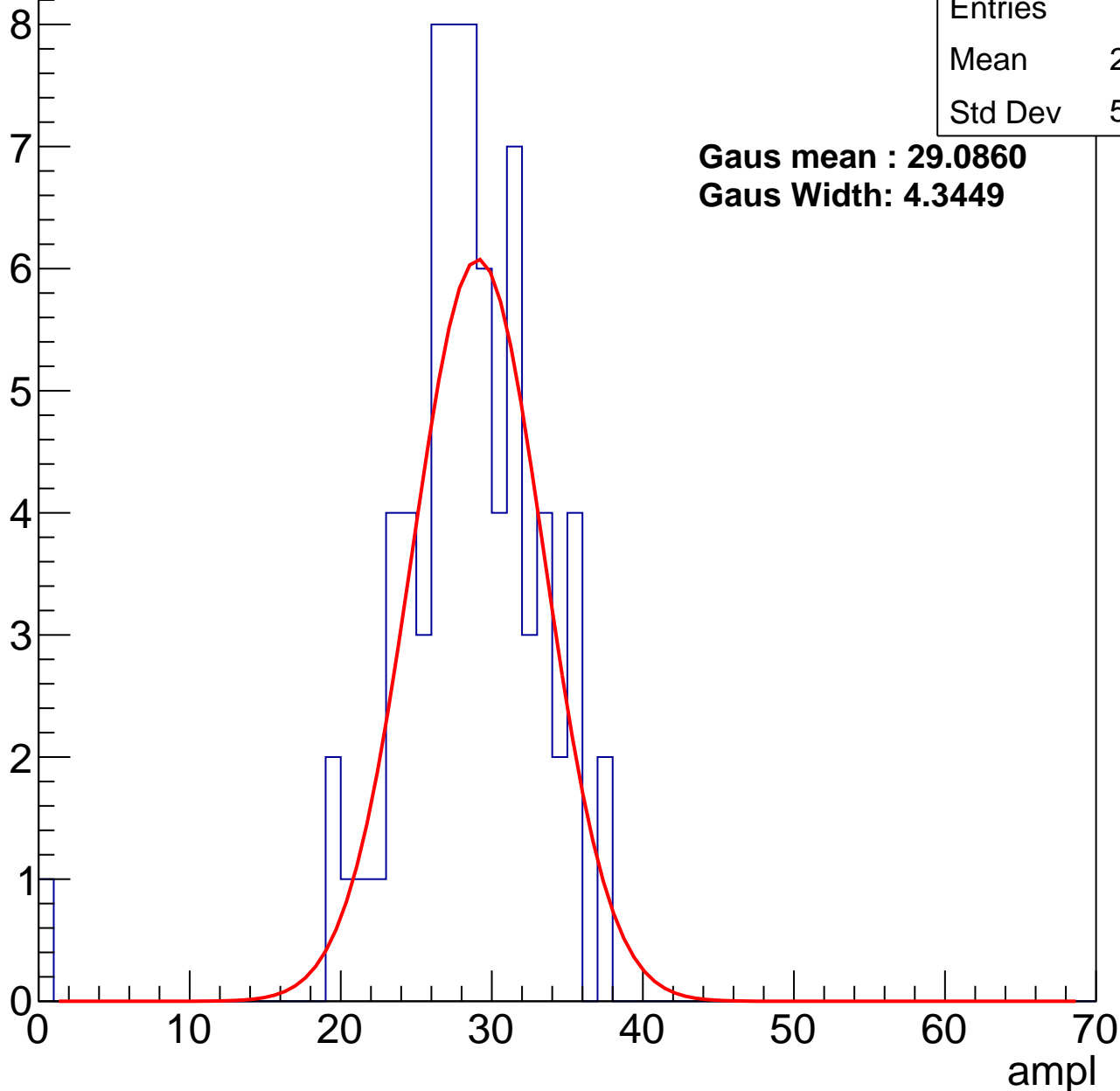
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.85
Std Dev	5.218

**Gaus mean : 29.0860**

**Gaus Width: 4.3449**



# B1L102S, U24-ch110, adc1

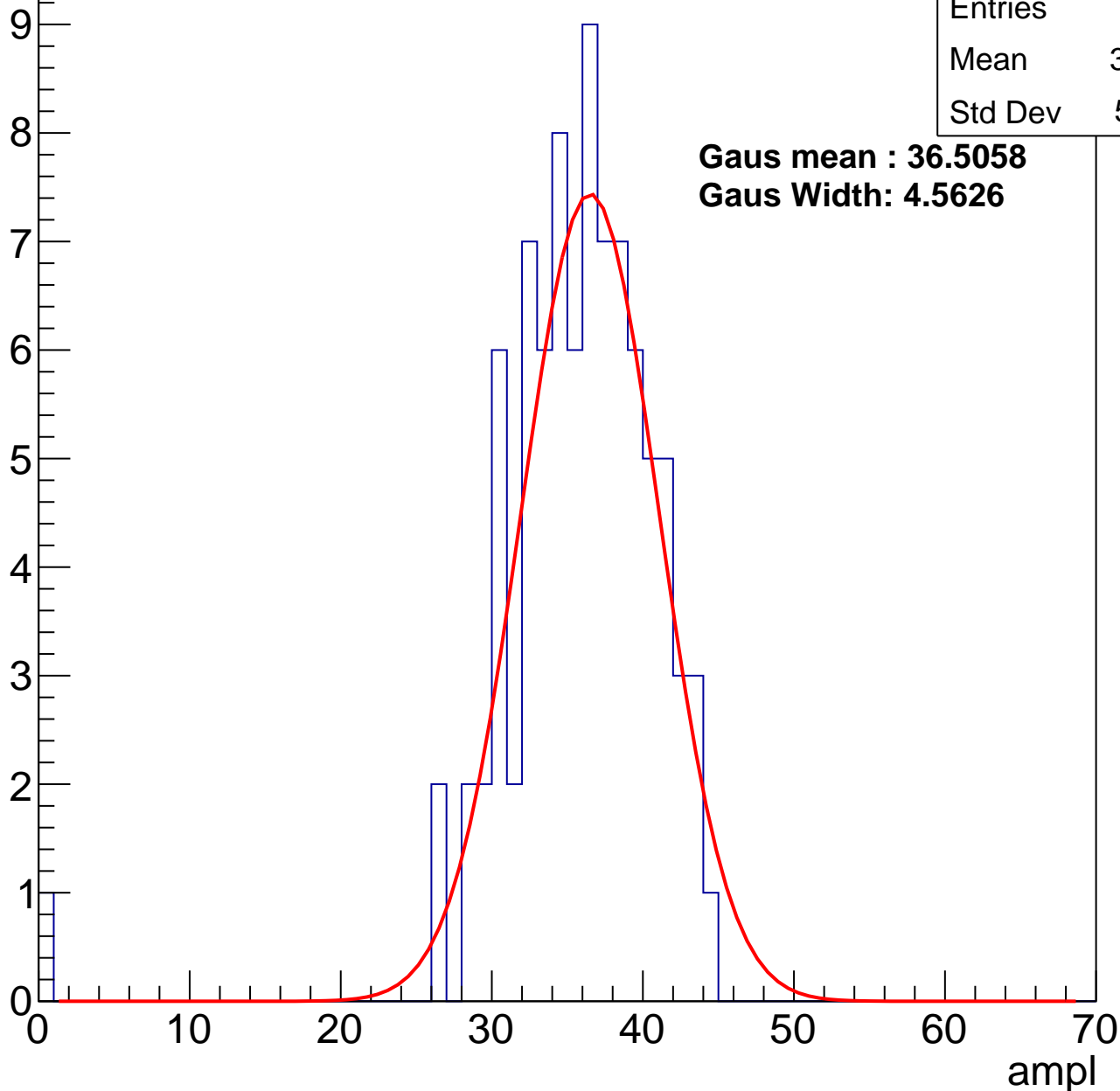
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	35.22
Std Dev	5.591

**Gaus mean : 36.5058**

**Gaus Width: 4.5626**



# B1L102S, U24-ch110, adc2

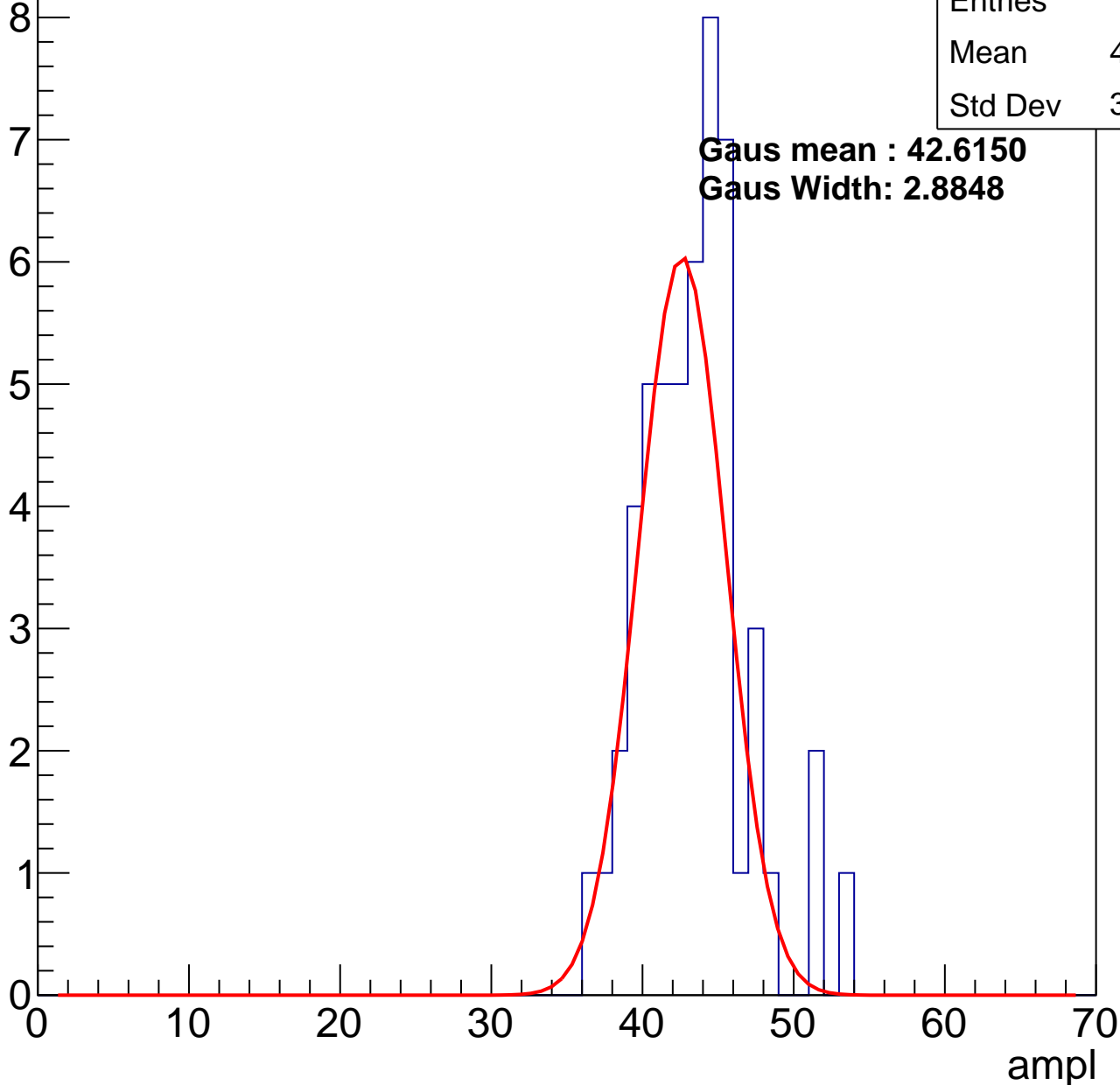
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.98
Std Dev	3.439

**Gaus mean : 42.6150**

**Gaus Width: 2.8848**

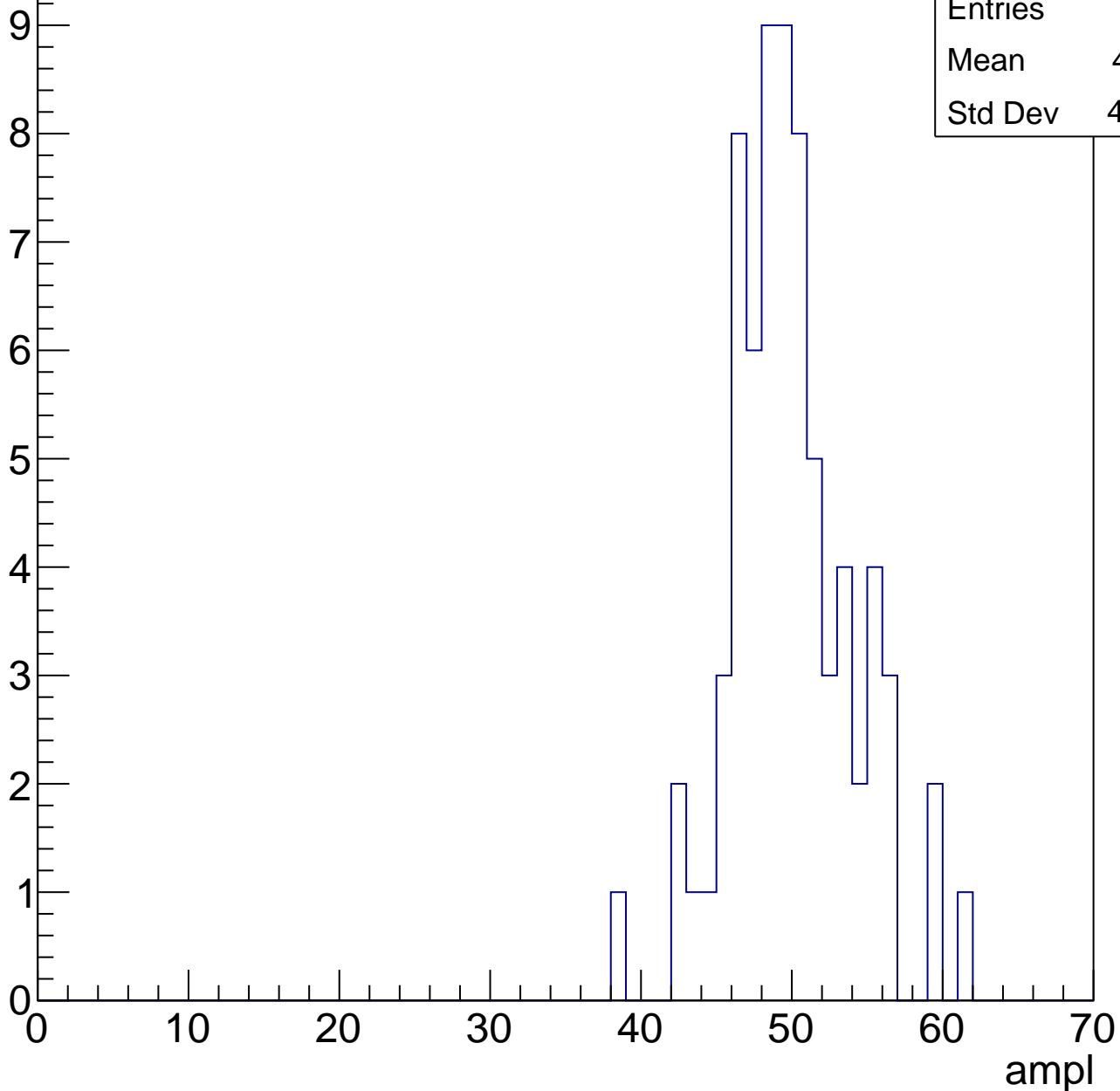


# B1L102S, U24-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.51
Std Dev	4.113

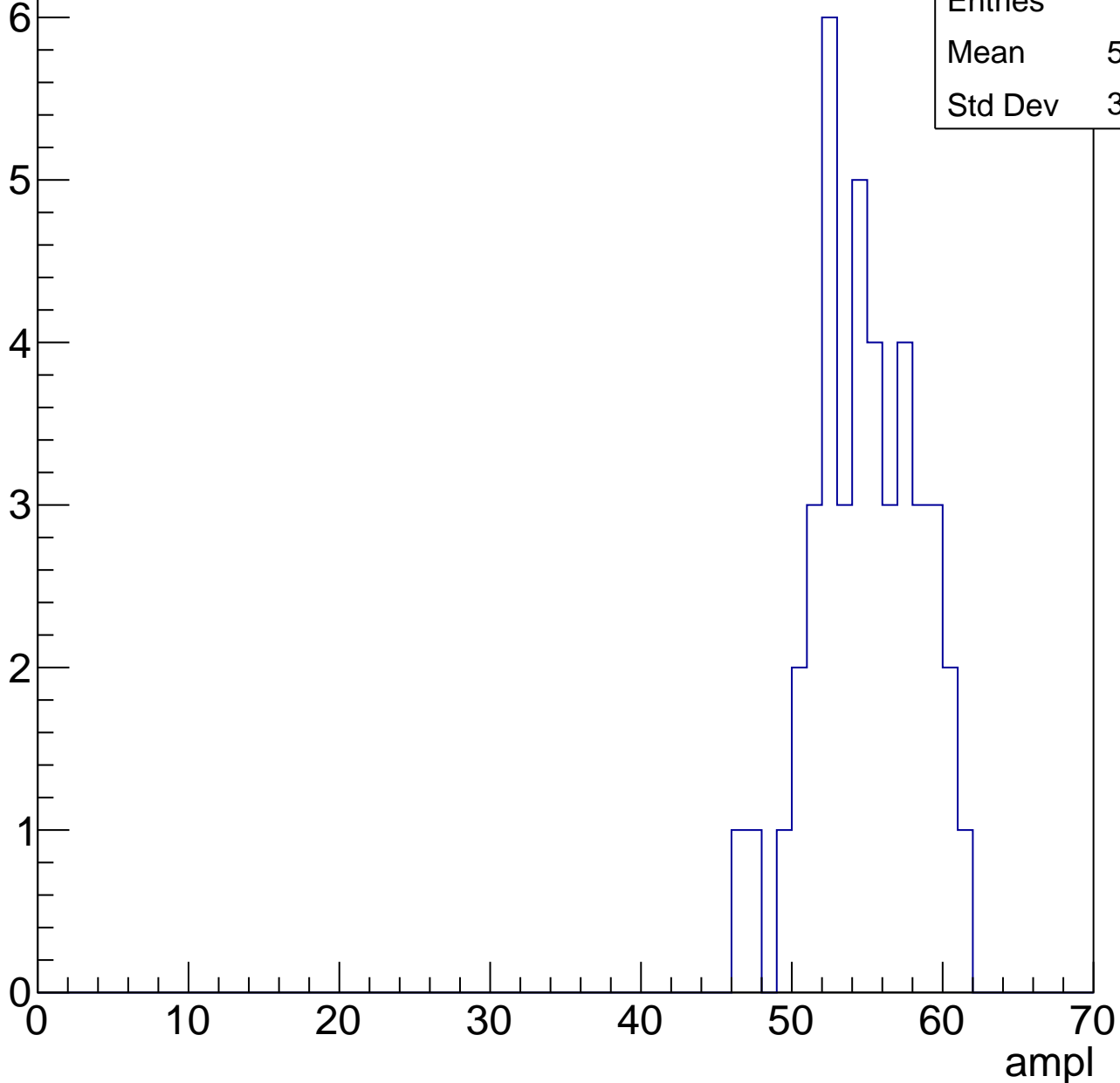


# B1L102S, U24-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	54.38
Std Dev	3.498

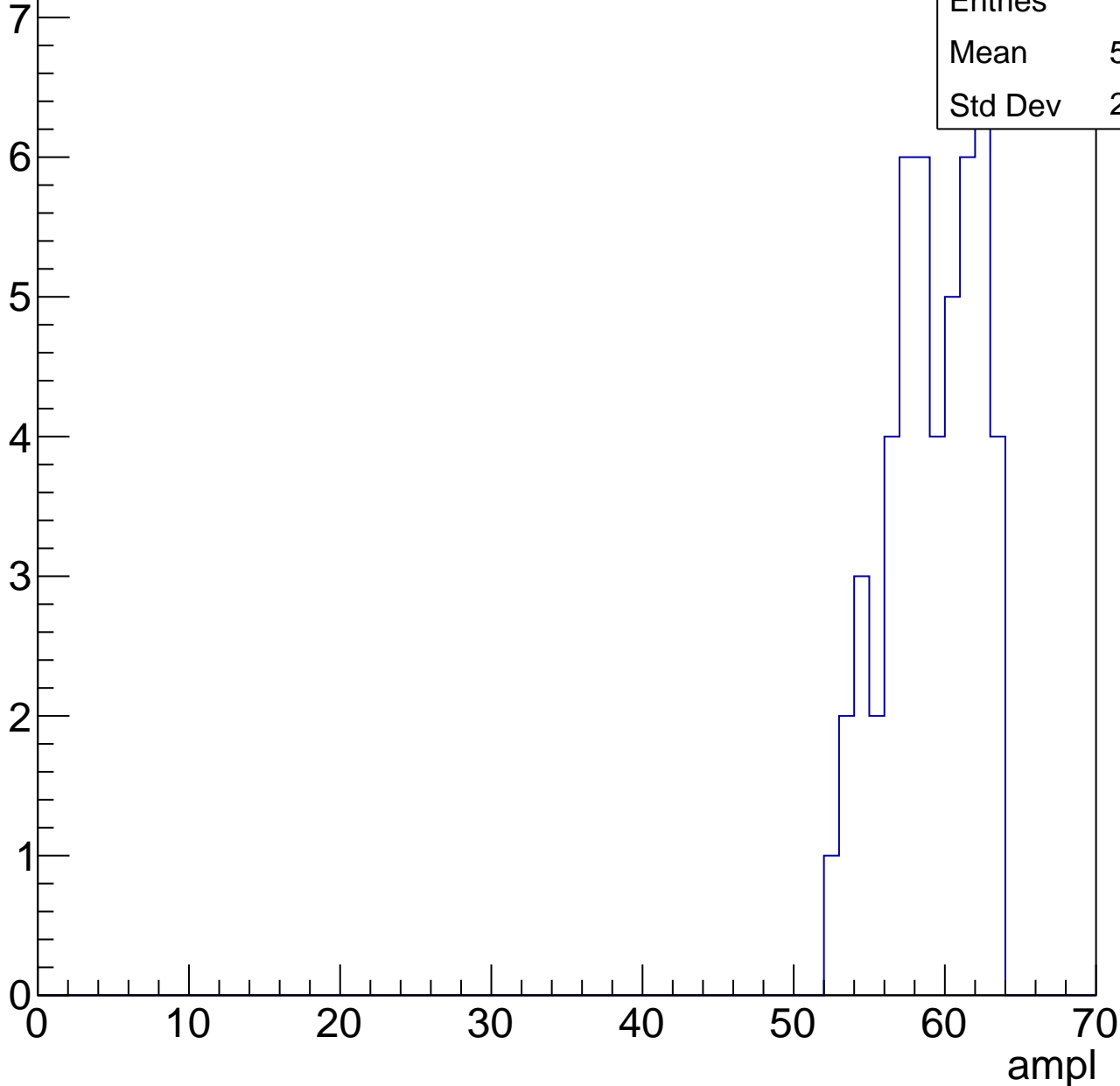


# B1L102S, U24-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.64
Std Dev	2.978

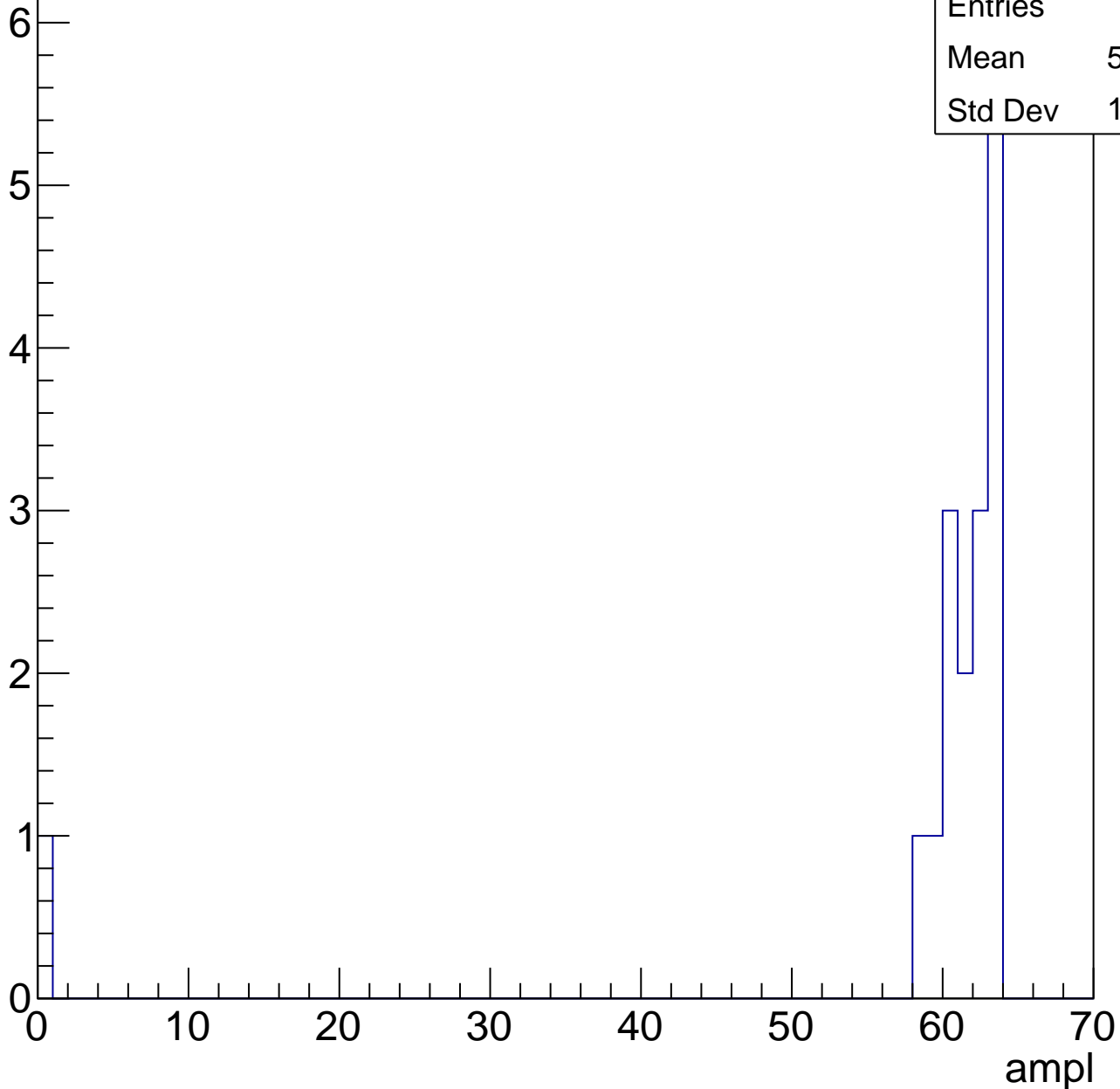


# B1L102S, U24-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.82
Std Dev	14.54





# B1L102S, U24-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch111, adc0

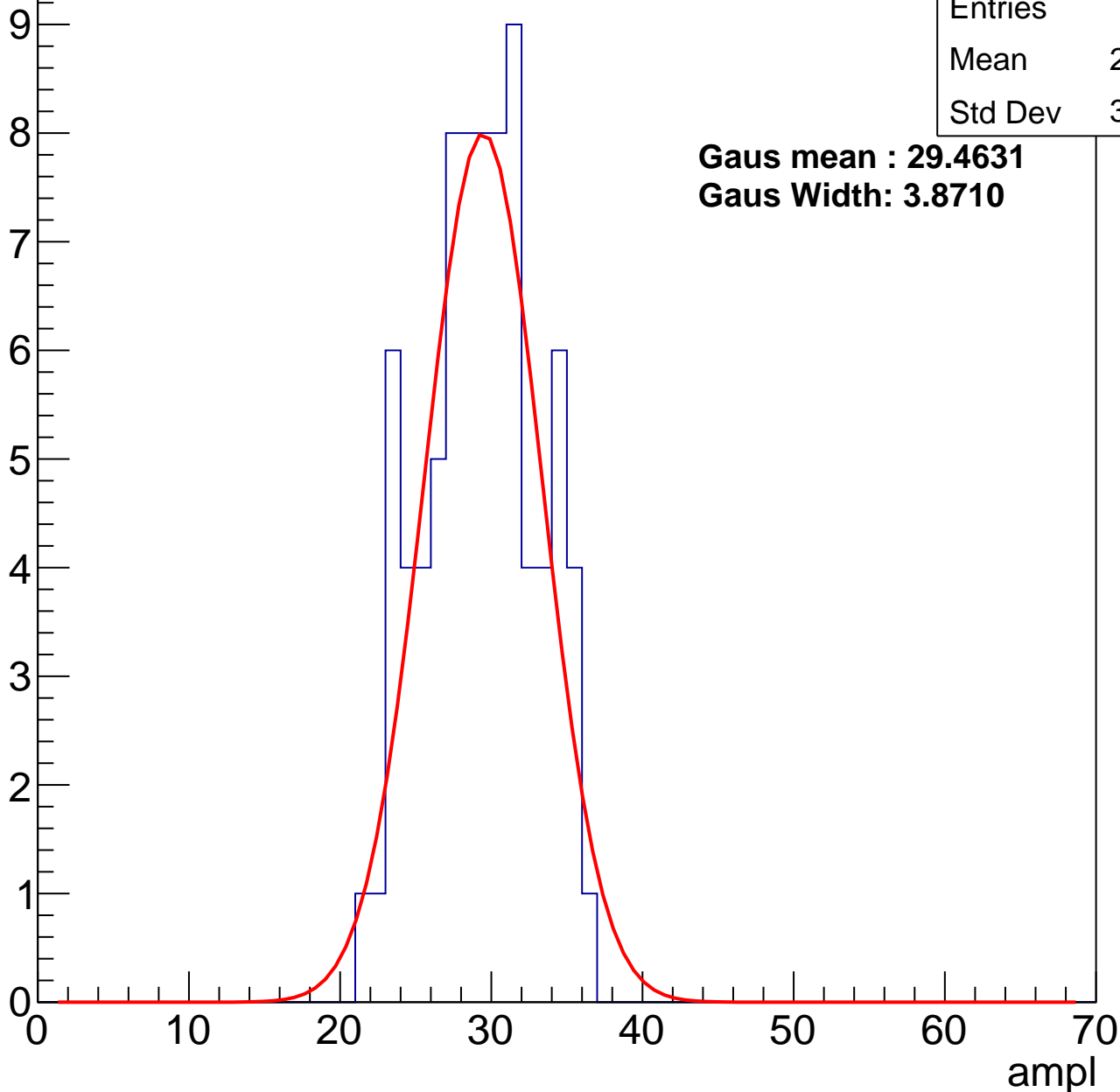
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.86
Std Dev	3.623

**Gaus mean : 29.4631**

**Gaus Width: 3.8710**



# B1L102S, U24-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	72
Mean	36.89
Std Dev	3.784

**Gaus mean : 37.8201**

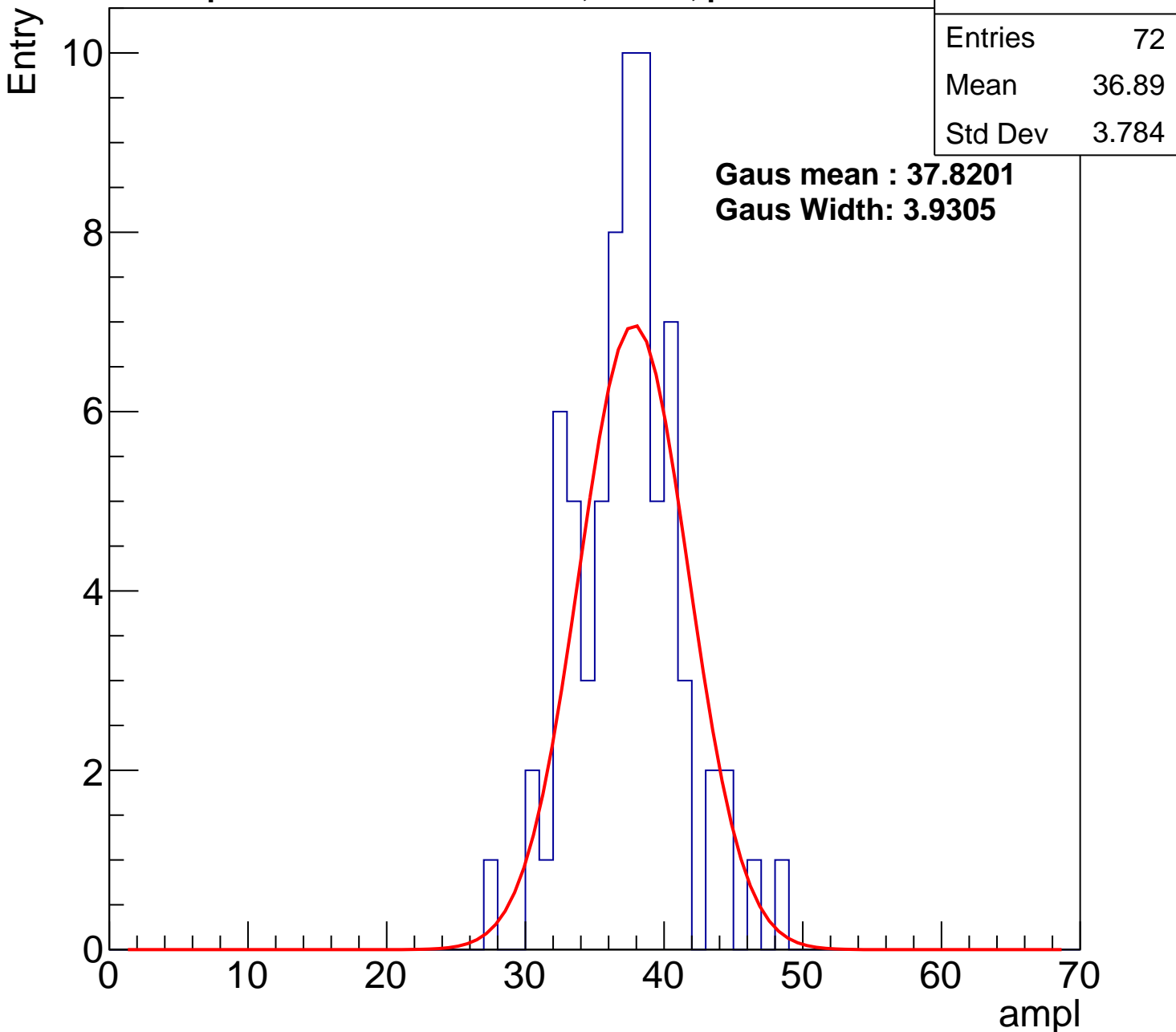
**Gaus Width: 3.9305**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U24-ch111, adc2

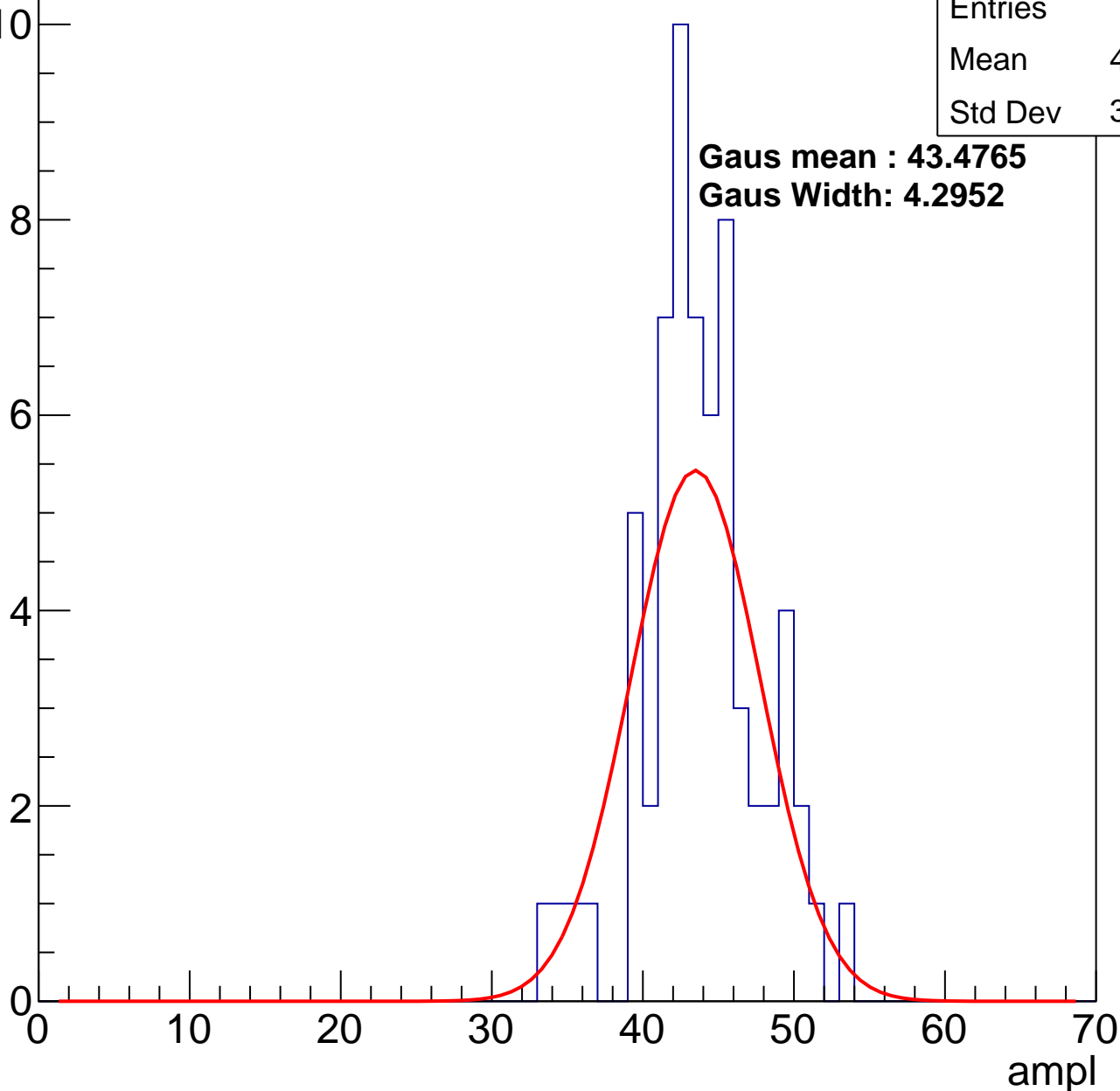
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.33
Std Dev	3.905

**Gaus mean : 43.4765**

**Gaus Width: 4.2952**

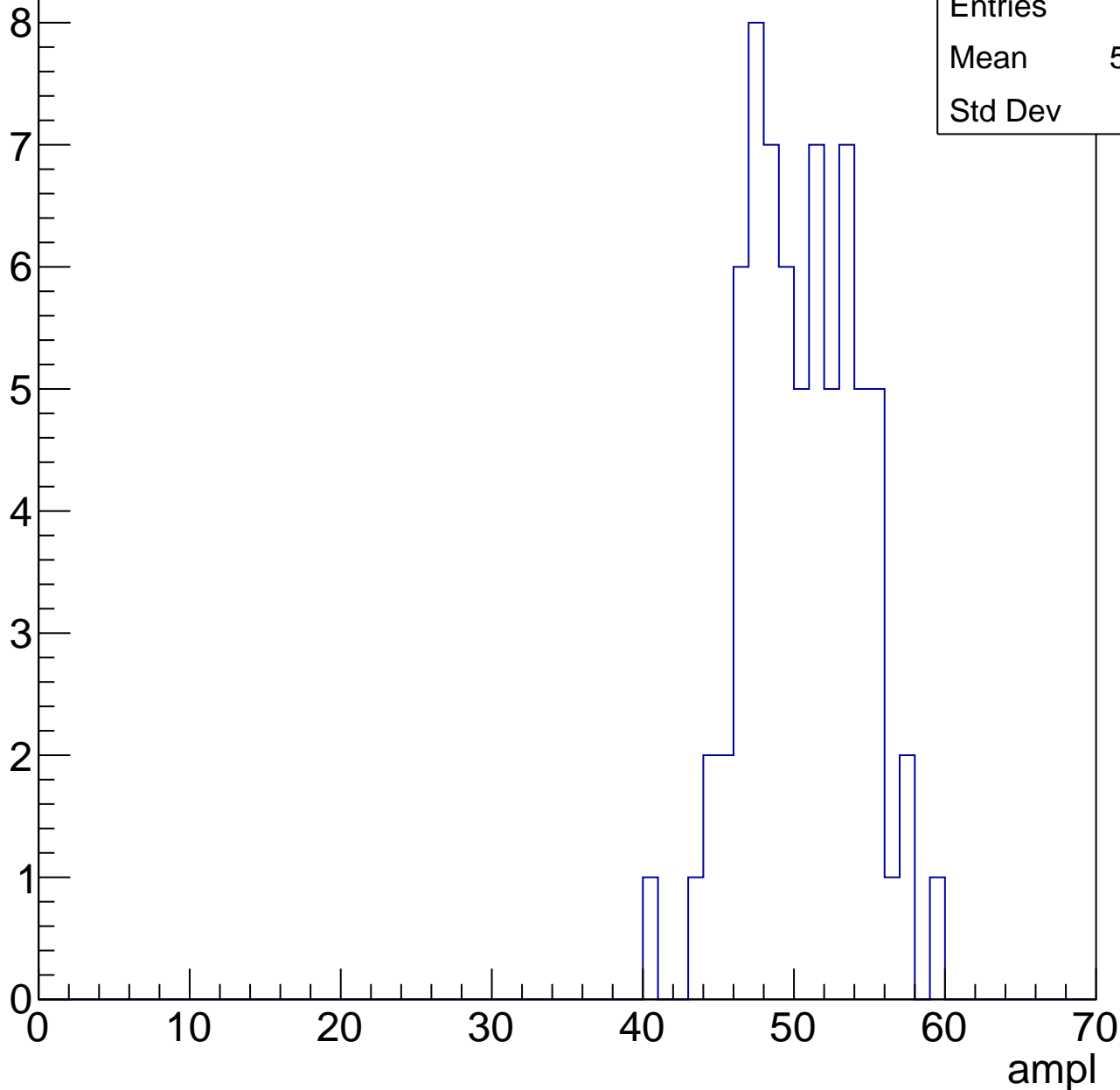


# B1L102S, U24-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.07
Std Dev	3.72

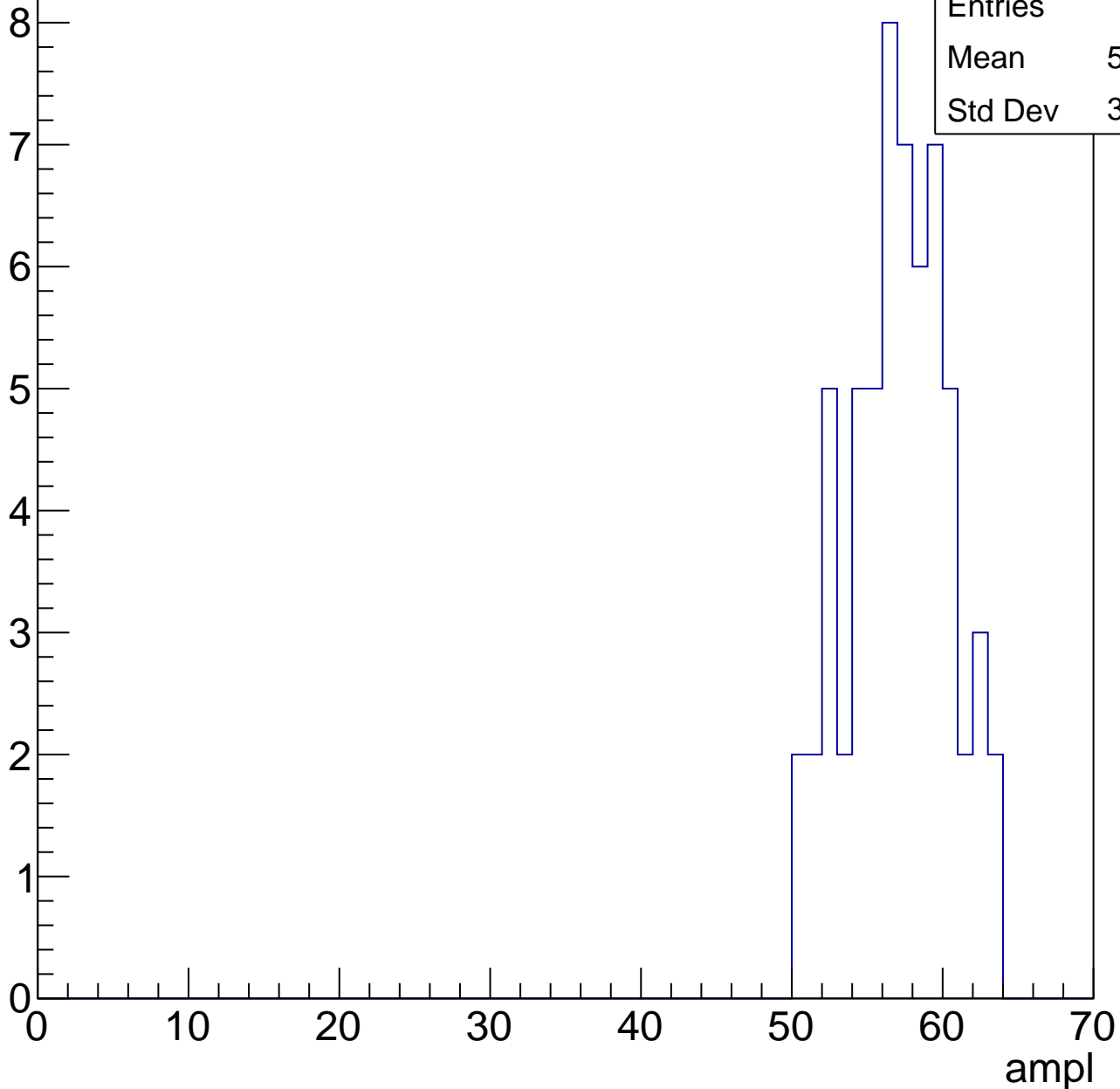


# B1L102S, U24-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.64
Std Dev	3.264

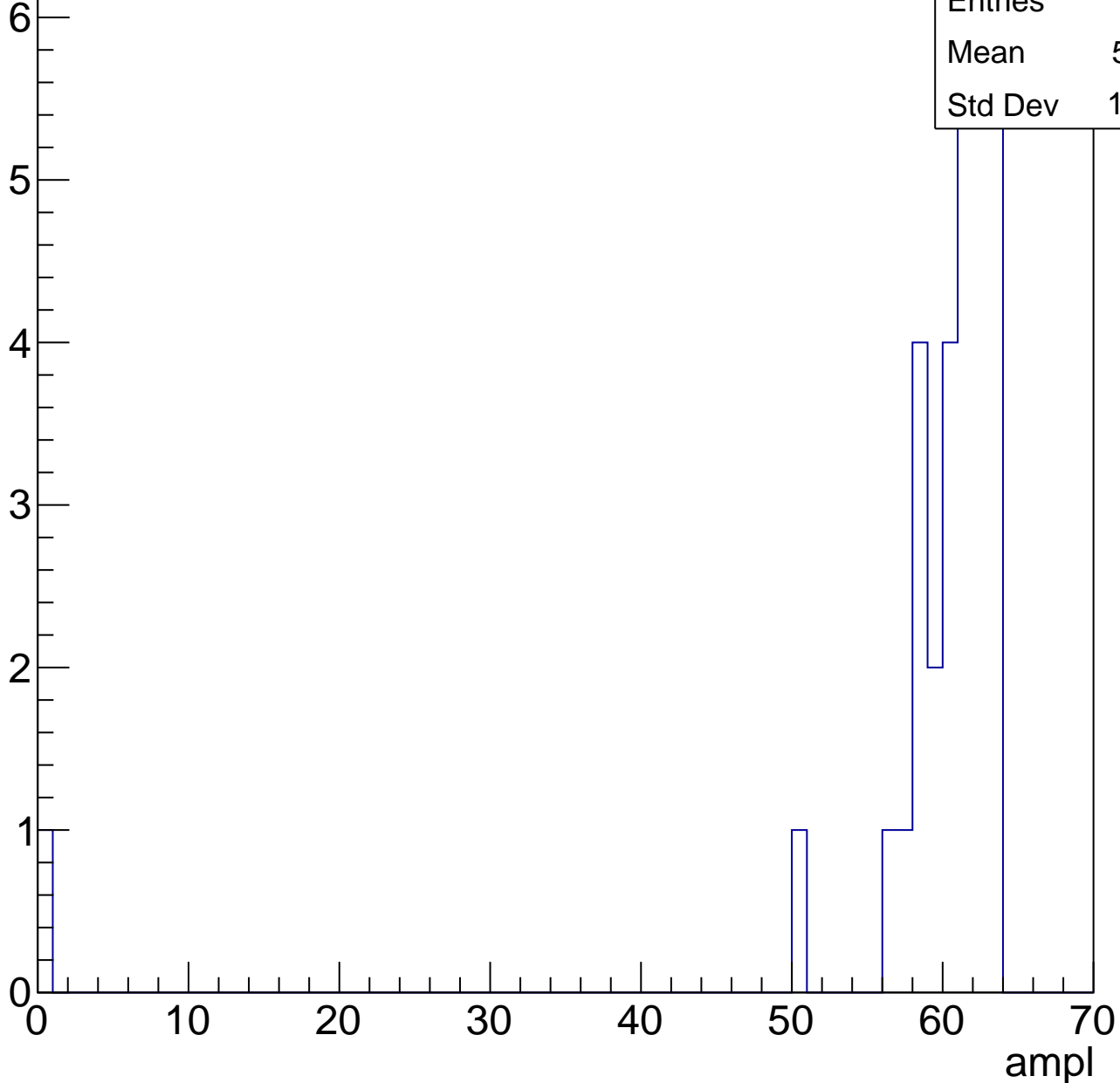


# B1L102S, U24-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	32
Mean	58.41
Std Dev	10.82



# B1L102S, U24-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

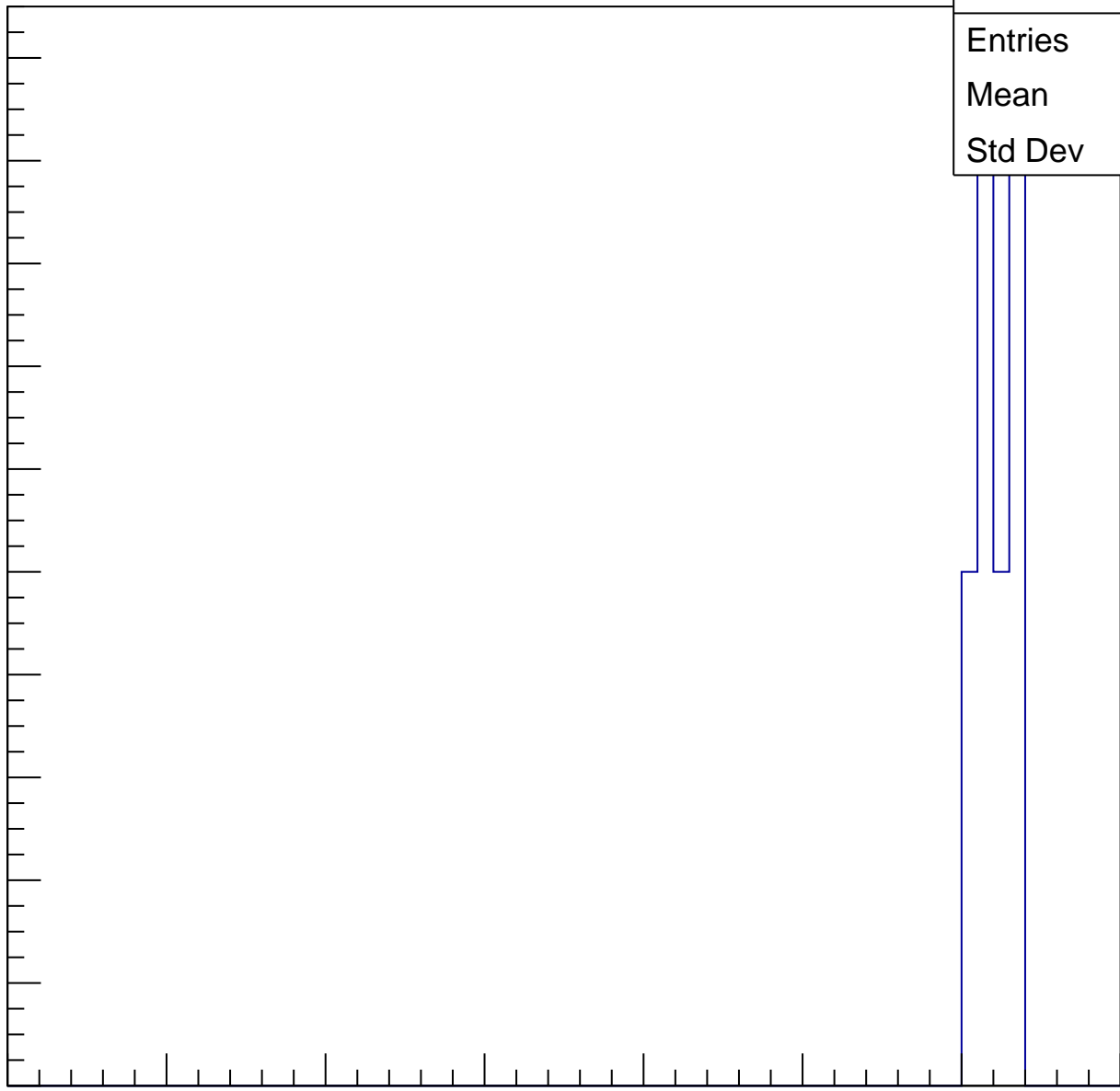
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.67
Std Dev	1.106

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch112, adc0

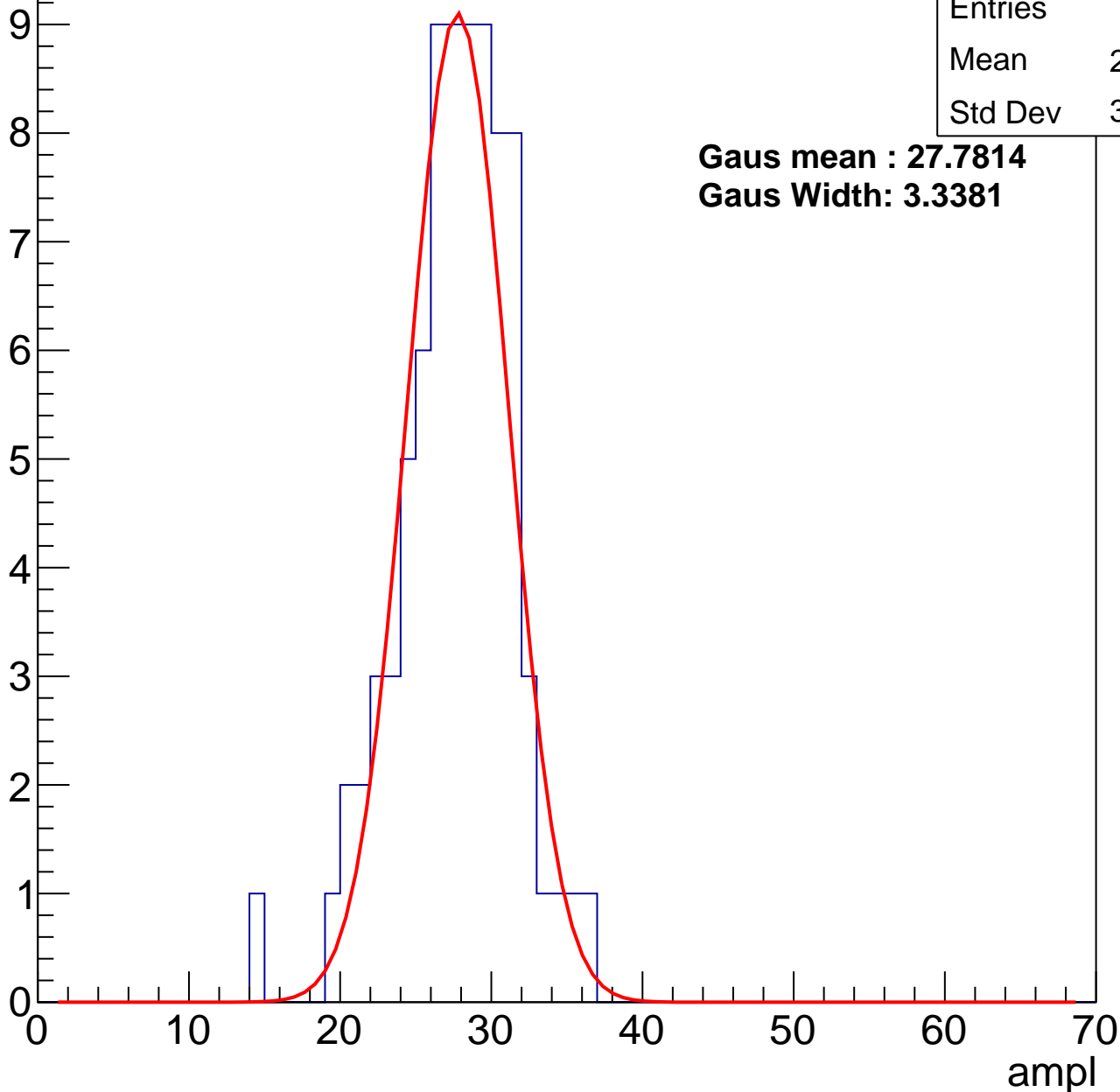
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	27.22
Std Dev	3.745

**Gaus mean : 27.7814**

**Gaus Width: 3.3381**



# B1L102S, U24-ch112, adc1

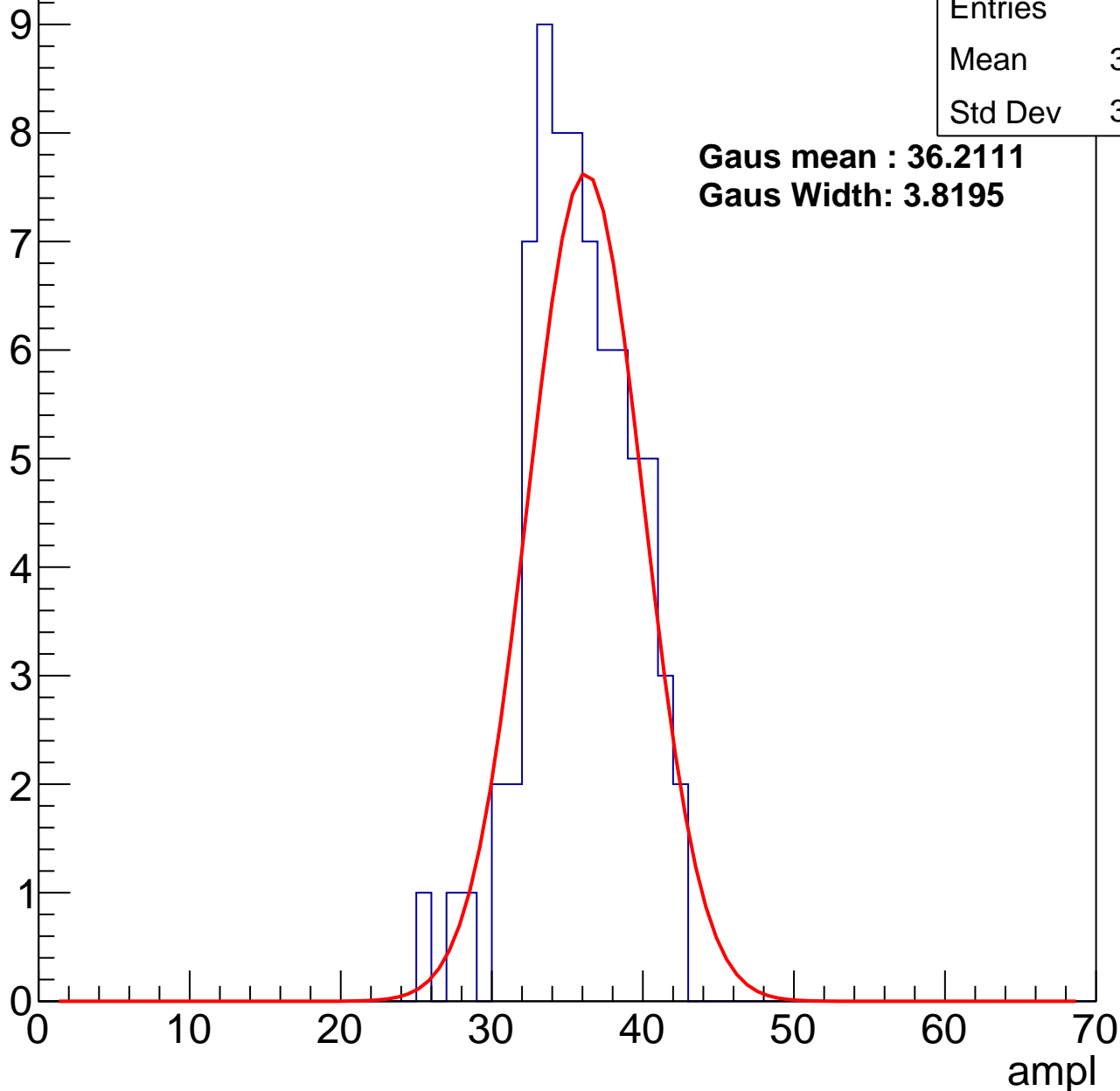
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.33
Std Dev	3.492

**Gaus mean : 36.2111**

**Gaus Width: 3.8195**



# B1L102S, U24-ch112, adc2

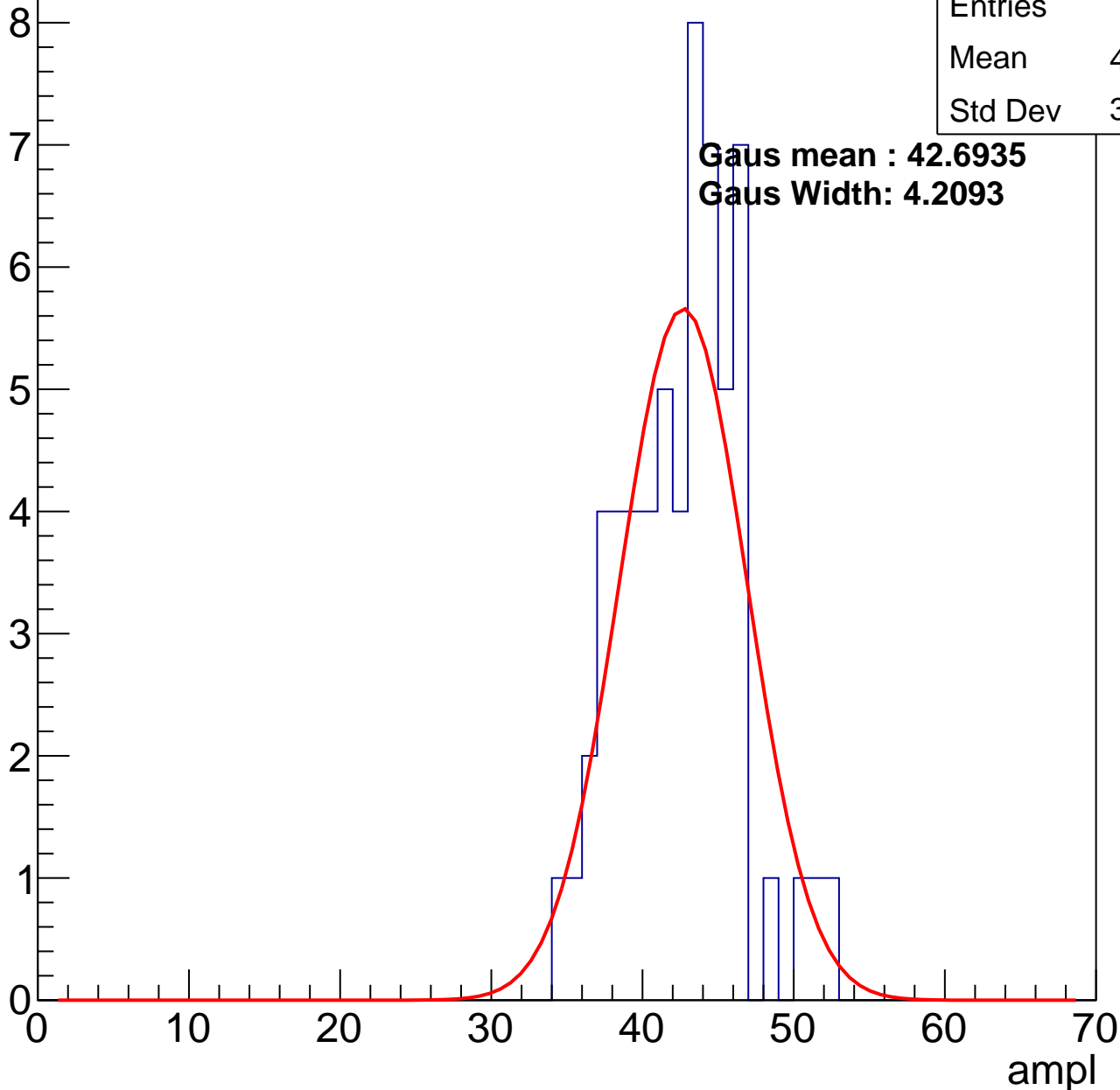
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.17
Std Dev	3.826

**Gaus mean : 42.6935**

**Gaus Width: 4.2093**

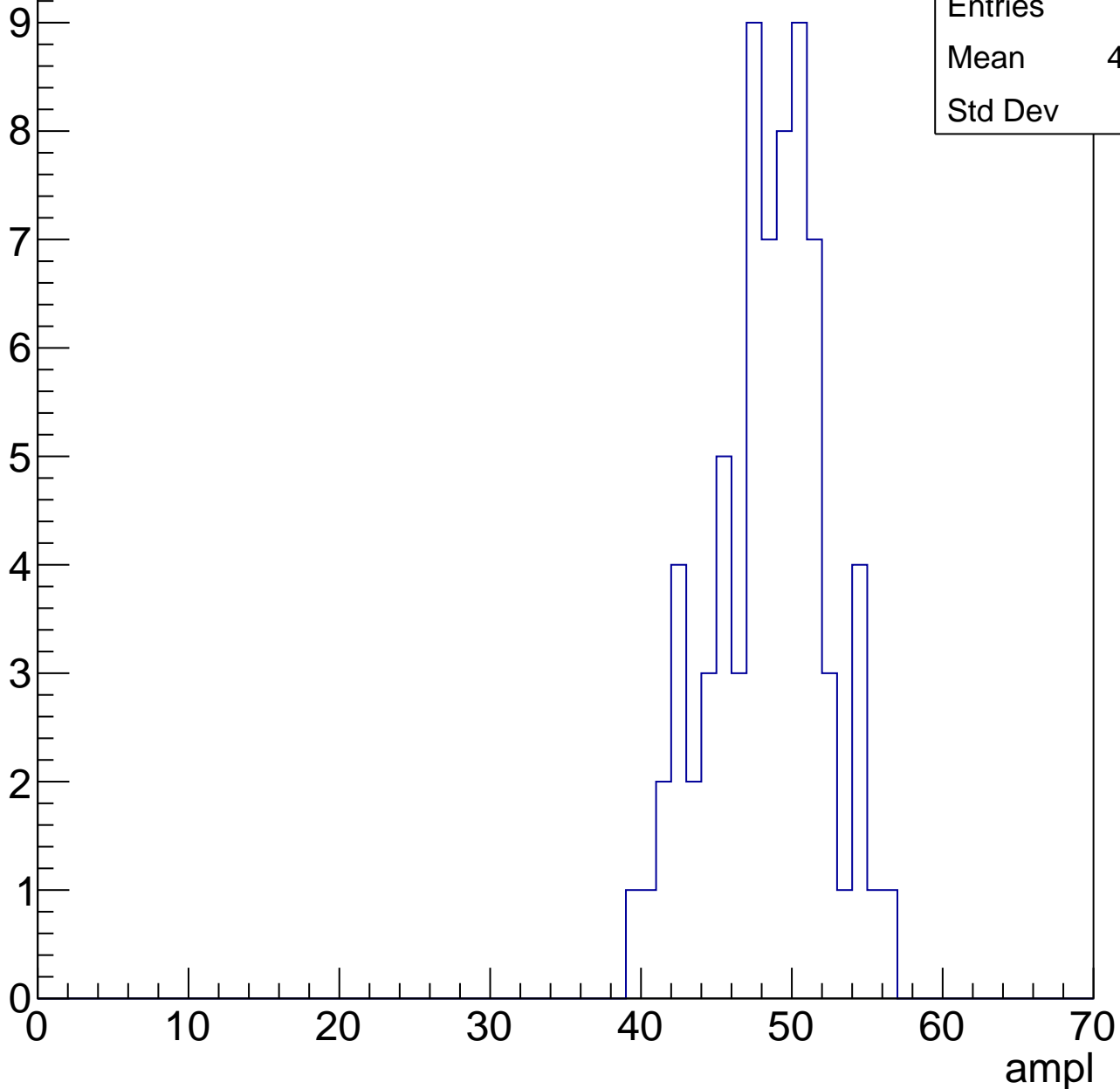


# B1L102S, U24-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	47.94
Std Dev	3.73

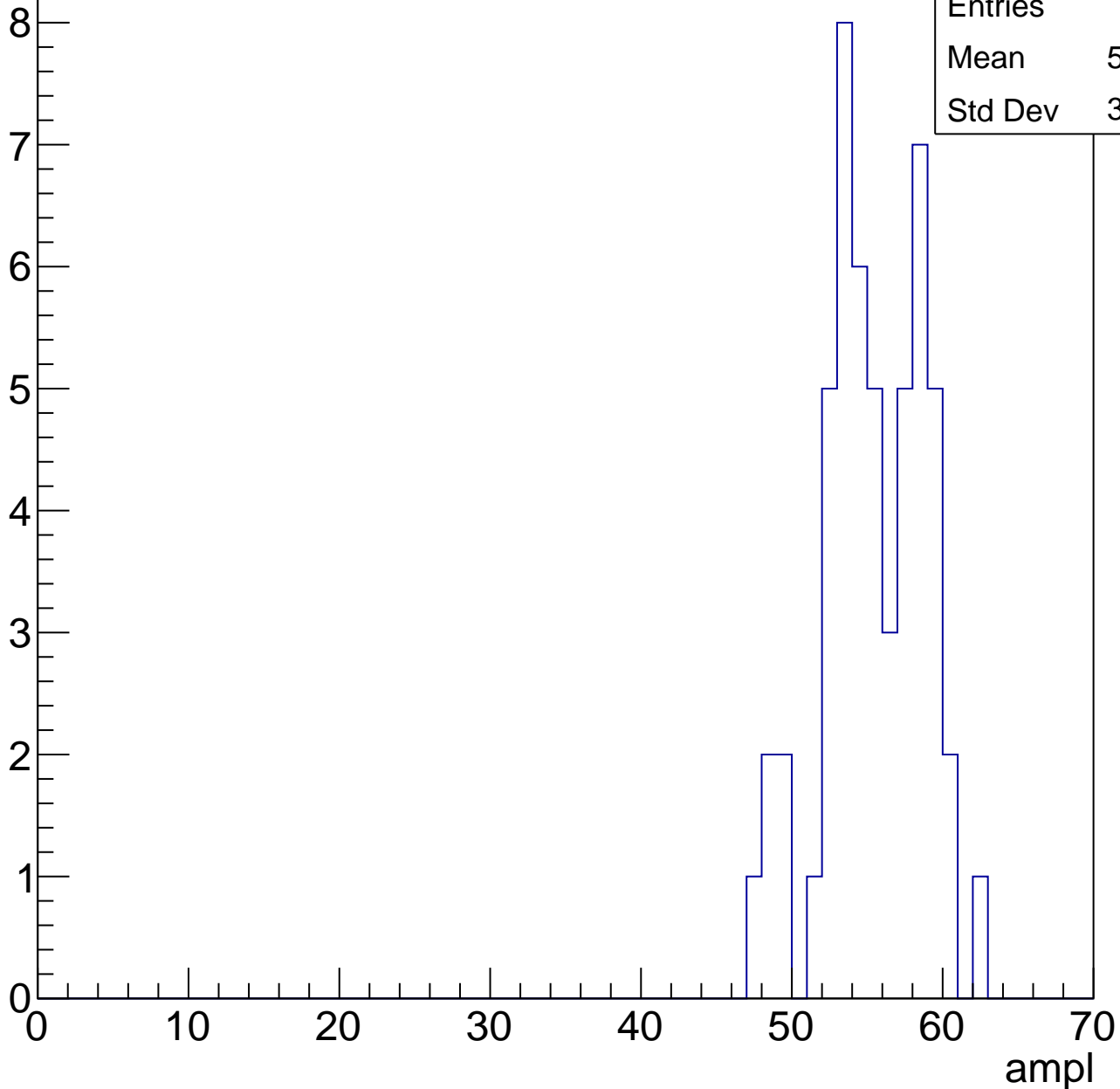


# B1L102S, U24-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.92
Std Dev	3.358

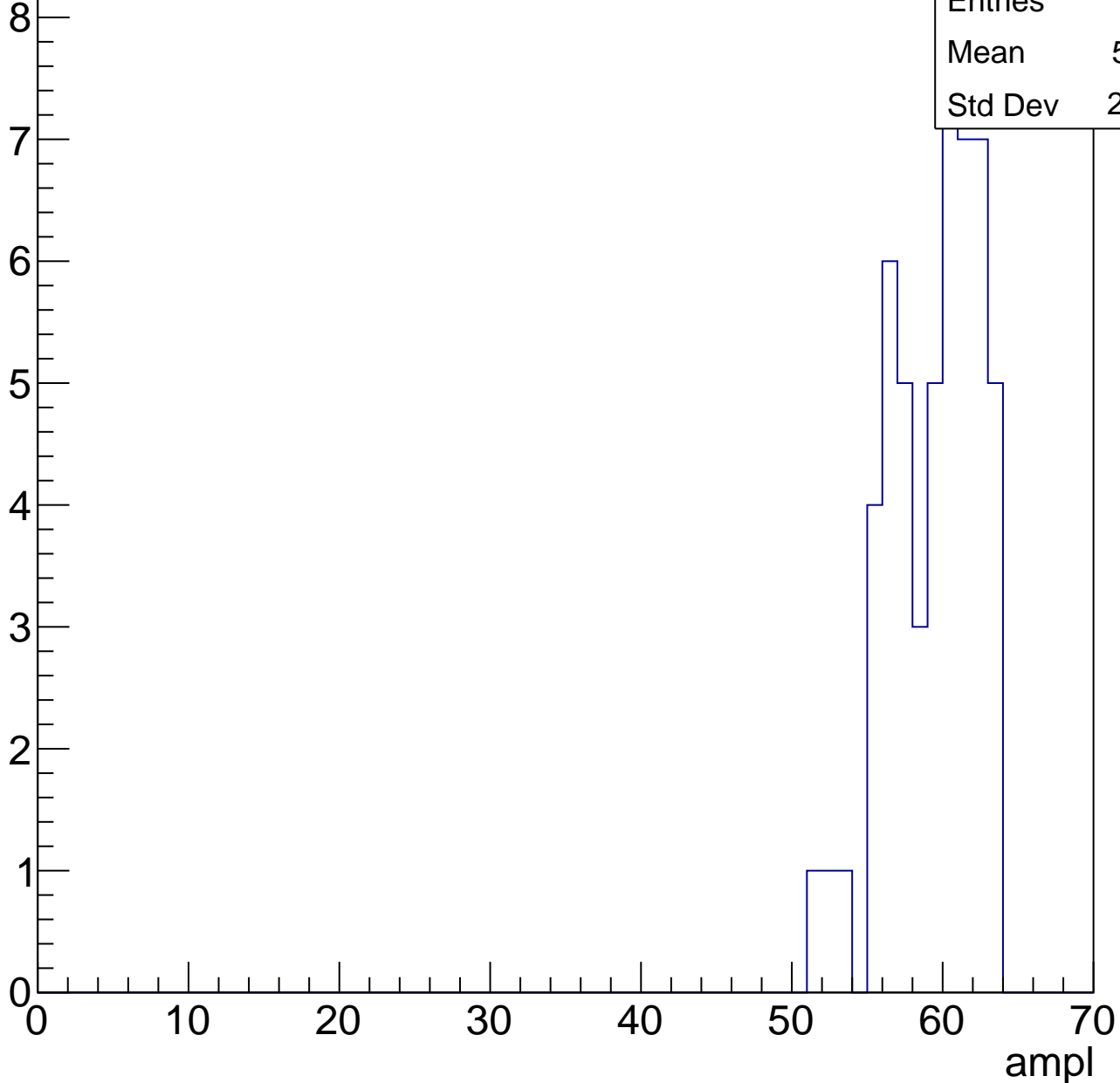


# B1L102S, U24-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.91
Std Dev	2.973

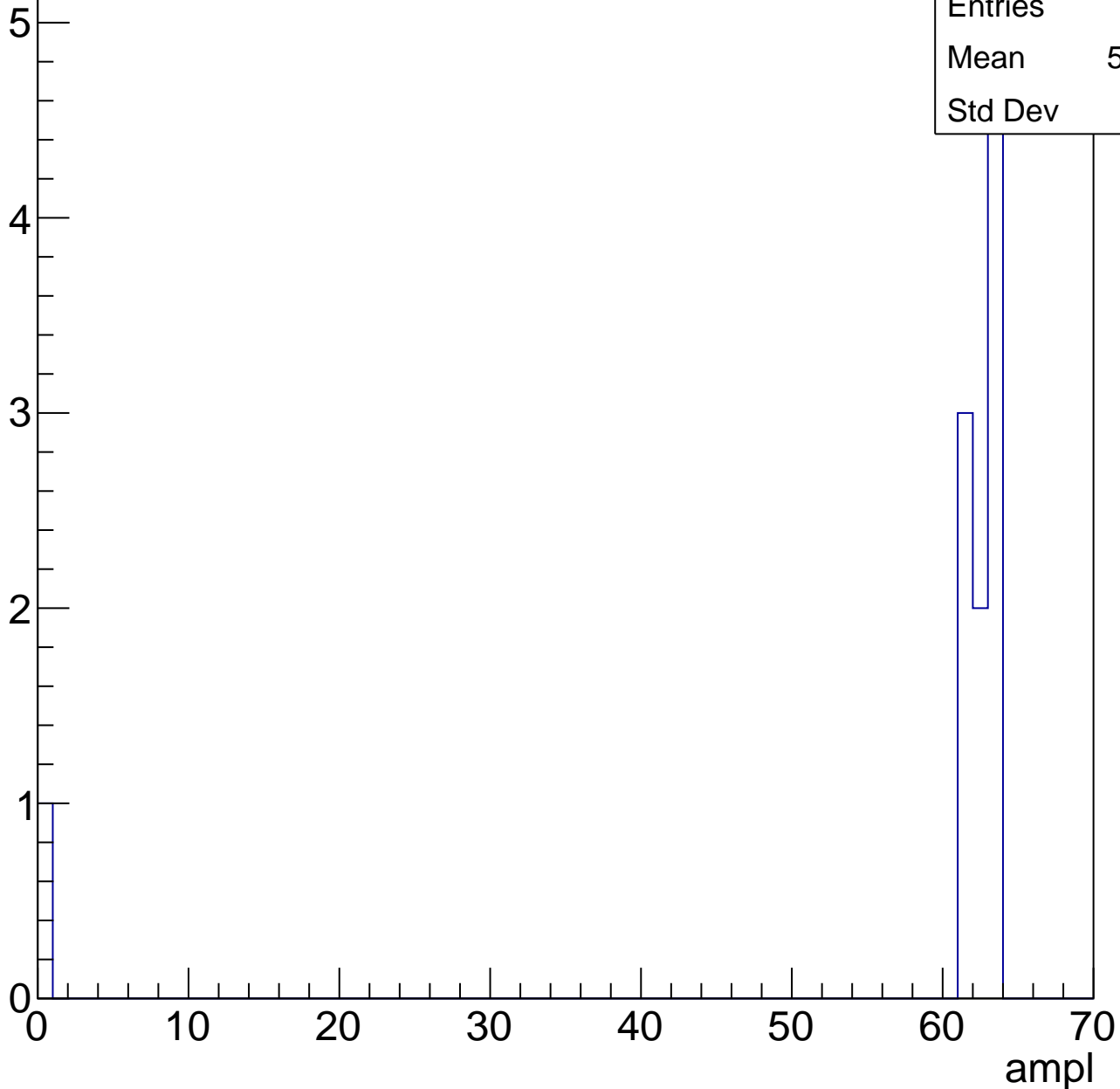


# B1L102S, U24-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.55
Std Dev	17.9





# B1L102S, U24-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch113, adc0

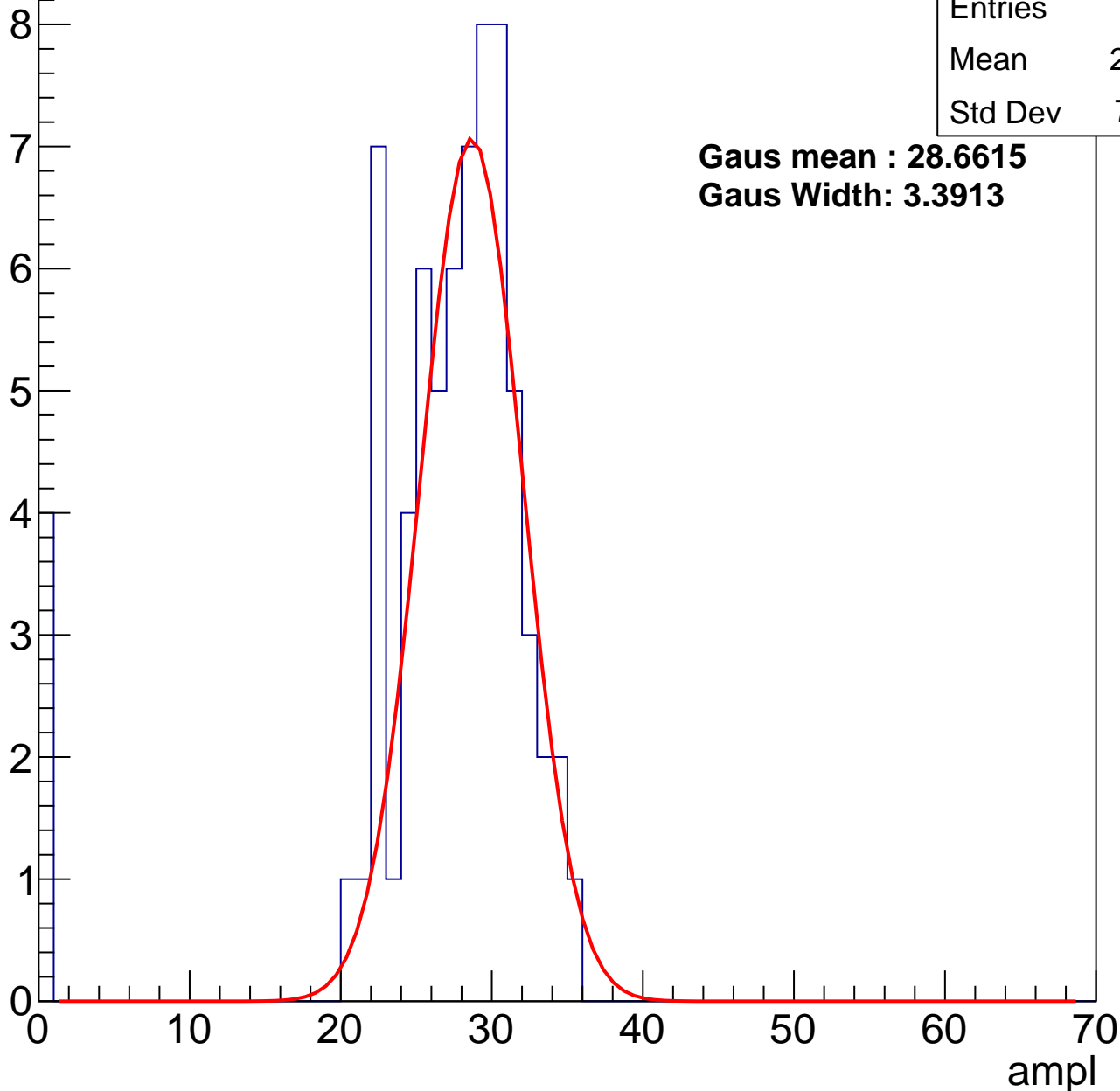
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	25.97
Std Dev	7.201

**Gaus mean : 28.6615**

**Gaus Width: 3.3913**



# B1L102S, U24-ch113, adc1

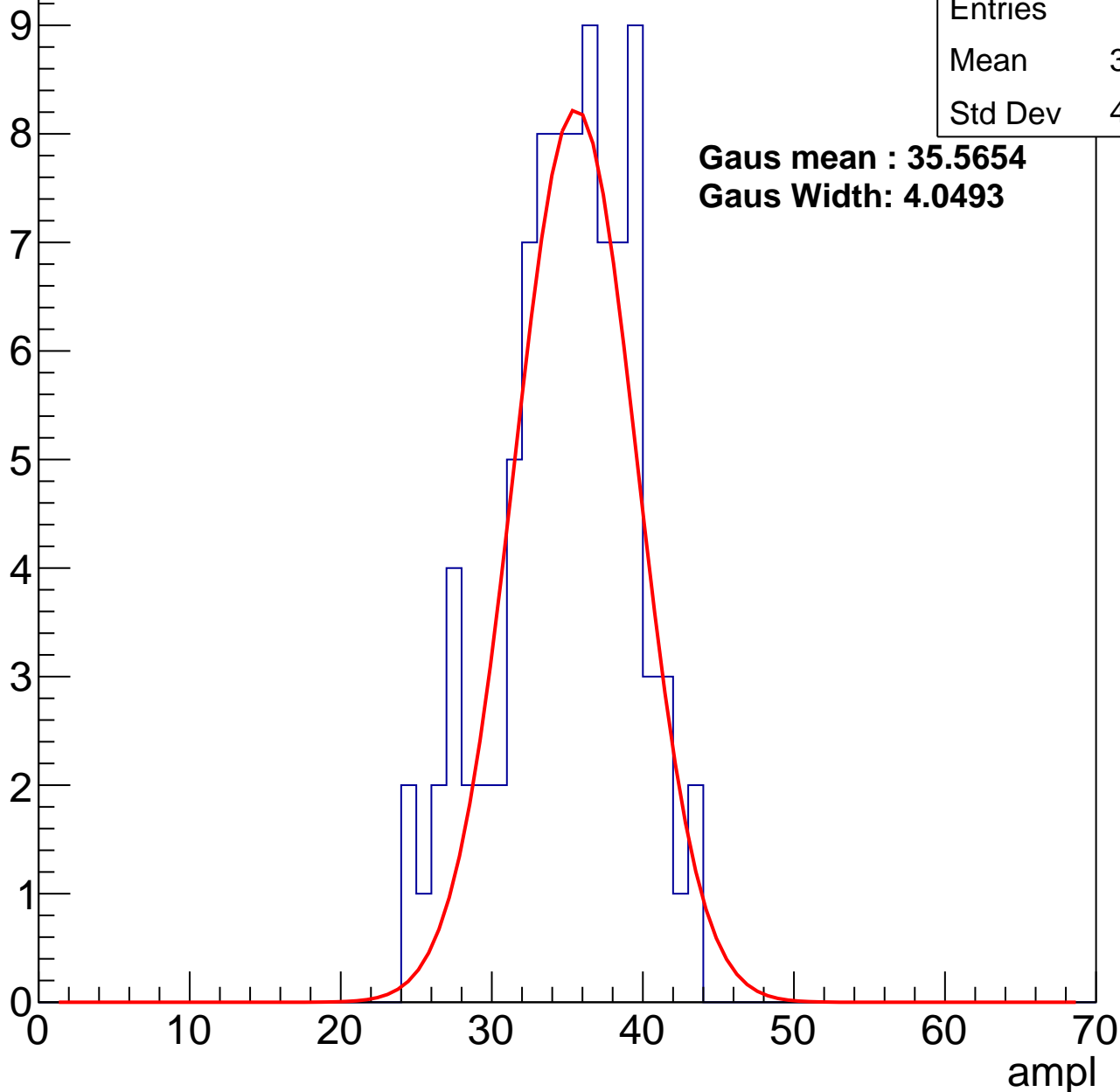
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	92
Mean	34.49
Std Dev	4.353

**Gaus mean : 35.5654**

**Gaus Width: 4.0493**



# B1L102S, U24-ch113, adc2

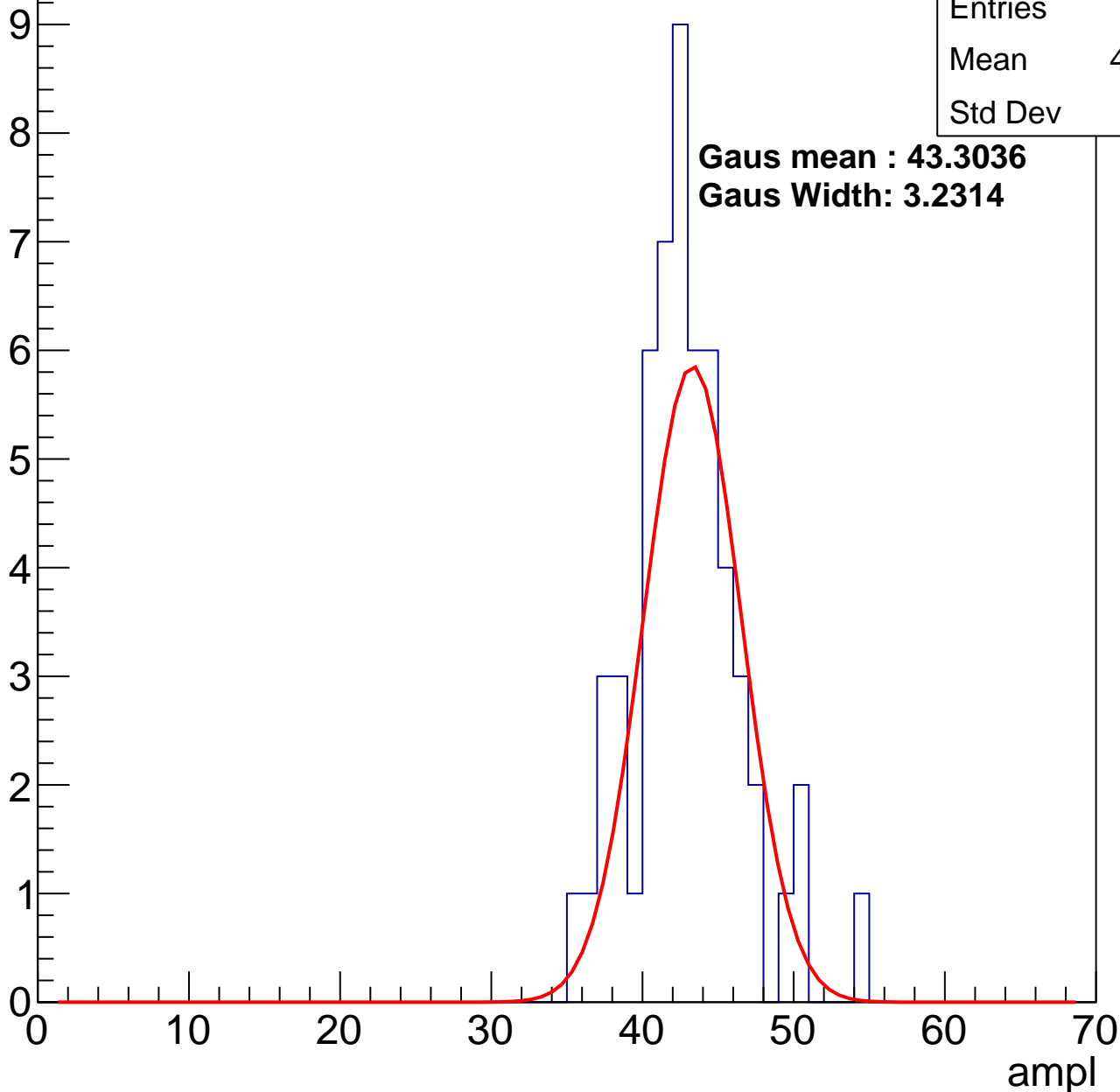
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.45
Std Dev	3.59

**Gaus mean : 43.3036**

**Gaus Width: 3.2314**

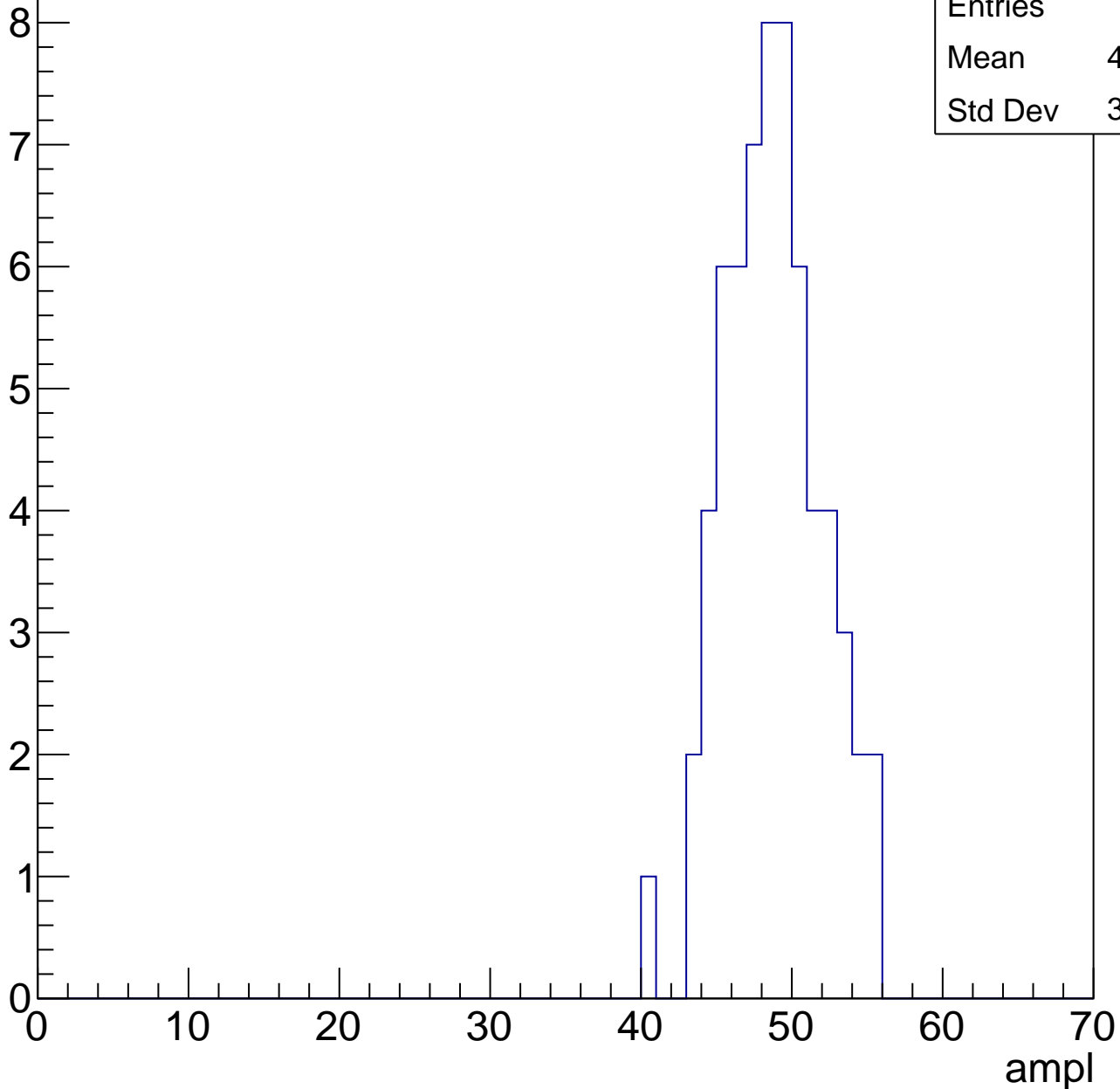


# B1L102S, U24-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.29
Std Dev	3.174

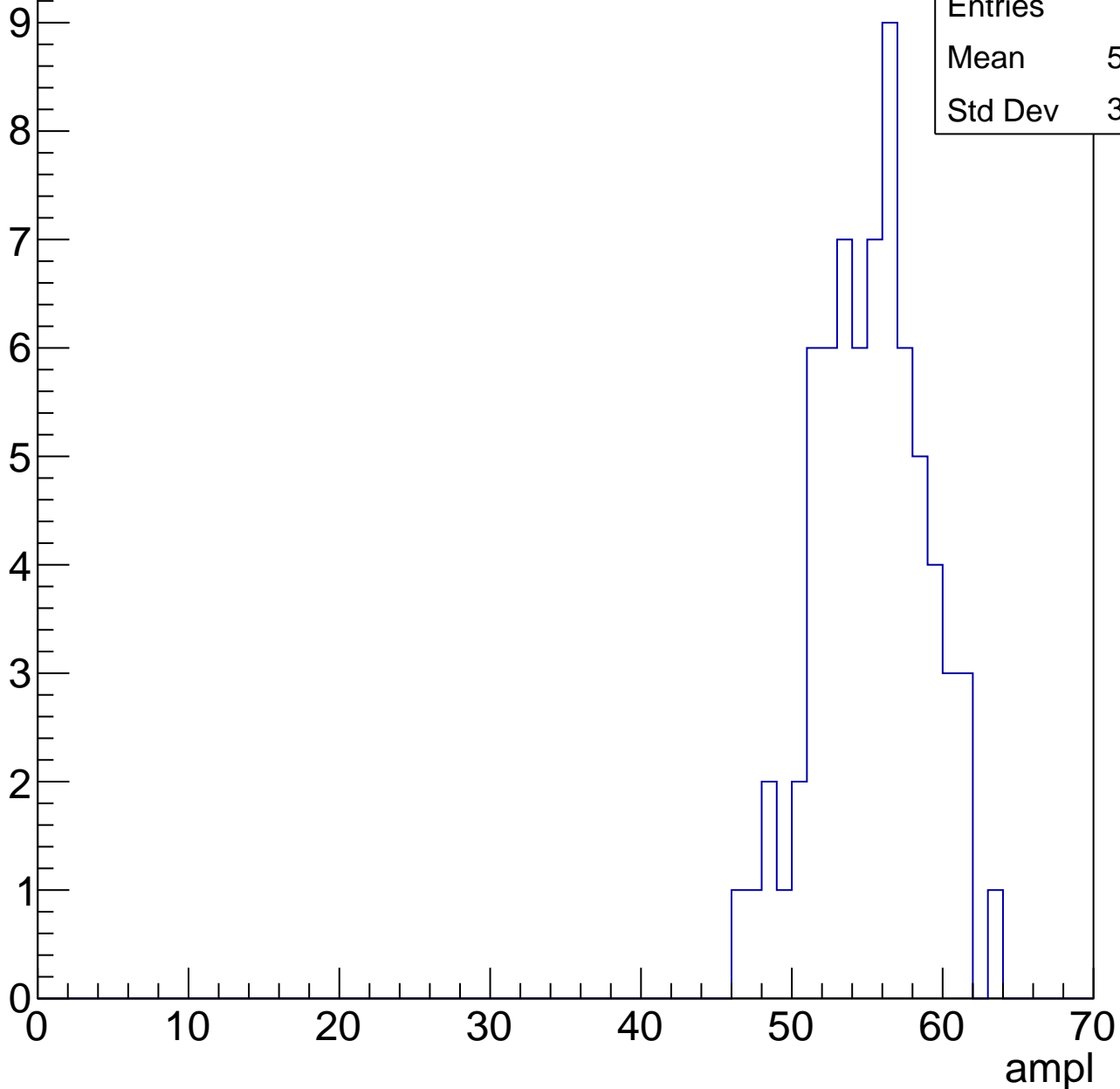


# B1L102S, U24-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.77
Std Dev	3.582

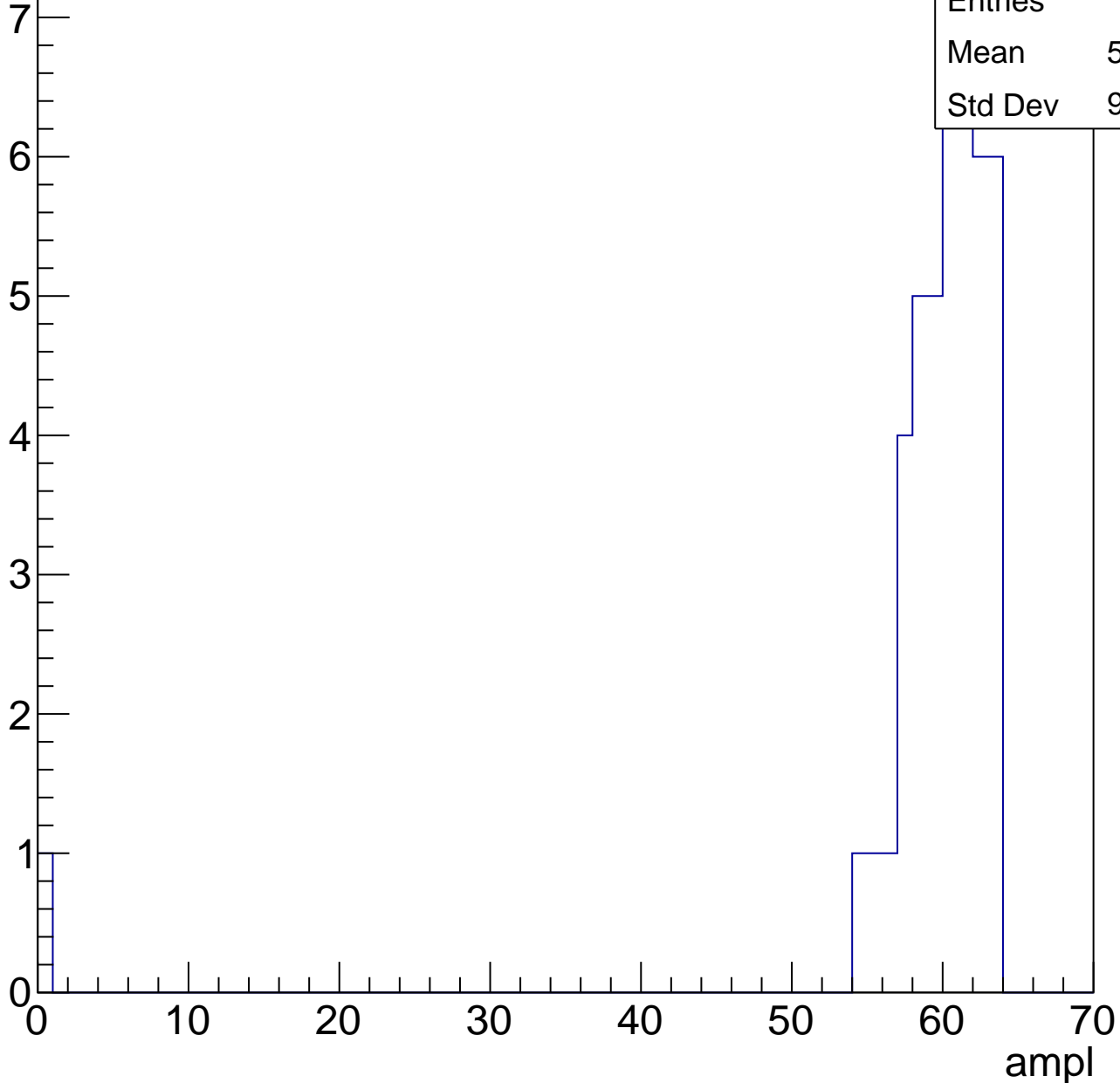


# B1L102S, U24-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

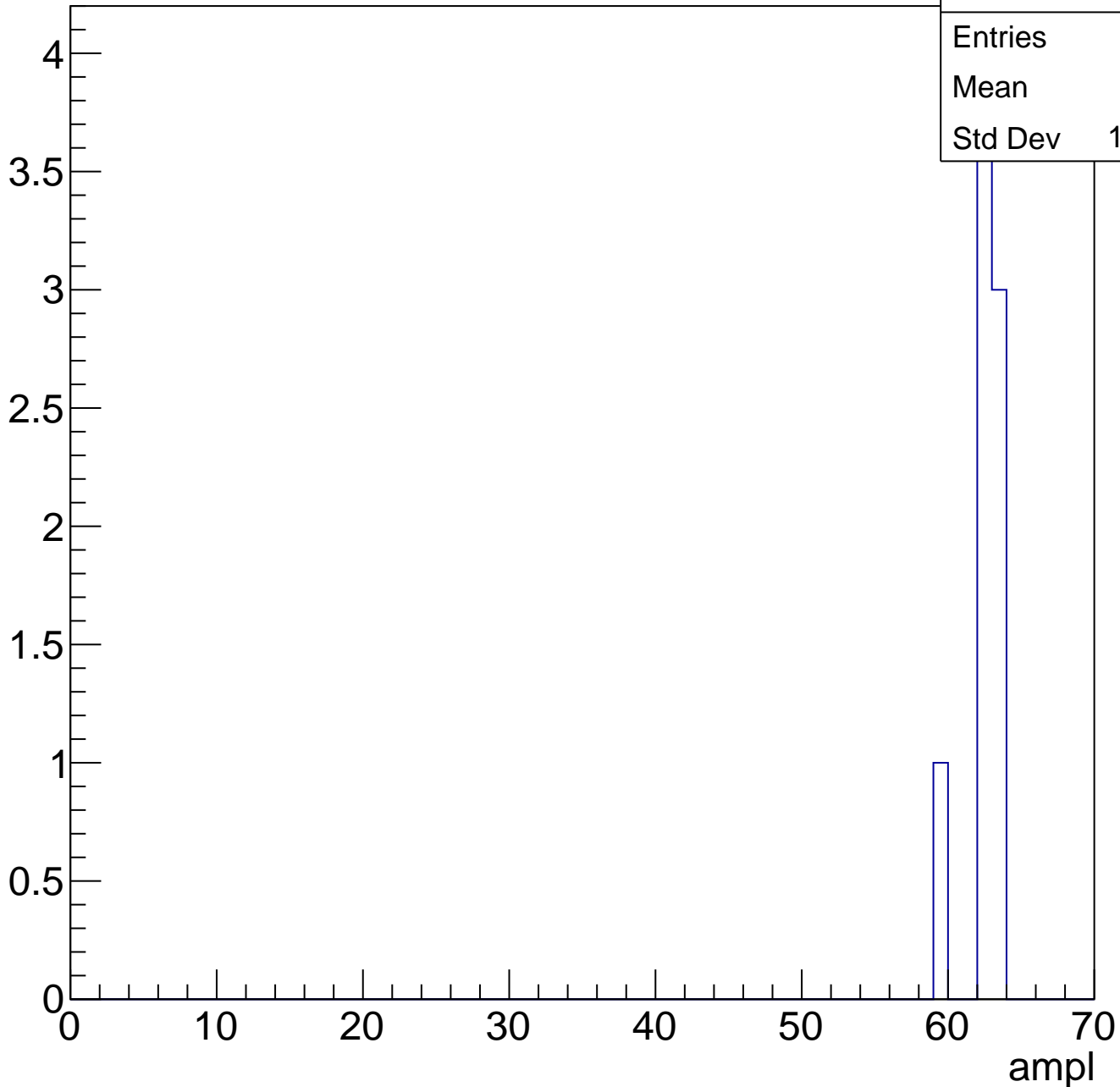
Entries	44
Mean	58.52
Std Dev	9.203



# B1L102S, U24-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch114, adc0

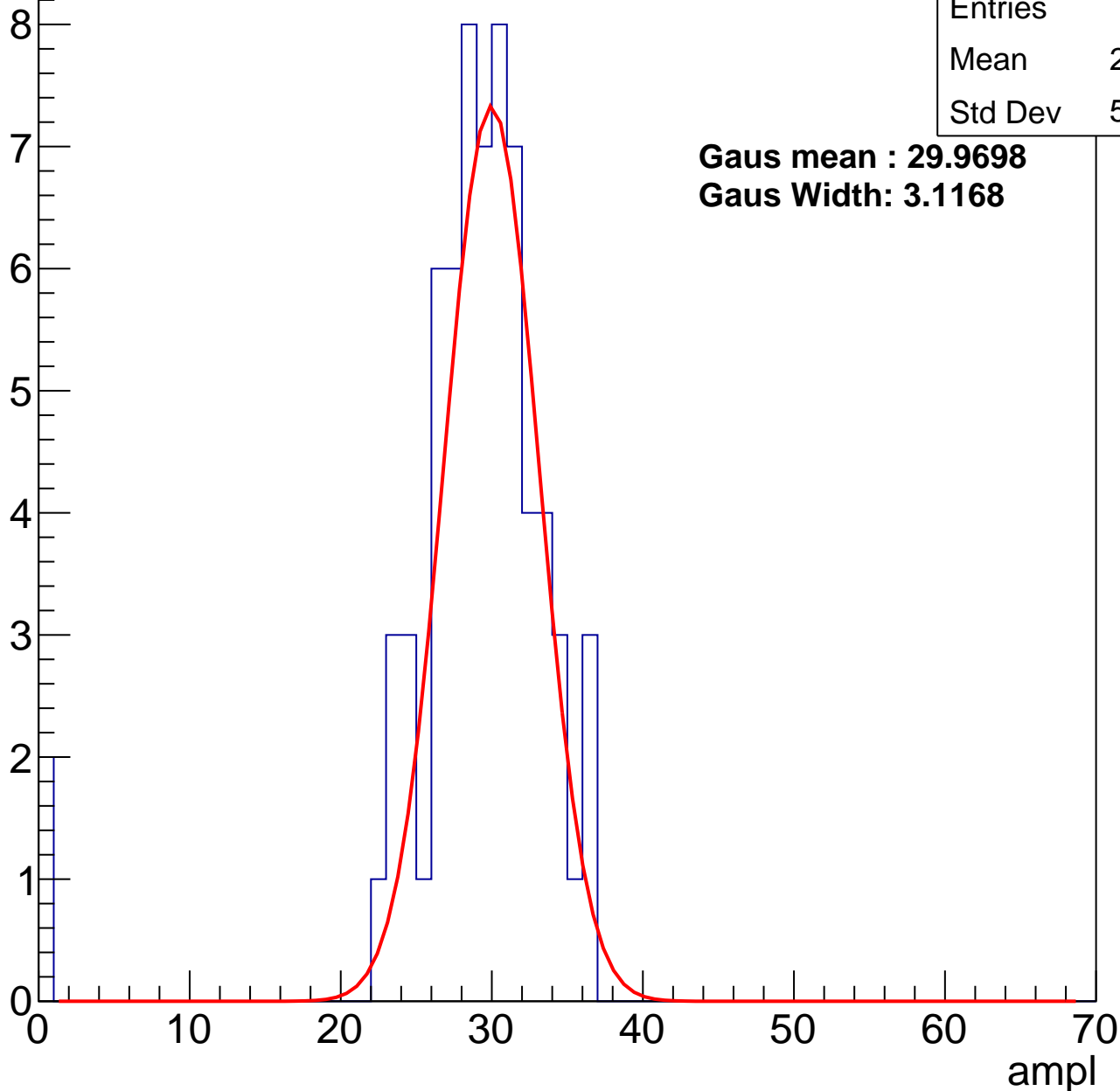
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.28
Std Dev	5.955

**Gaus mean : 29.9698**

**Gaus Width: 3.1168**



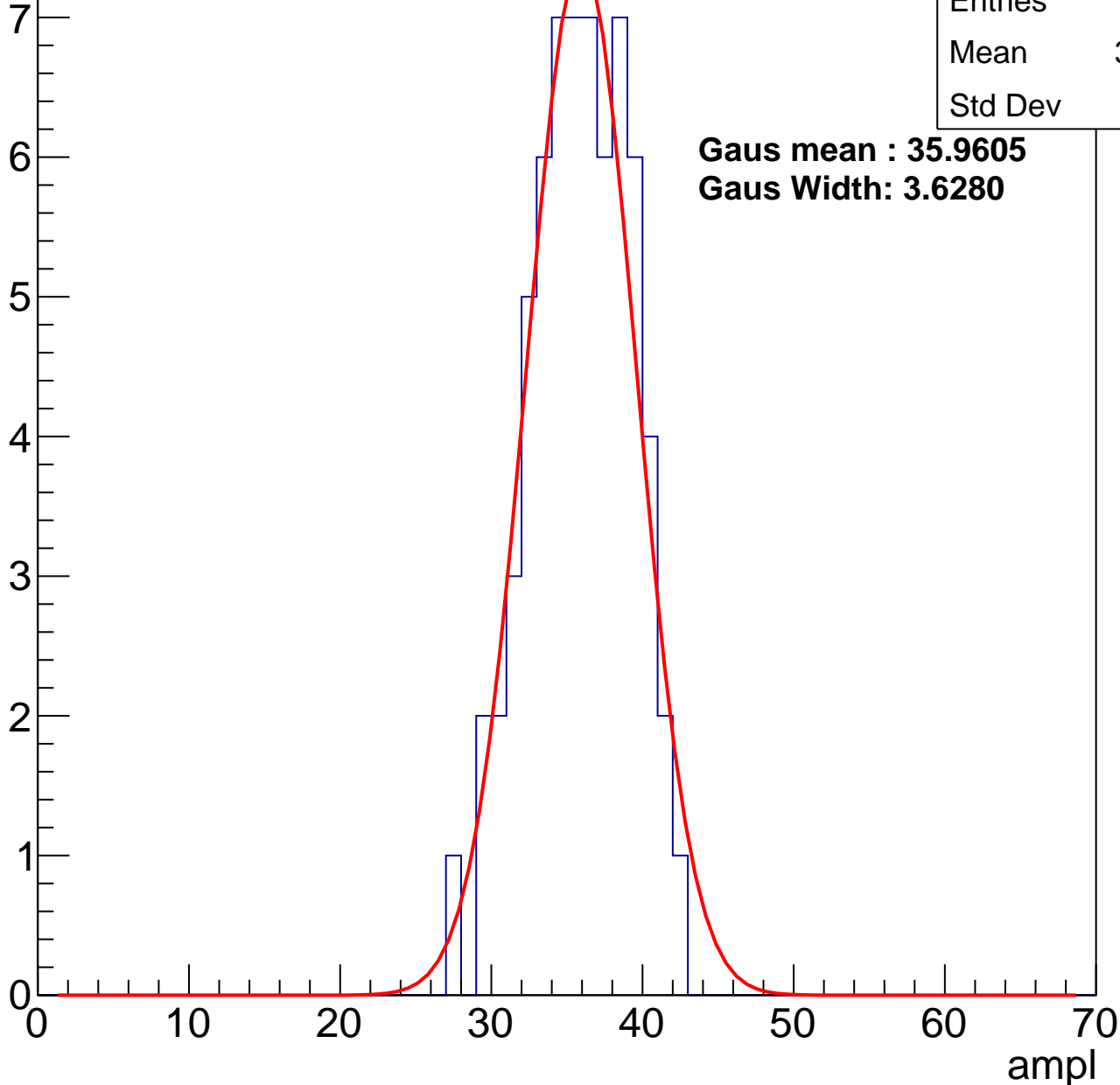
# B1L102S, U24-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.41
Std Dev	3.28

**Gaus mean : 35.9605**  
**Gaus Width: 3.6280**



# B1L102S, U24-ch114, adc2

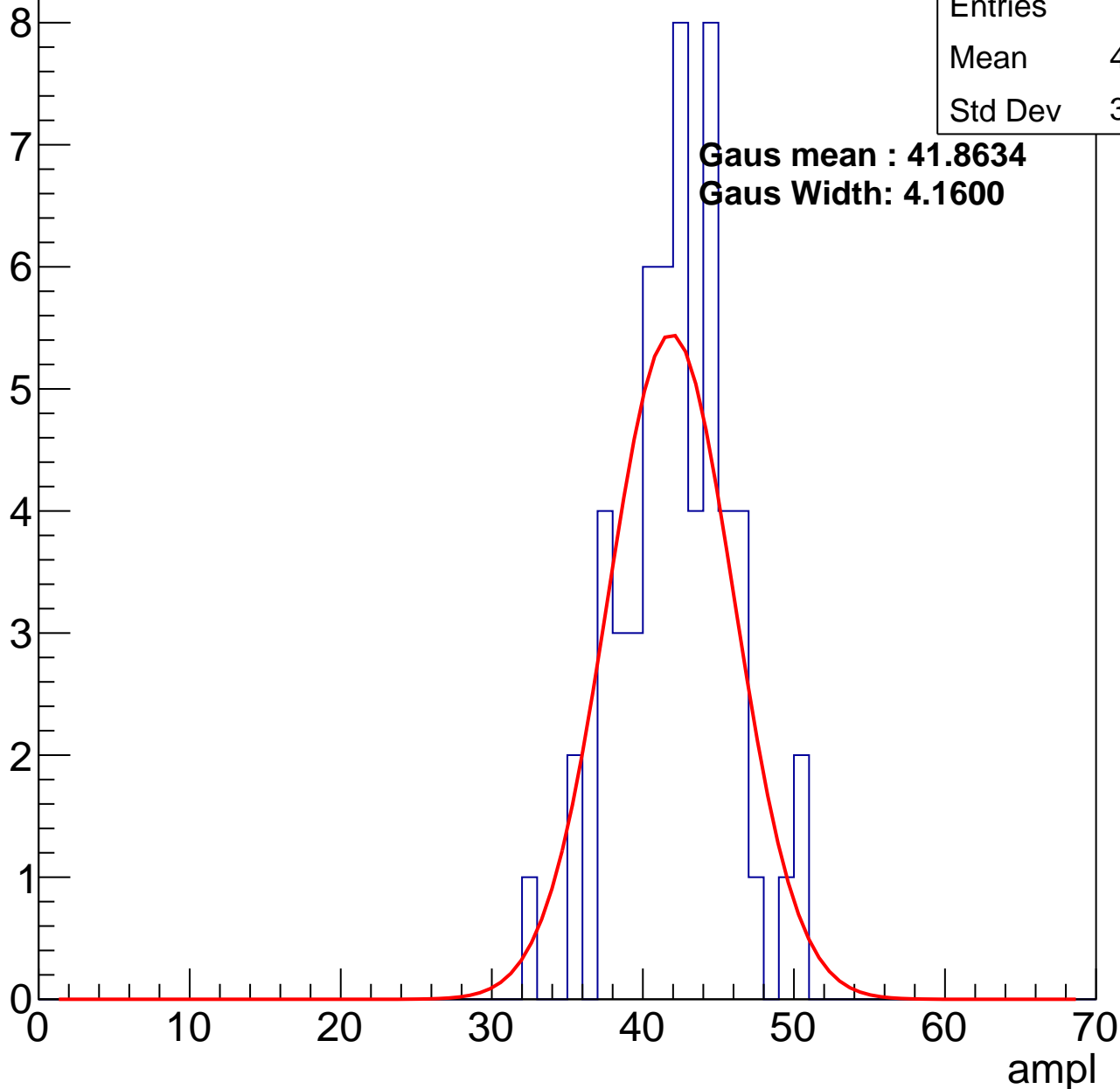
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.88
Std Dev	3.603

**Gaus mean : 41.8634**

**Gaus Width: 4.1600**

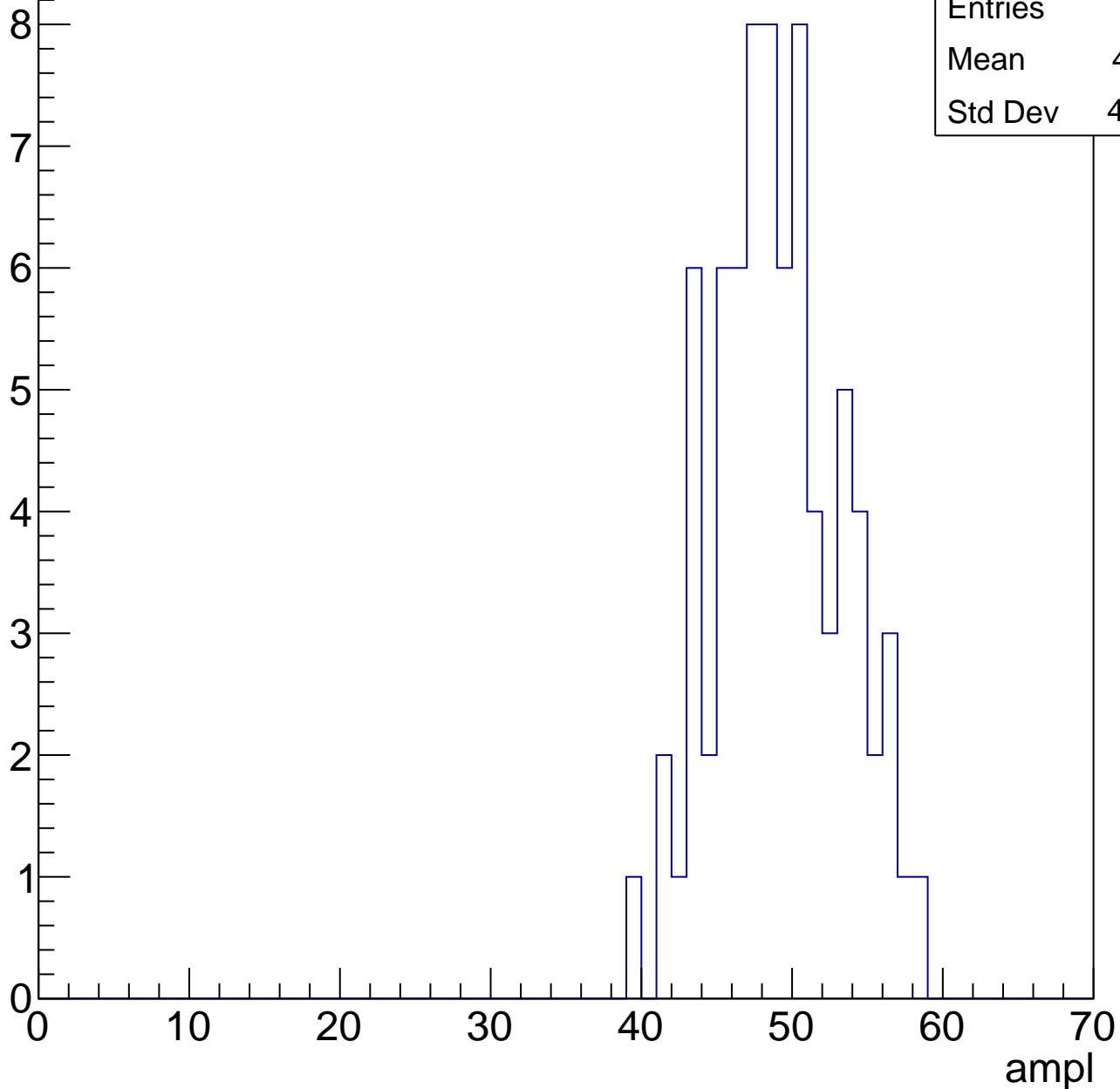


# B1L102S, U24-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.61
Std Dev	4.135

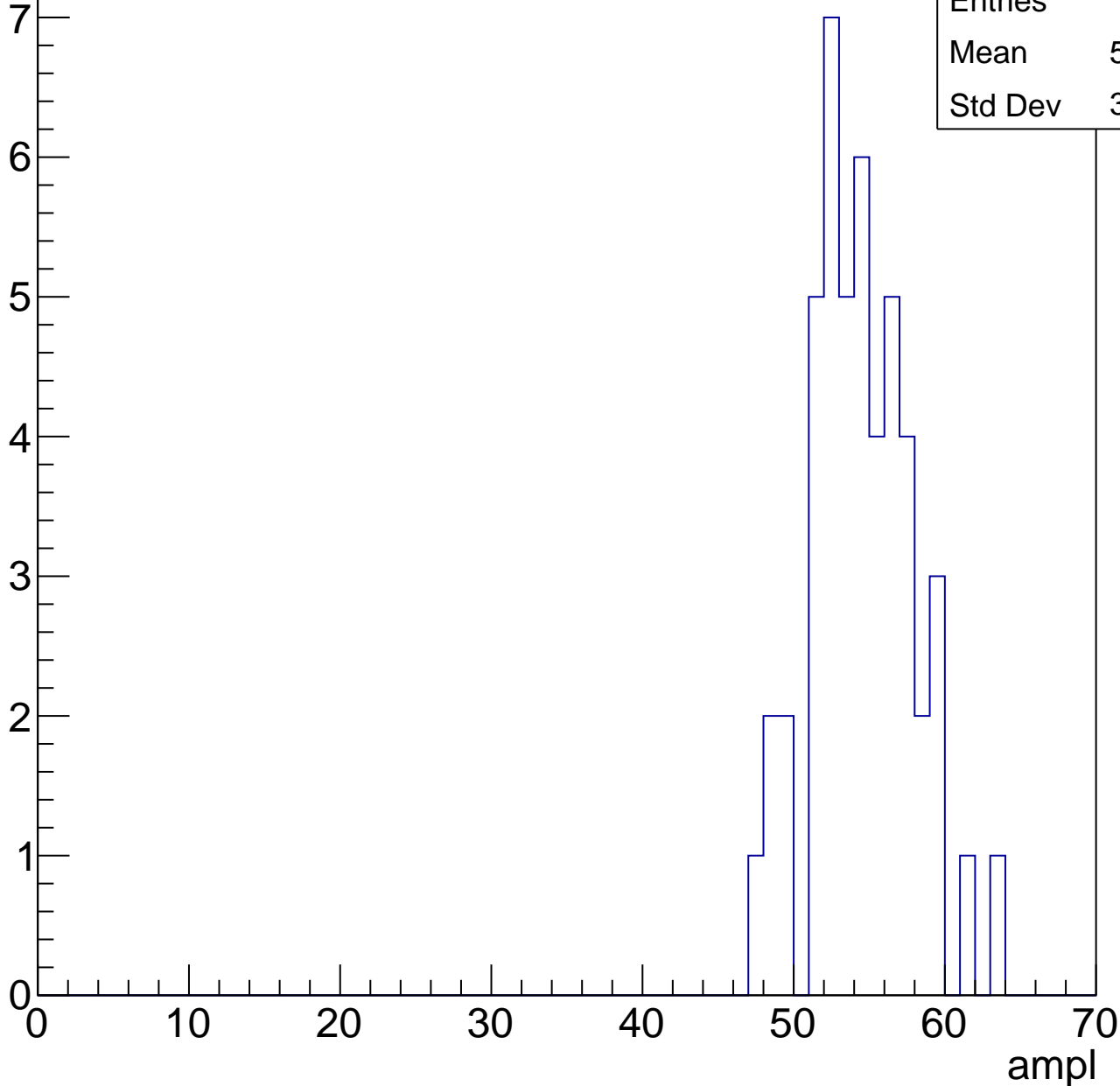


# B1L102S, U24-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.04
Std Dev	3.372

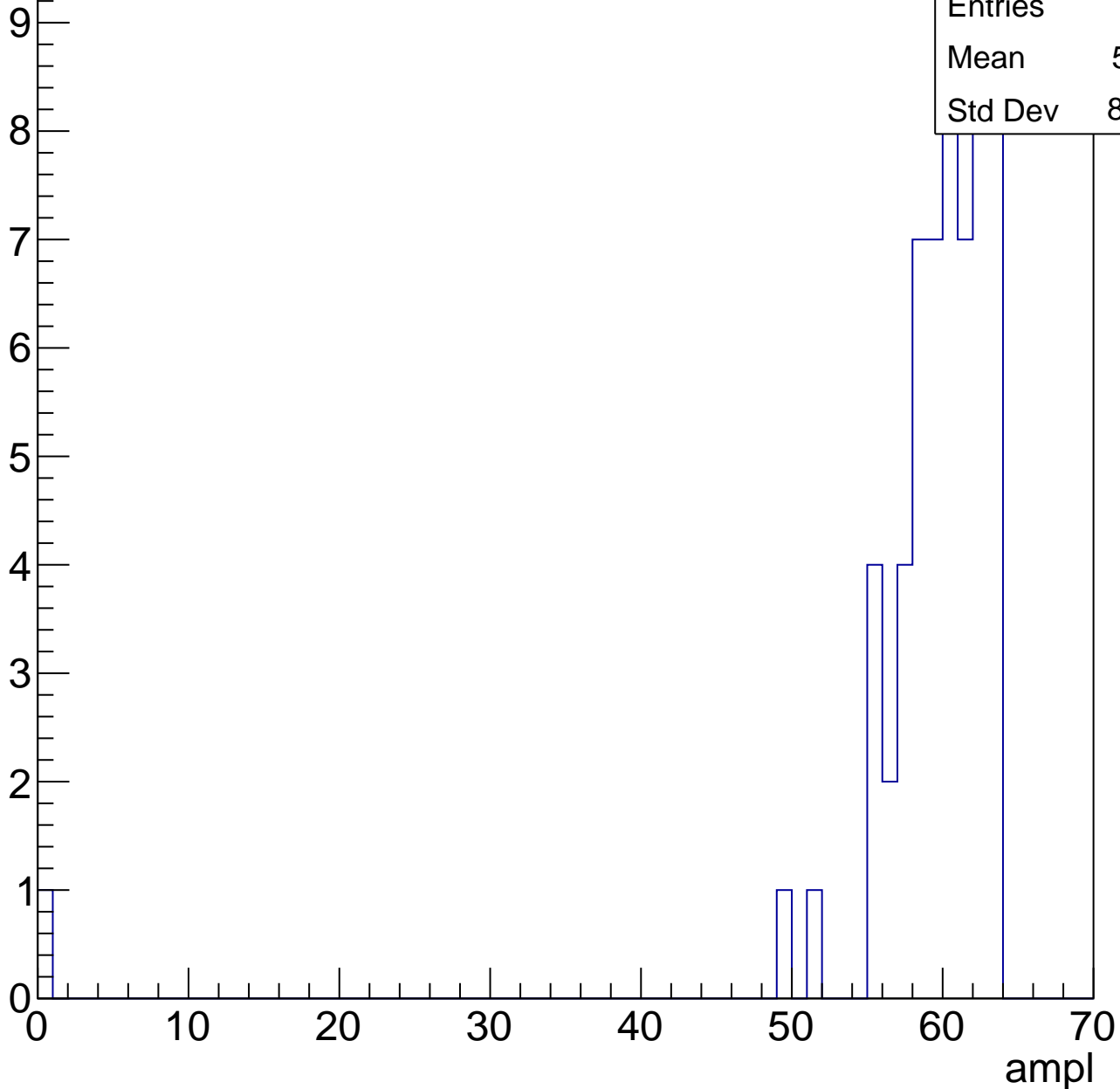


# B1L102S, U24-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.41
Std Dev	8.199



# B1L102S, U24-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

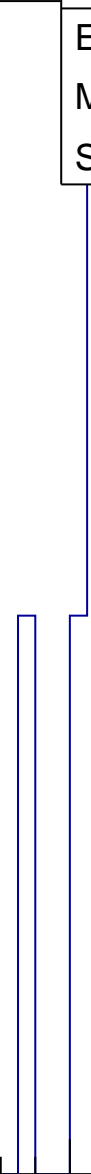
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	60.86
Std Dev	1.807

0 10 20 30 40 50 60 70

ampl





# B1L102S, U24-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U24-ch115, adc0

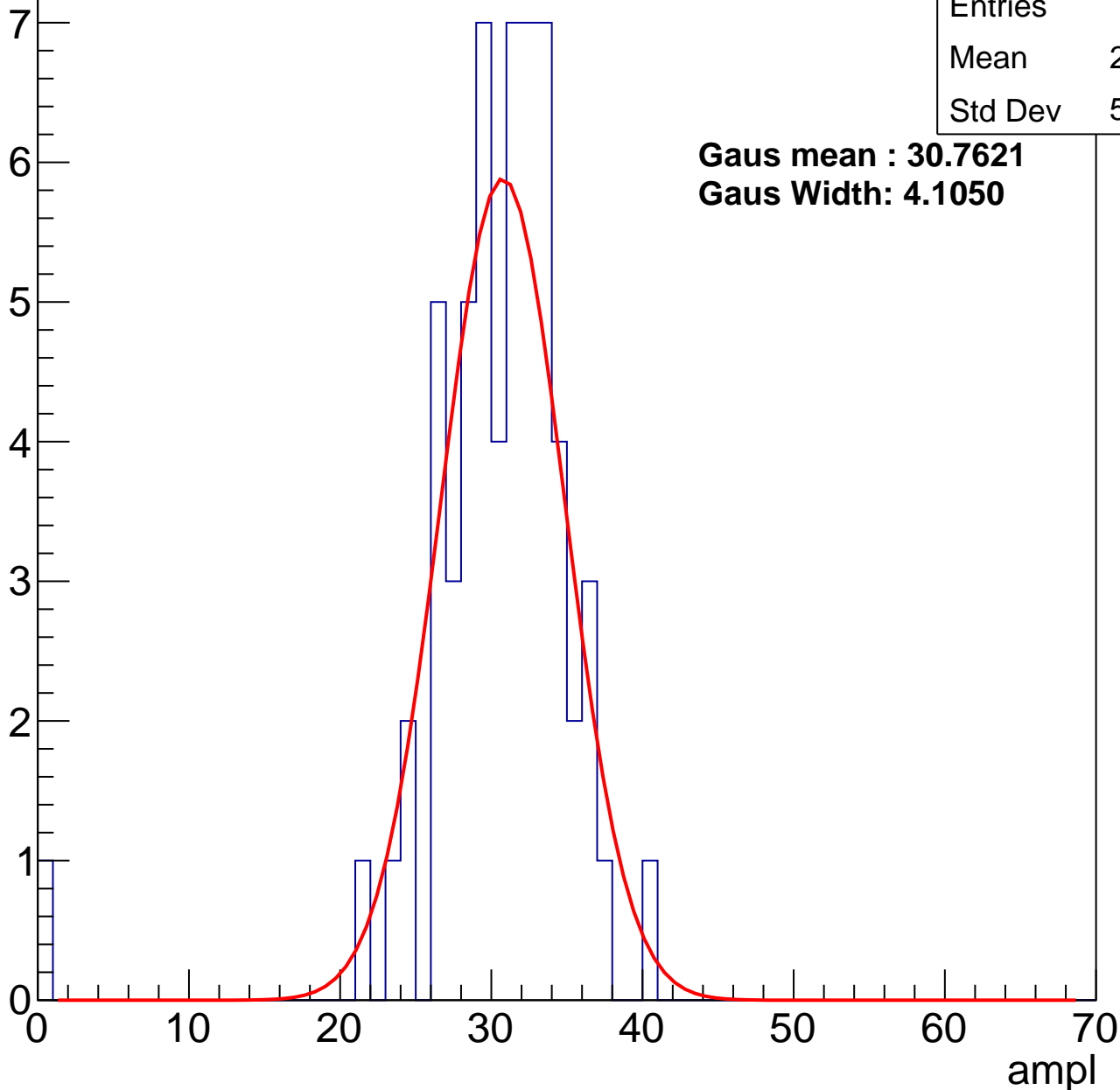
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.98
Std Dev	5.287

**Gaus mean : 30.7621**

**Gaus Width: 4.1050**



# B1L102S, U24-ch115, adc1

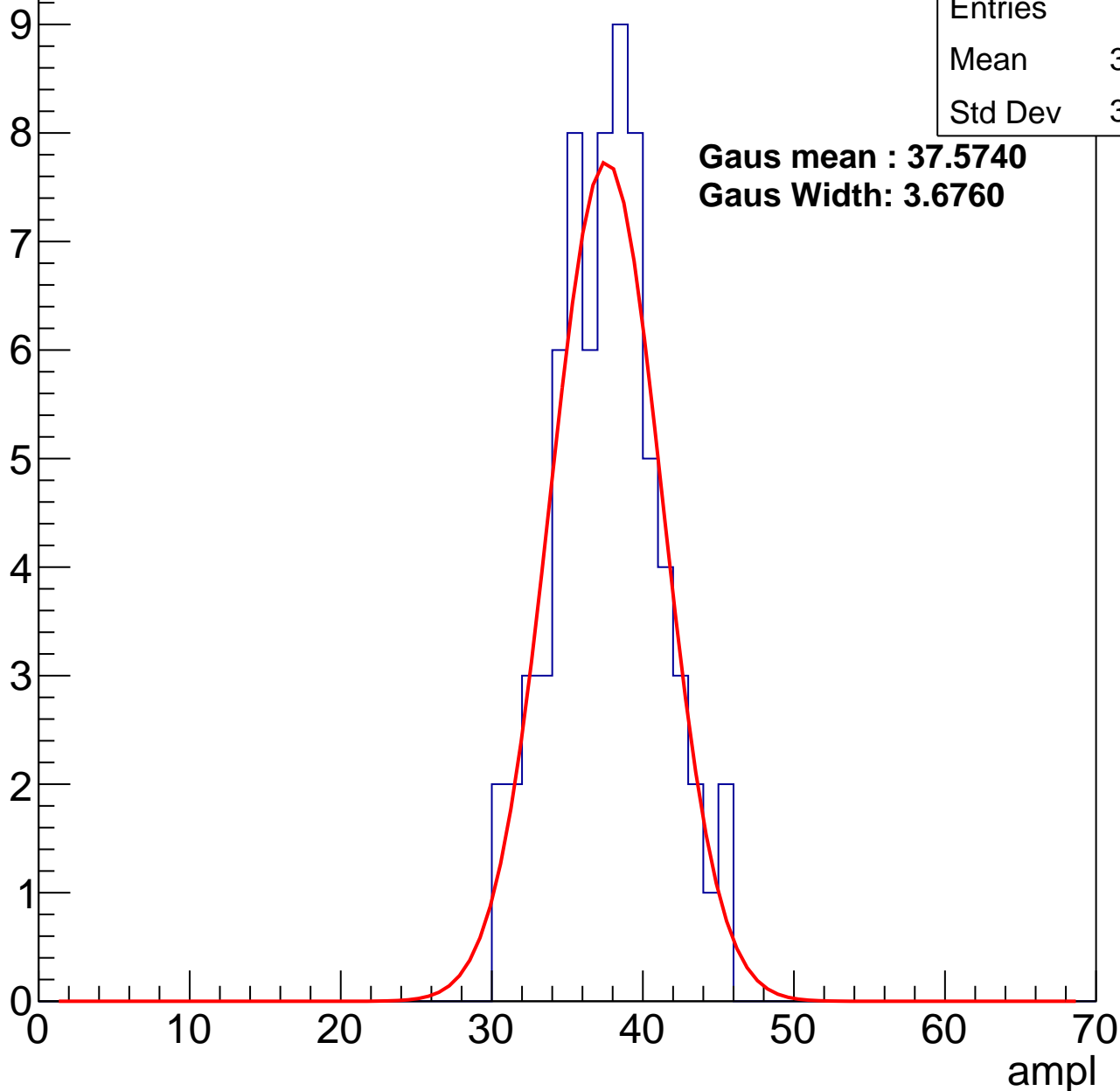
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.18
Std Dev	3.449

**Gaus mean : 37.5740**

**Gaus Width: 3.6760**



# B1L102S, U24-ch115, adc2

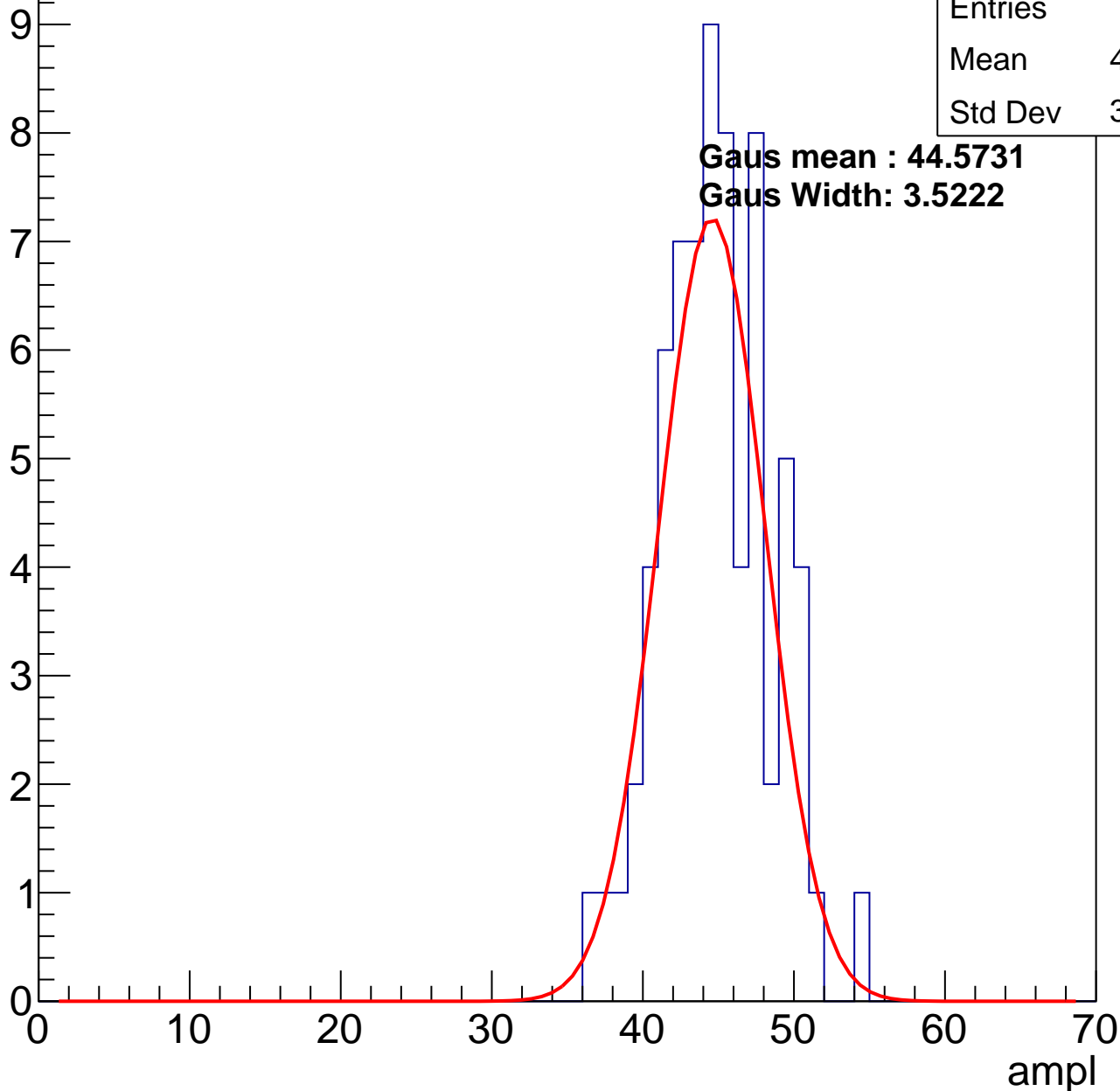
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	44.39
Std Dev	3.534

**Gaus mean : 44.5731**

**Gaus Width: 3.5222**

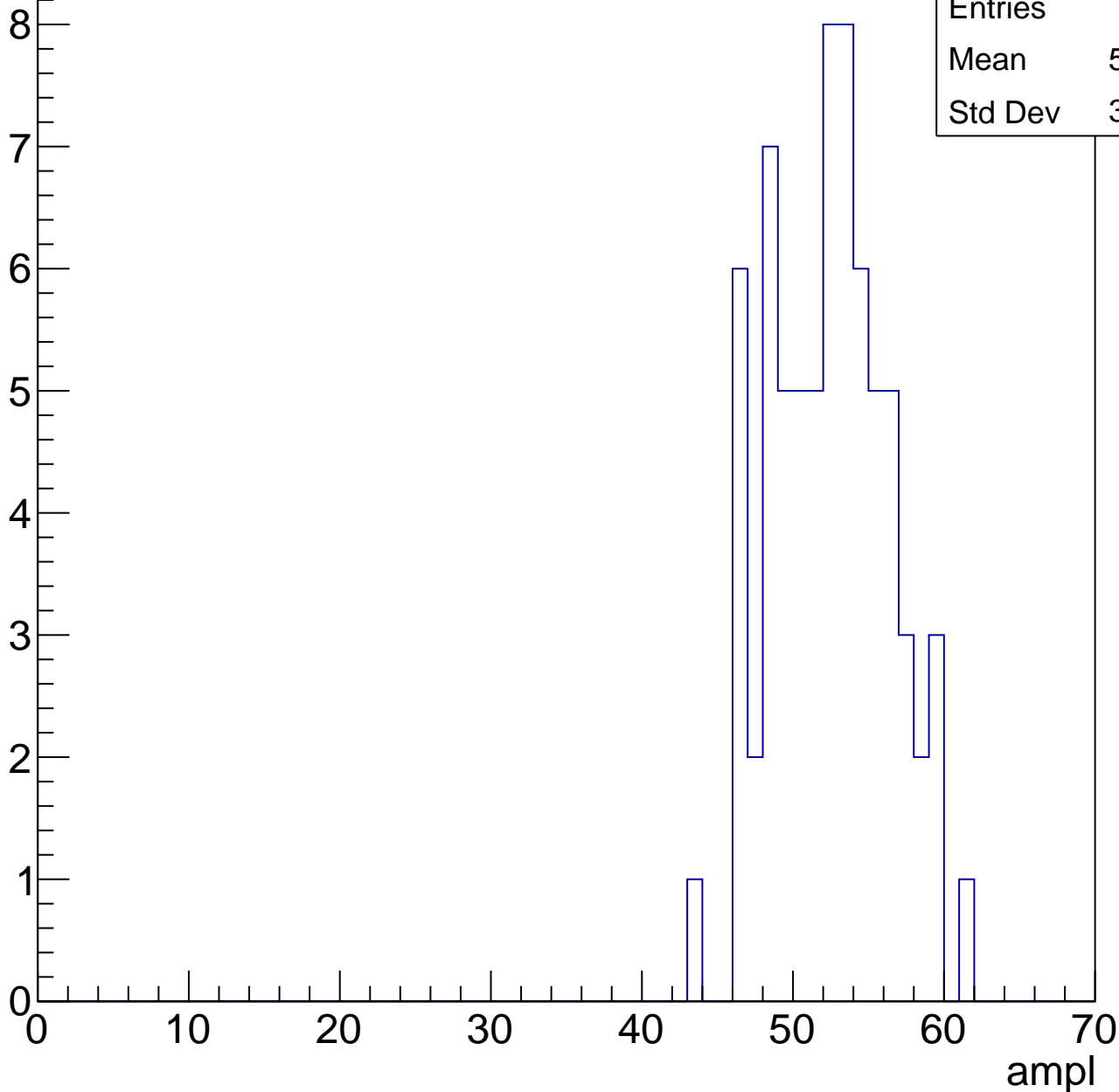


# B1L102S, U24-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	51.99
Std Dev	3.839

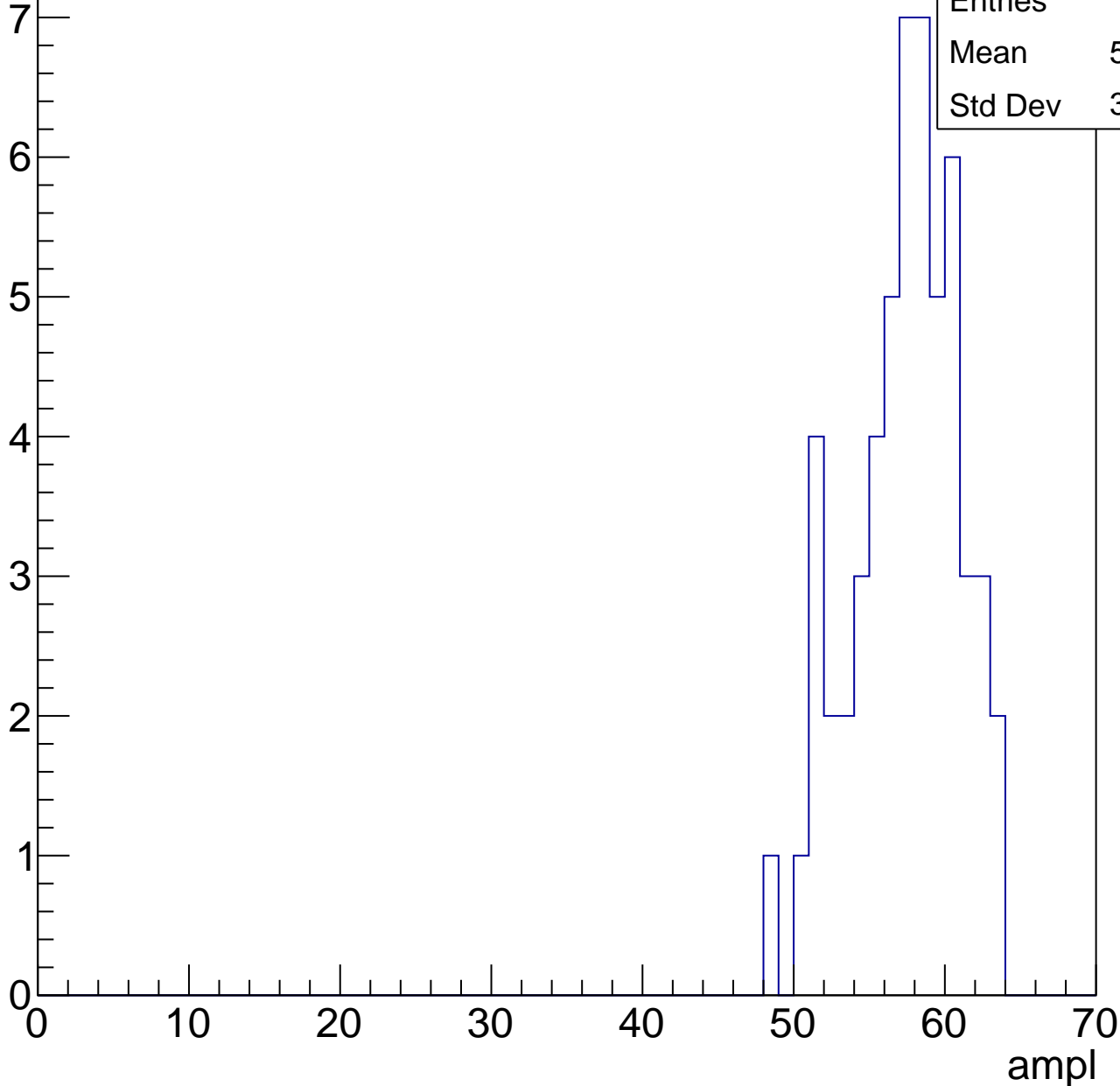


# B1L102S, U24-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.89
Std Dev	3.515

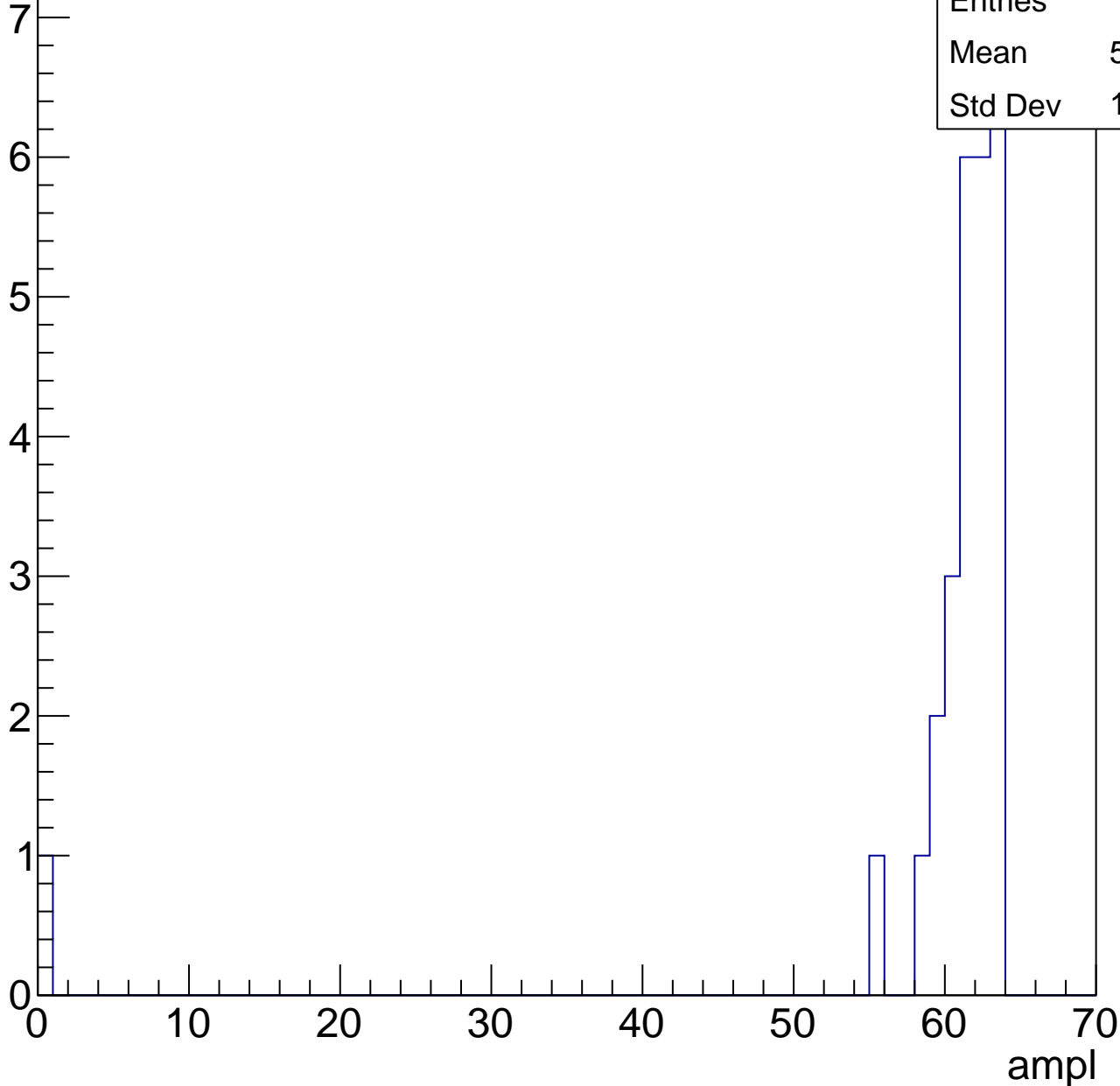


# B1L102S, U24-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	27
Mean	58.89
Std Dev	11.69



# B1L102S, U24-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch116, adc0

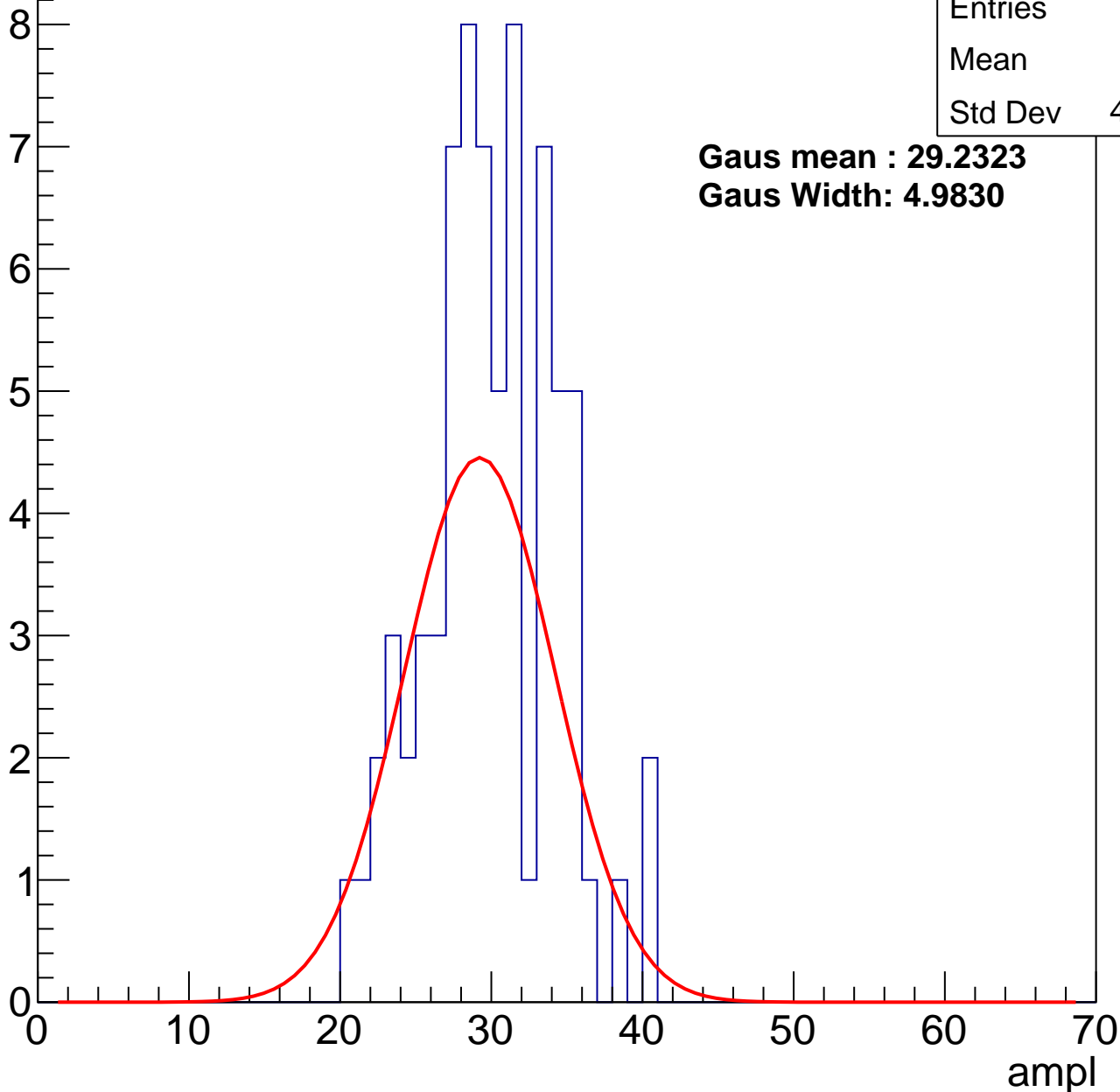
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.6
Std Dev	4.284

**Gaus mean : 29.2323**

**Gaus Width: 4.9830**



# B1L102S, U24-ch116, adc1

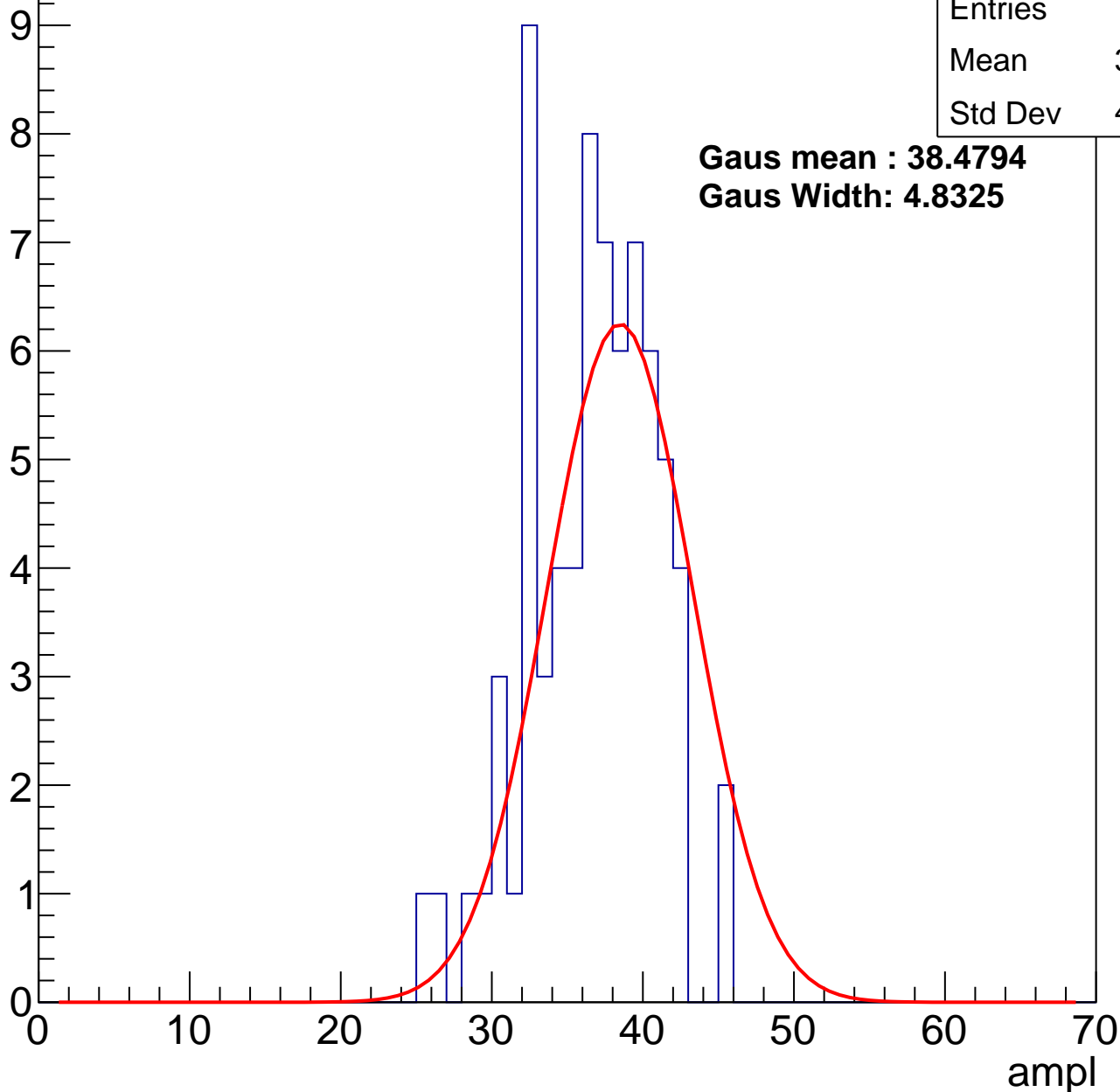
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.21
Std Dev	4.181

**Gaus mean : 38.4794**

**Gaus Width: 4.8325**



# B1L102S, U24-ch116, adc2

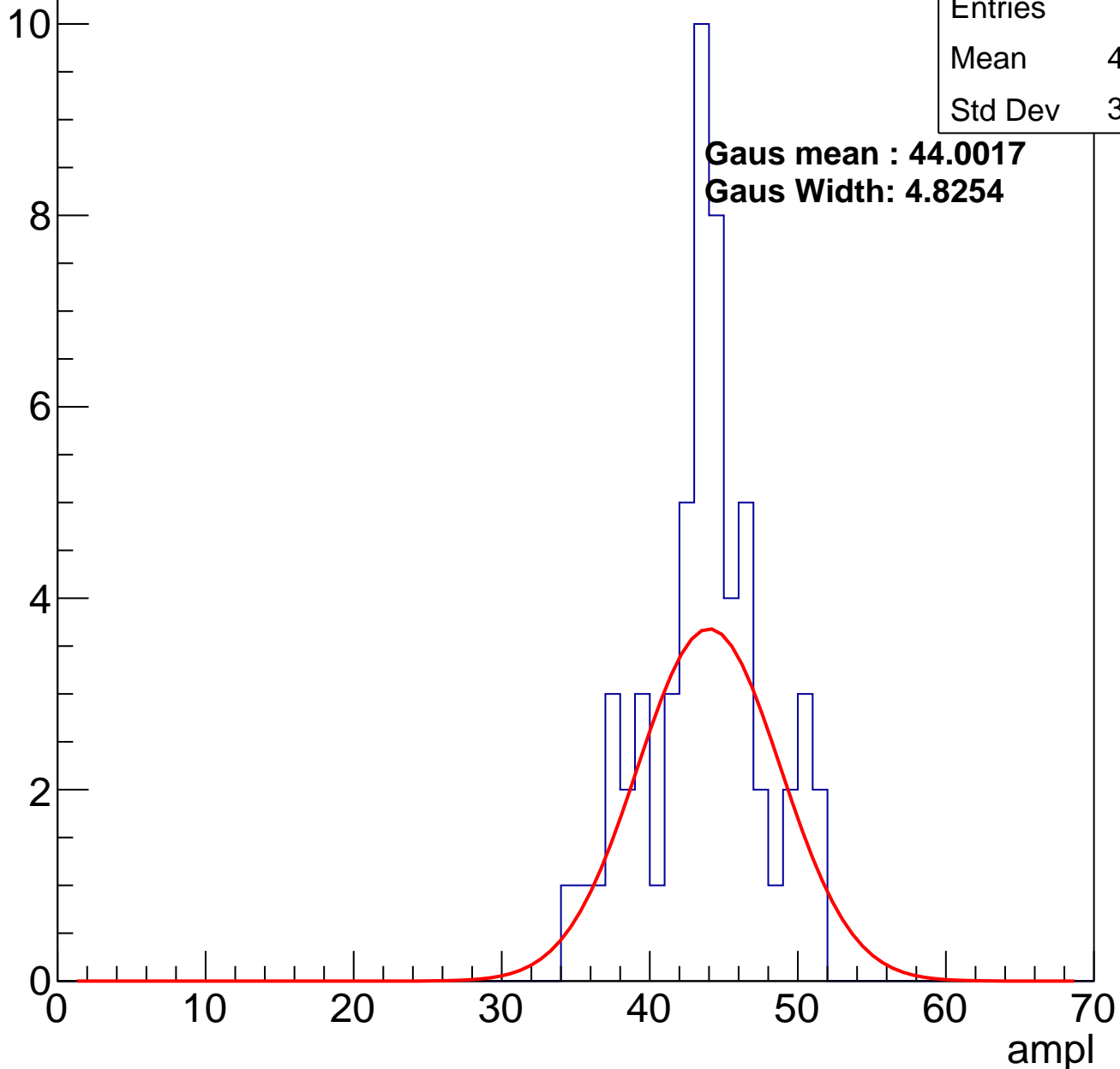
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	57
Mean	43.26
Std Dev	3.963

**Gaus mean : 44.0017**

**Gaus Width: 4.8254**

Entry

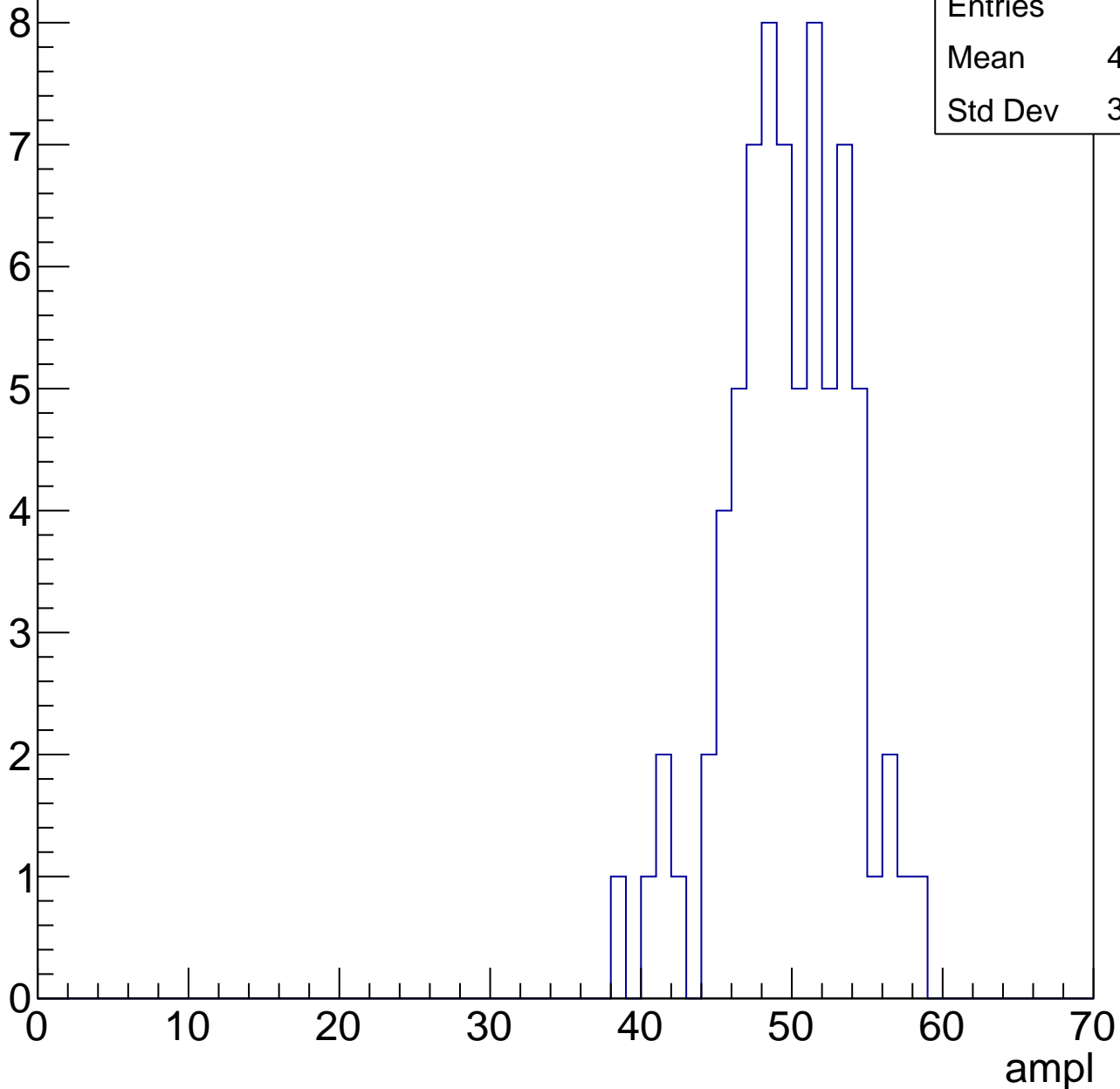


# B1L102S, U24-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.27
Std Dev	3.997

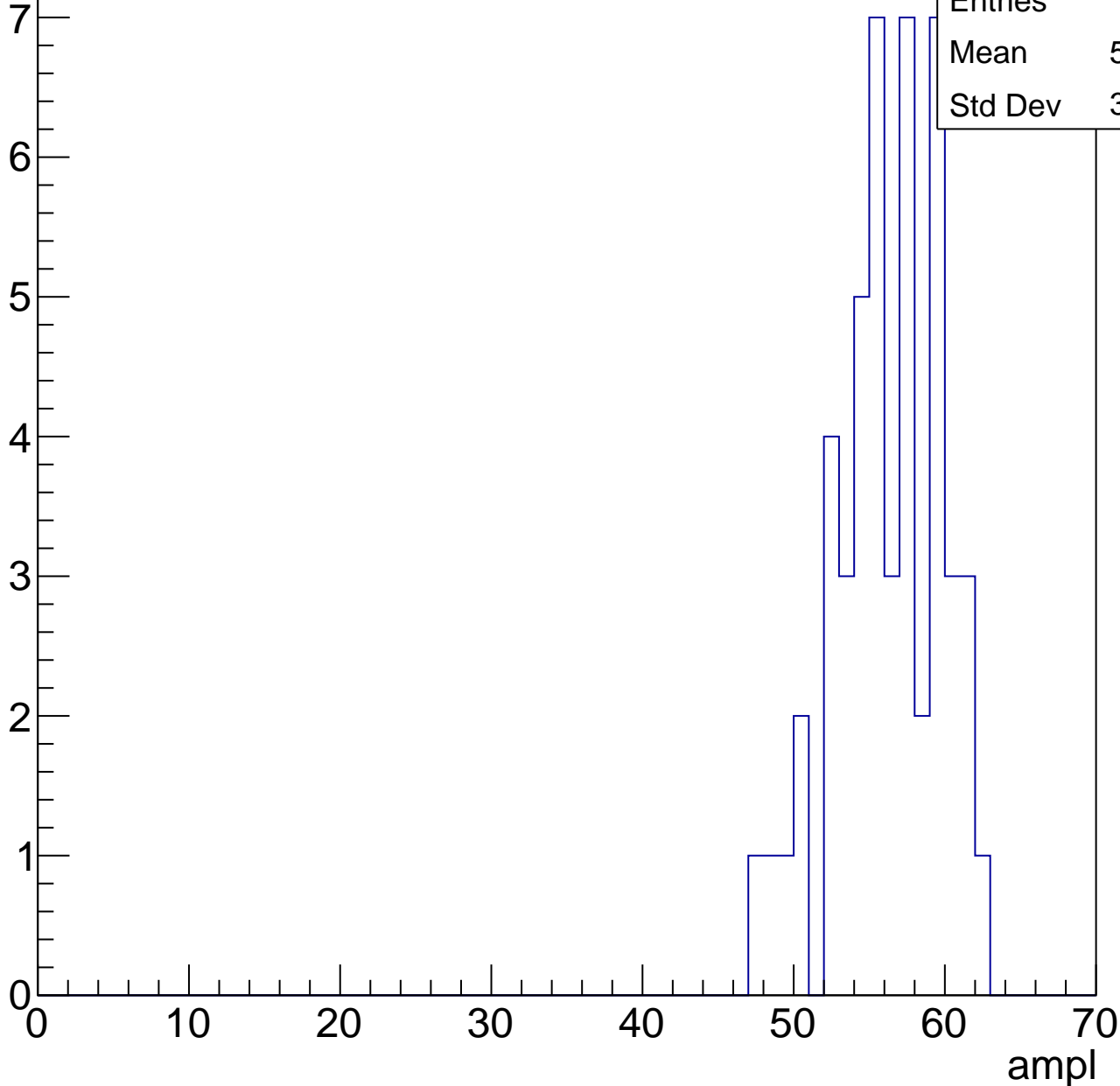


# B1L102S, U24-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.74
Std Dev	3.515

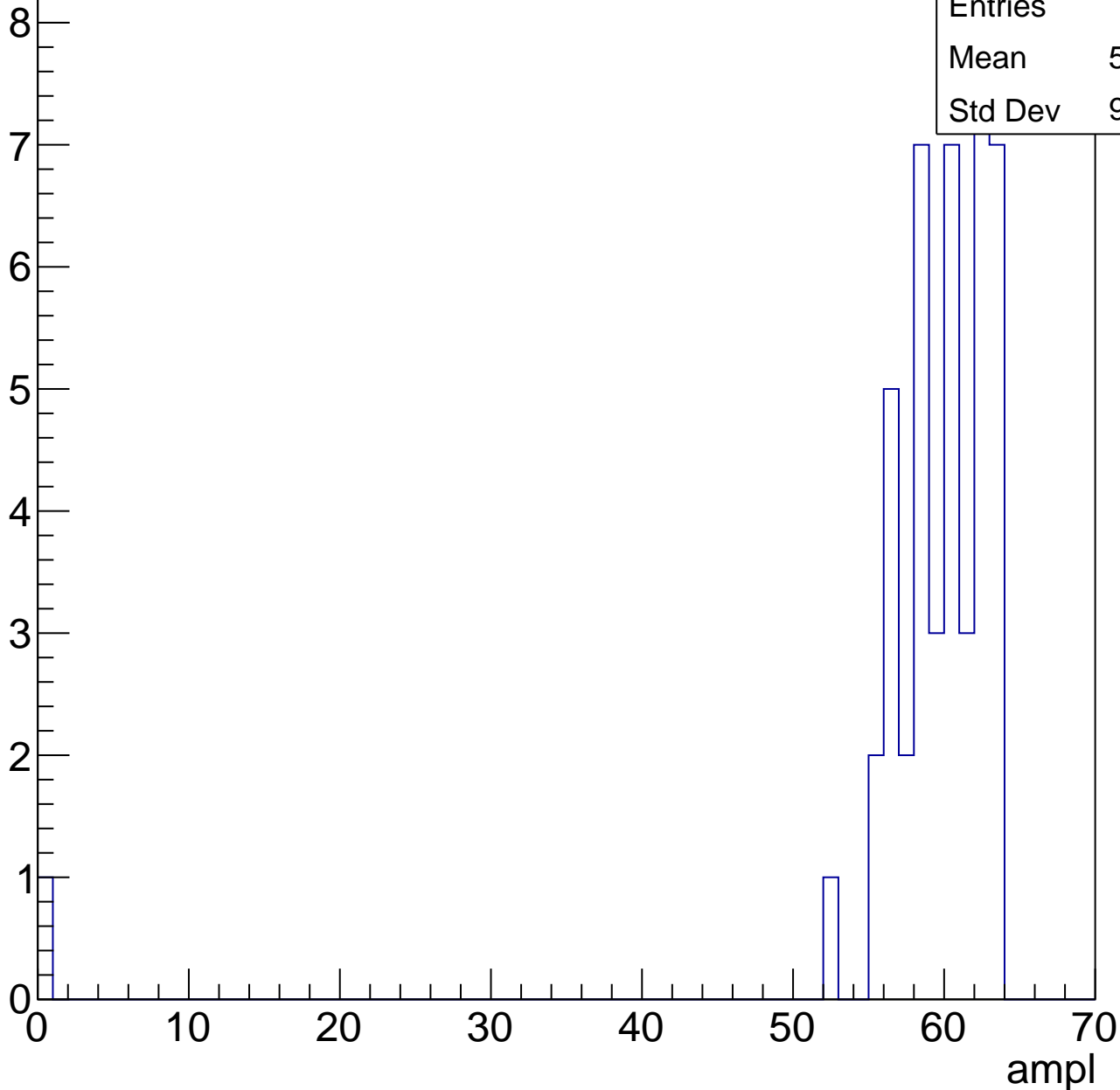


# B1L102S, U24-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

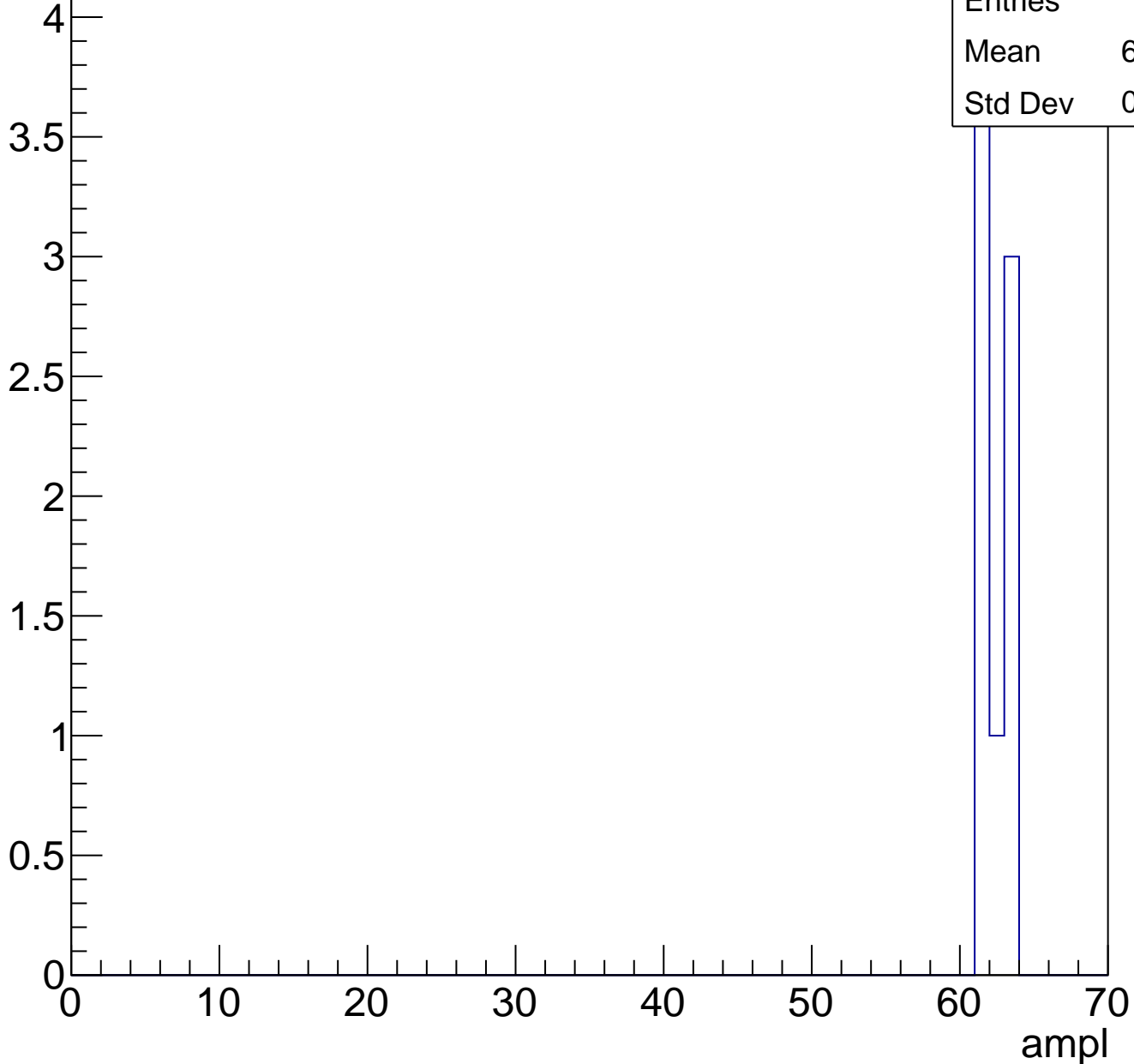
Entries	46
Mean	58.24
Std Dev	9.087



# B1L102S, U24-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

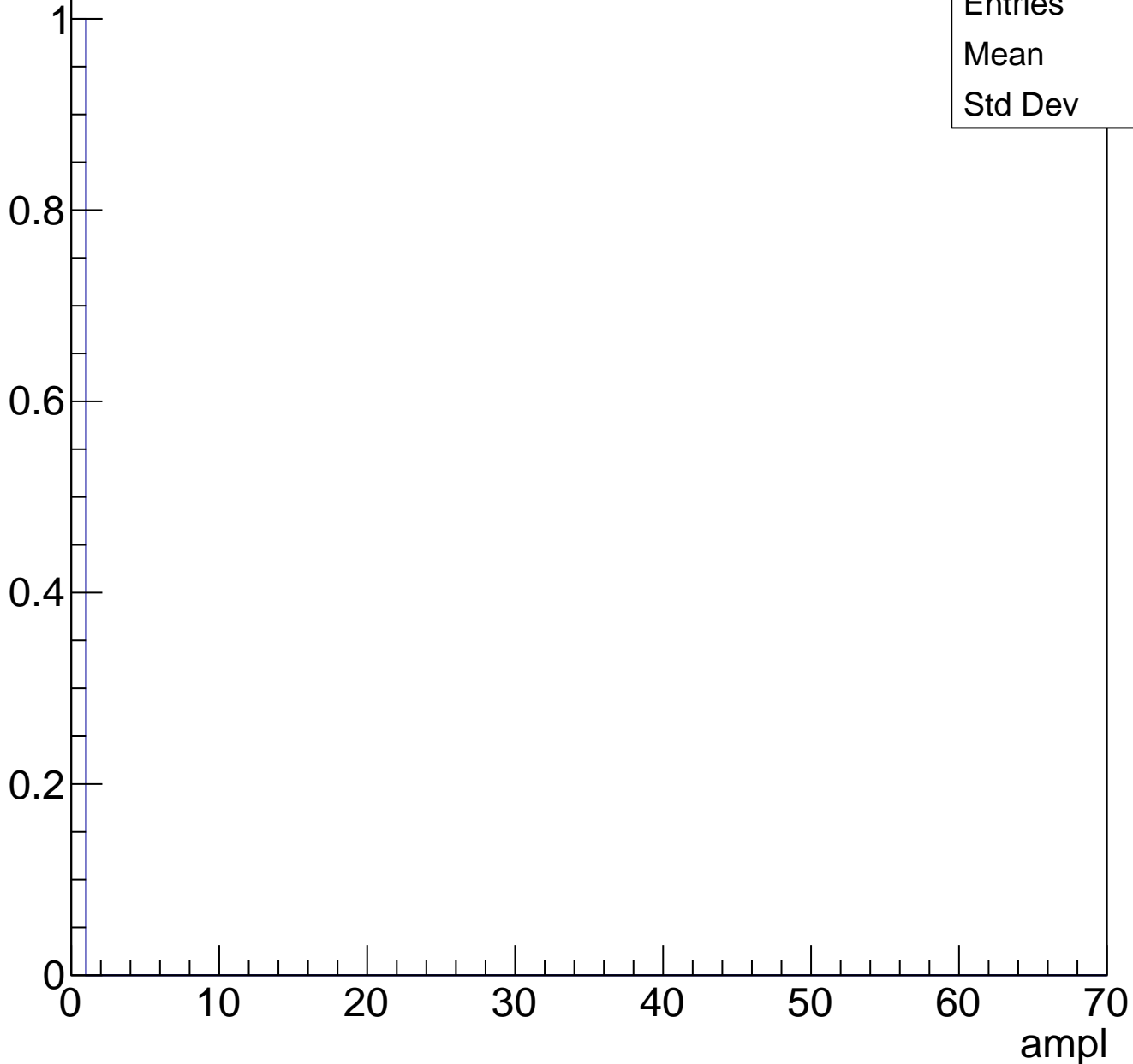




# B1L102S, U24-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch117, adc0

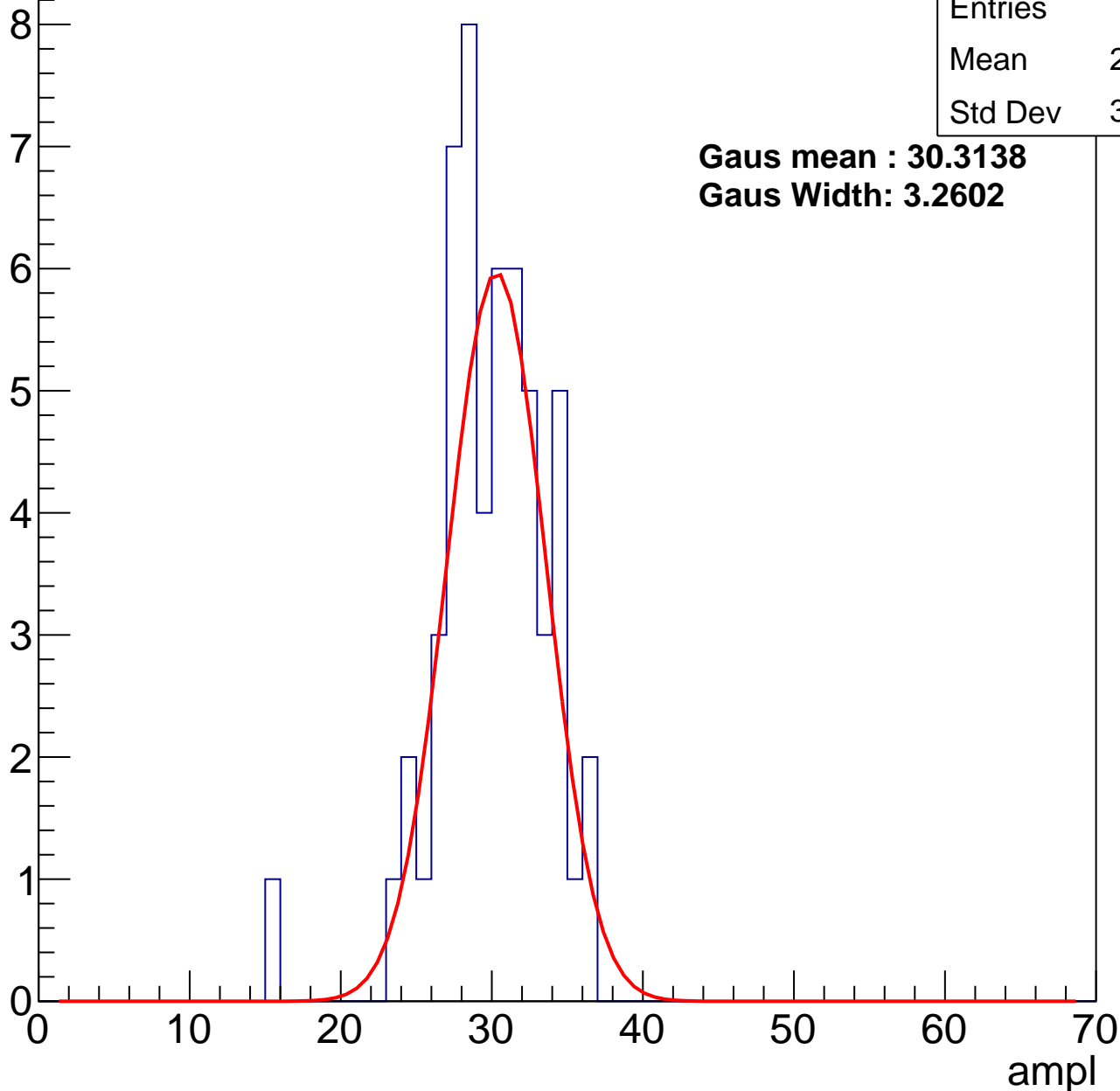
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	29.45
Std Dev	3.647

**Gaus mean : 30.3138**

**Gaus Width: 3.2602**



# B1L102S, U24-ch117, adc1

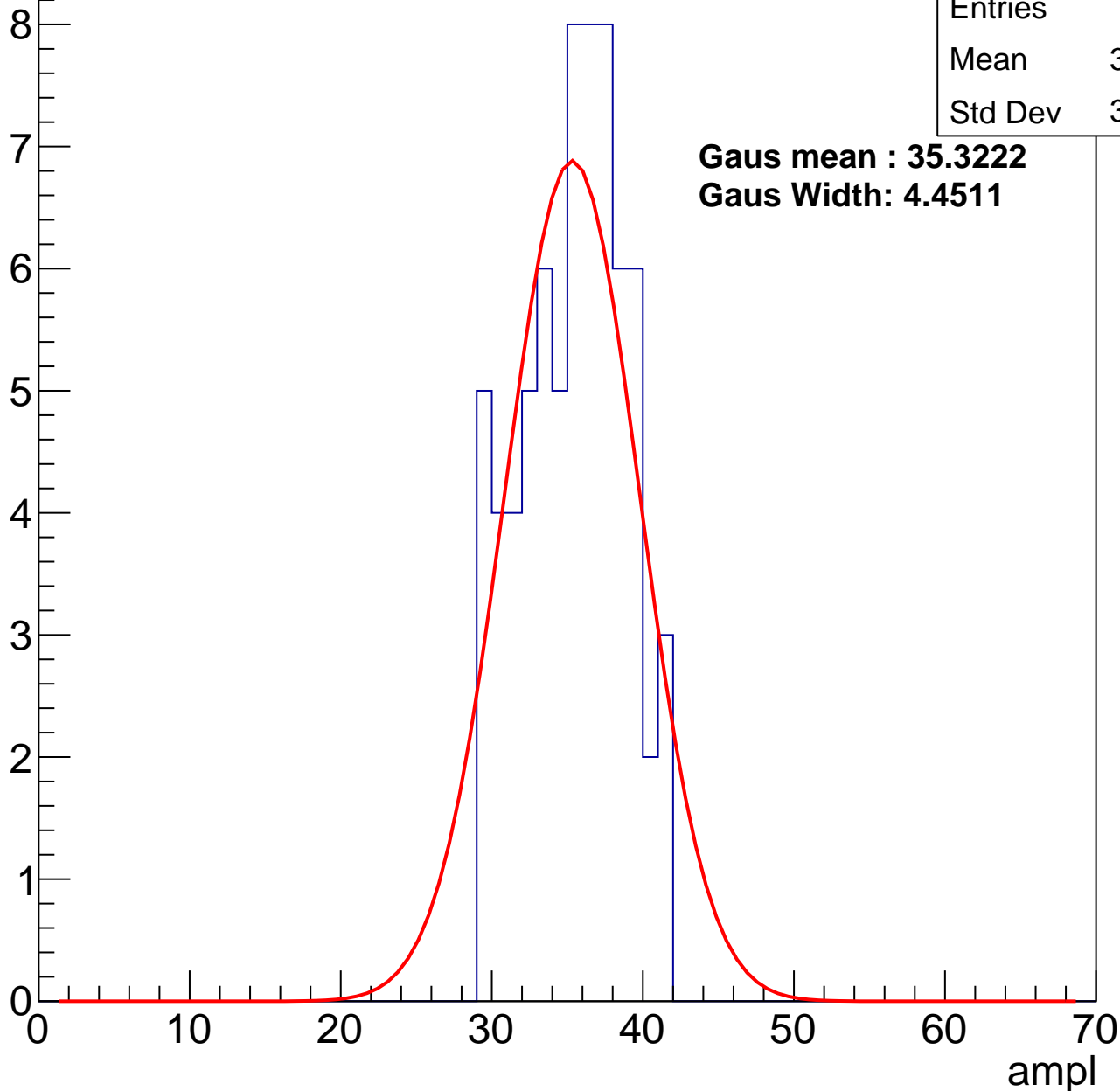
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.94
Std Dev	3.308

**Gaus mean : 35.3222**

**Gaus Width: 4.4511**



# B1L102S, U24-ch117, adc2

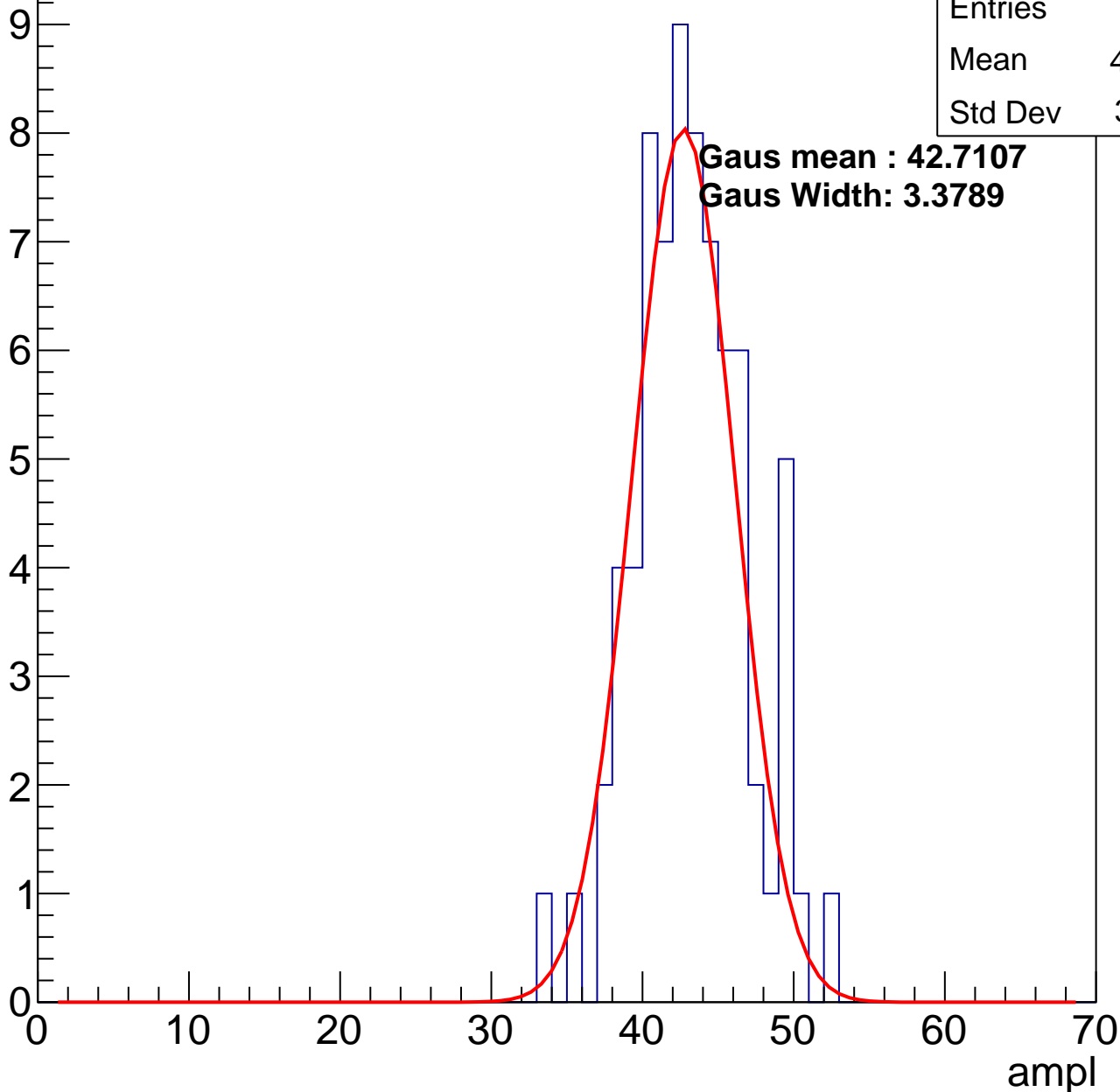
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.77
Std Dev	3.621

**Gaus mean : 42.7107**

**Gaus Width: 3.3789**

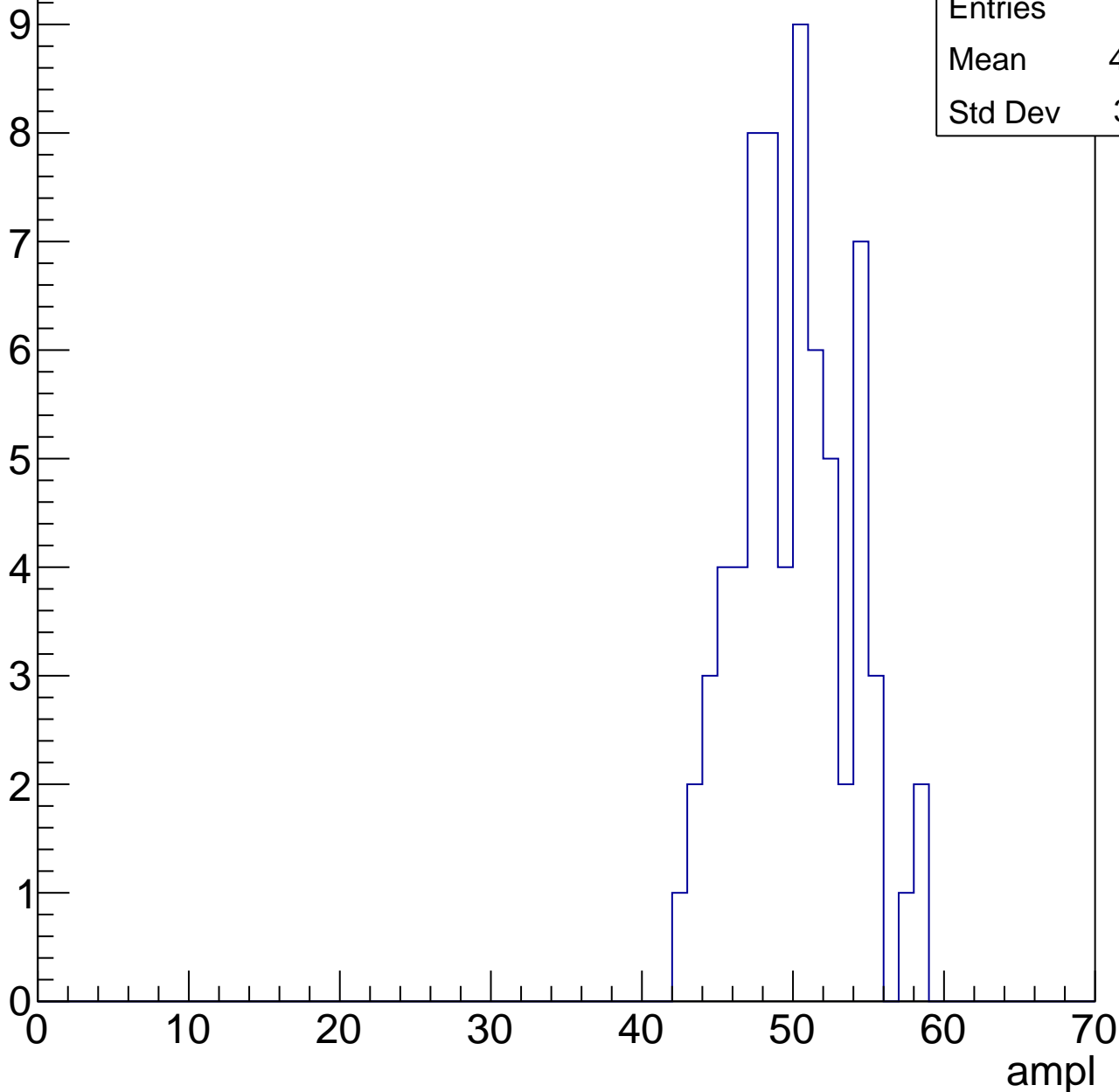


# B1L102S, U24-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.54
Std Dev	3.681

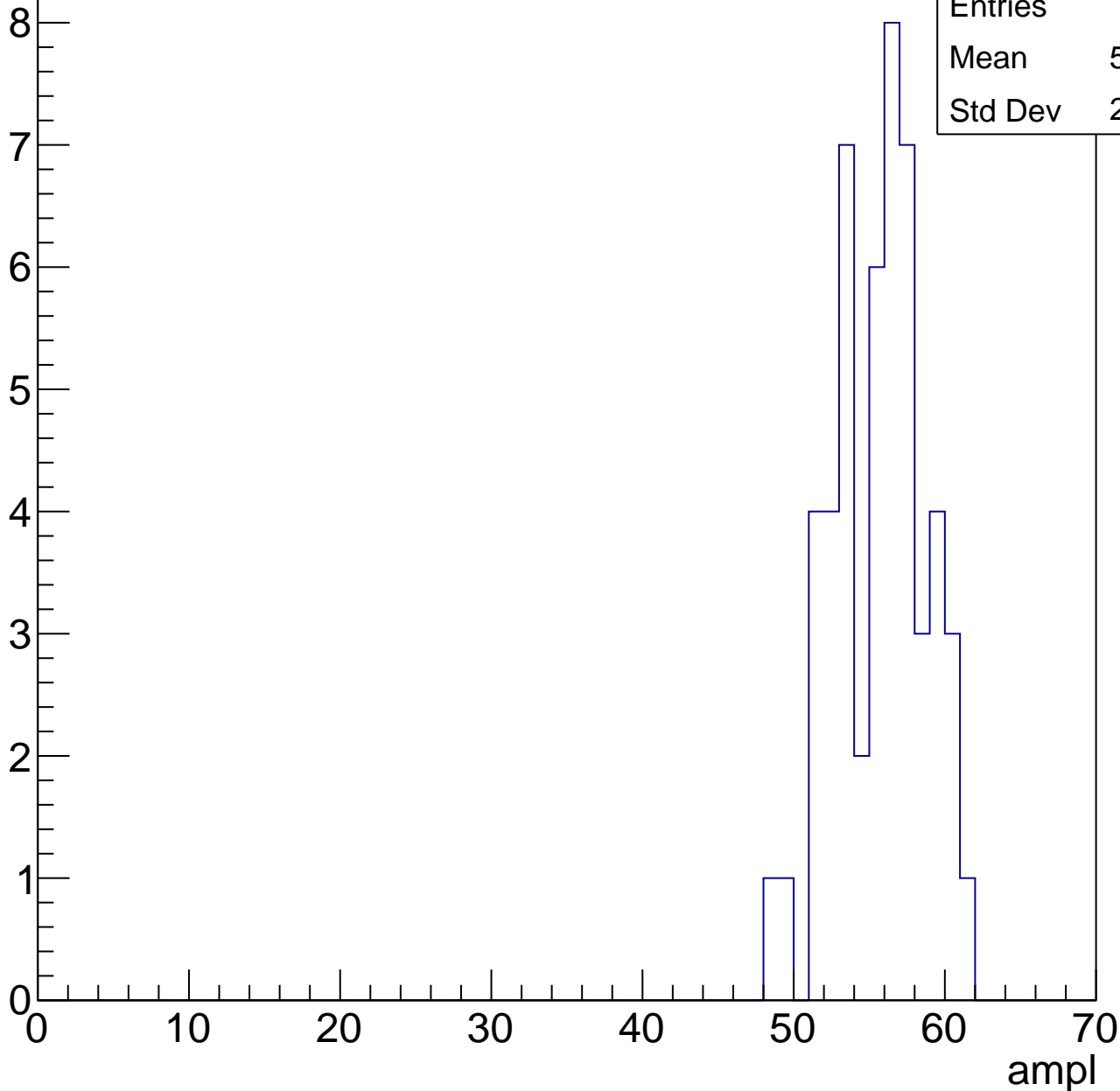


# B1L102S, U24-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	55.22
Std Dev	2.973

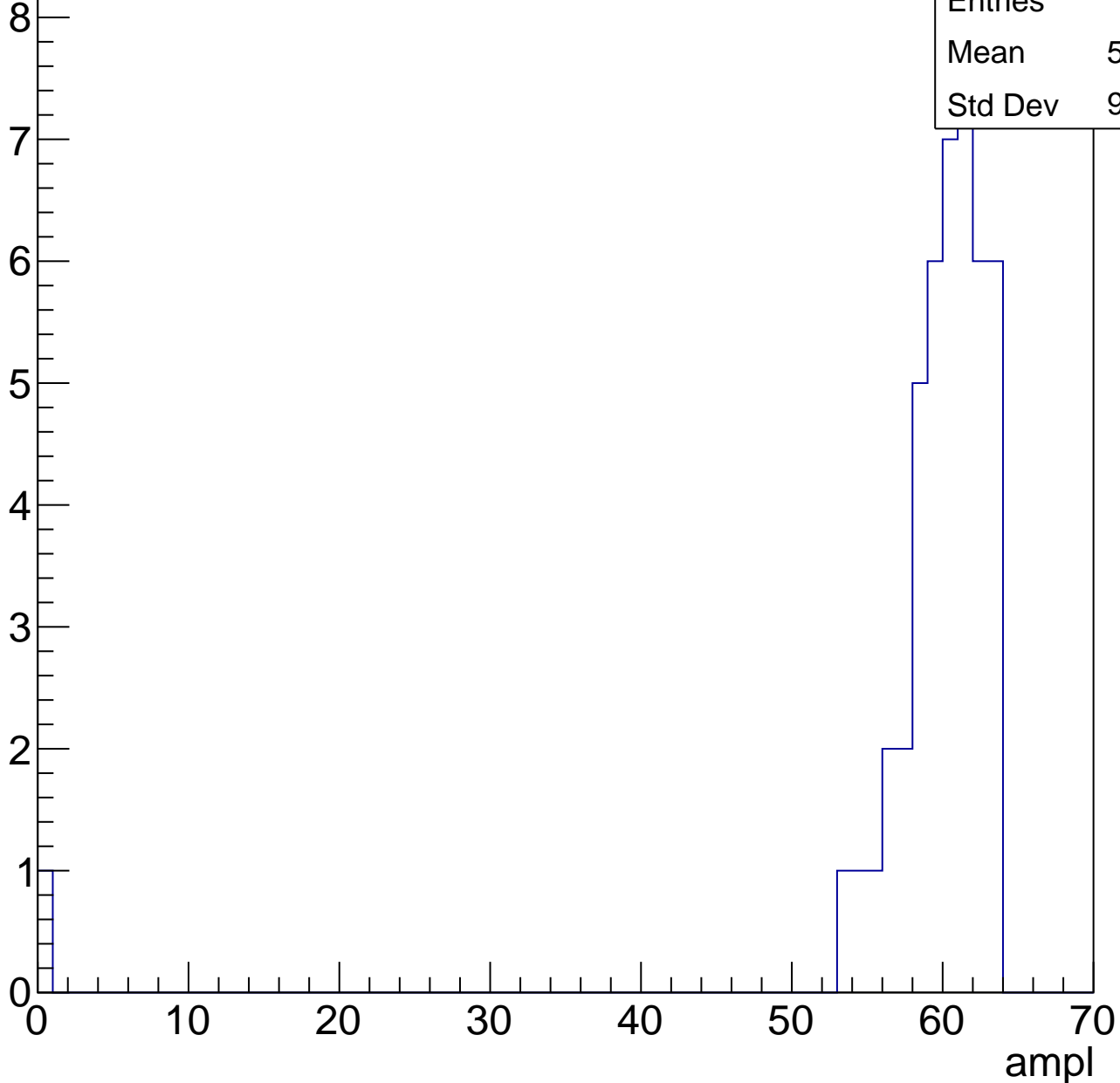


# B1L102S, U24-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

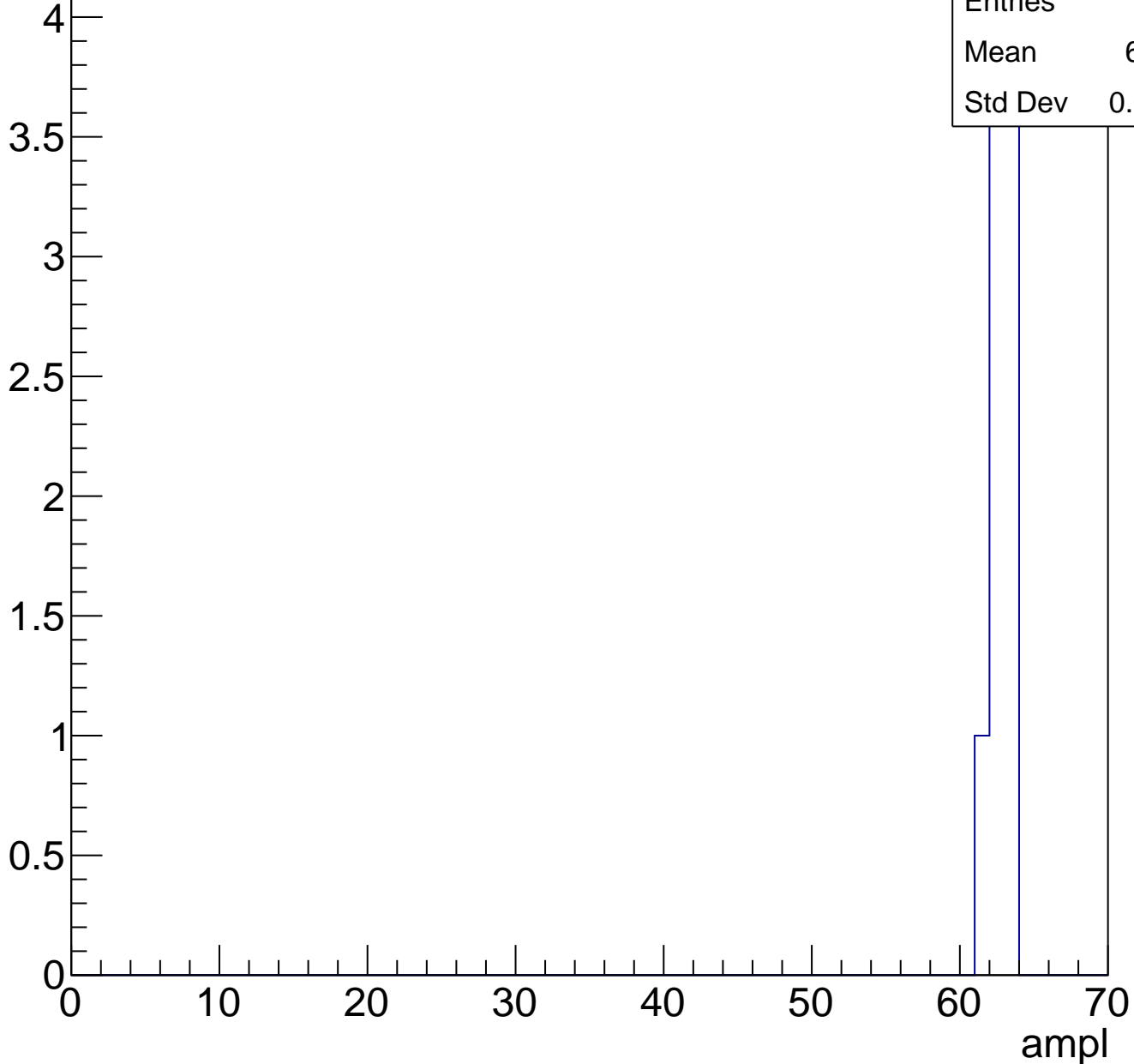
Entries	46
Mean	58.48
Std Dev	9.048



# B1L102S, U24-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

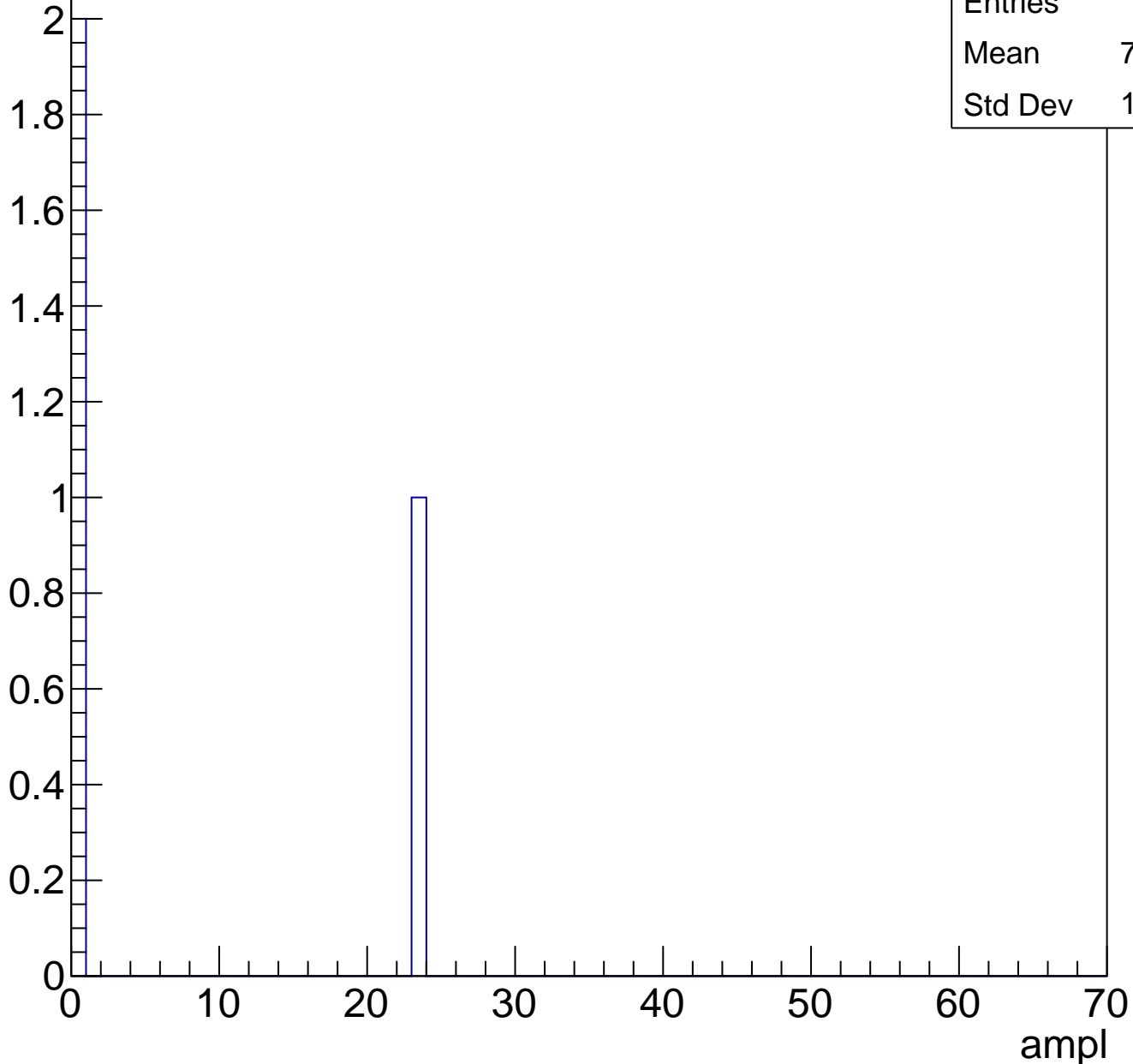




# B1L102S, U24-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B1L102S, U24-ch118, adc0

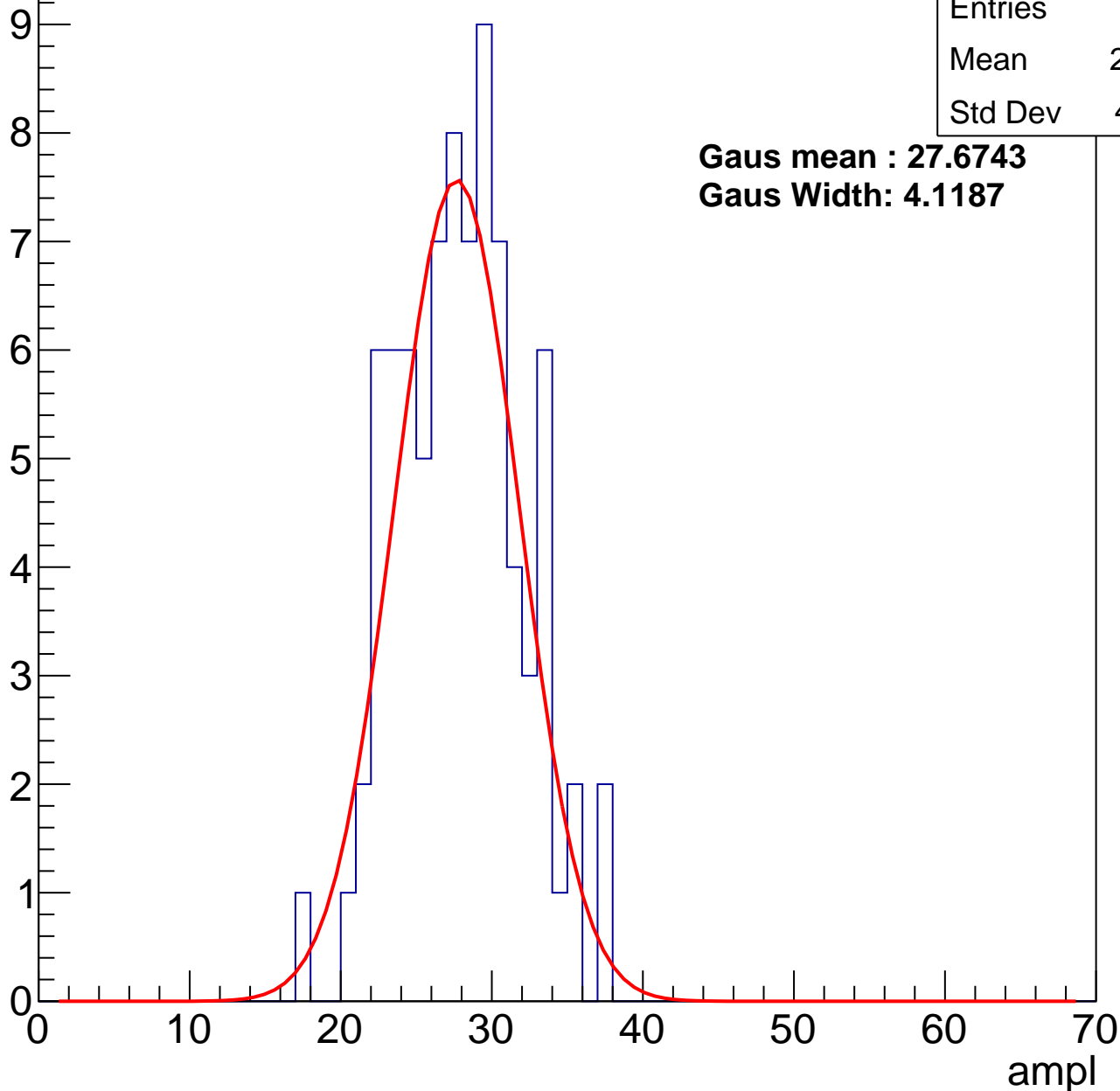
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.46
Std Dev	4.061

**Gaus mean : 27.6743**

**Gaus Width: 4.1187**



# B1L102S, U24-ch118, adc1

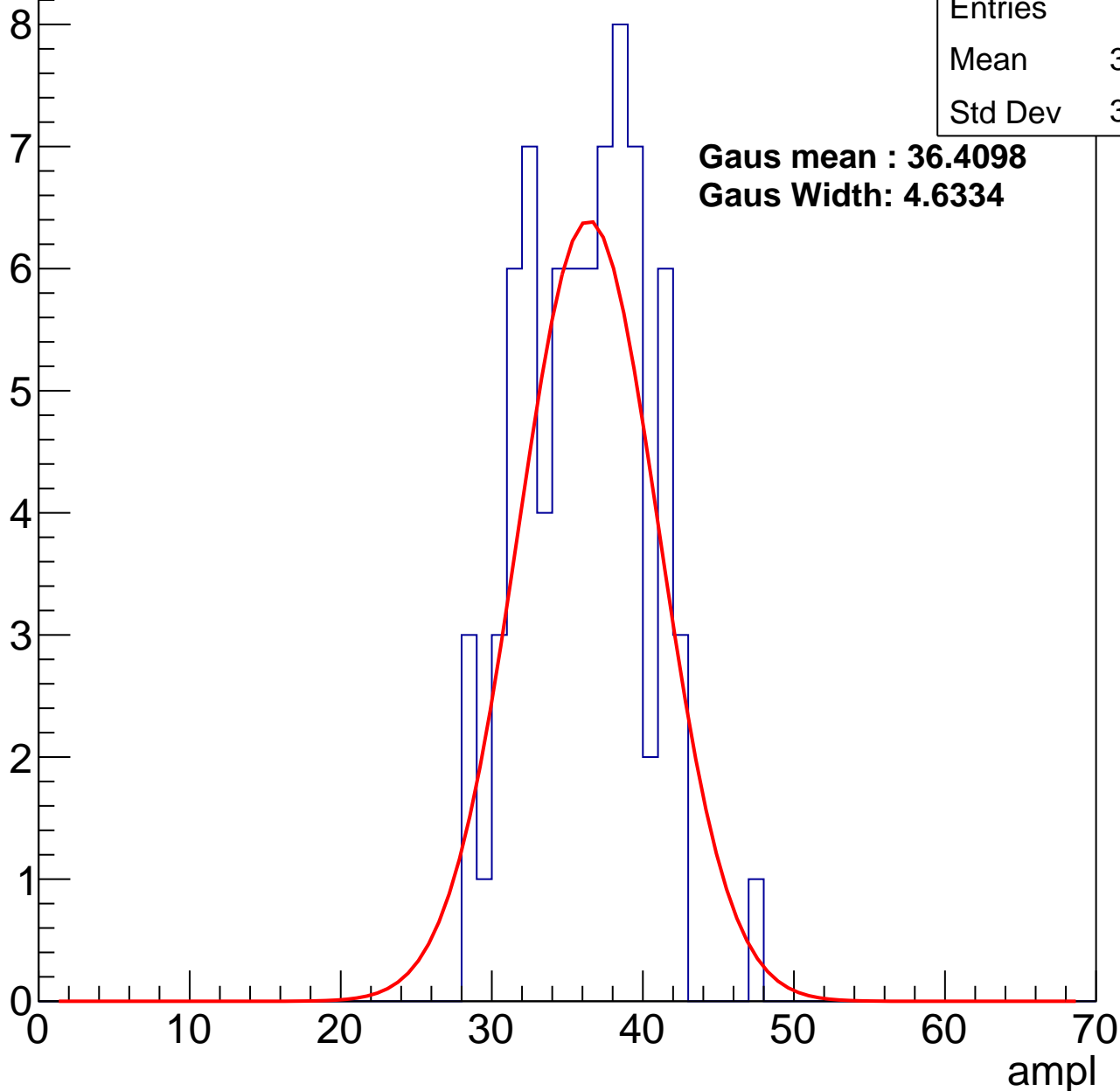
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.66
Std Dev	3.942

**Gaus mean : 36.4098**

**Gaus Width: 4.6334**



# B1L102S, U24-ch118, adc2

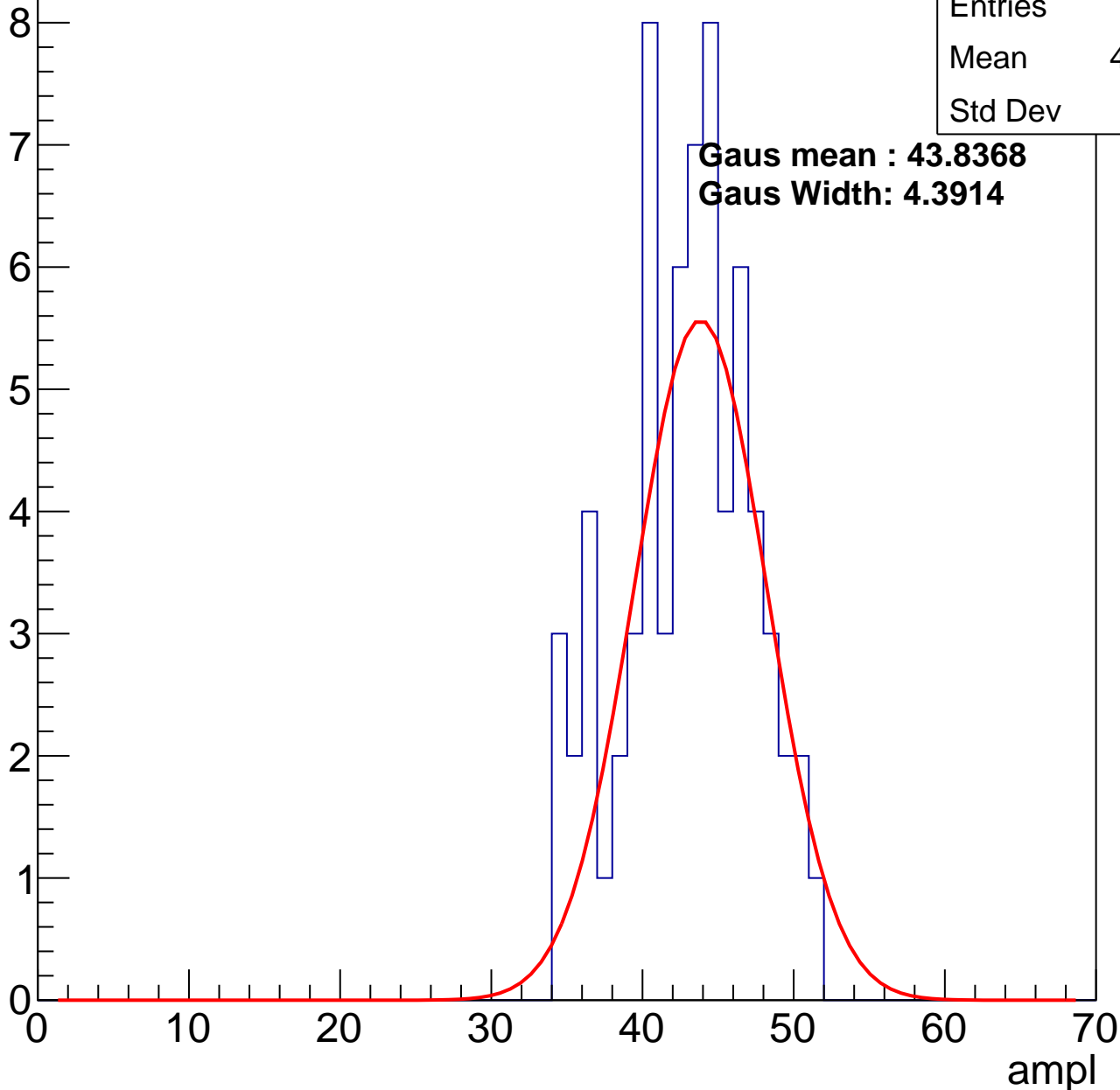
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.48
Std Dev	4.21

**Gaus mean : 43.8368**

**Gaus Width: 4.3914**

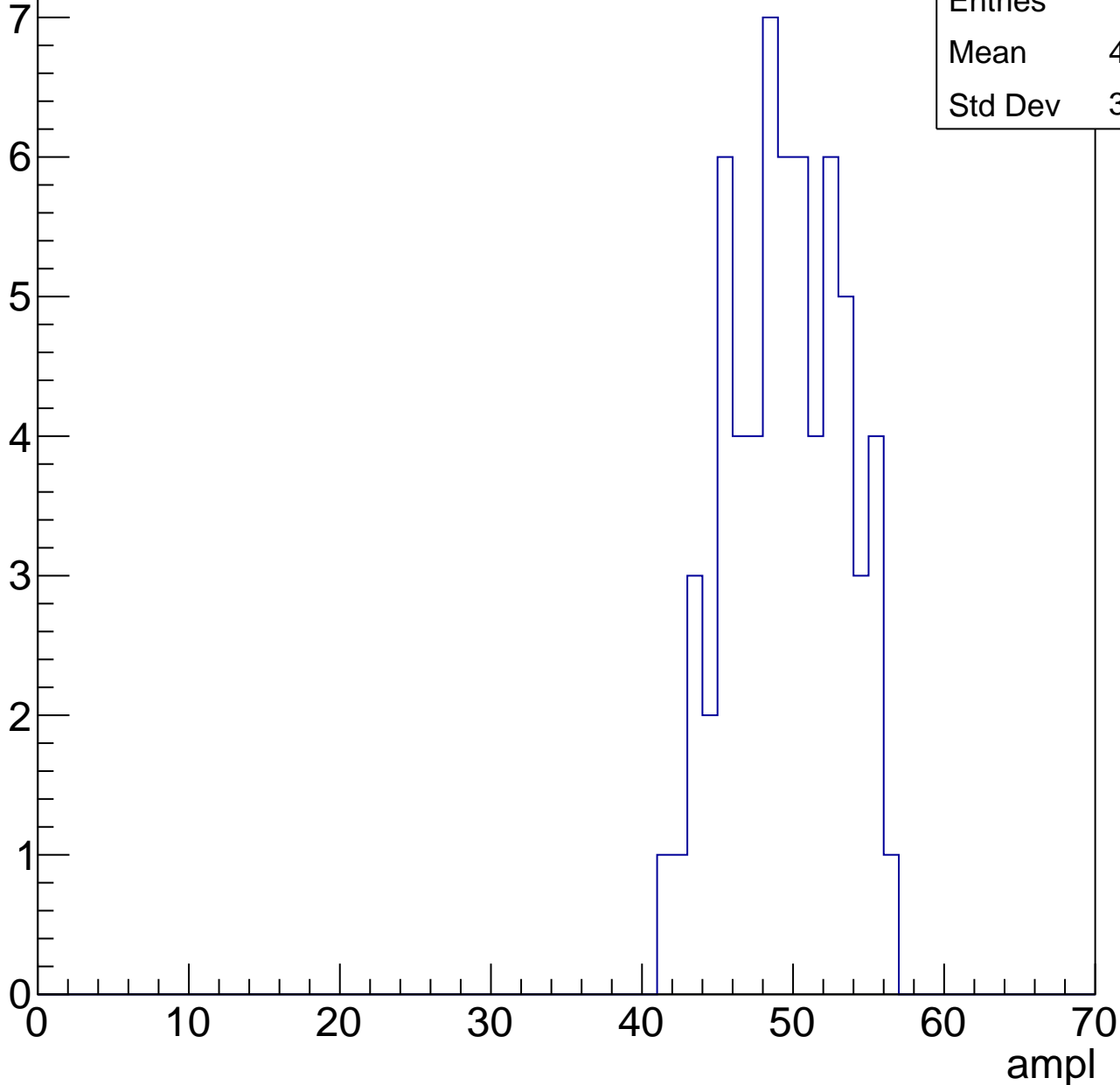


# B1L102S, U24-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.06
Std Dev	3.673

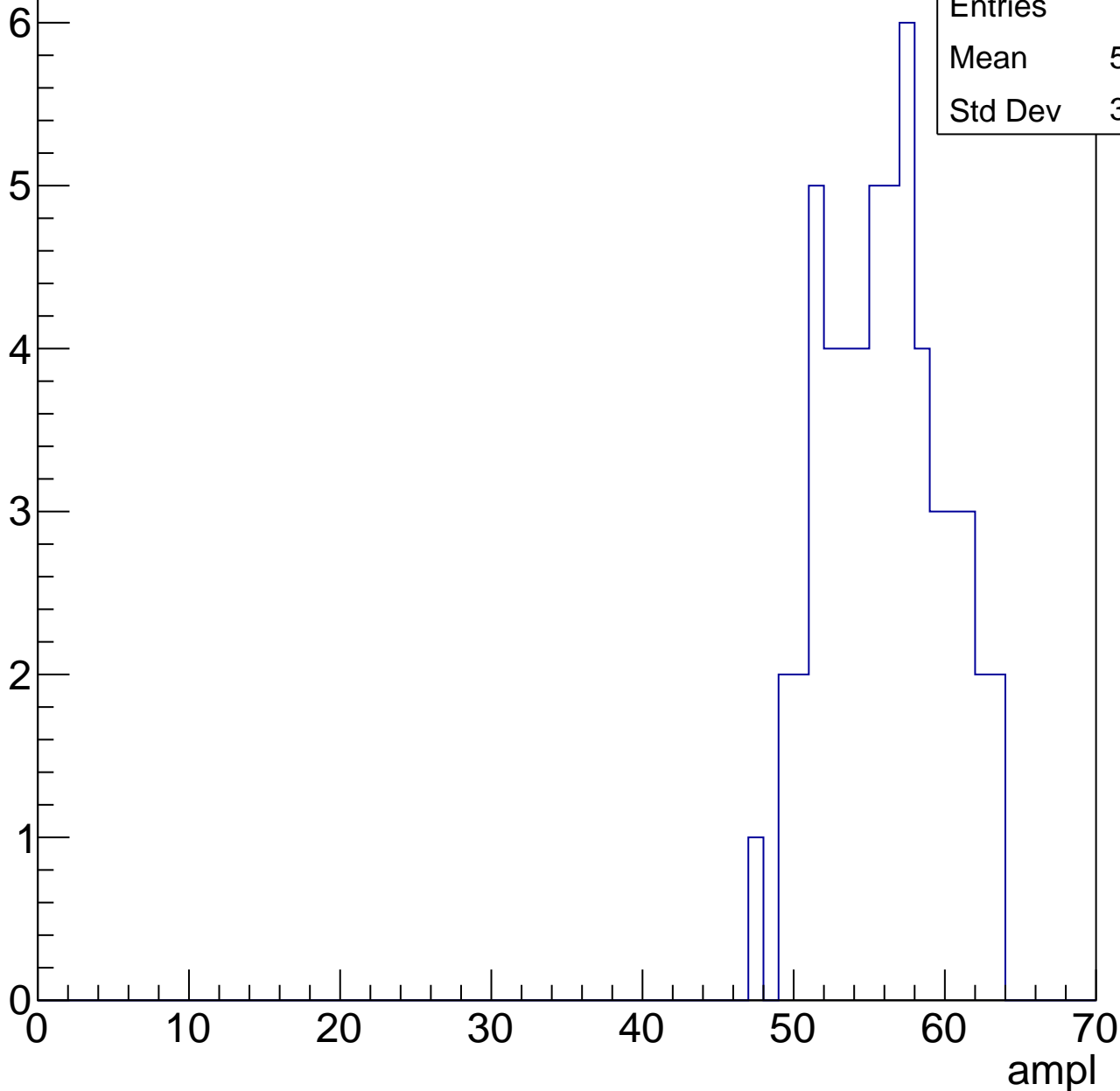


# B1L102S, U24-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.55
Std Dev	3.879

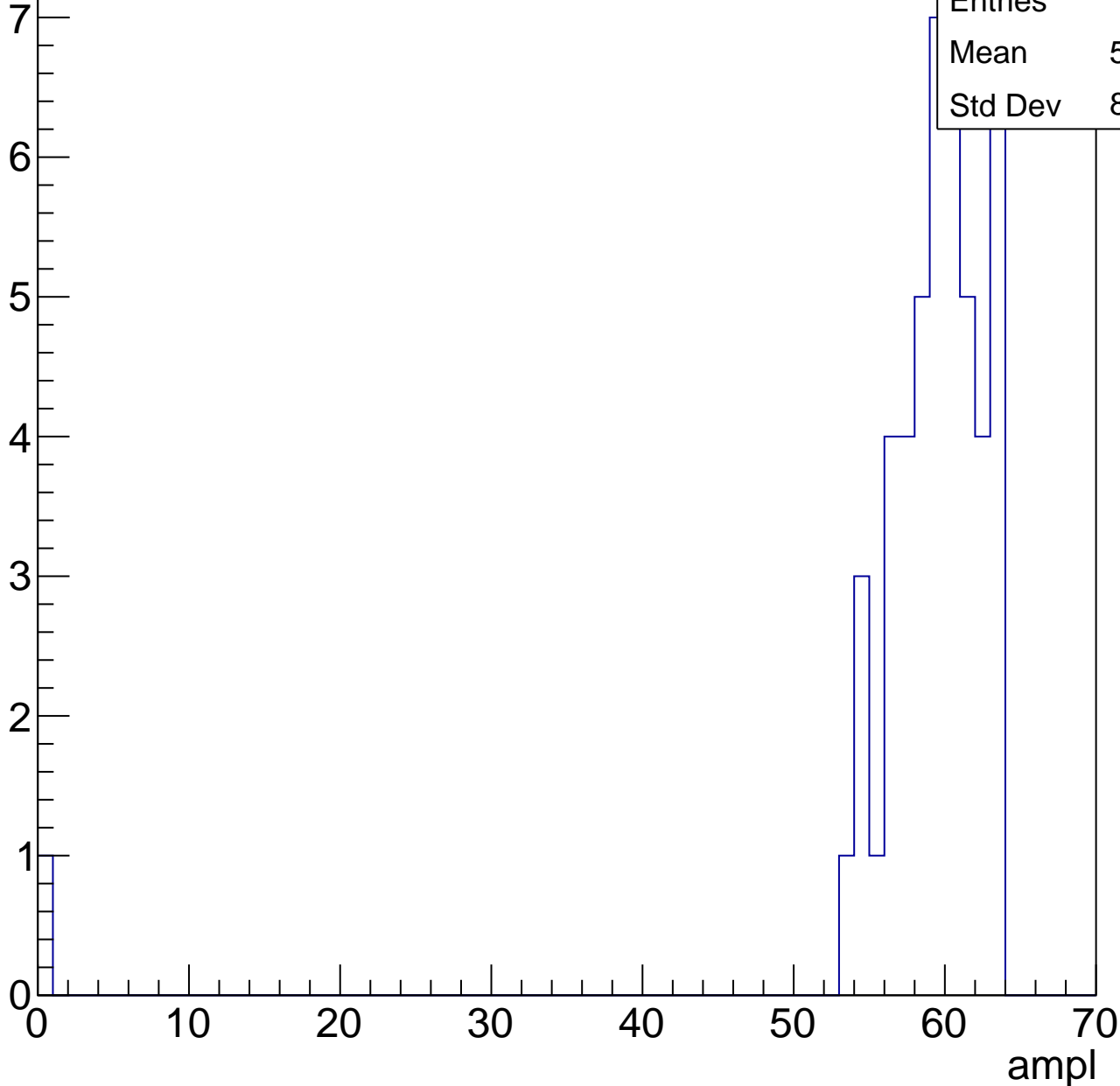


# B1L102S, U24-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

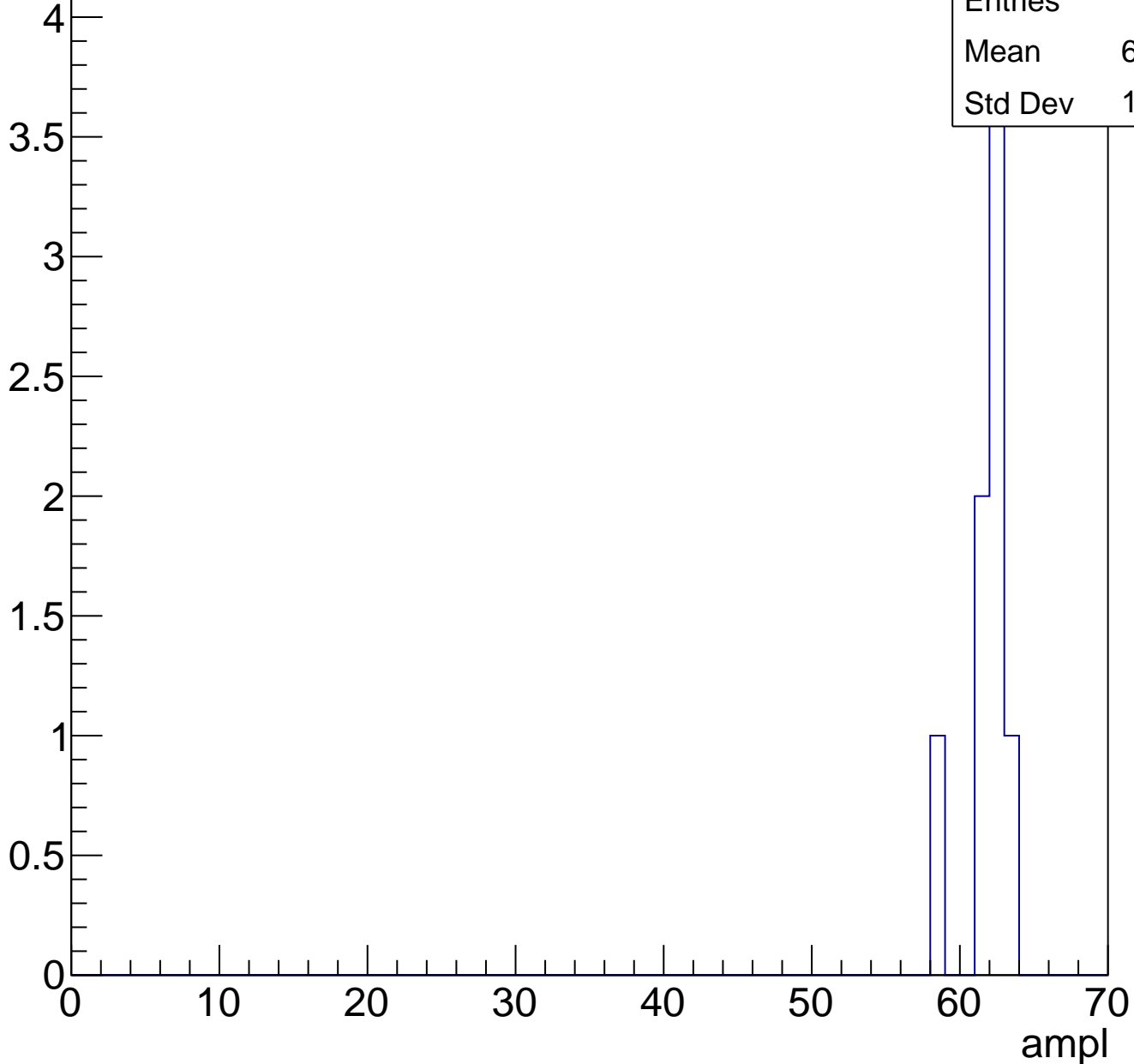
Entries	49
Mean	57.94
Std Dev	8.789



# B1L102S, U24-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch119, adc0

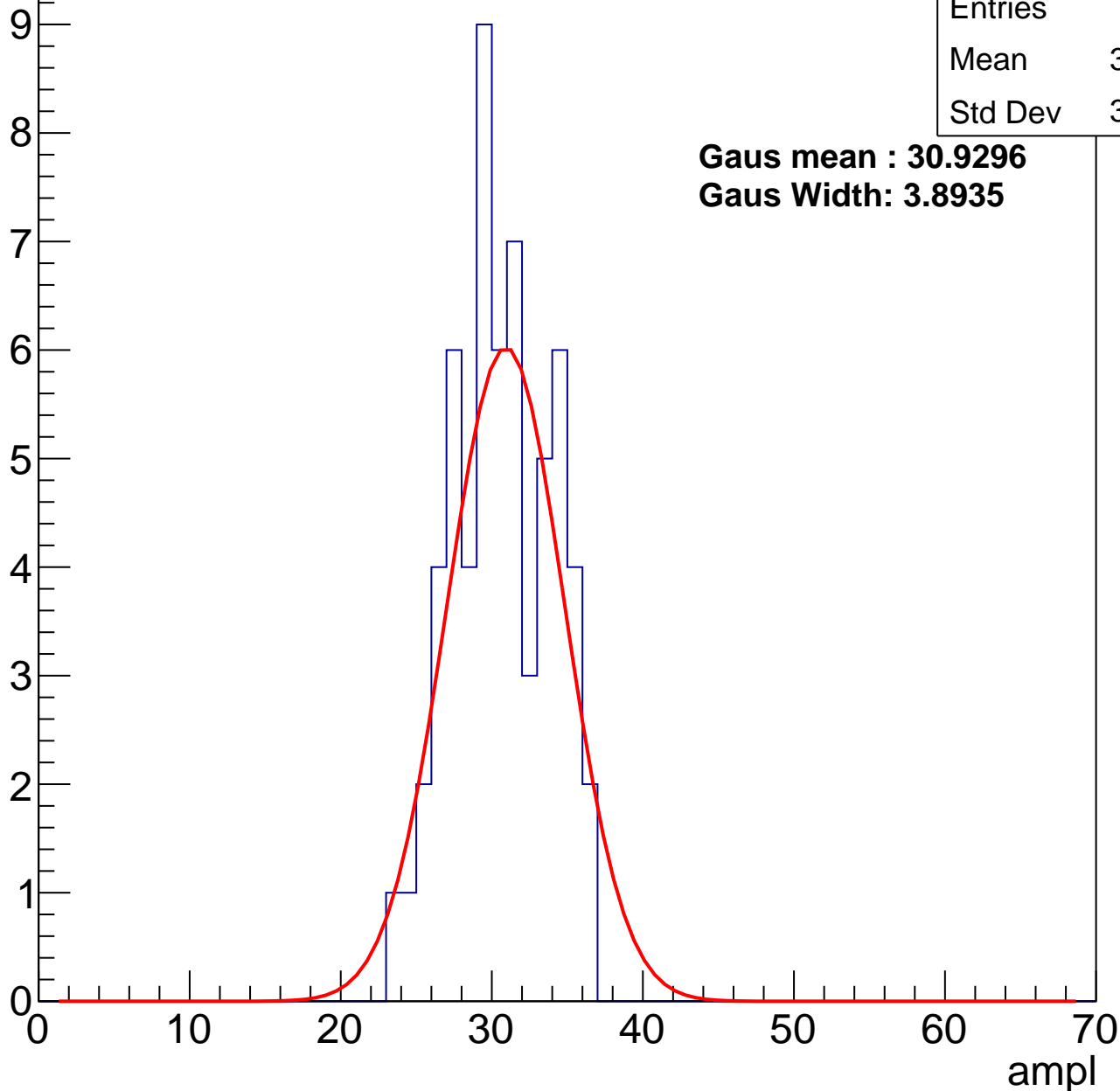
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	30.17
Std Dev	3.184

**Gaus mean : 30.9296**

**Gaus Width: 3.8935**



# B1L102S, U24-ch119, adc1

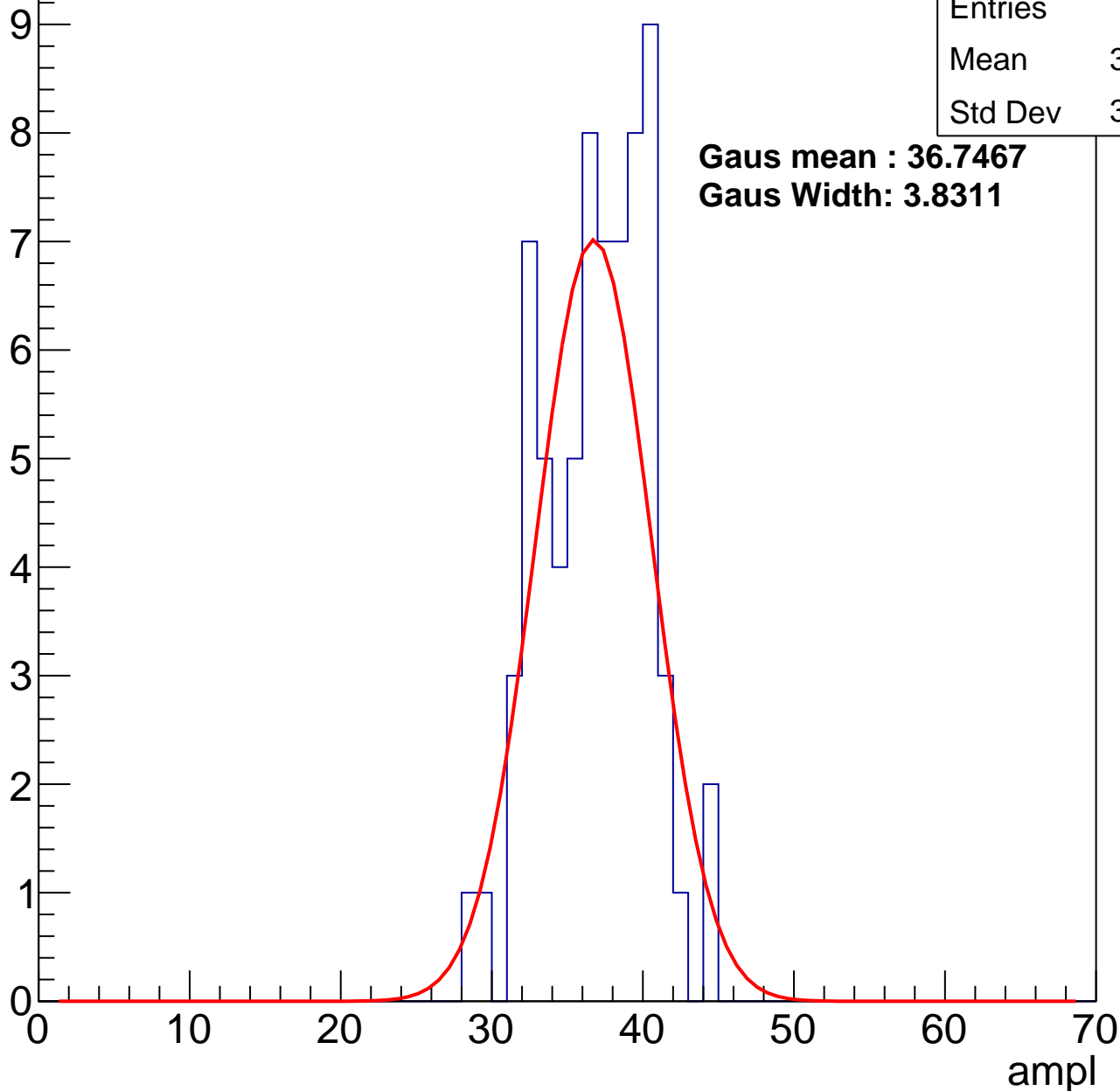
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.45
Std Dev	3.443

**Gaus mean : 36.7467**

**Gaus Width: 3.8311**



# B1L102S, U24-ch119, adc2

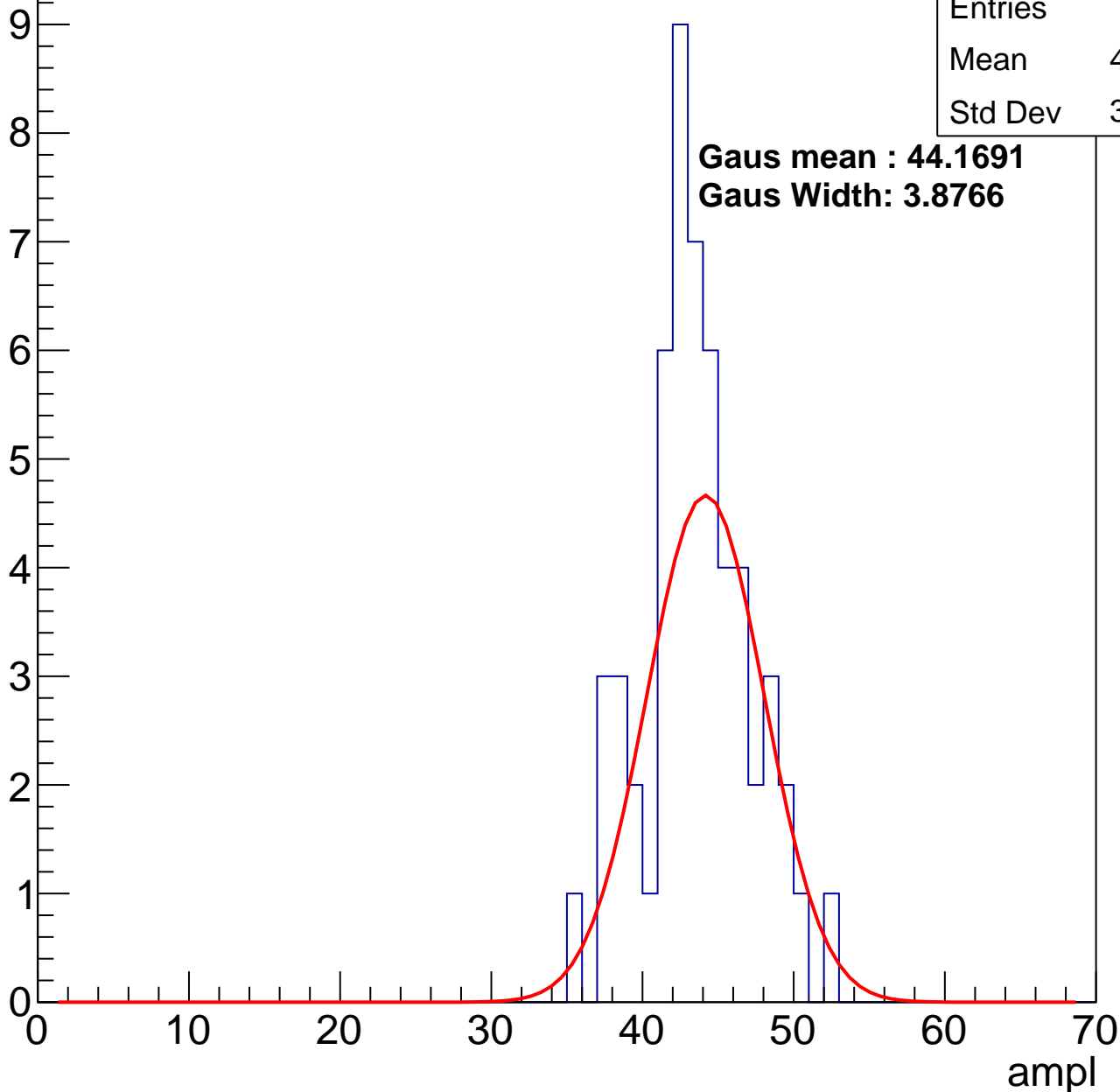
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.07
Std Dev	3.536

**Gaus mean : 44.1691**

**Gaus Width: 3.8766**

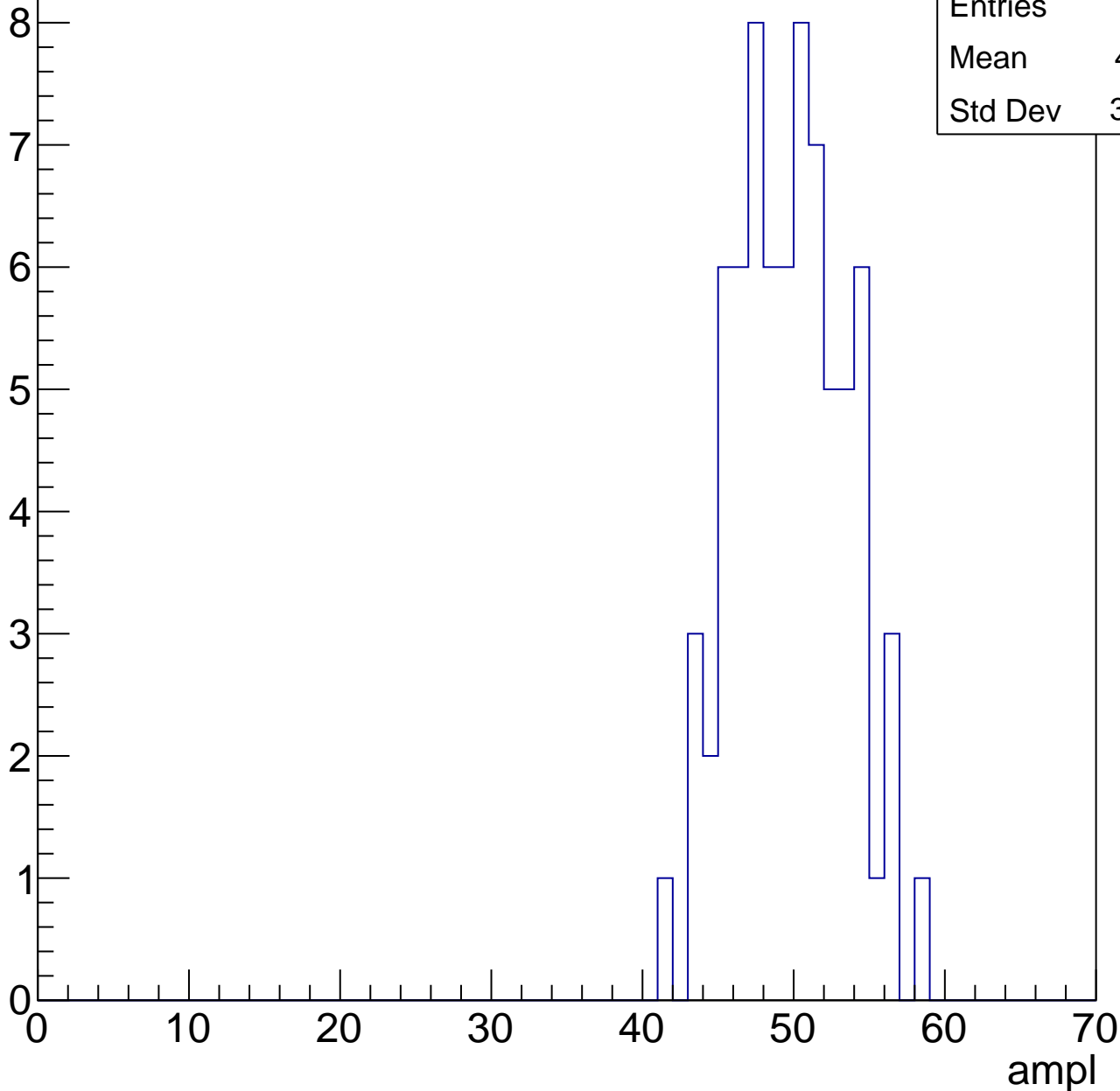


# B1L102S, U24-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	49.31
Std Dev	3.635

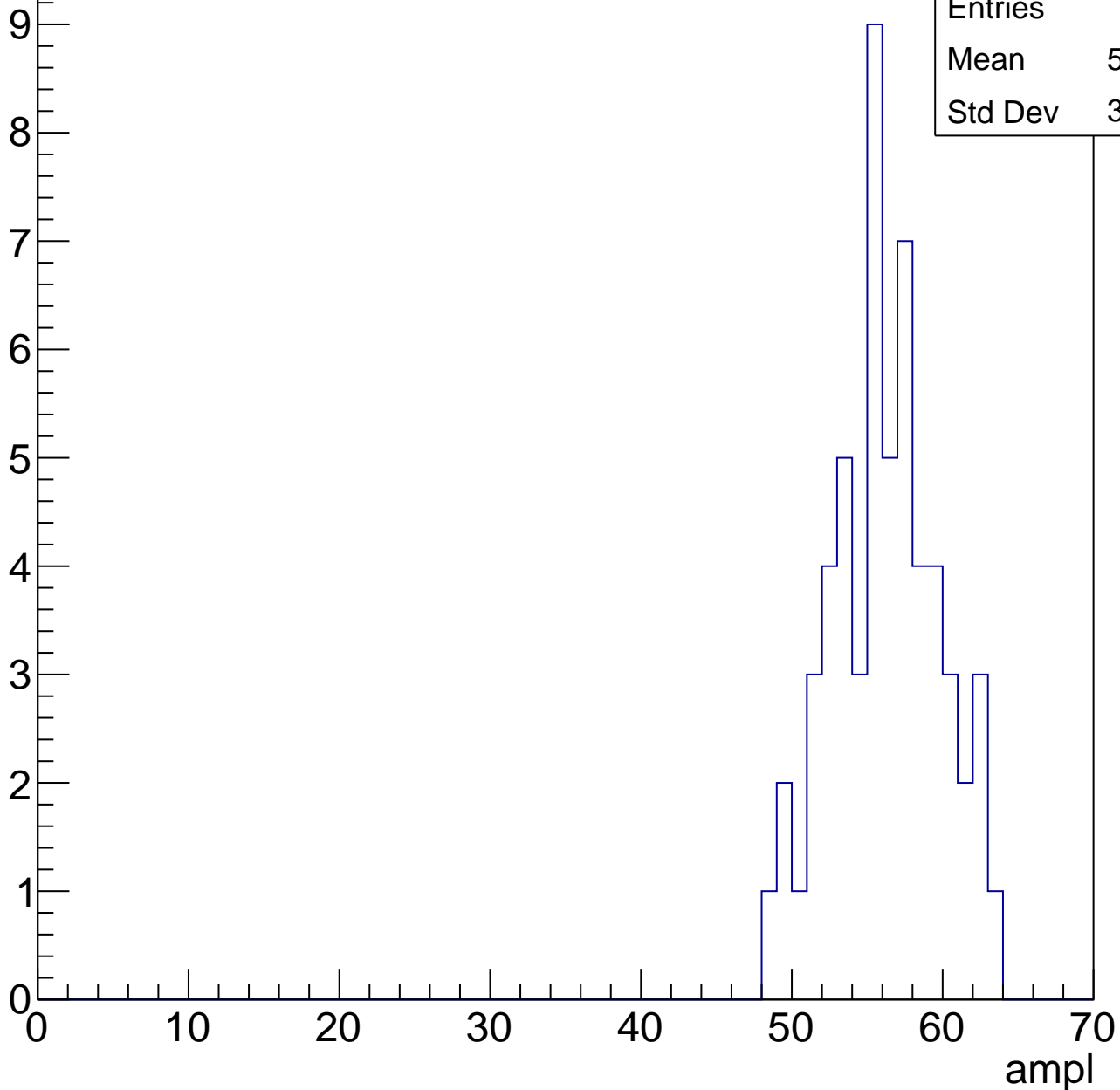


# B1L102S, U24-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.74
Std Dev	3.537

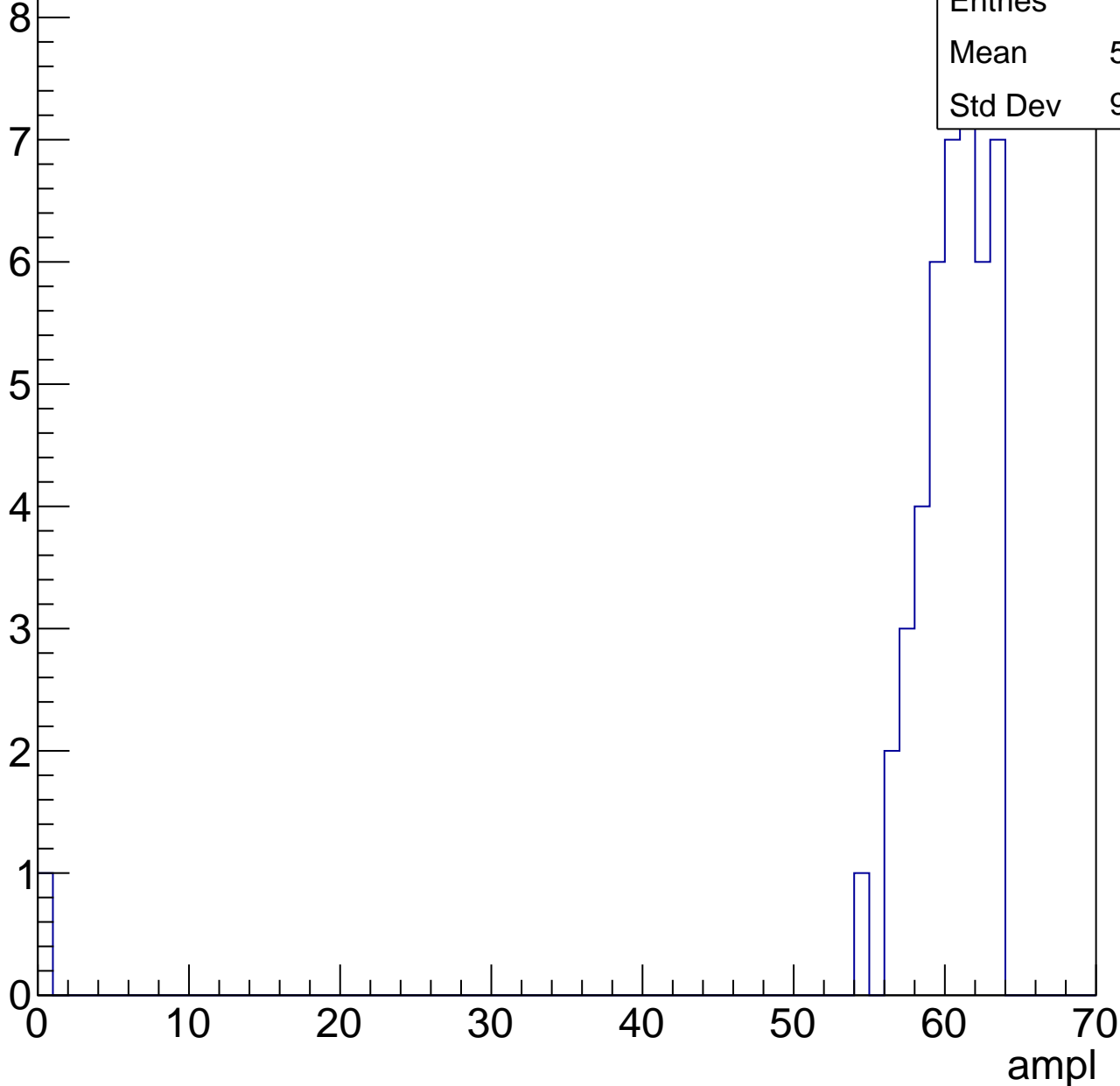


# B1L102S, U24-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

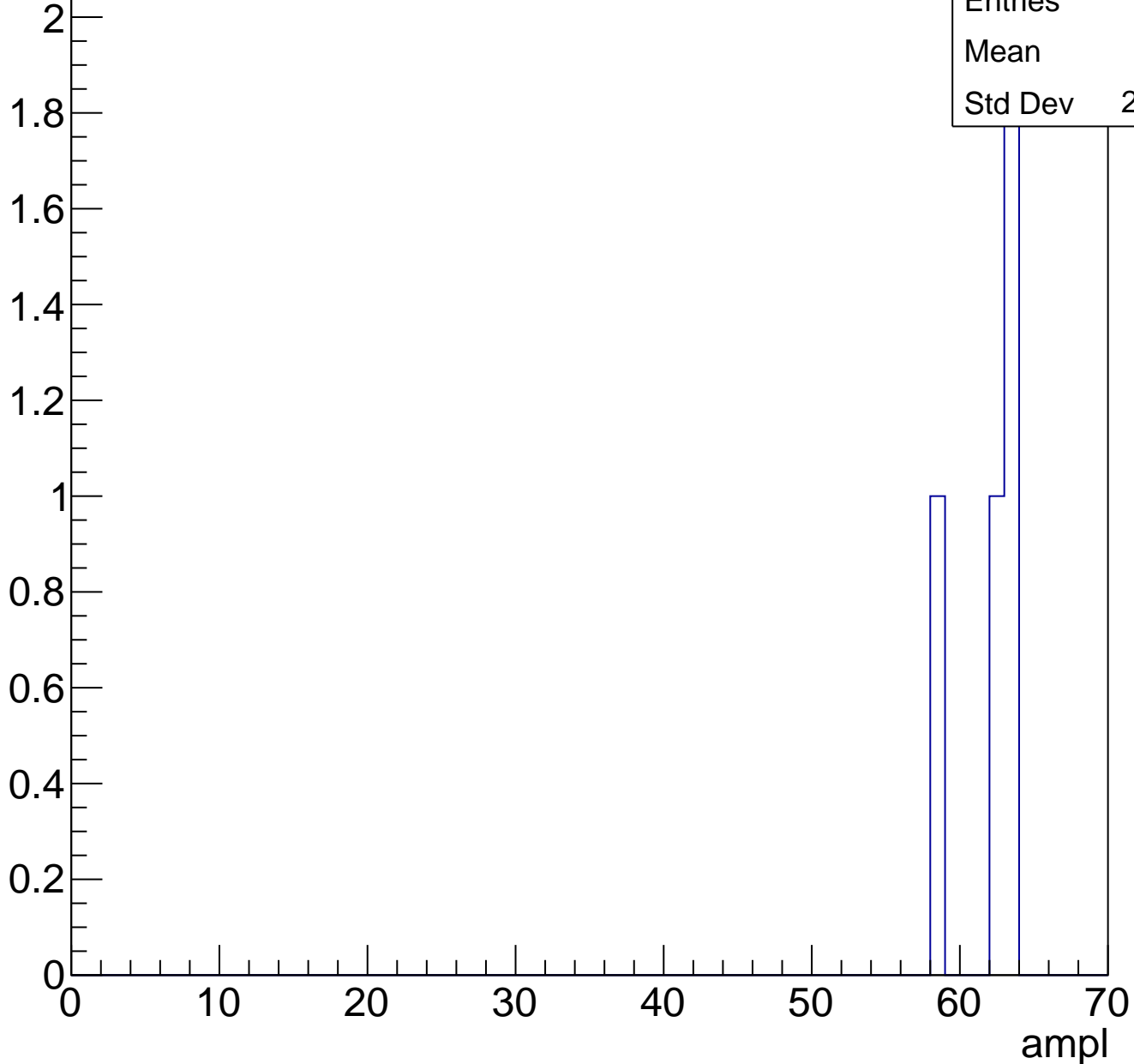
Entries	45
Mean	58.76
Std Dev	9.119



# B1L102S, U24-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U24-ch120, adc0

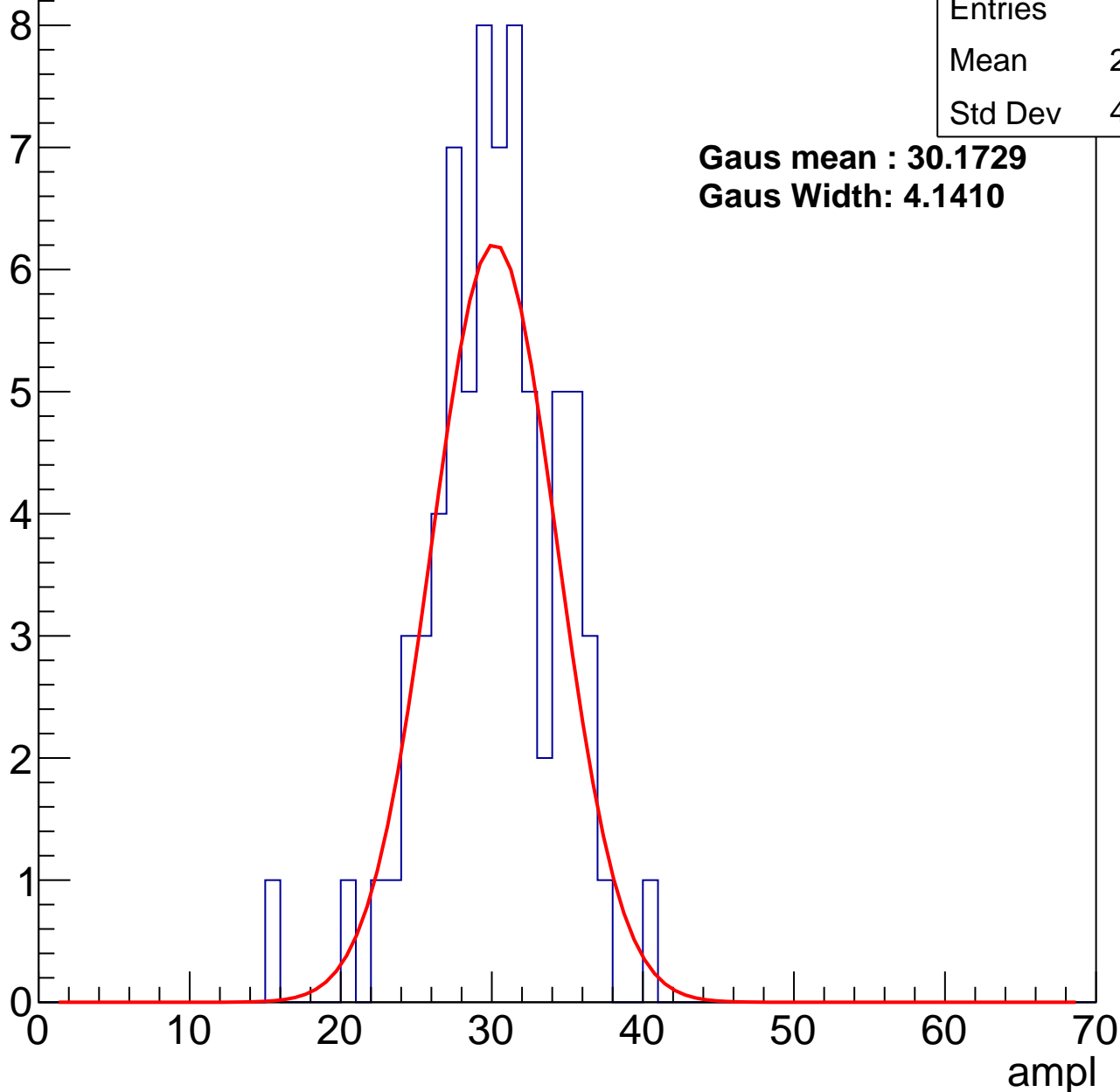
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.66
Std Dev	4.239

**Gaus mean : 30.1729**

**Gaus Width: 4.1410**



# B1L102S, U24-ch120, adc1

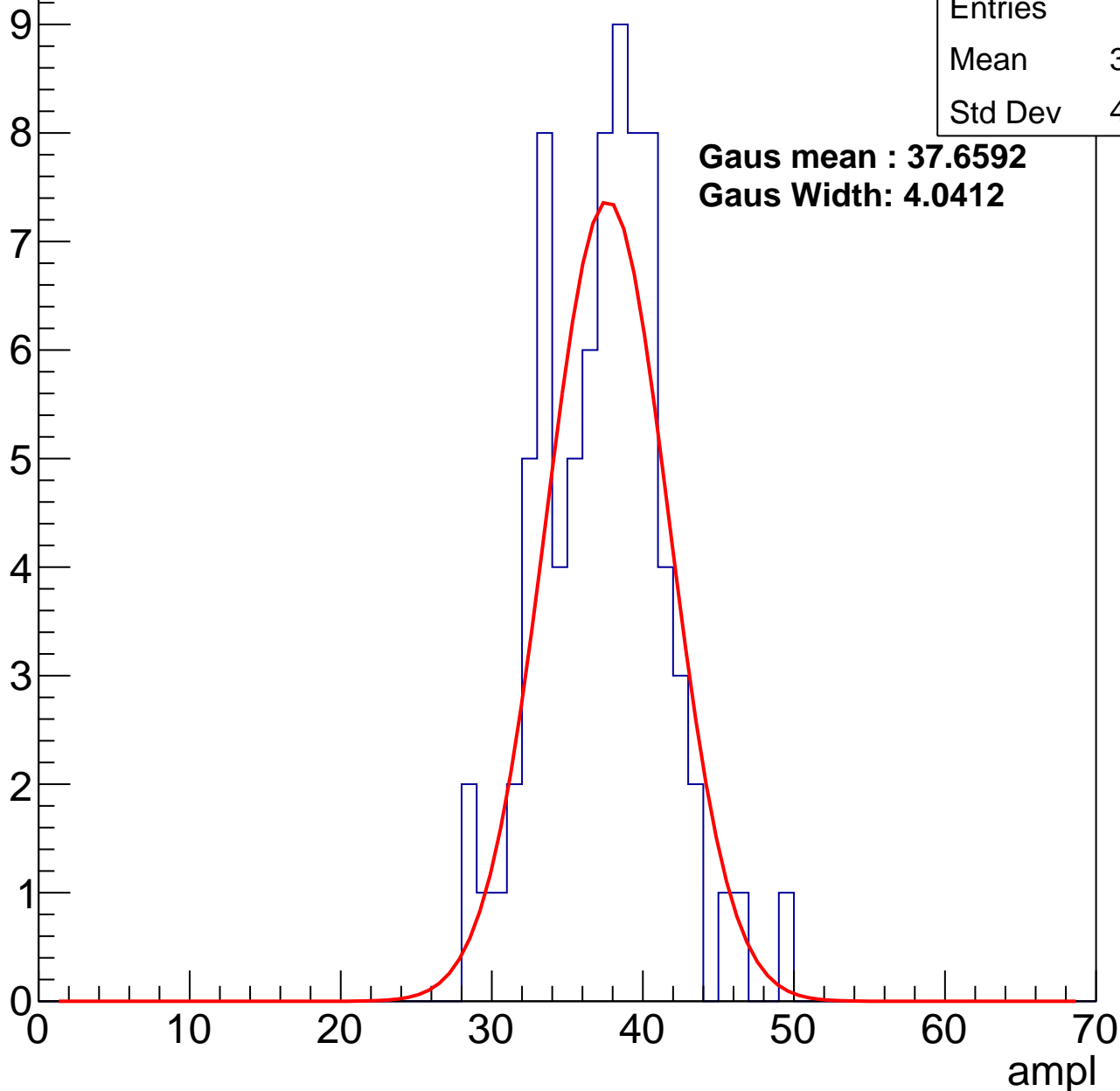
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.89
Std Dev	4.016

**Gaus mean : 37.6592**

**Gaus Width: 4.0412**

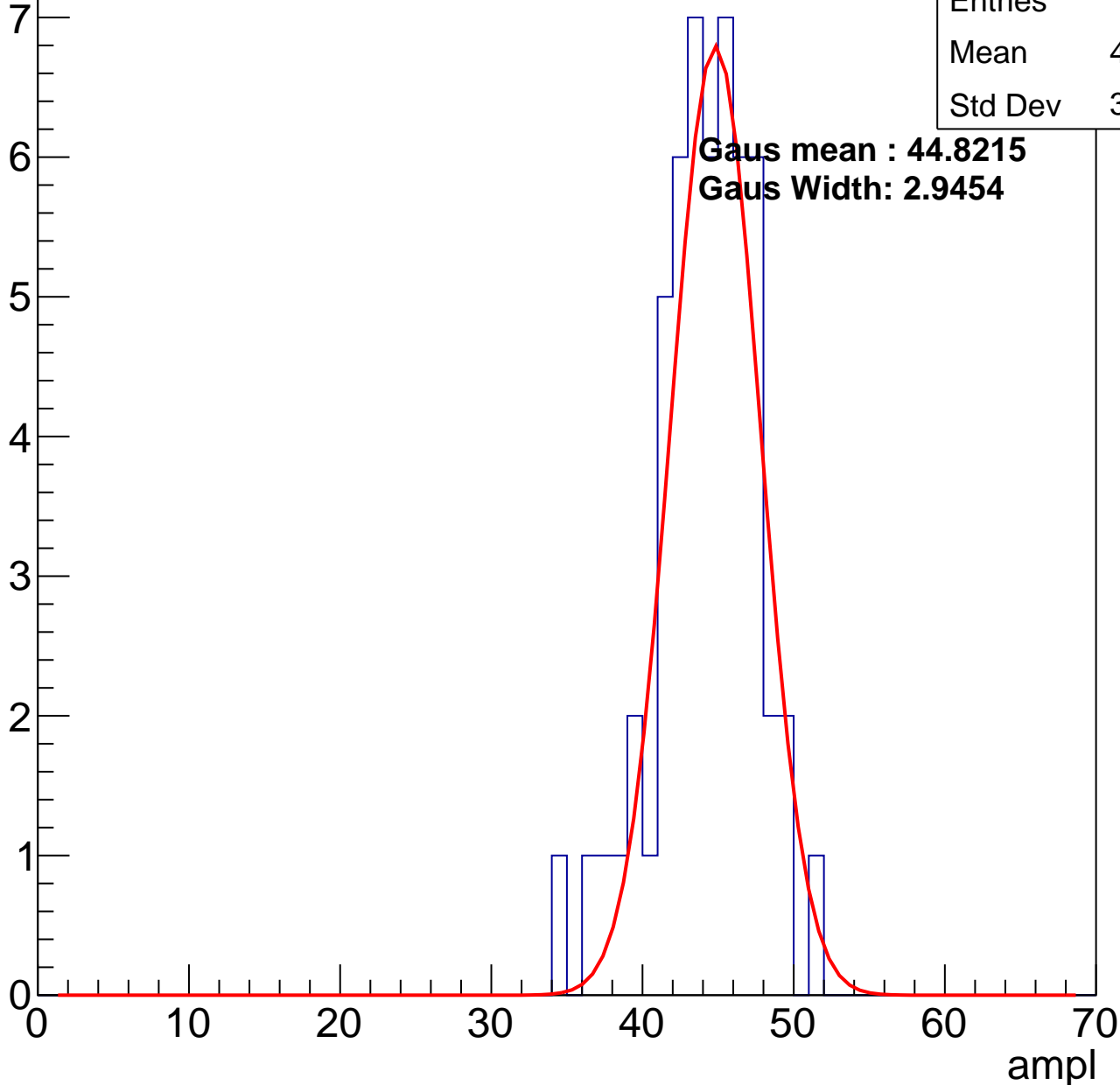


# B1L102S, U24-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.69
Std Dev	3.308

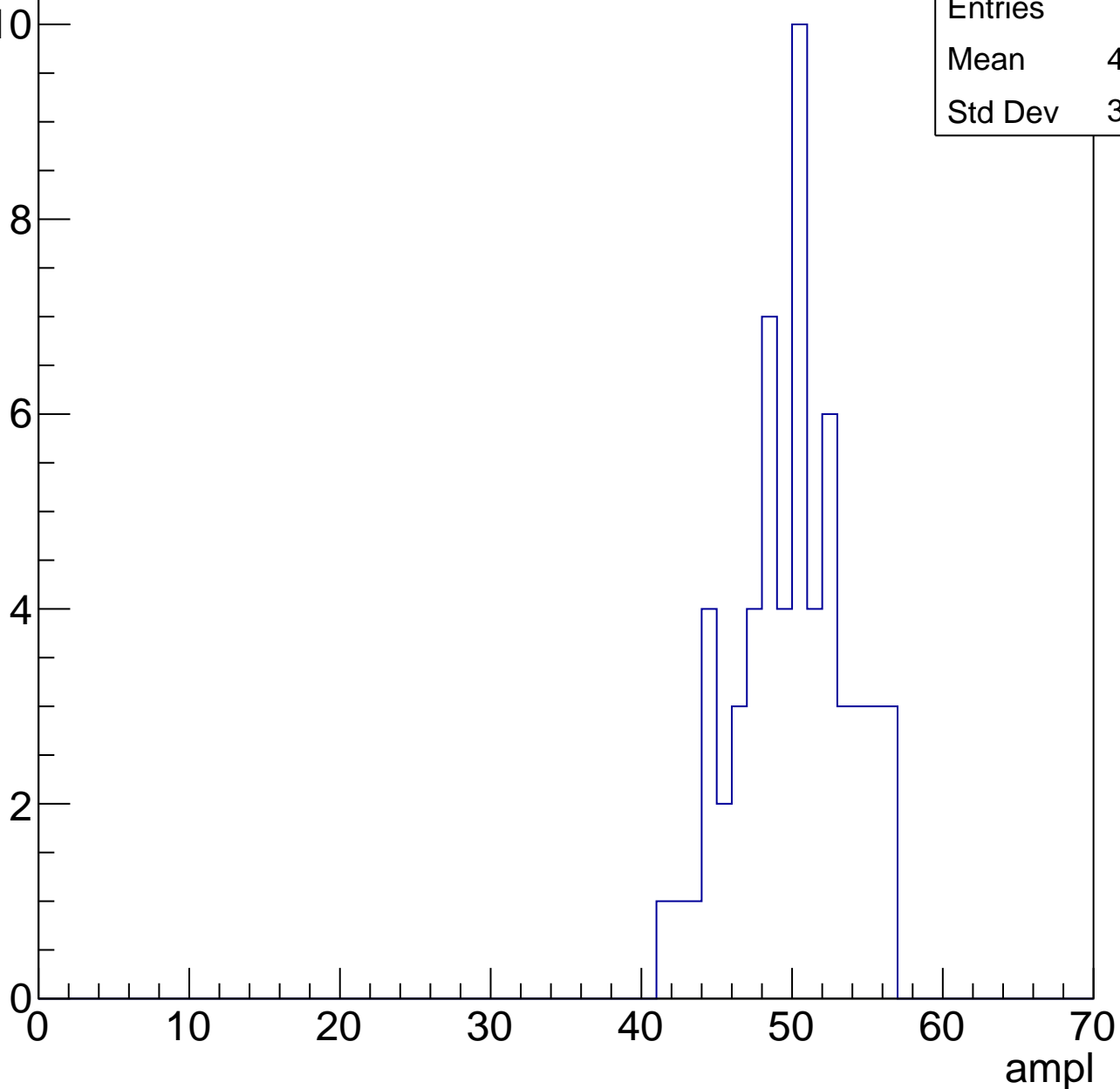


# B1L102S, U24-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

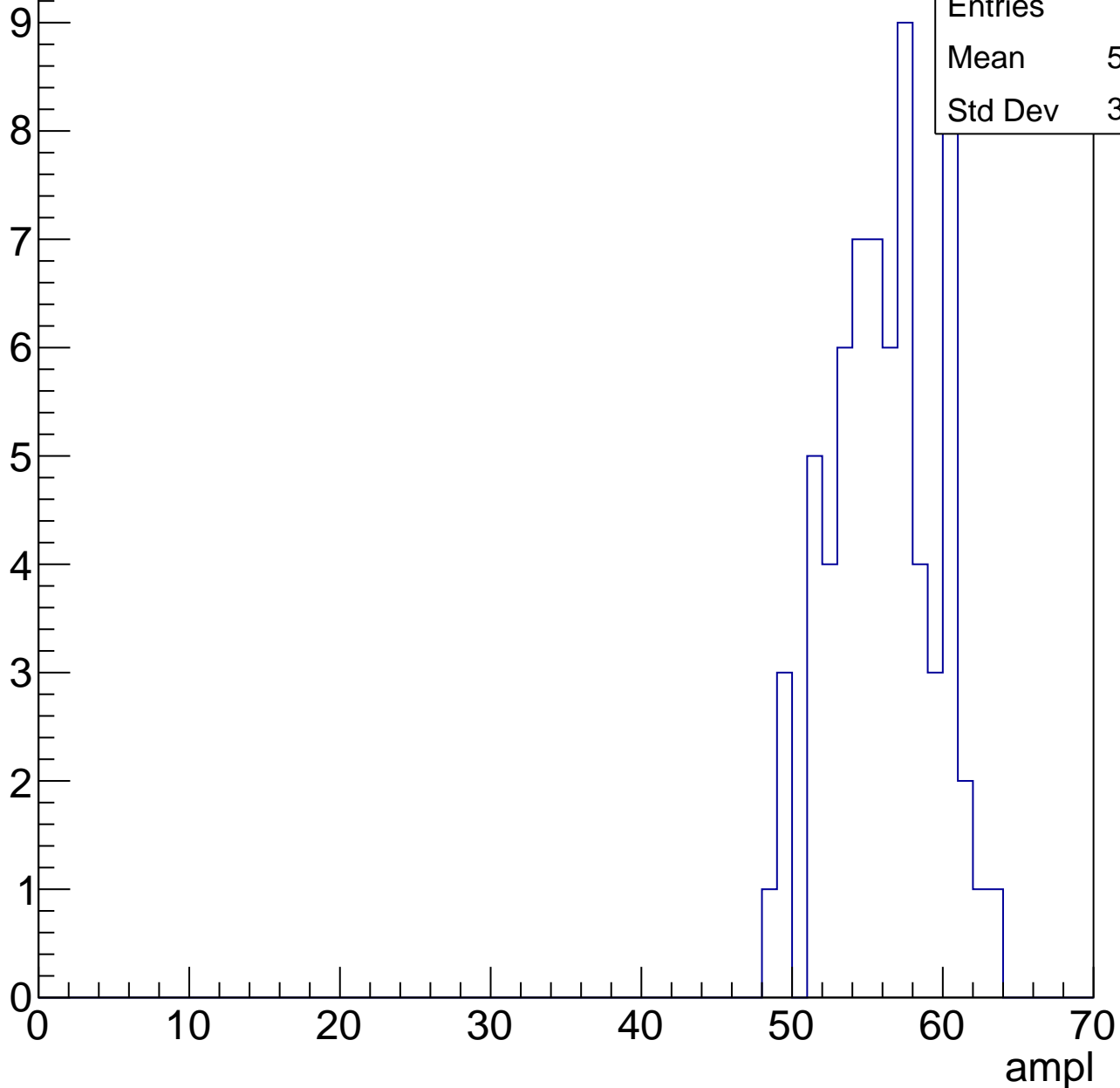
Entries	59
Mean	49.49
Std Dev	3.624



# B1L102S, U24-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

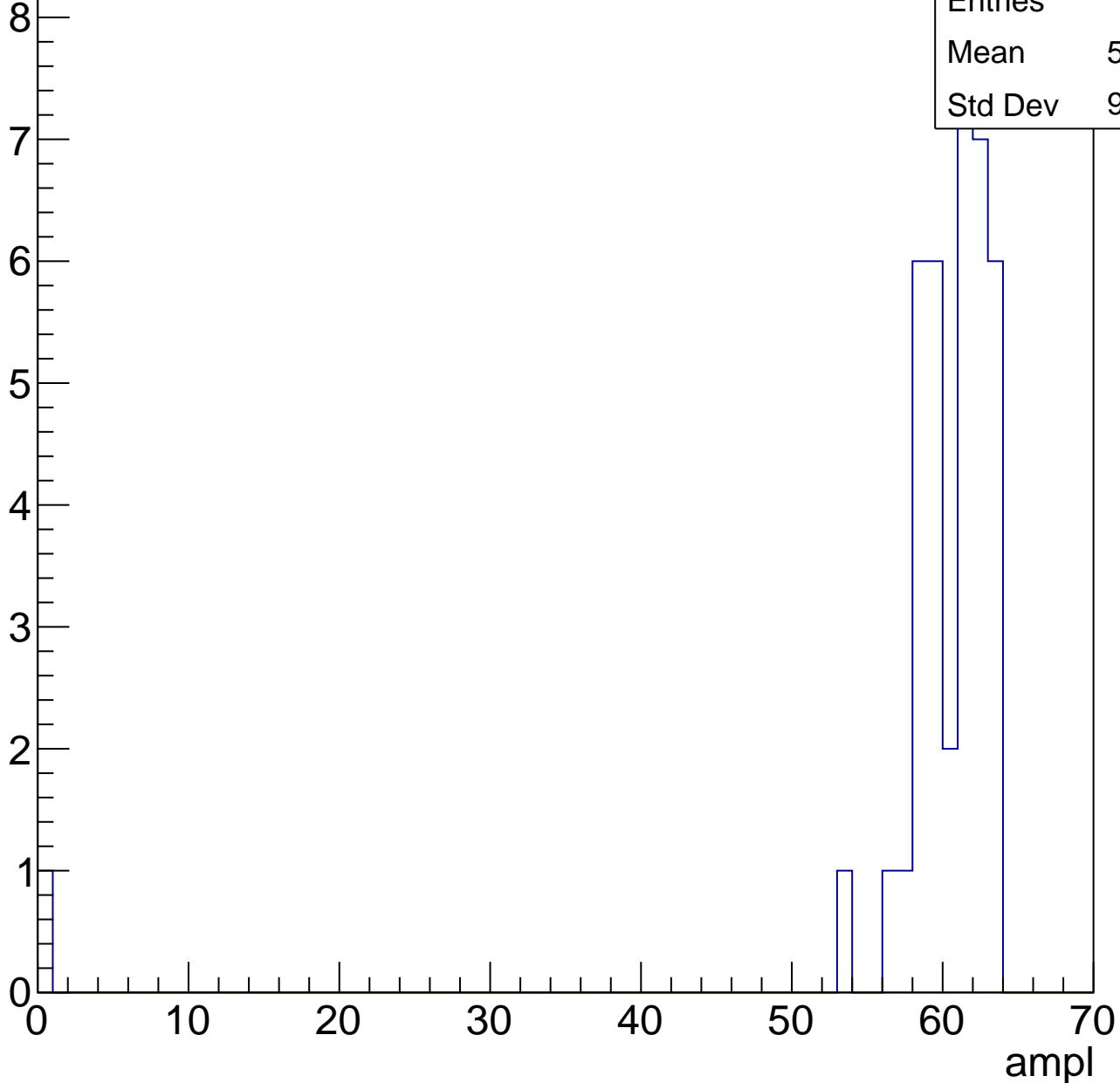


# B1L102S, U24-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.67
Std Dev	9.773



# B1L102S, U24-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U24-ch121, adc0

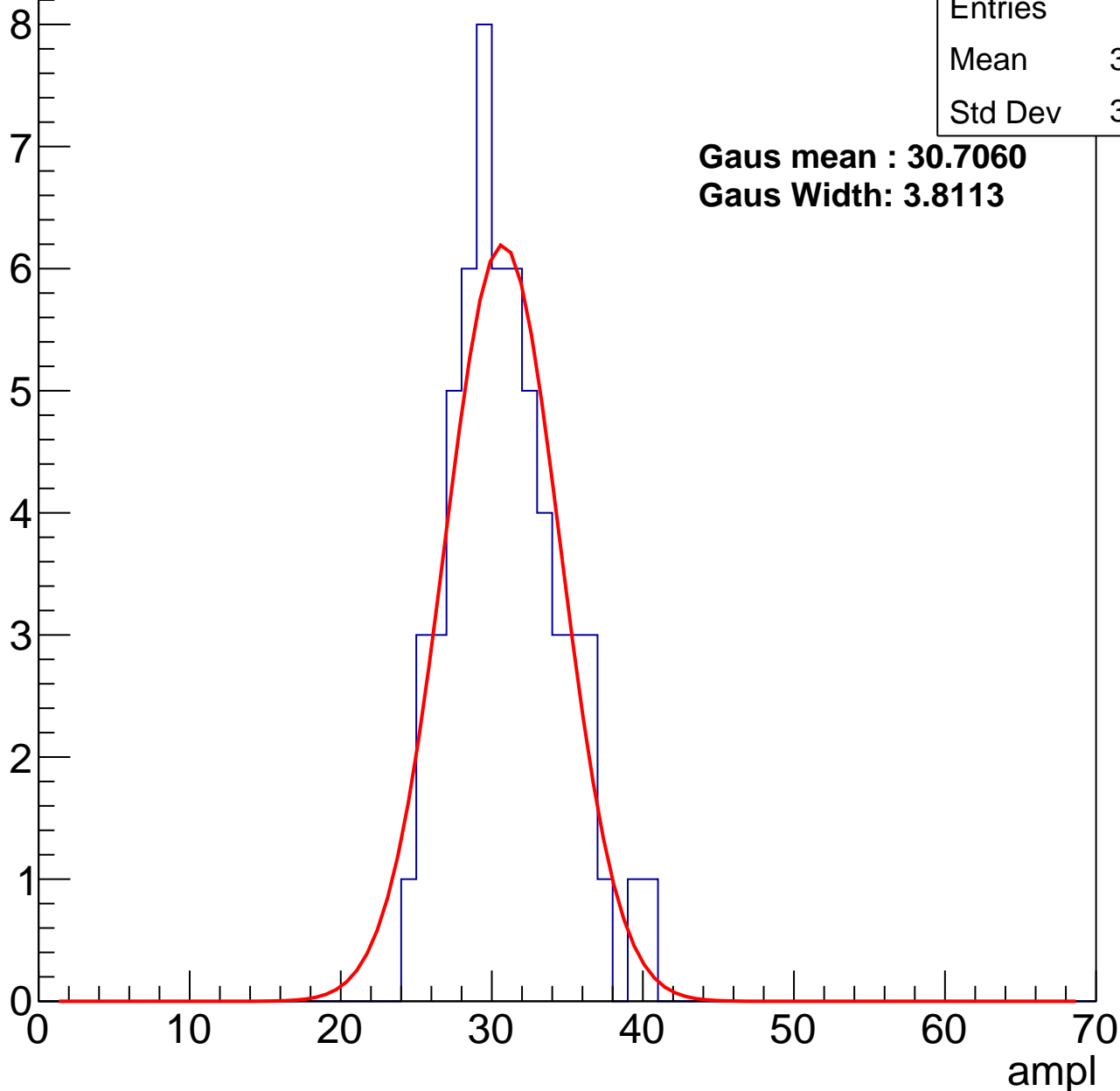
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	30.53
Std Dev	3.548

**Gaus mean : 30.7060**

**Gaus Width: 3.8113**



# B1L102S, U24-ch121, adc1

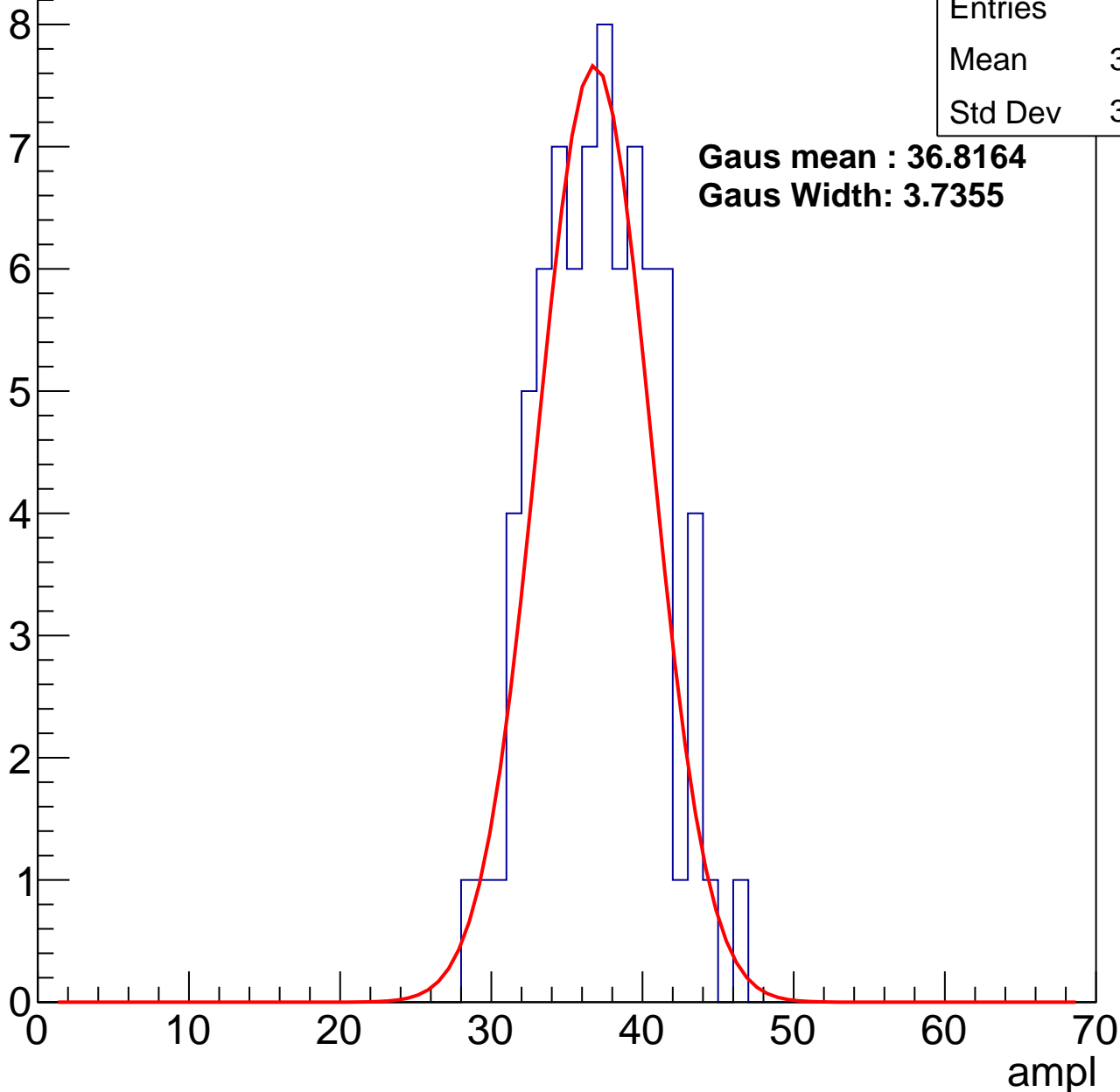
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.62
Std Dev	3.797

**Gaus mean : 36.8164**

**Gaus Width: 3.7355**



# B1L102S, U24-ch121, adc2

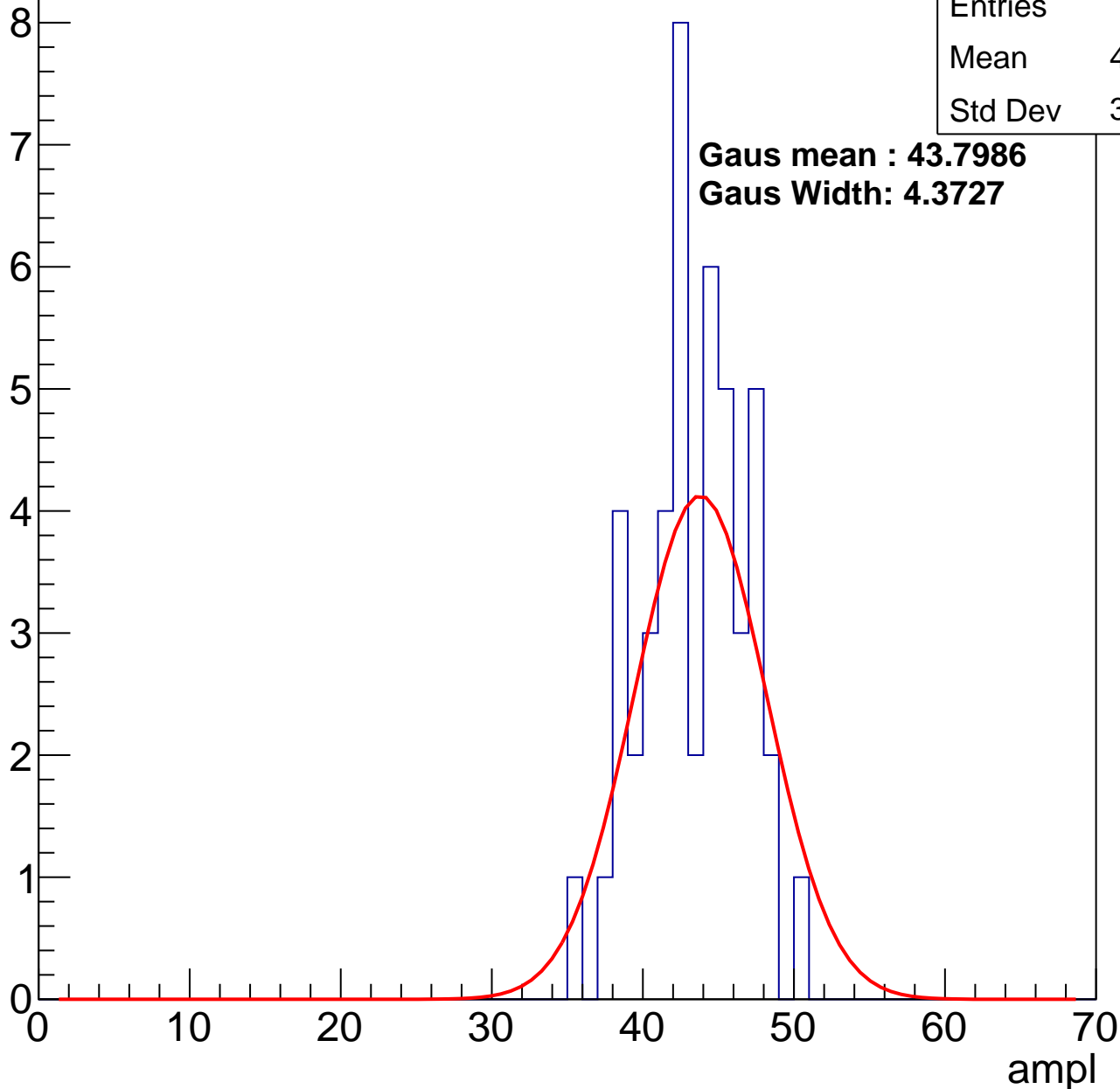
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	42.89
Std Dev	3.308

**Gaus mean : 43.7986**

**Gaus Width: 4.3727**

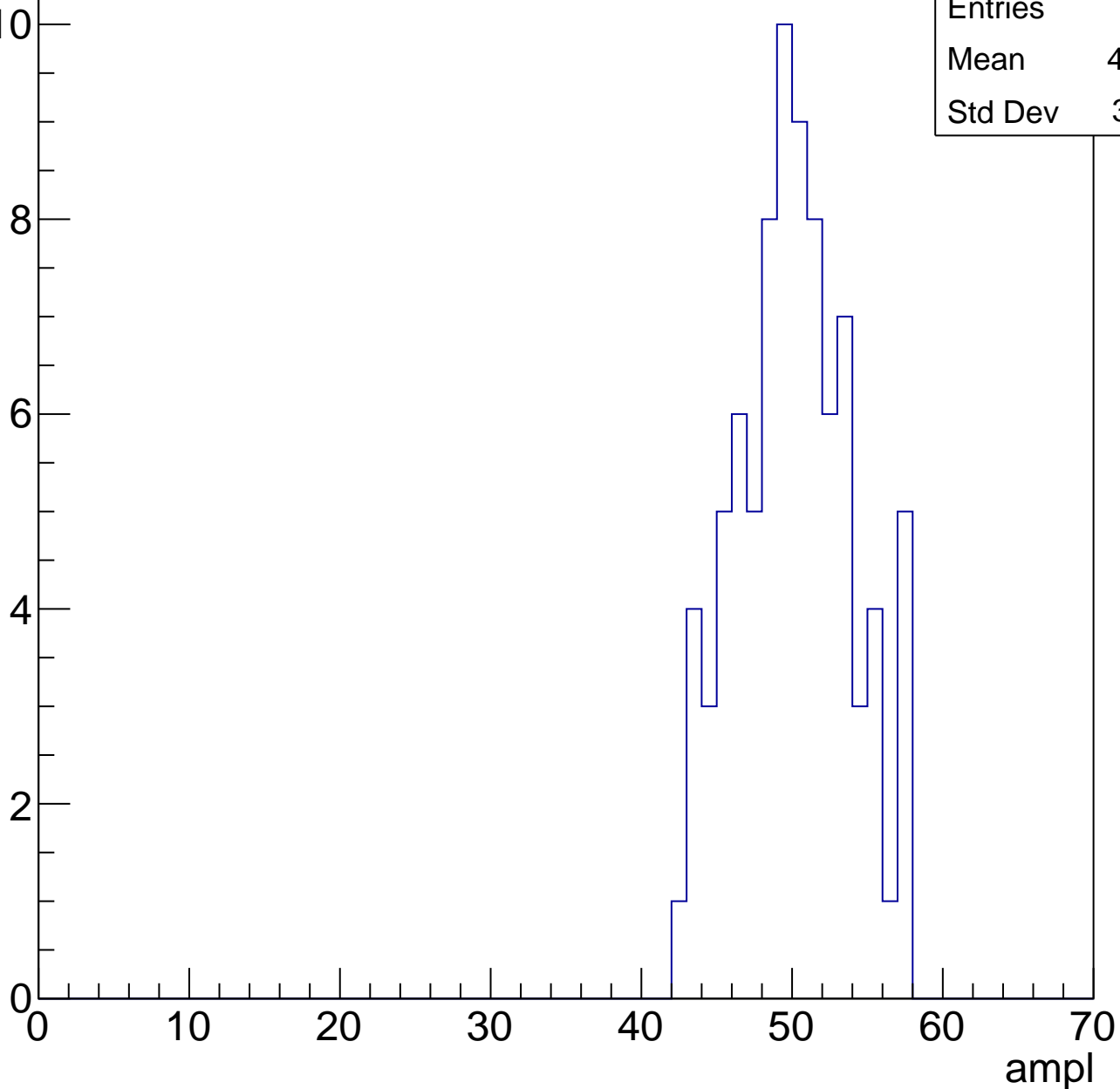


# B1L102S, U24-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	49.65
Std Dev	3.741

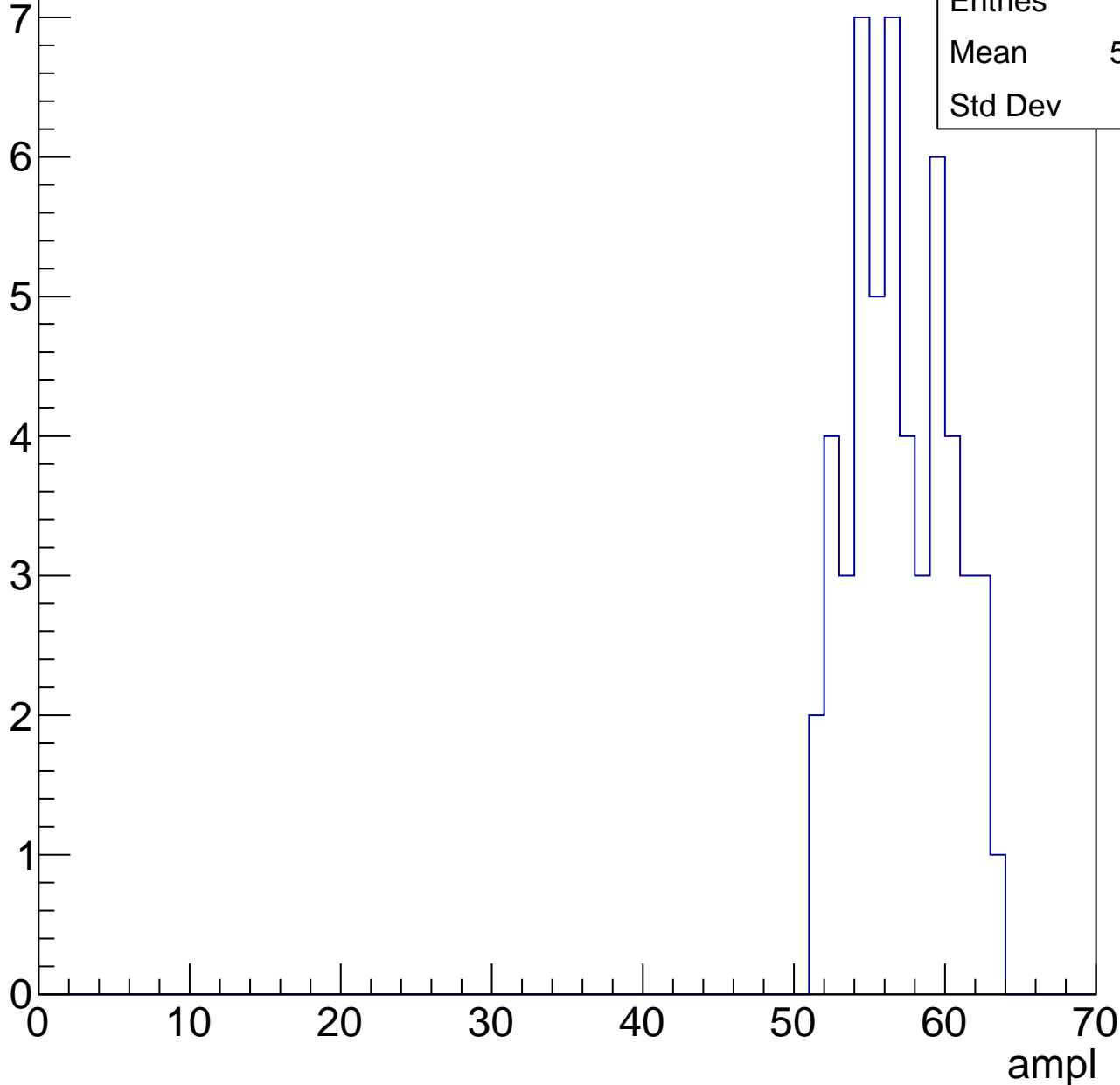


# B1L102S, U24-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

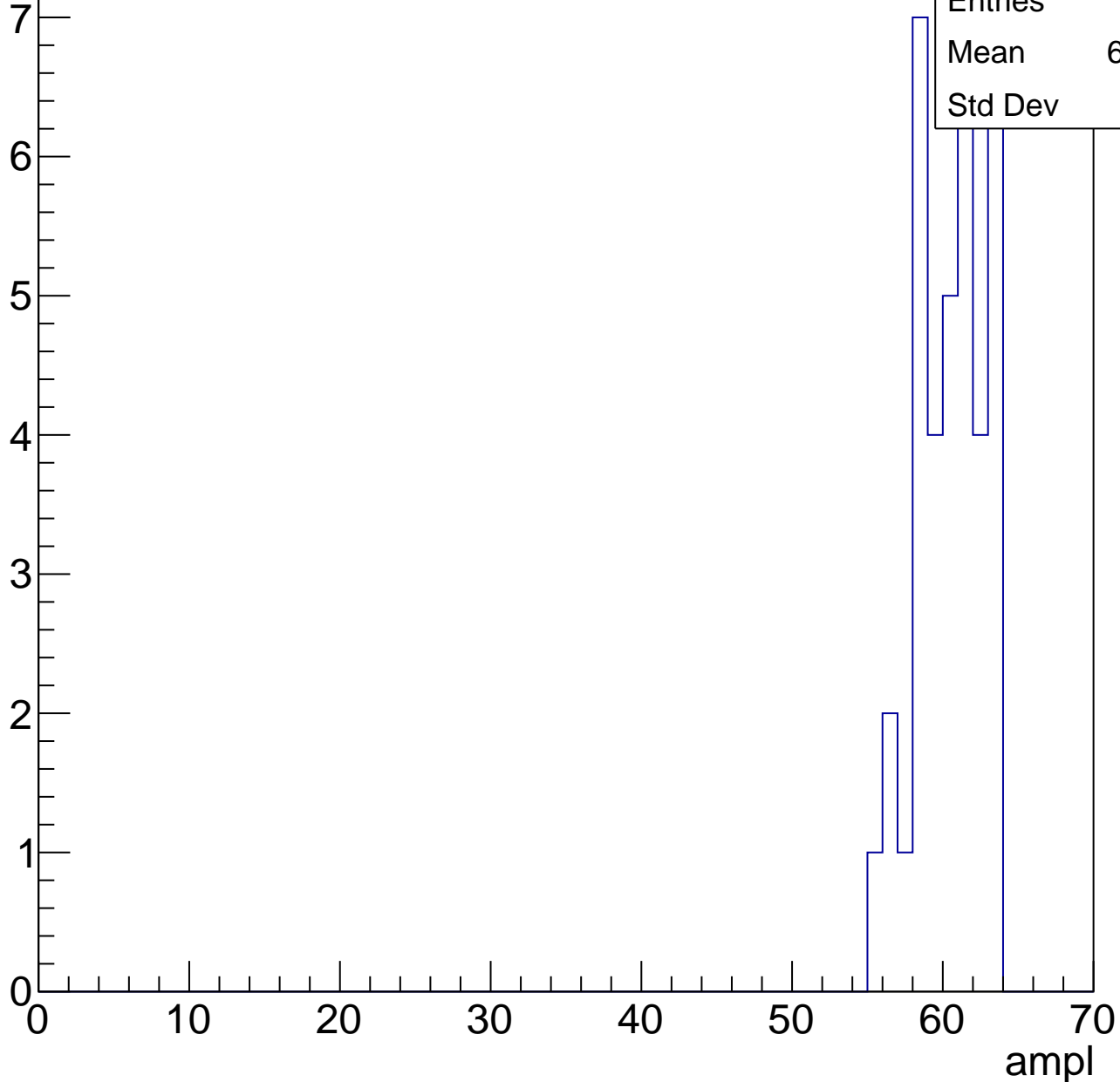
Entries	52
Mean	56.58
Std Dev	3.17



# B1L102S, U24-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

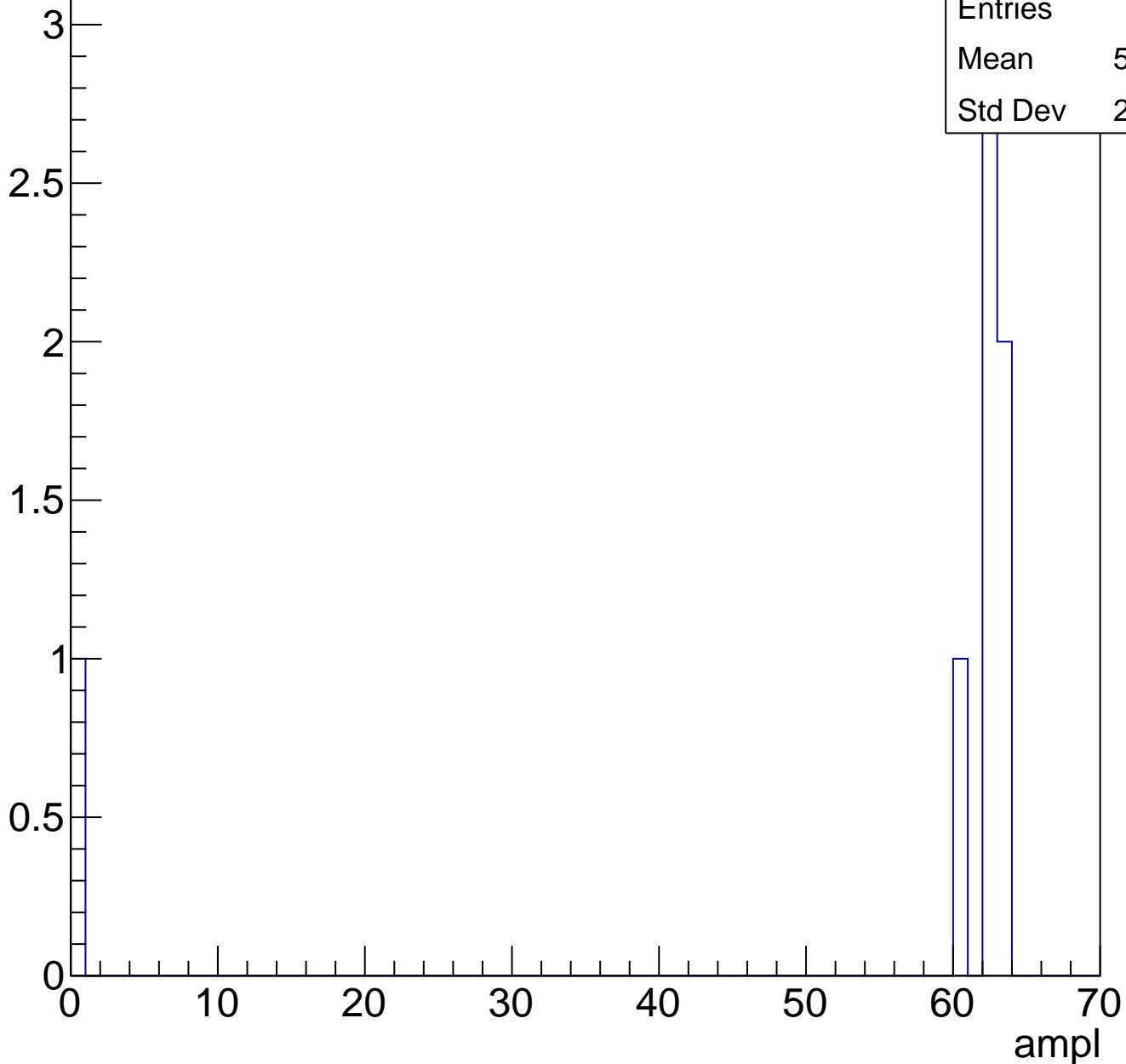


Entries	38
Mean	60.05
Std Dev	2.2

# B1L102S, U24-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

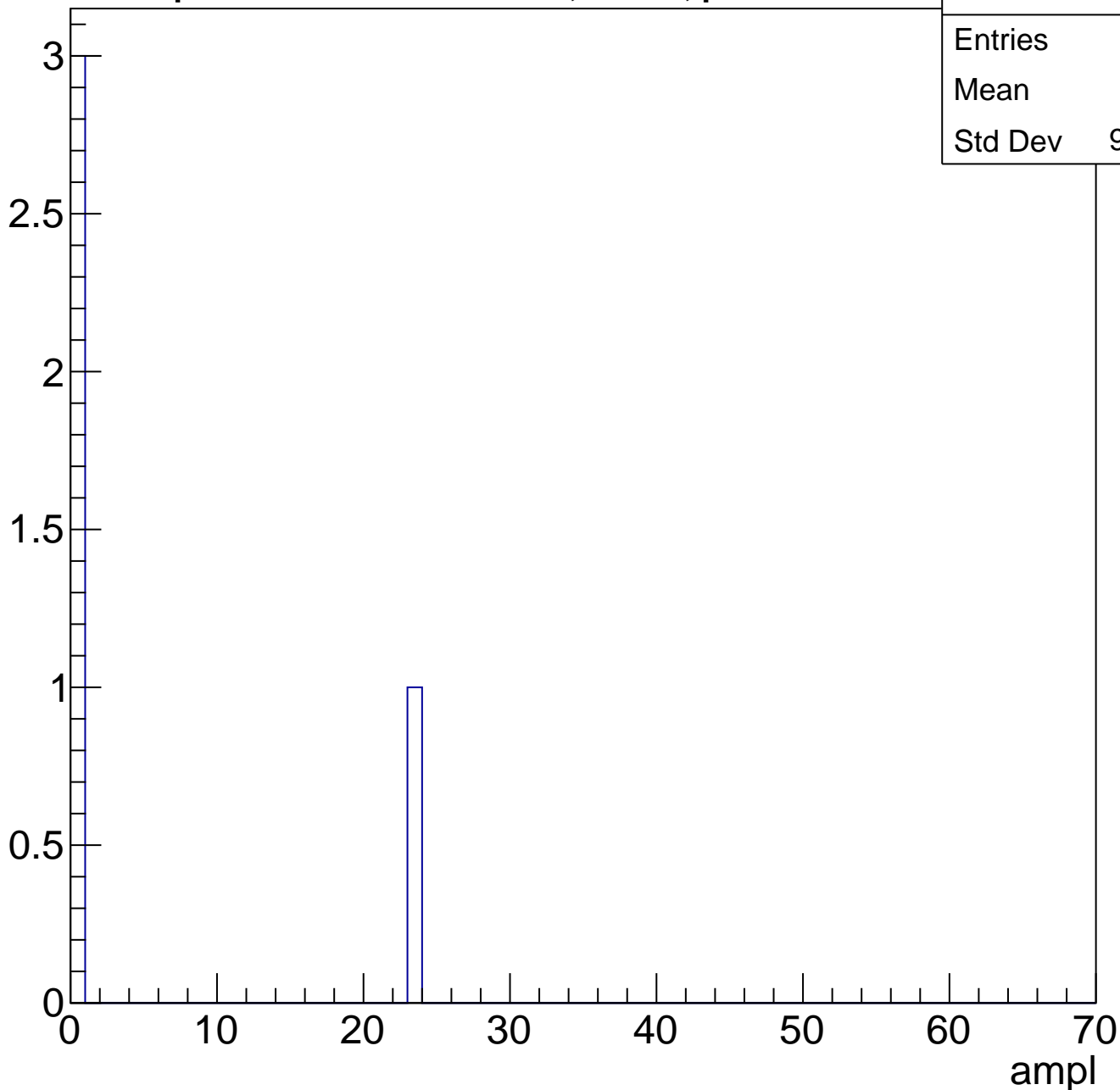




# B1L102S, U24-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch122, adc0

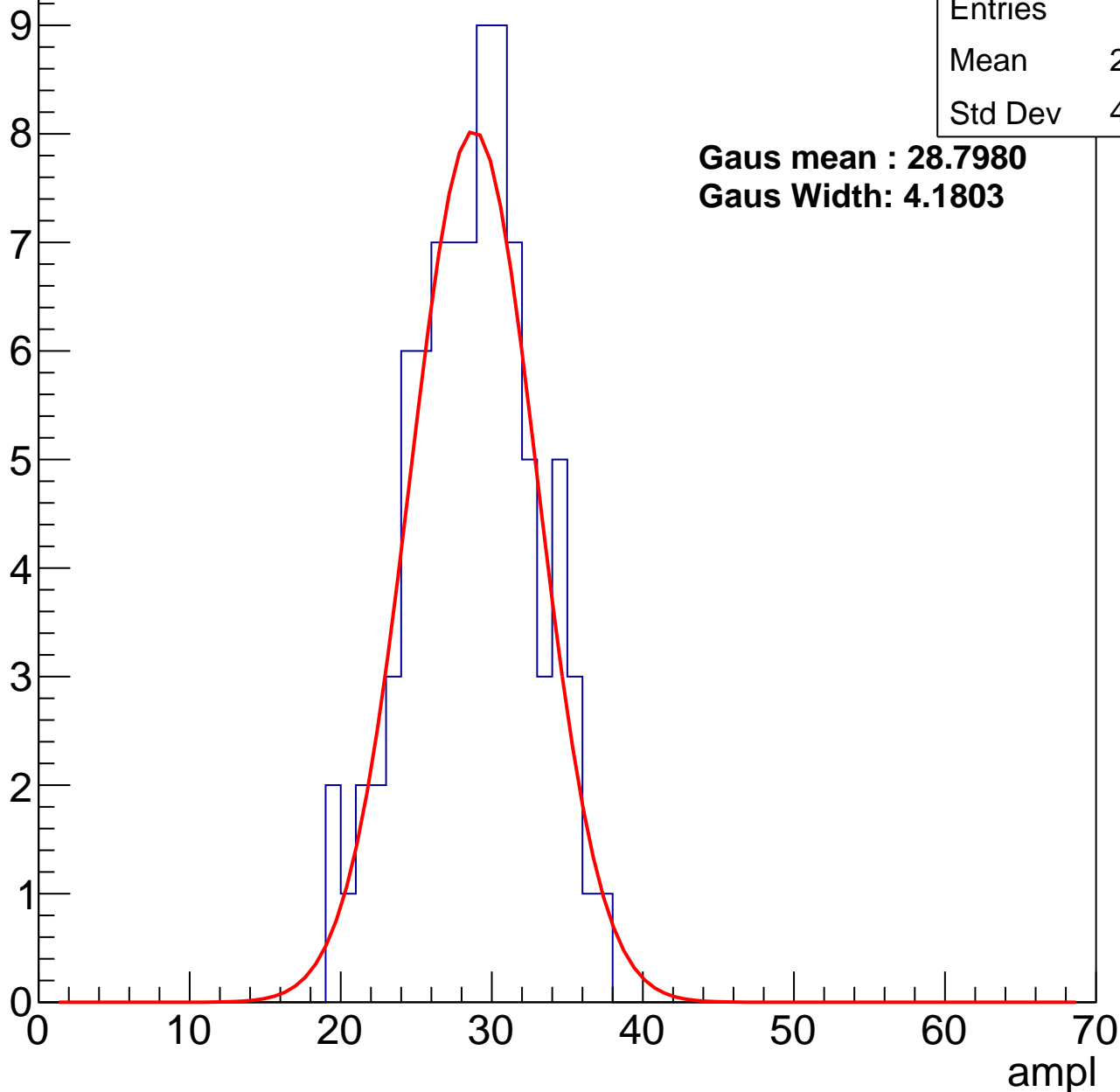
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.24
Std Dev	4.014

**Gaus mean : 28.7980**

**Gaus Width: 4.1803**



# B1L102S, U24-ch122, adc1

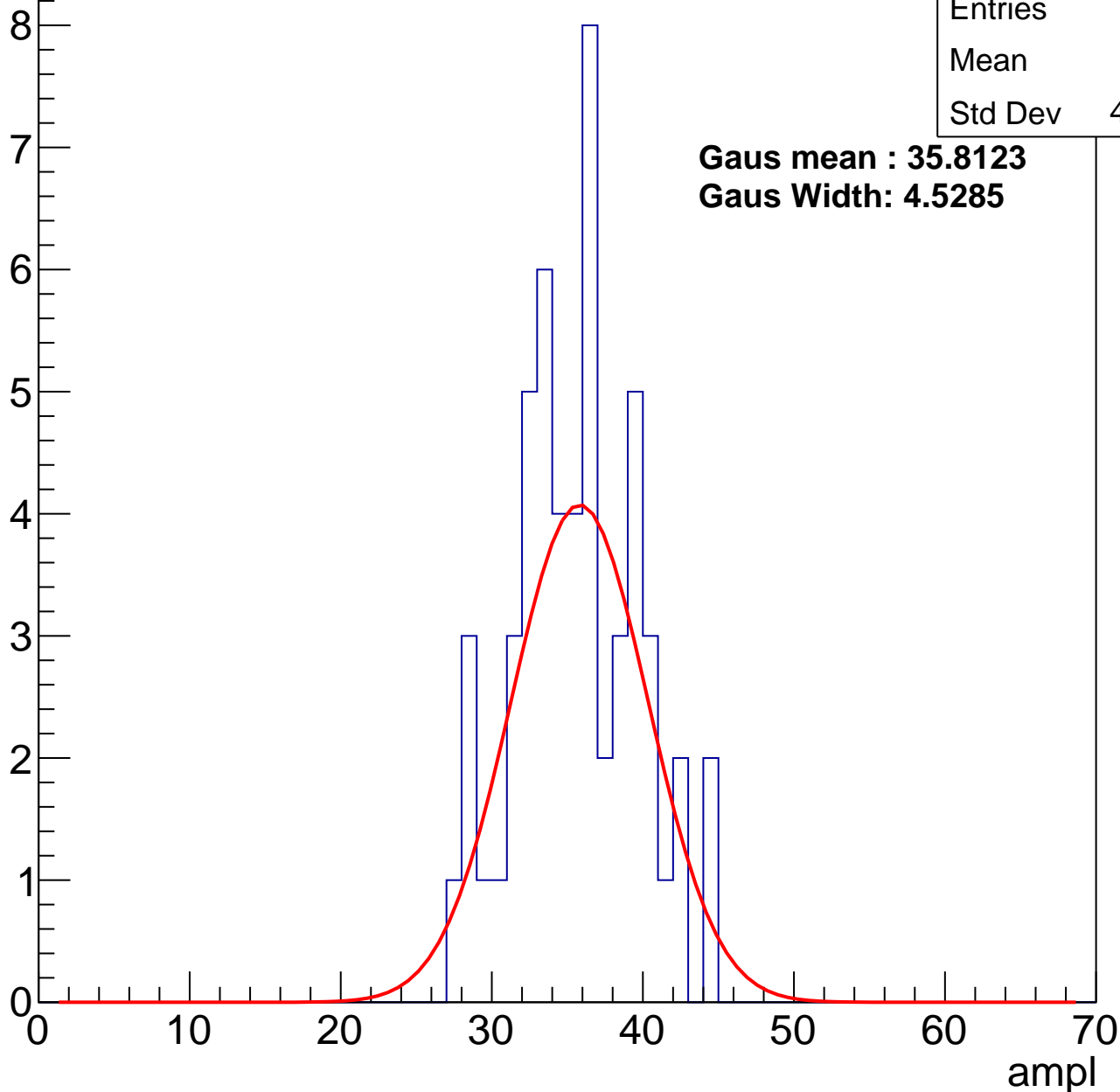
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.2
Std Dev	4.048

**Gaus mean : 35.8123**

**Gaus Width: 4.5285**



# B1L102S, U24-ch122, adc2

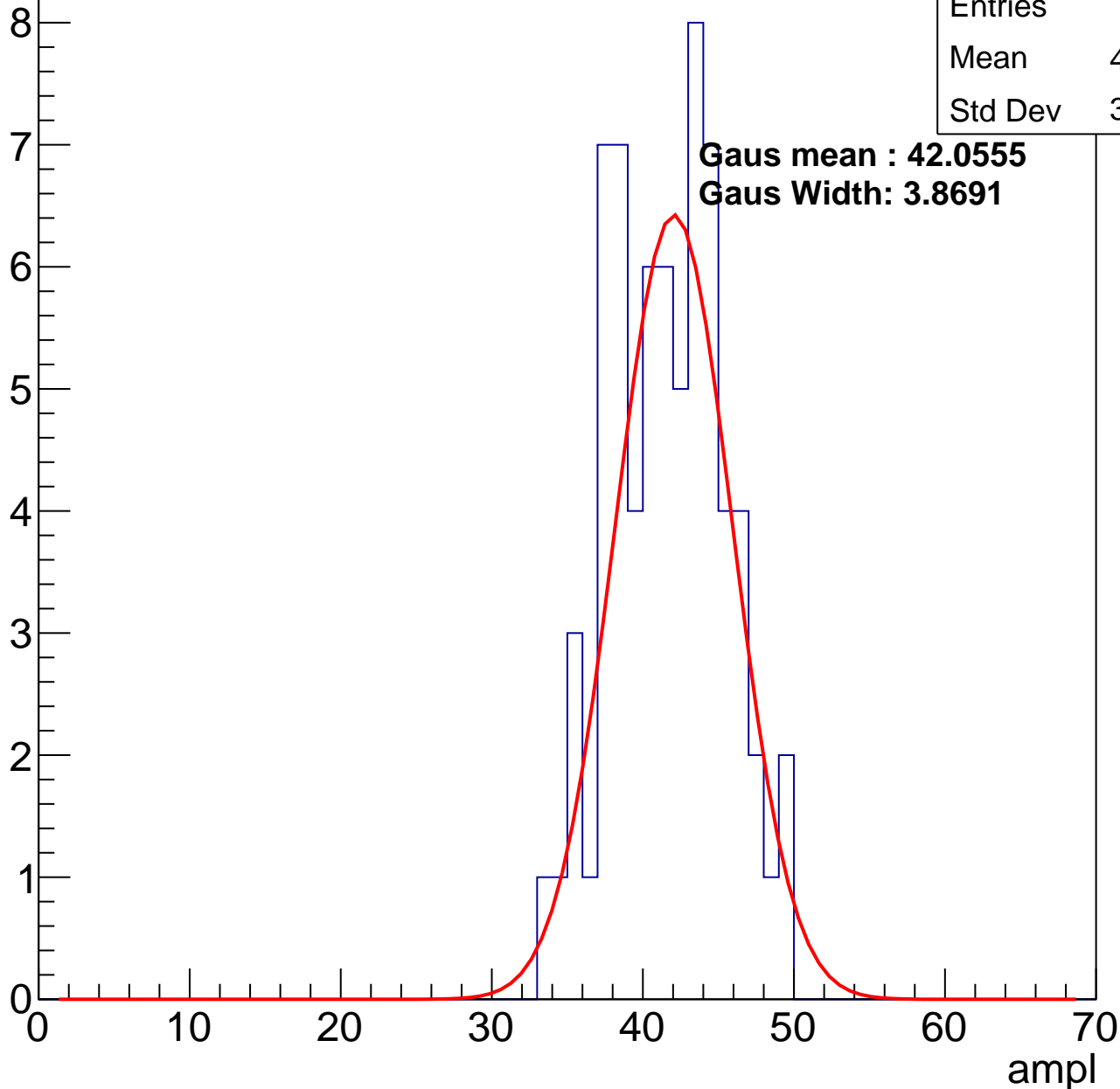
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.17
Std Dev	3.718

**Gaus mean : 42.0555**

**Gaus Width: 3.8691**

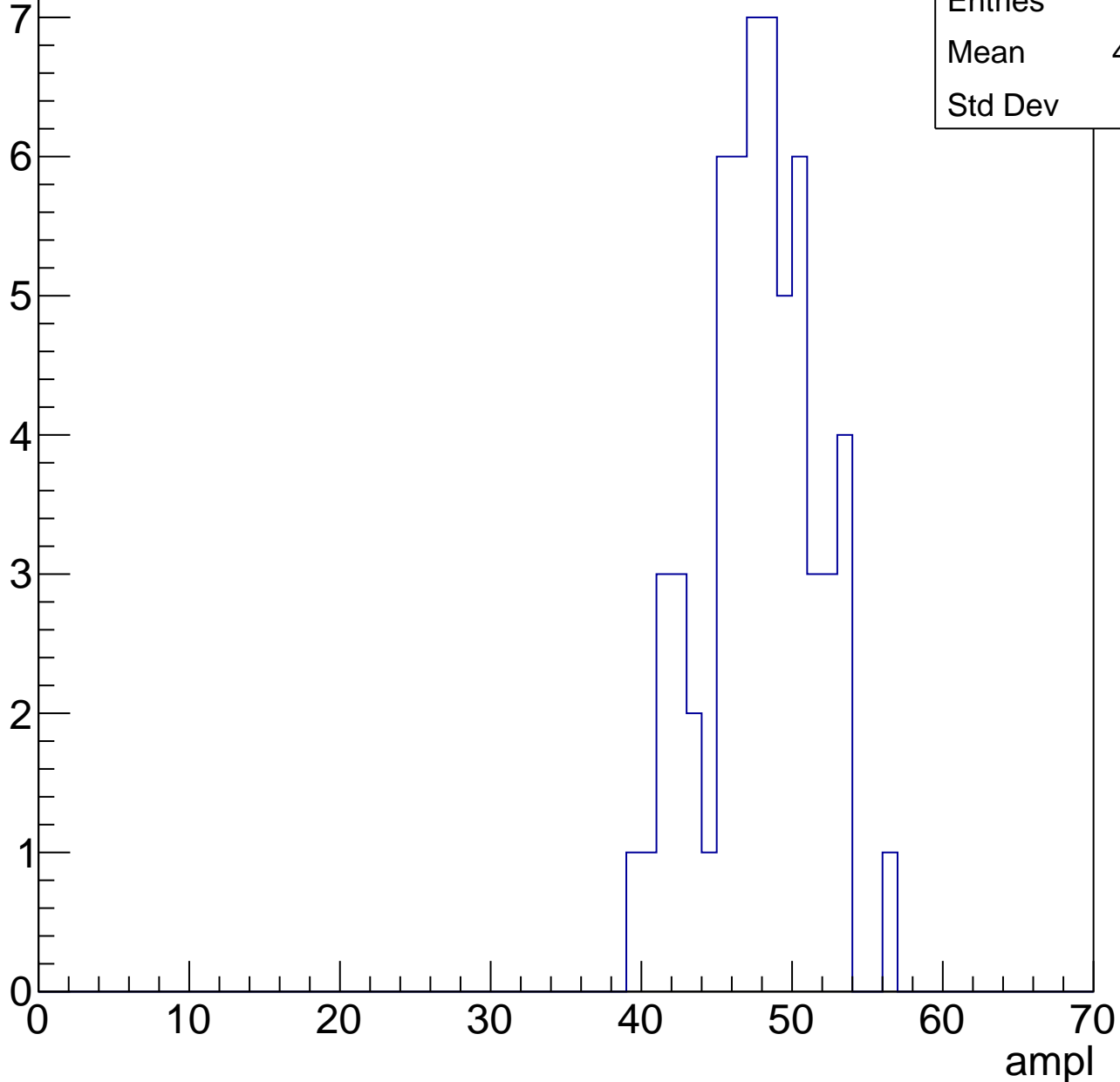


# B1L102S, U24-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.31
Std Dev	3.66

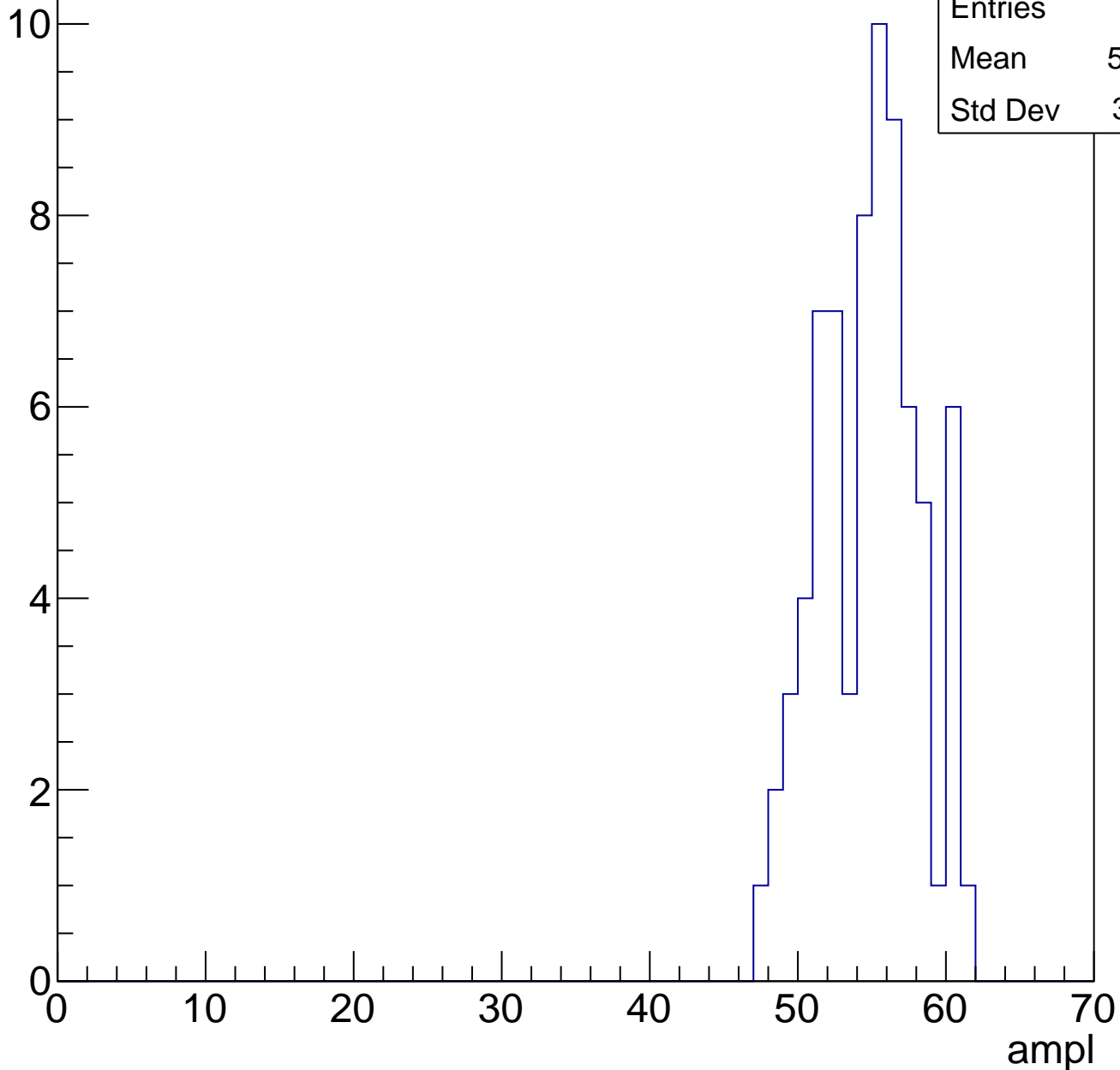


# B1L102S, U24-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	54.36
Std Dev	3.341

Entry



# B1L102S, U24-ch122, adc5

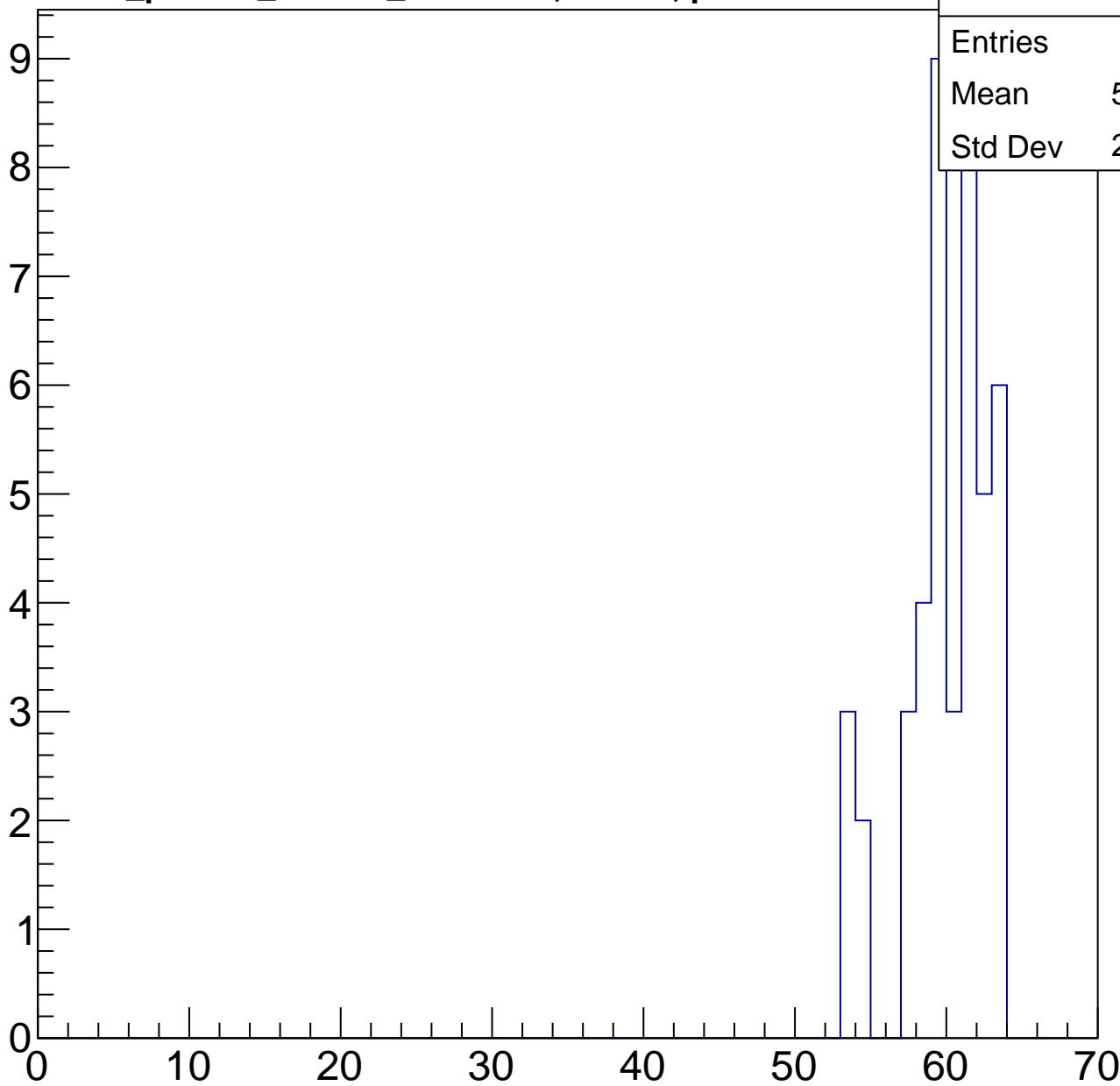
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	59.47
Std Dev	2.815

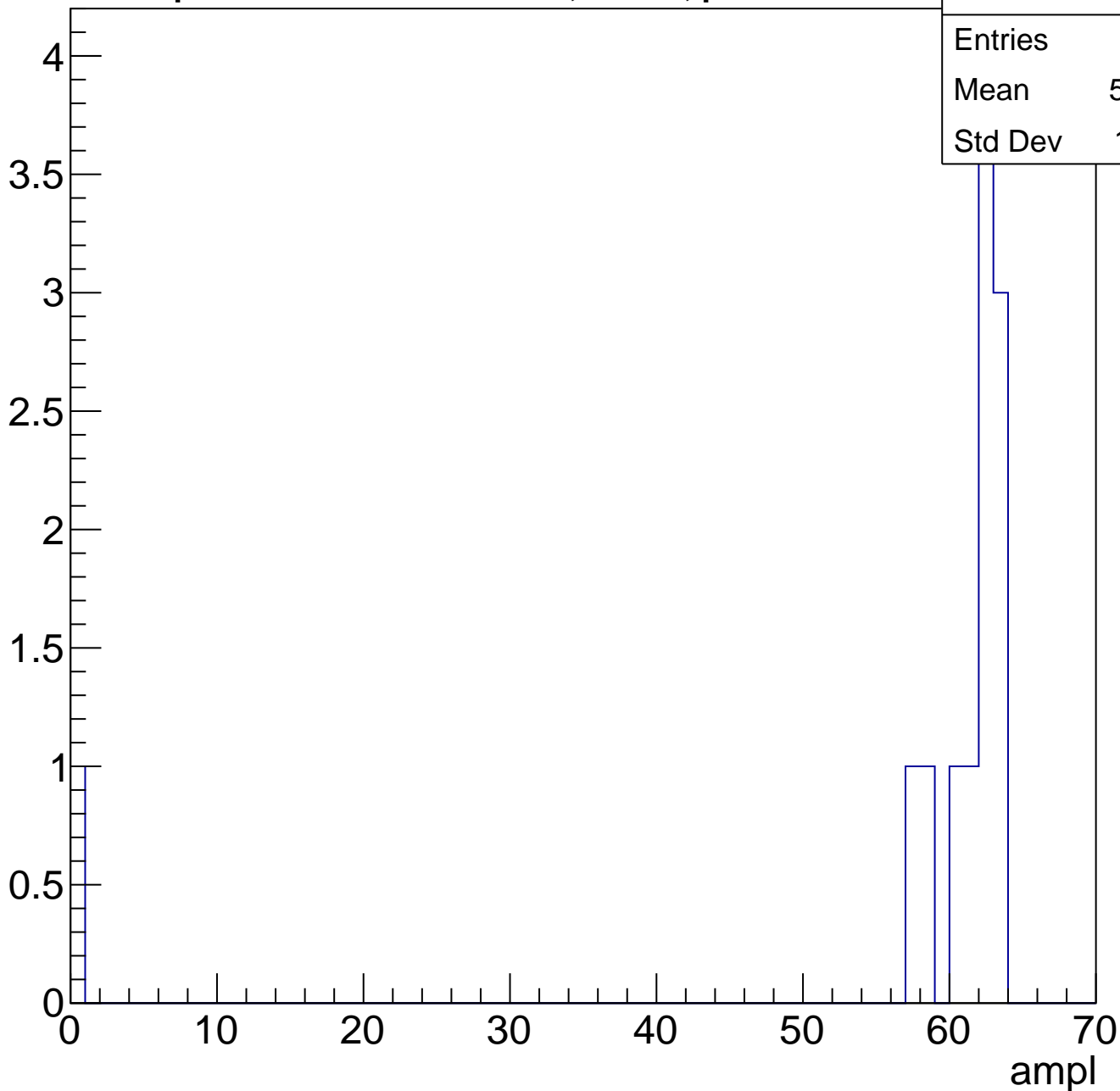
ampl



# B1L102S, U24-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch123, adc0

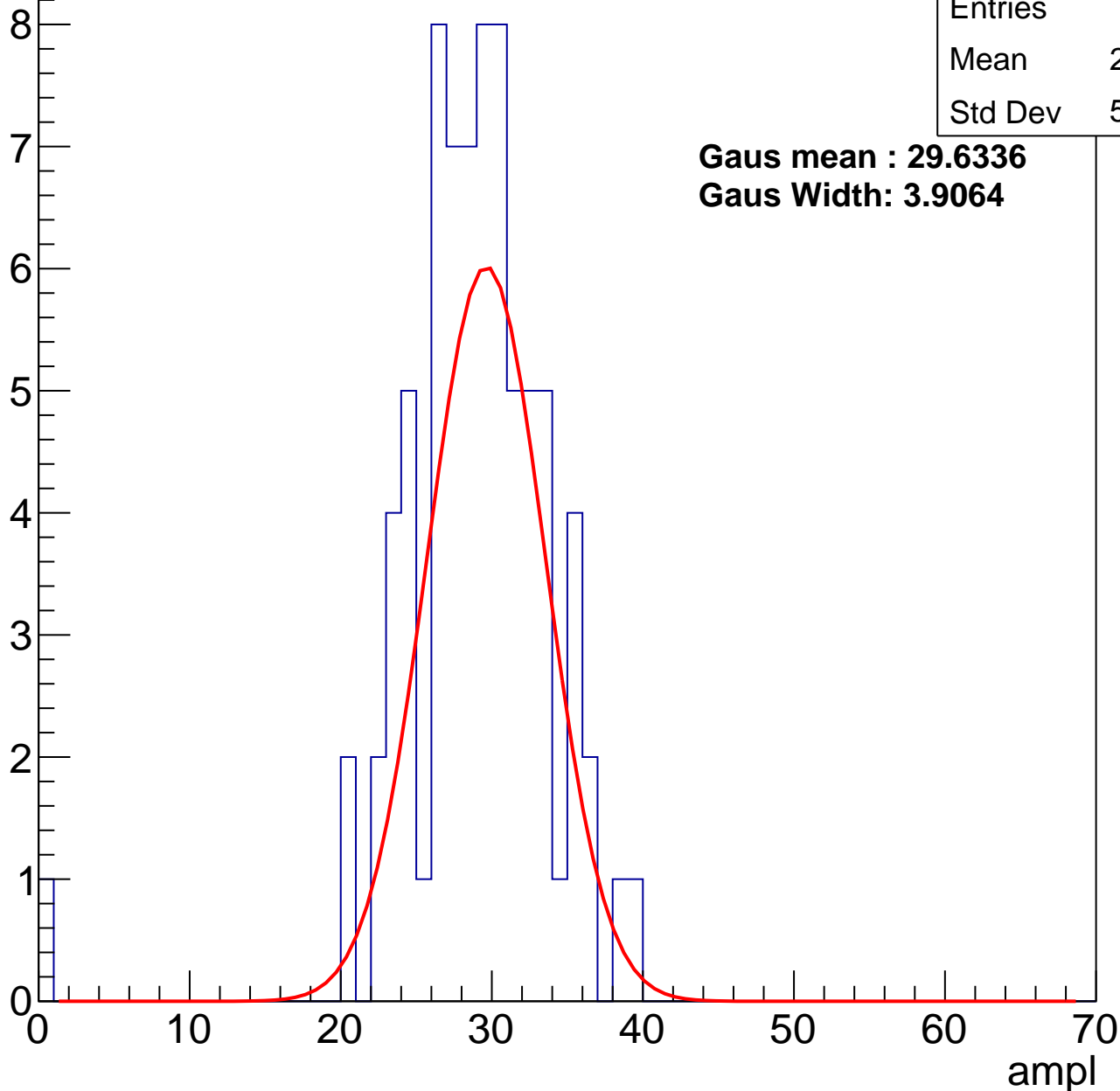
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.43
Std Dev	5.183

**Gaus mean : 29.6336**

**Gaus Width: 3.9064**



# B1L102S, U24-ch123, adc1

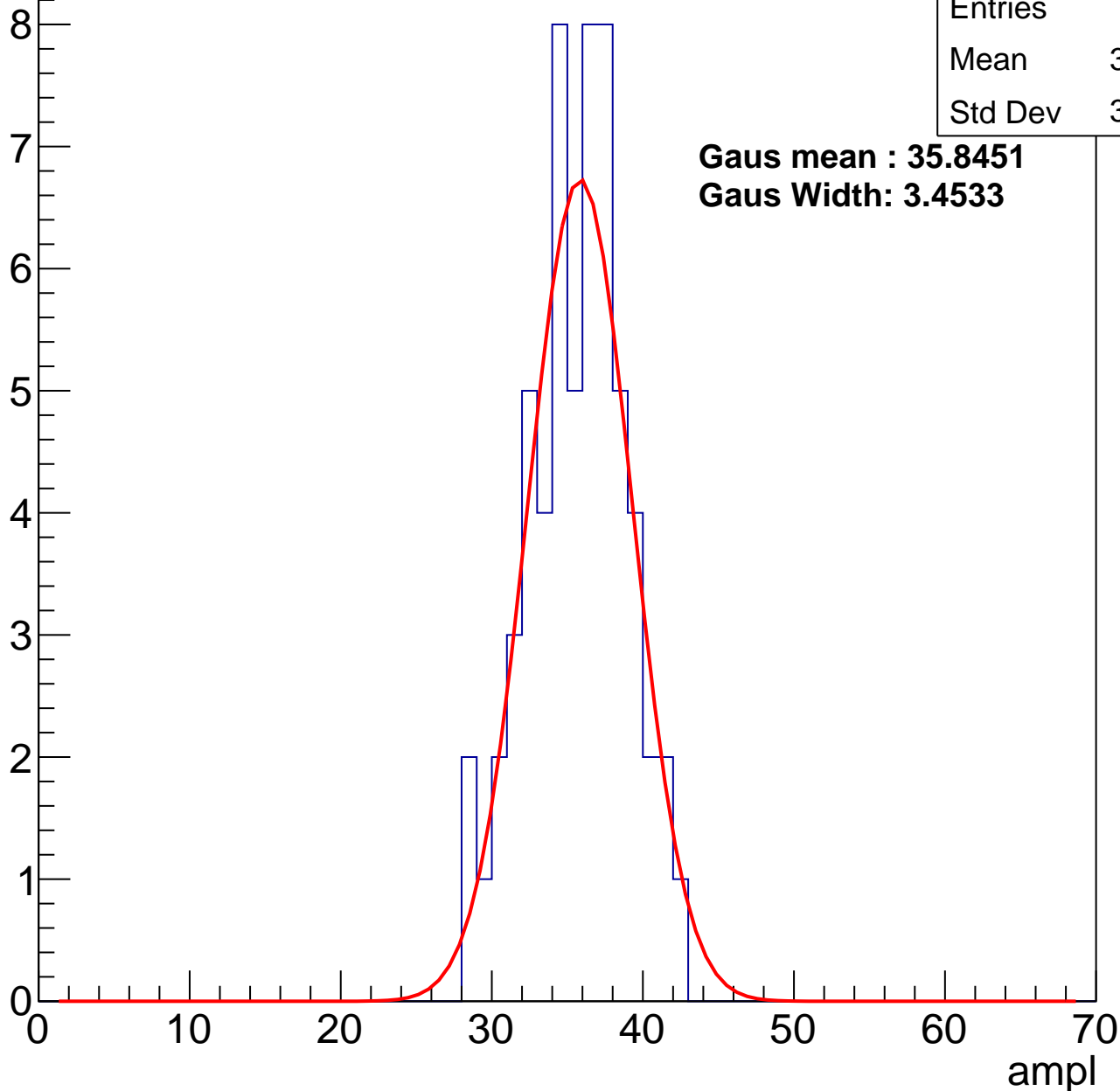
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.18
Std Dev	3.212

**Gaus mean : 35.8451**

**Gaus Width: 3.4533**



# B1L102S, U24-ch123, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.45
Std Dev	3.365

**Gaus mean : 42.9677**

**Gaus Width: 3.4523**

10

8

6

4

2

0

0

10

20

30

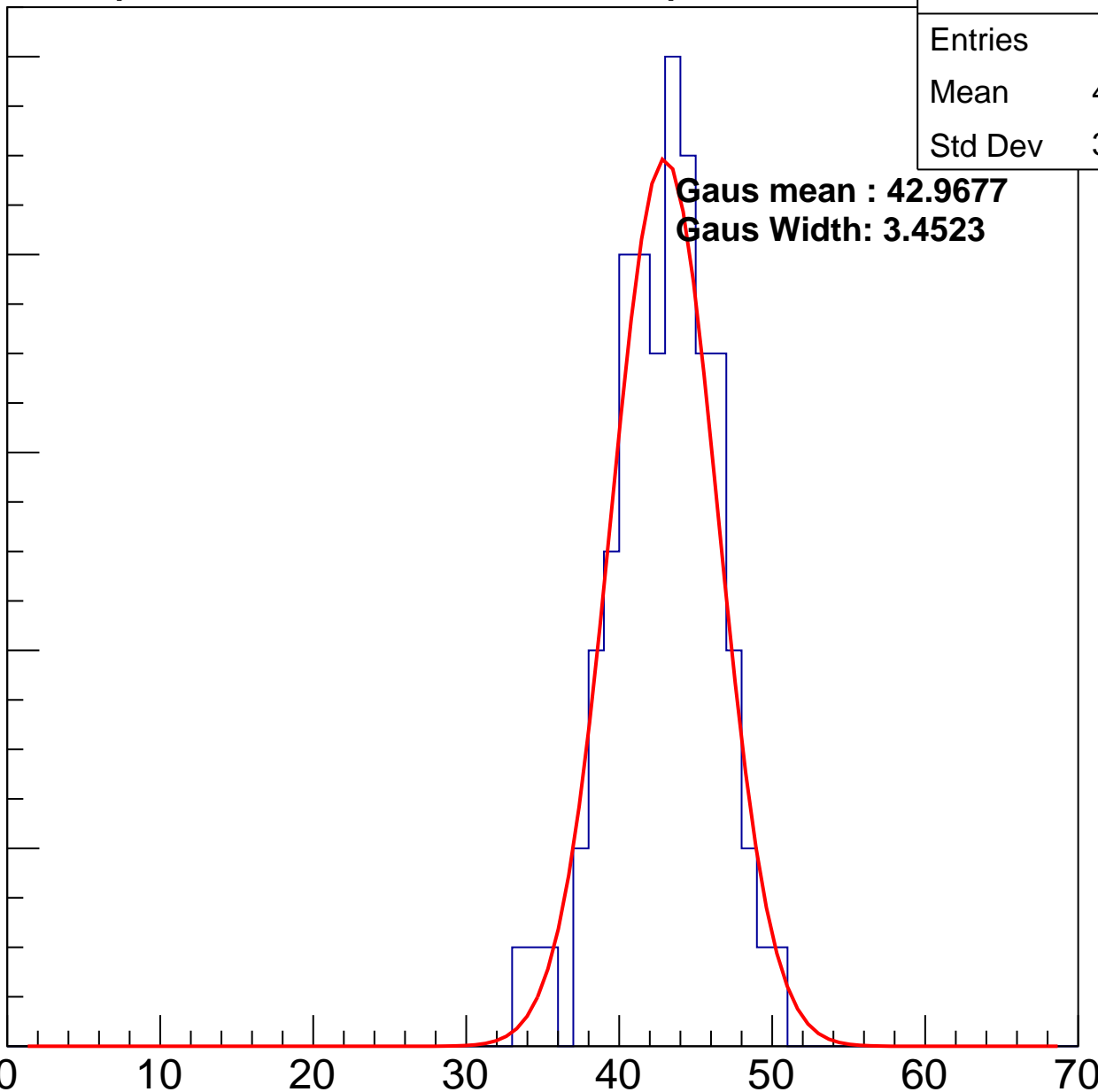
40

50

60

70

ampl

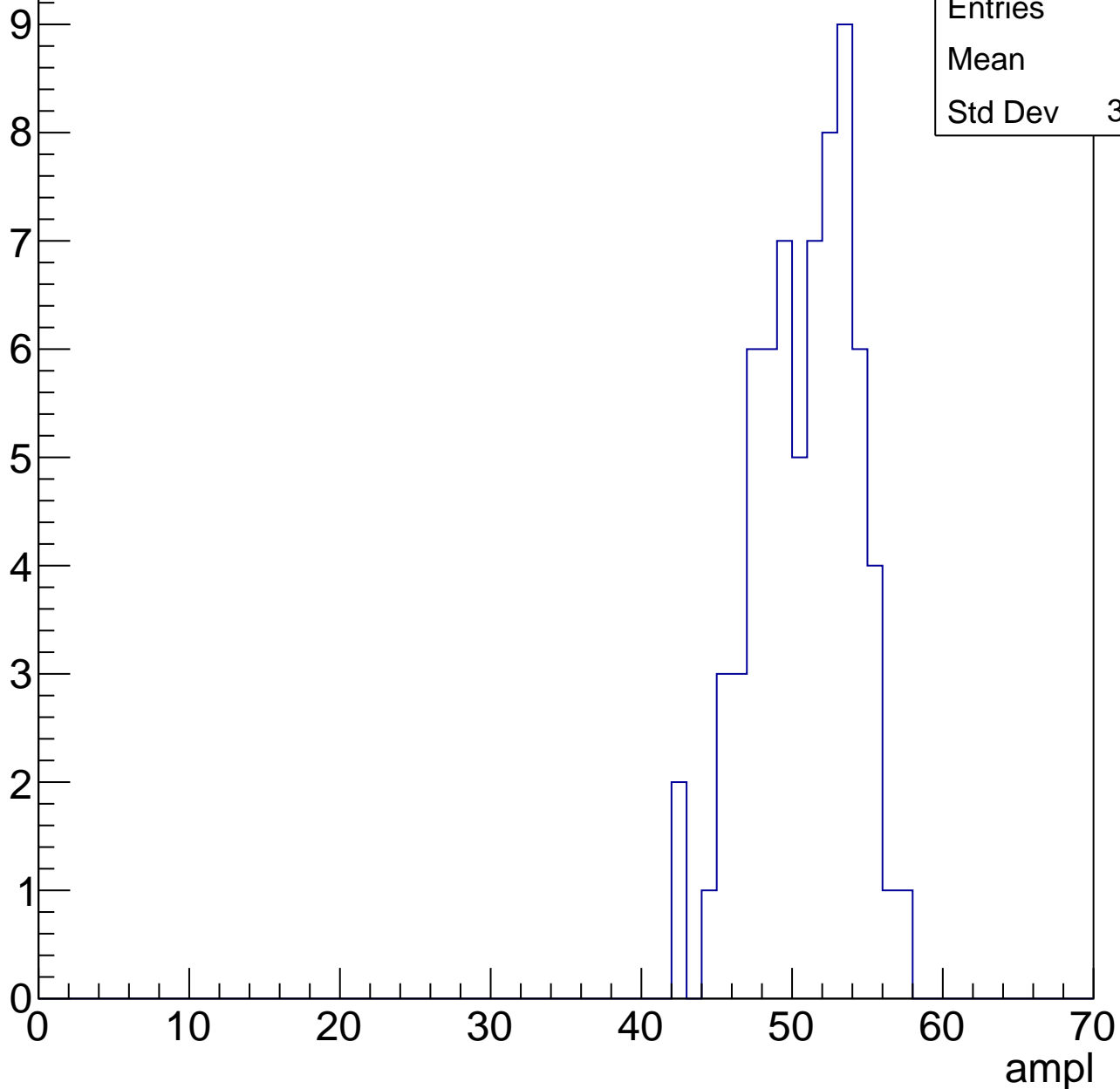


# B1L102S, U24-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.3
Std Dev	3.333

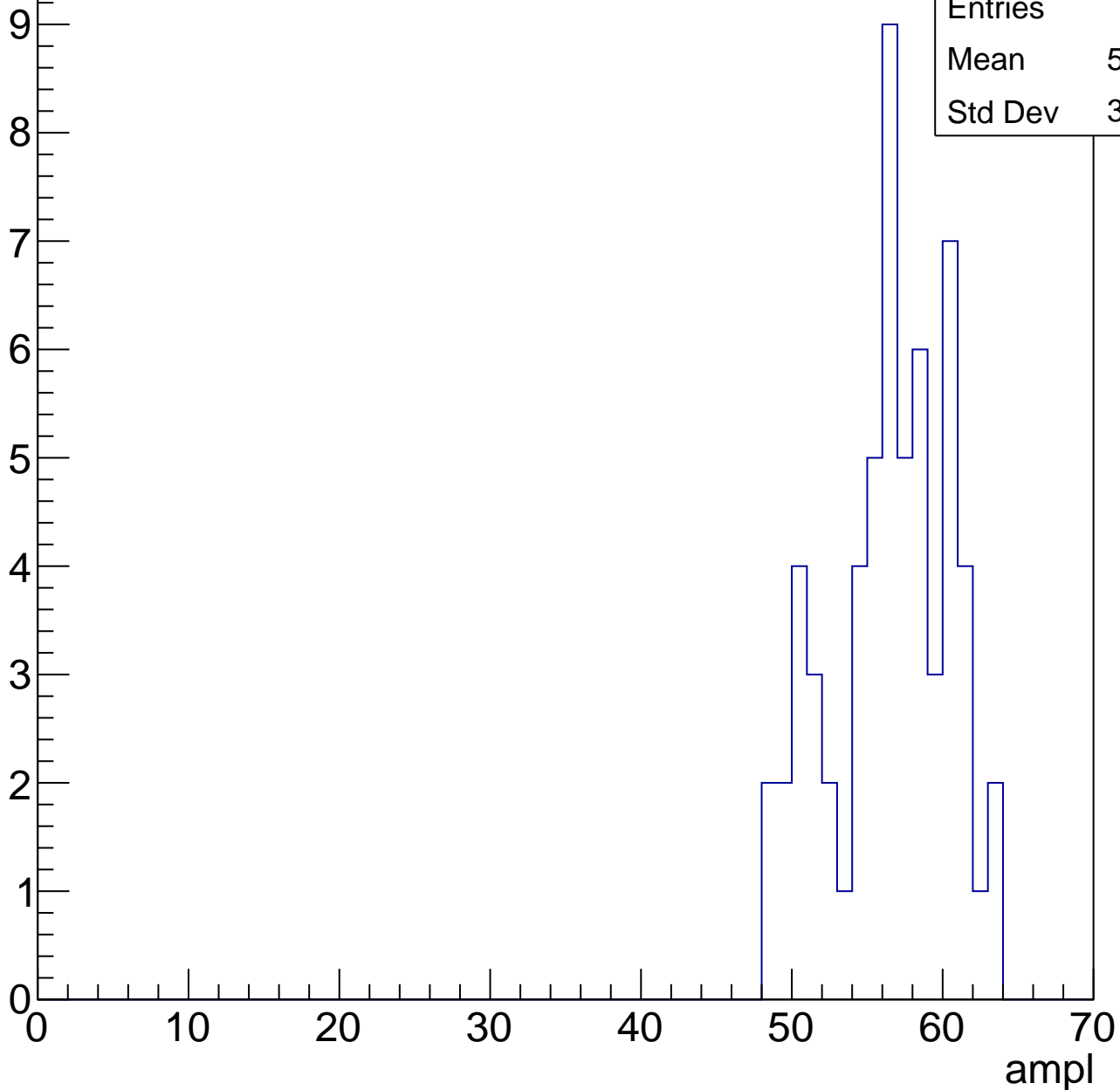


# B1L102S, U24-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

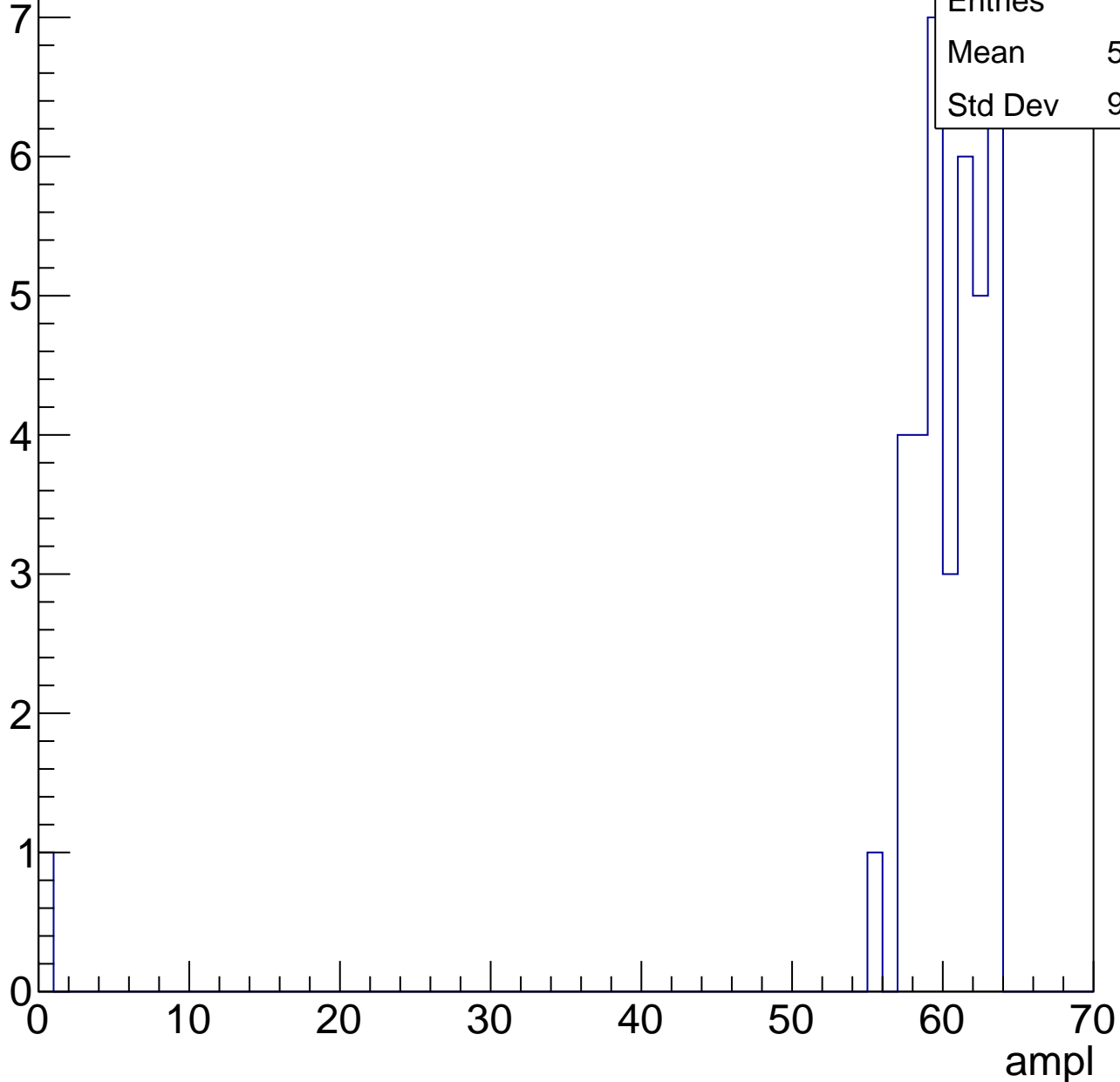
Entries	60
Mean	56.02
Std Dev	3.892



# B1L102S, U24-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

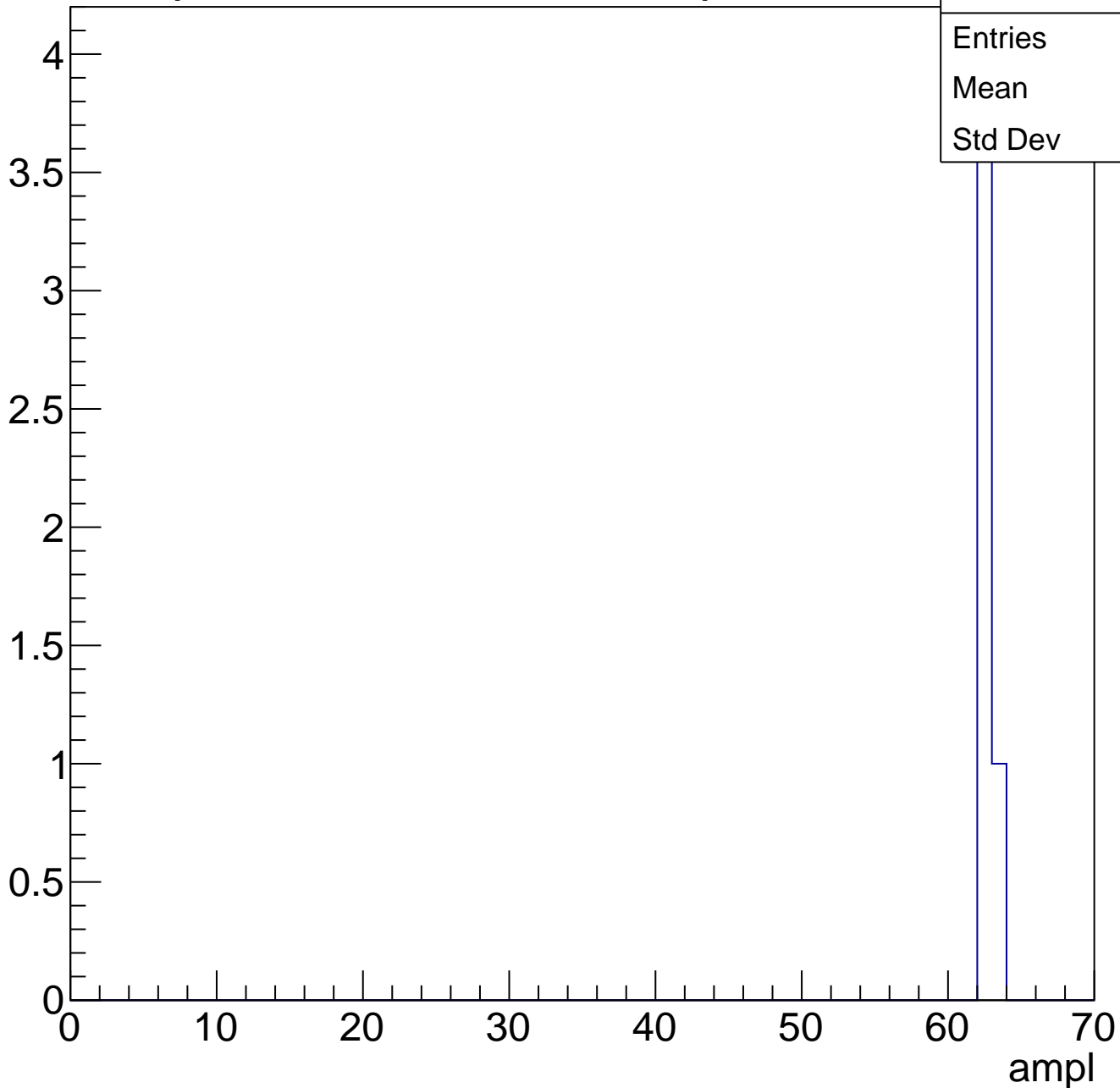
Entry



# B1L102S, U24-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	5
Mean	62.2
Std Dev	0.4



# B1L102S, U24-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch124, adc0

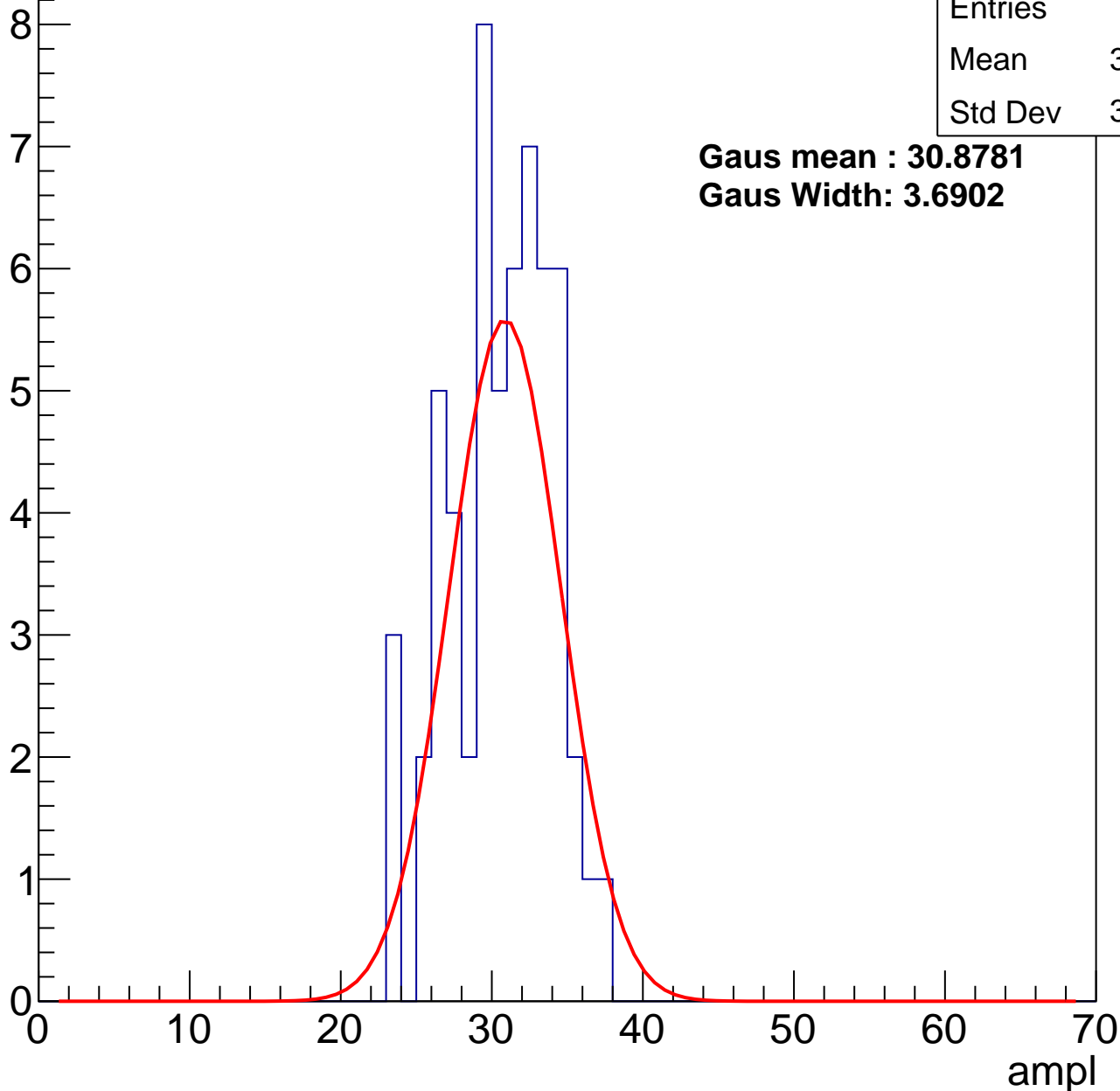
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	30.17
Std Dev	3.338

**Gaus mean : 30.8781**

**Gaus Width: 3.6902**



# B1L102S, U24-ch124, adc1

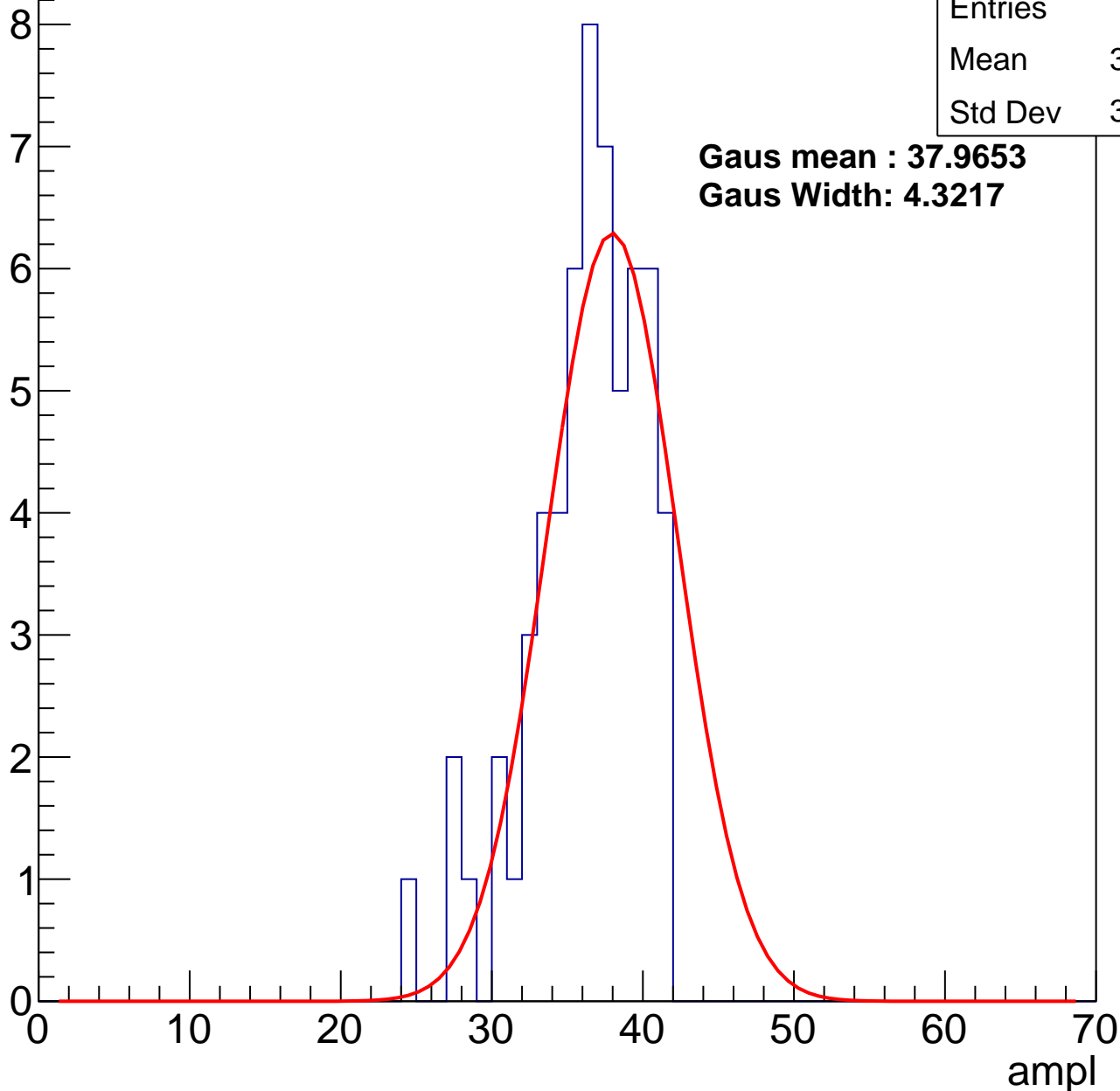
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.77
Std Dev	3.757

**Gaus mean : 37.9653**

**Gaus Width: 4.3217**



# B1L102S, U24-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

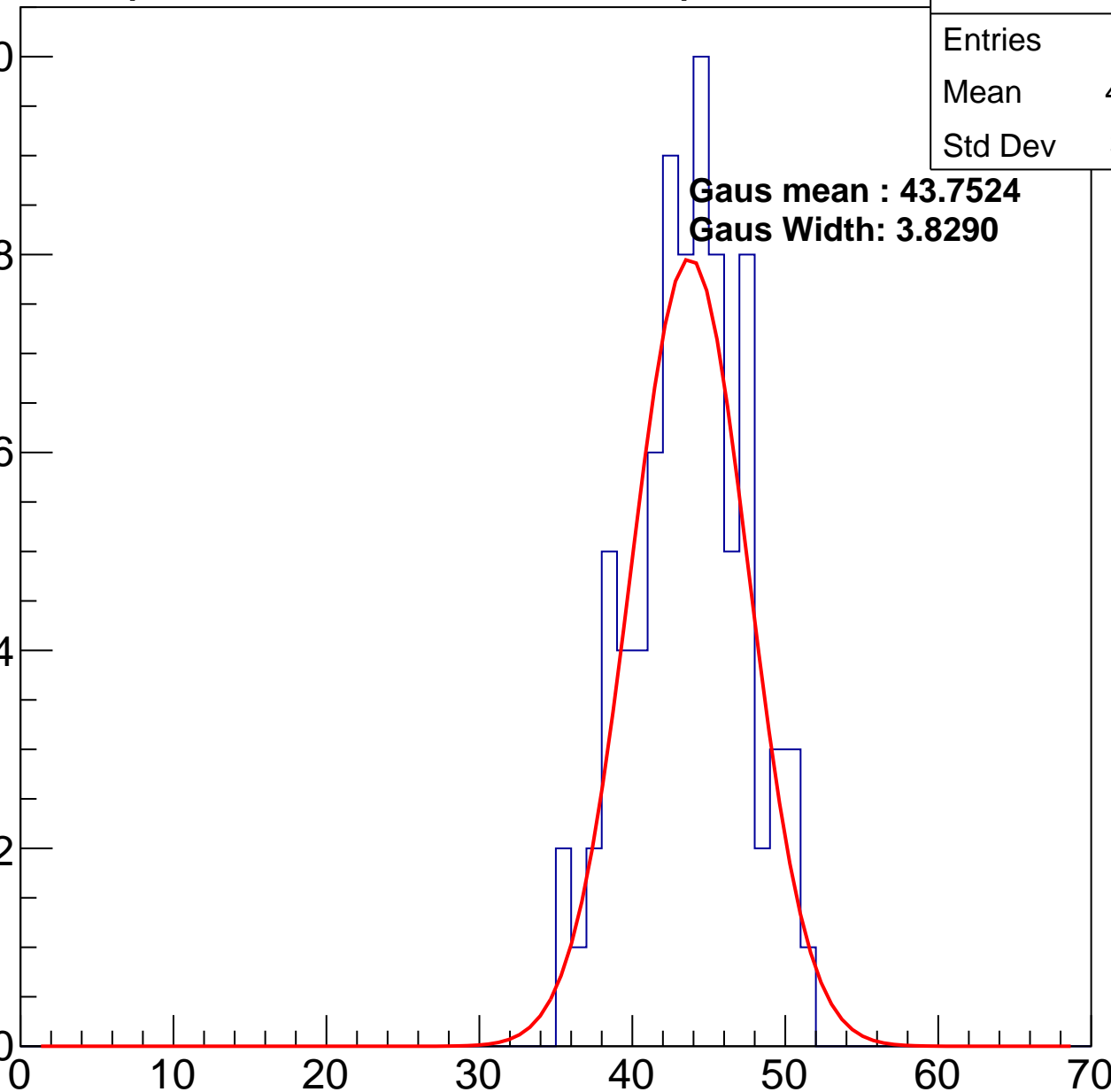
Entries	81
Mean	43.26
Std Dev	3.661

**Gaus mean : 43.7524**

**Gaus Width: 3.8290**

10  
8  
6  
4  
2  
0

ampl

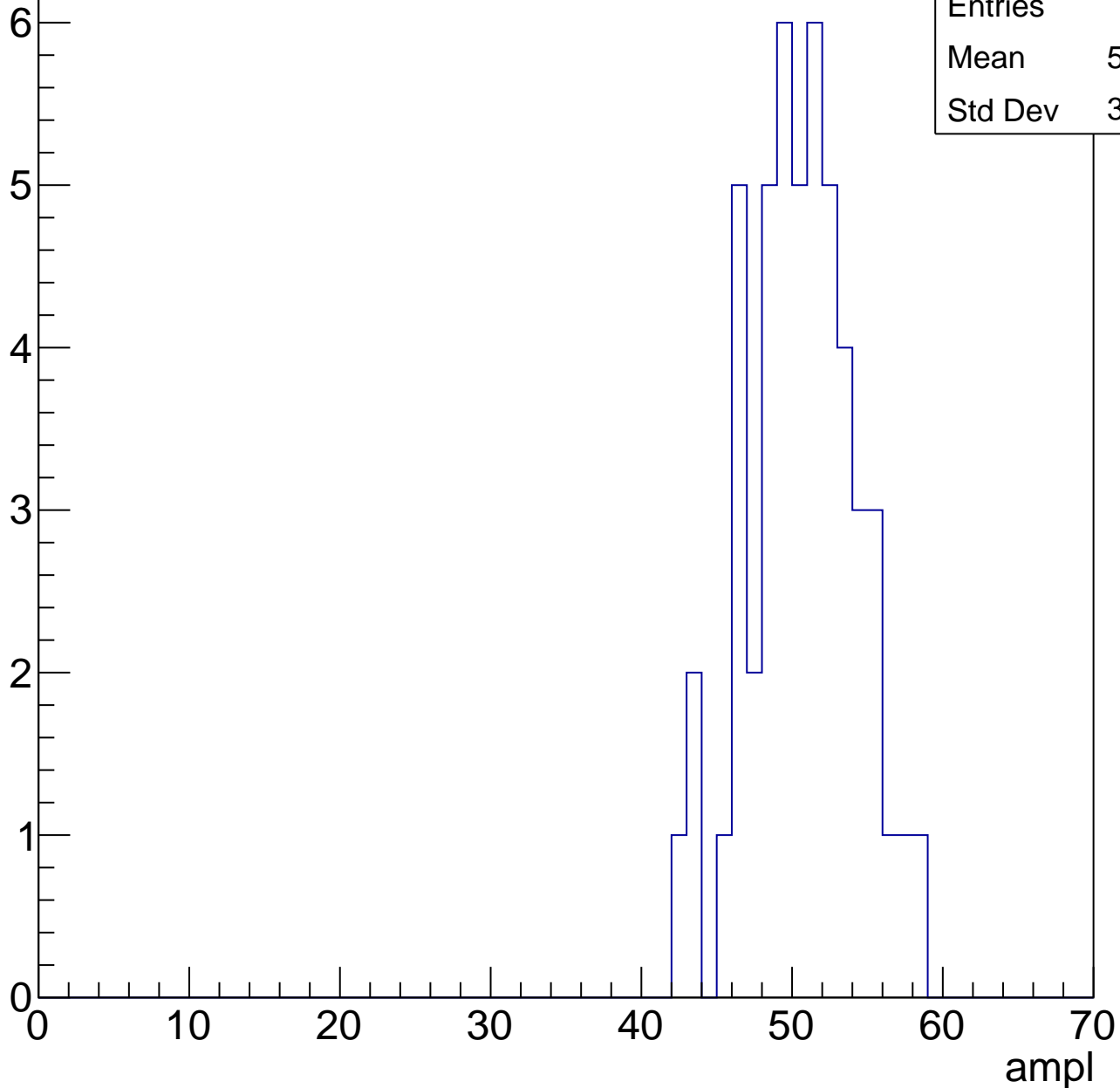


# B1L102S, U24-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	50.14
Std Dev	3.554

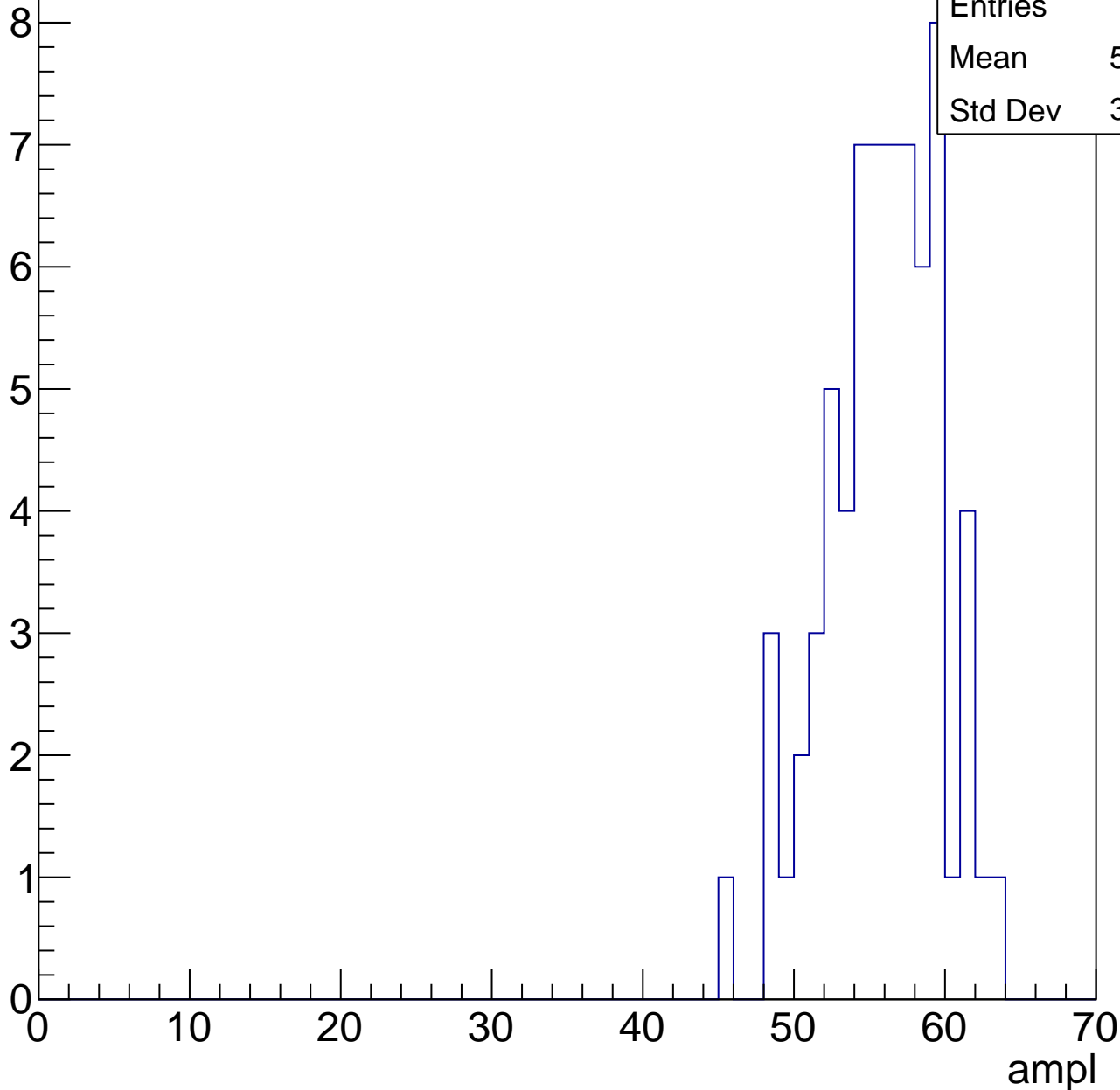


# B1L102S, U24-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

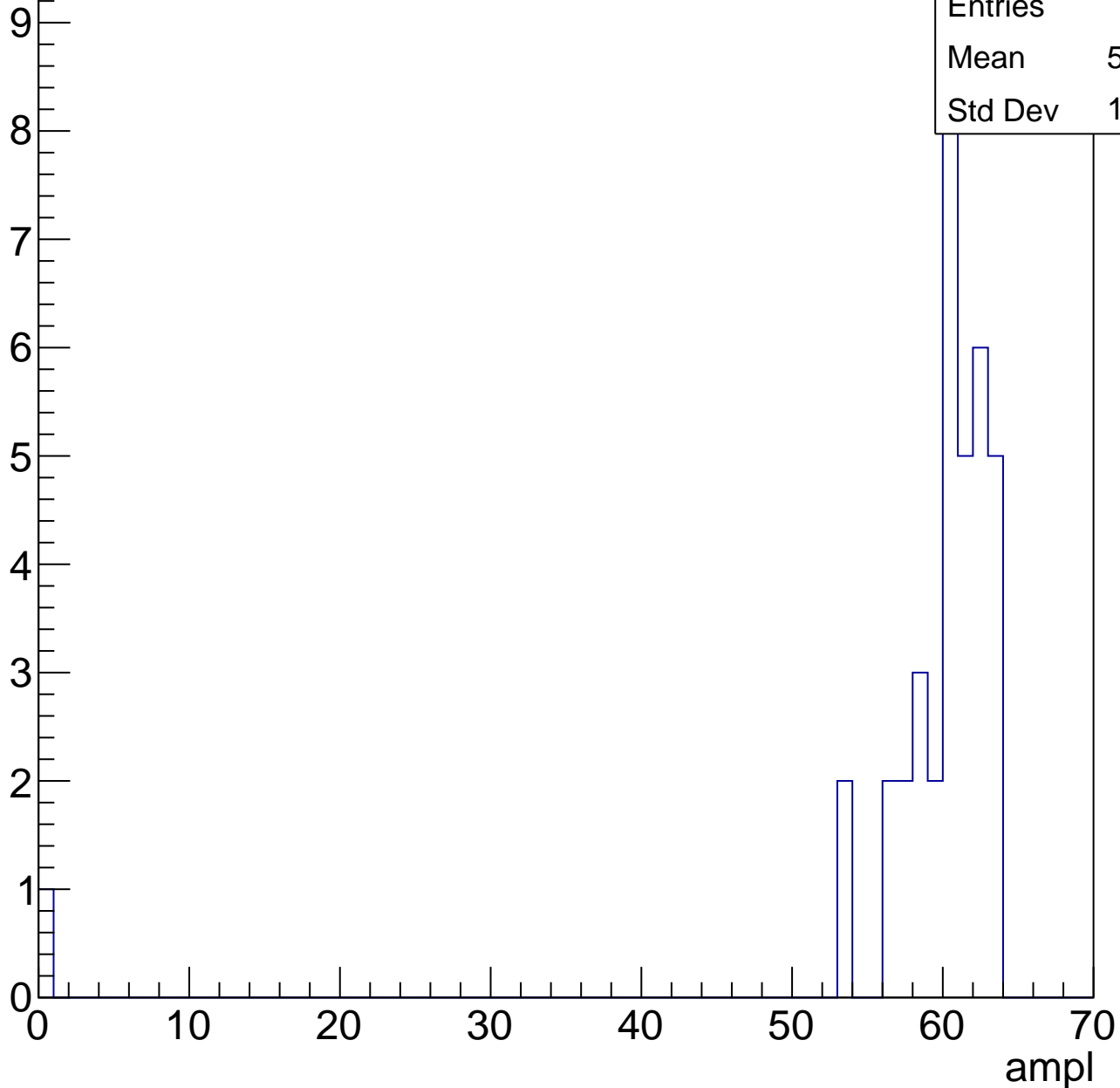
Entries	68
Mean	55.38
Std Dev	3.714



# B1L102S, U24-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch125, adc0

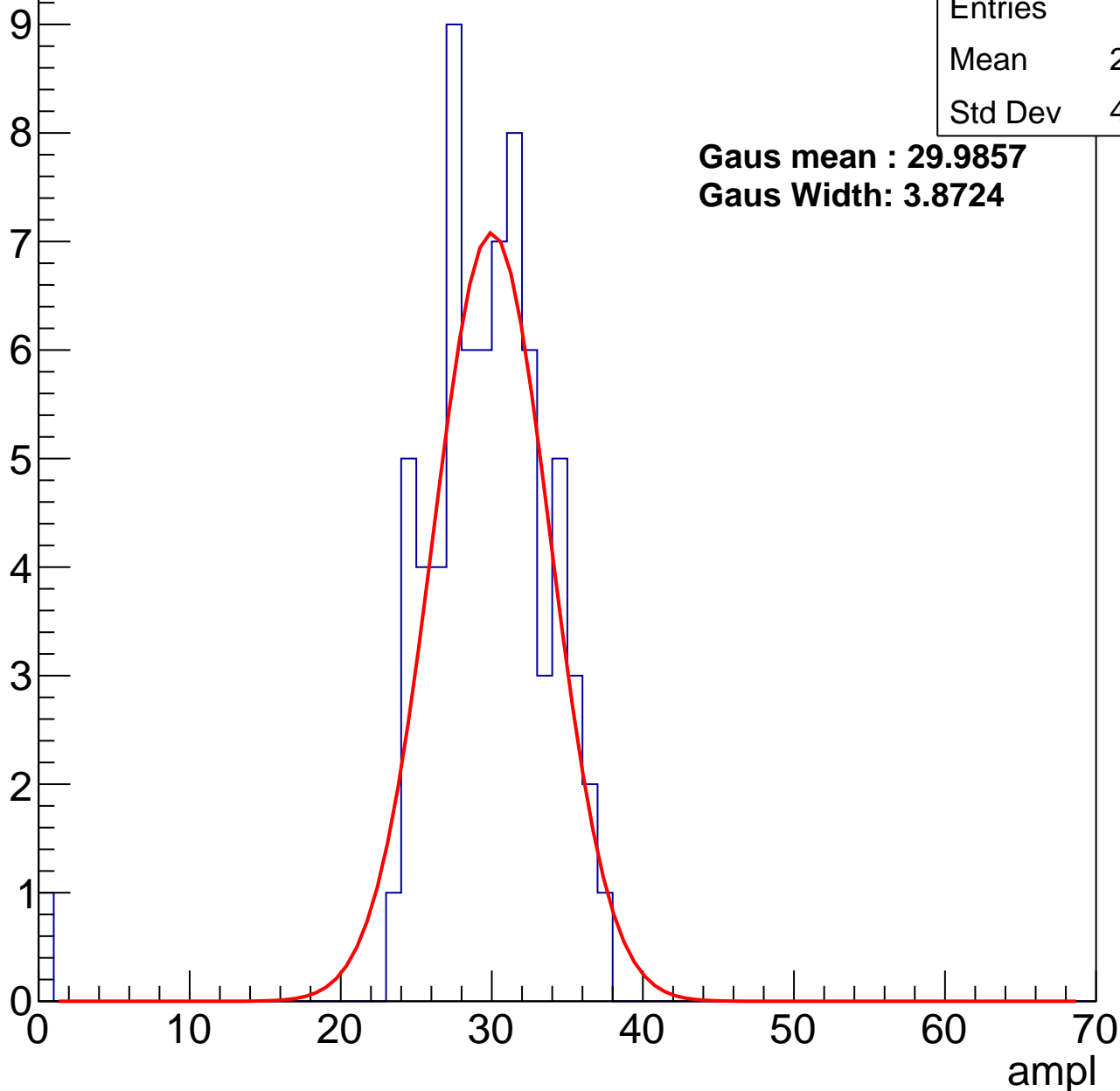
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.08
Std Dev	4.864

**Gaus mean : 29.9857**

**Gaus Width: 3.8724**



# B1L102S, U24-ch125, adc1

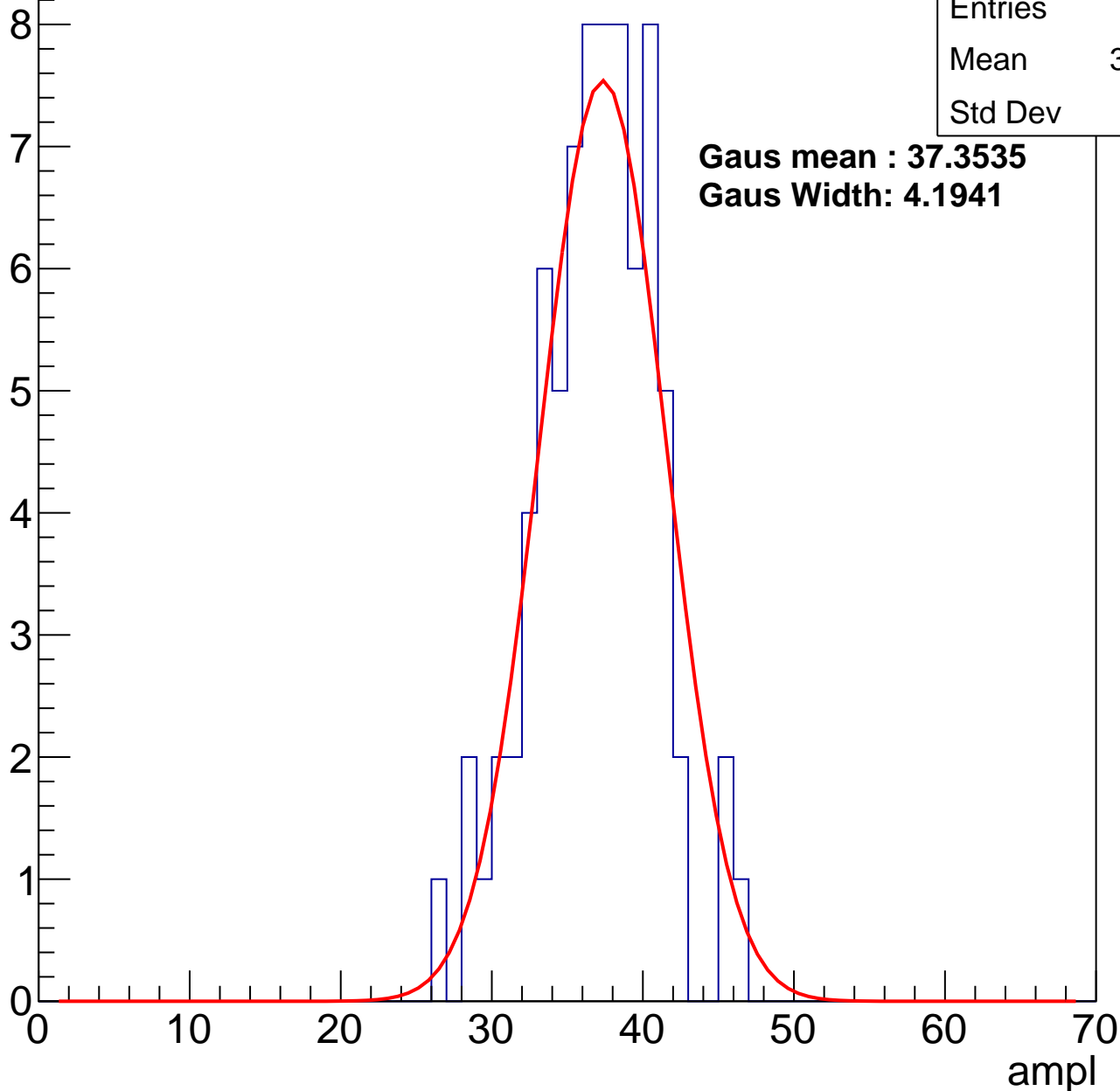
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.42
Std Dev	3.94

**Gaus mean : 37.3535**

**Gaus Width: 4.1941**



# B1L102S, U24-ch125, adc2

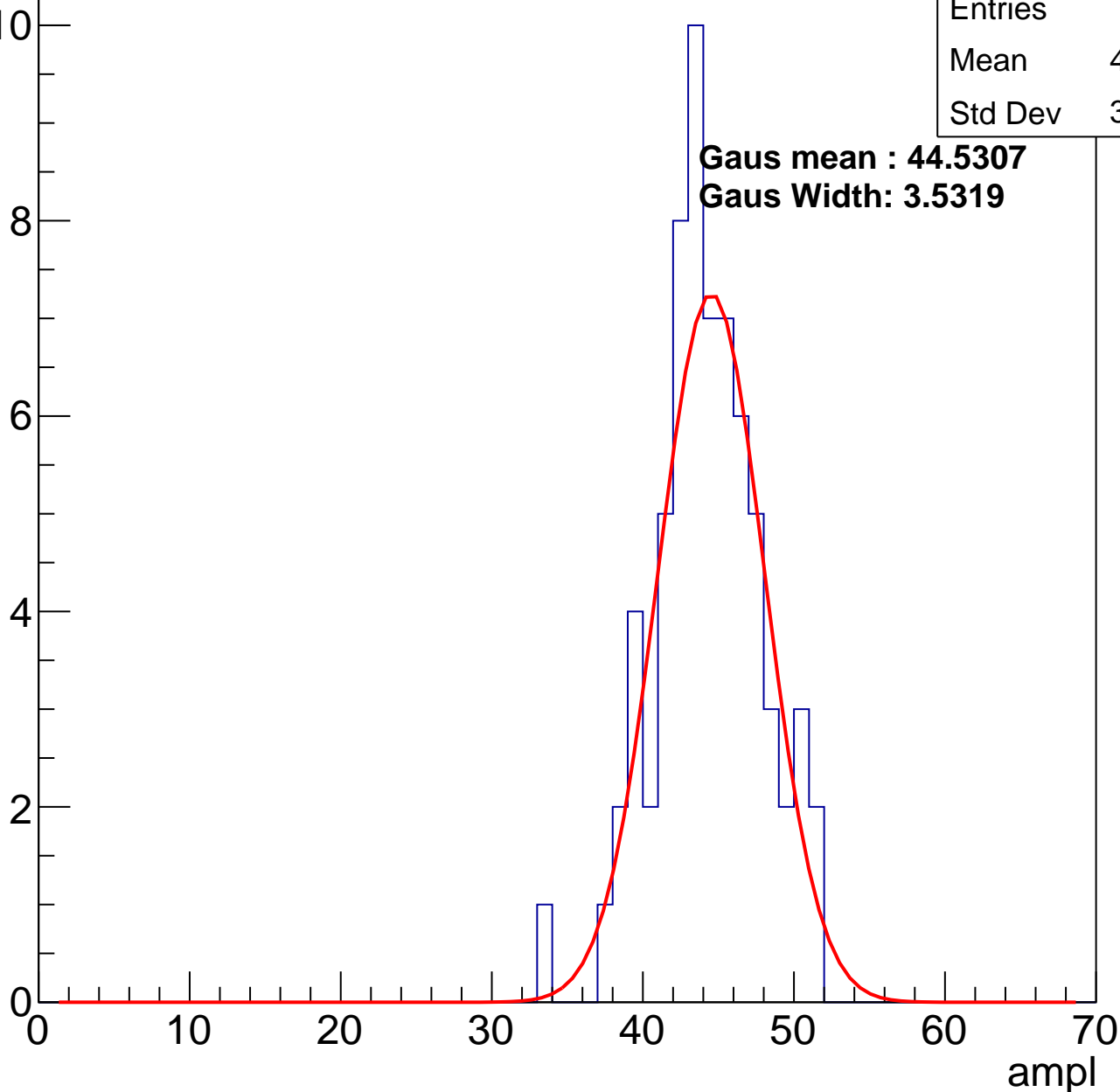
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.84
Std Dev	3.513

**Gaus mean : 44.5307**

**Gaus Width: 3.5319**

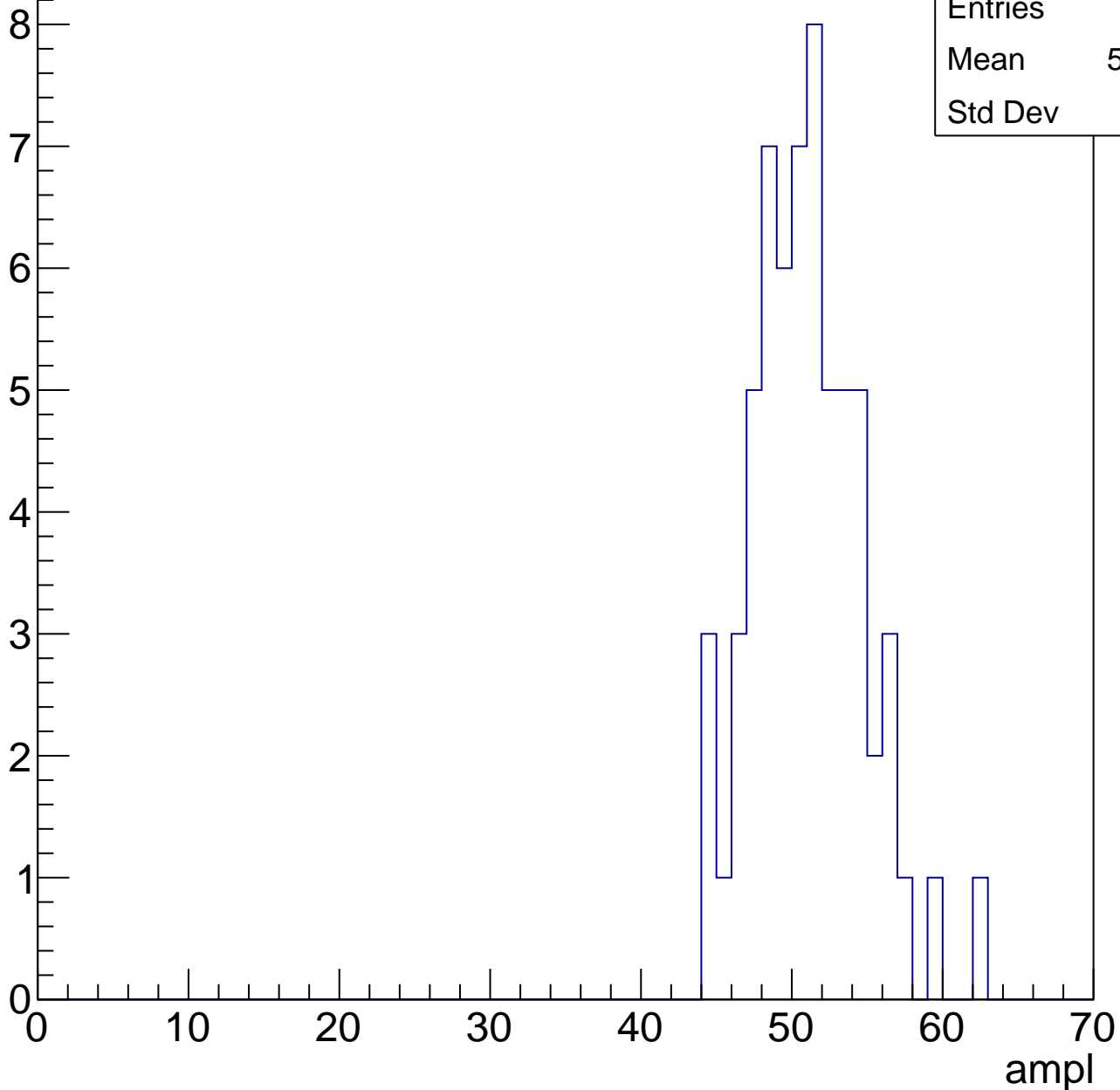


# B1L102S, U24-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.62
Std Dev	3.61

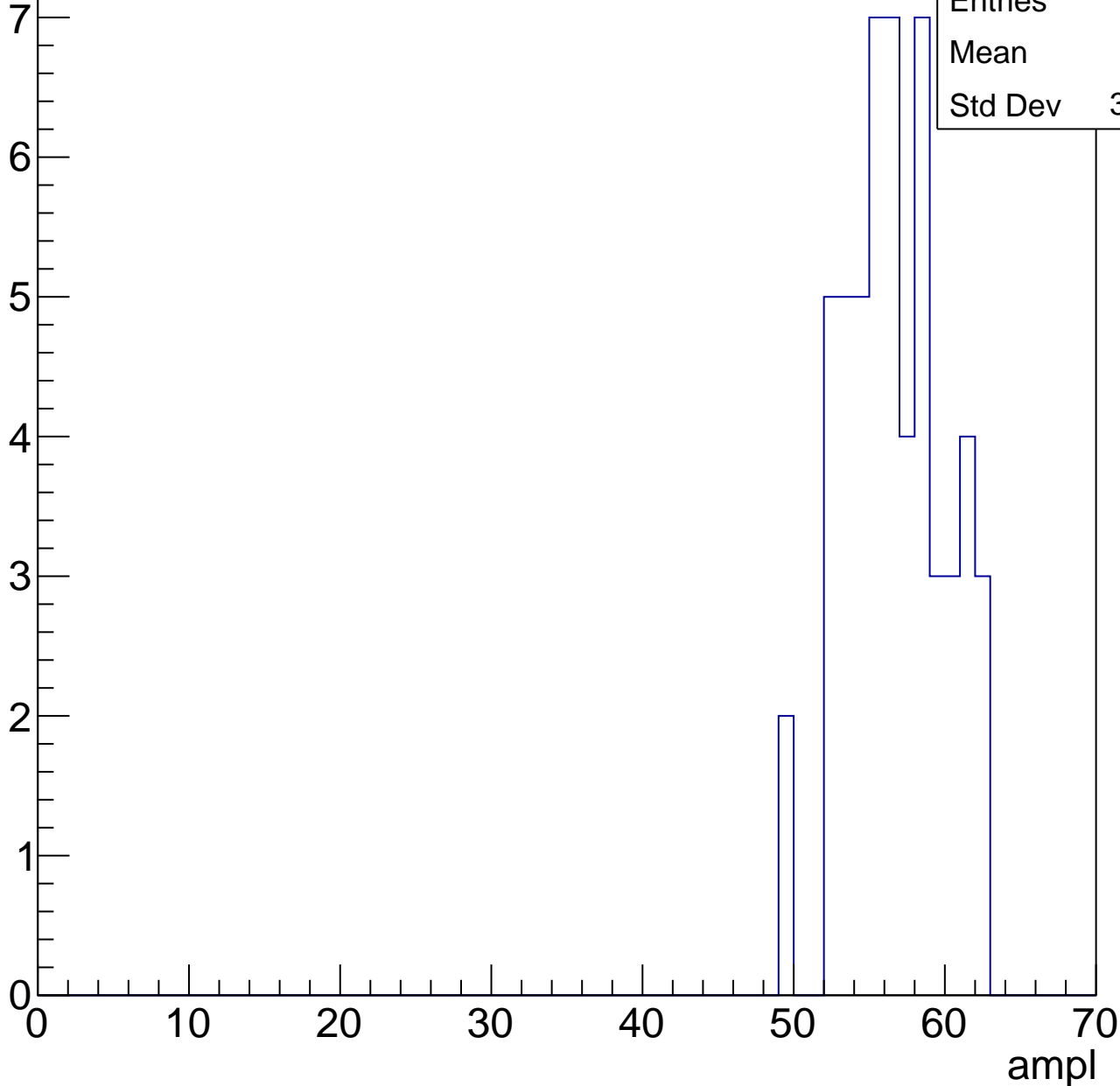


# B1L102S, U24-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.2
Std Dev	3.199

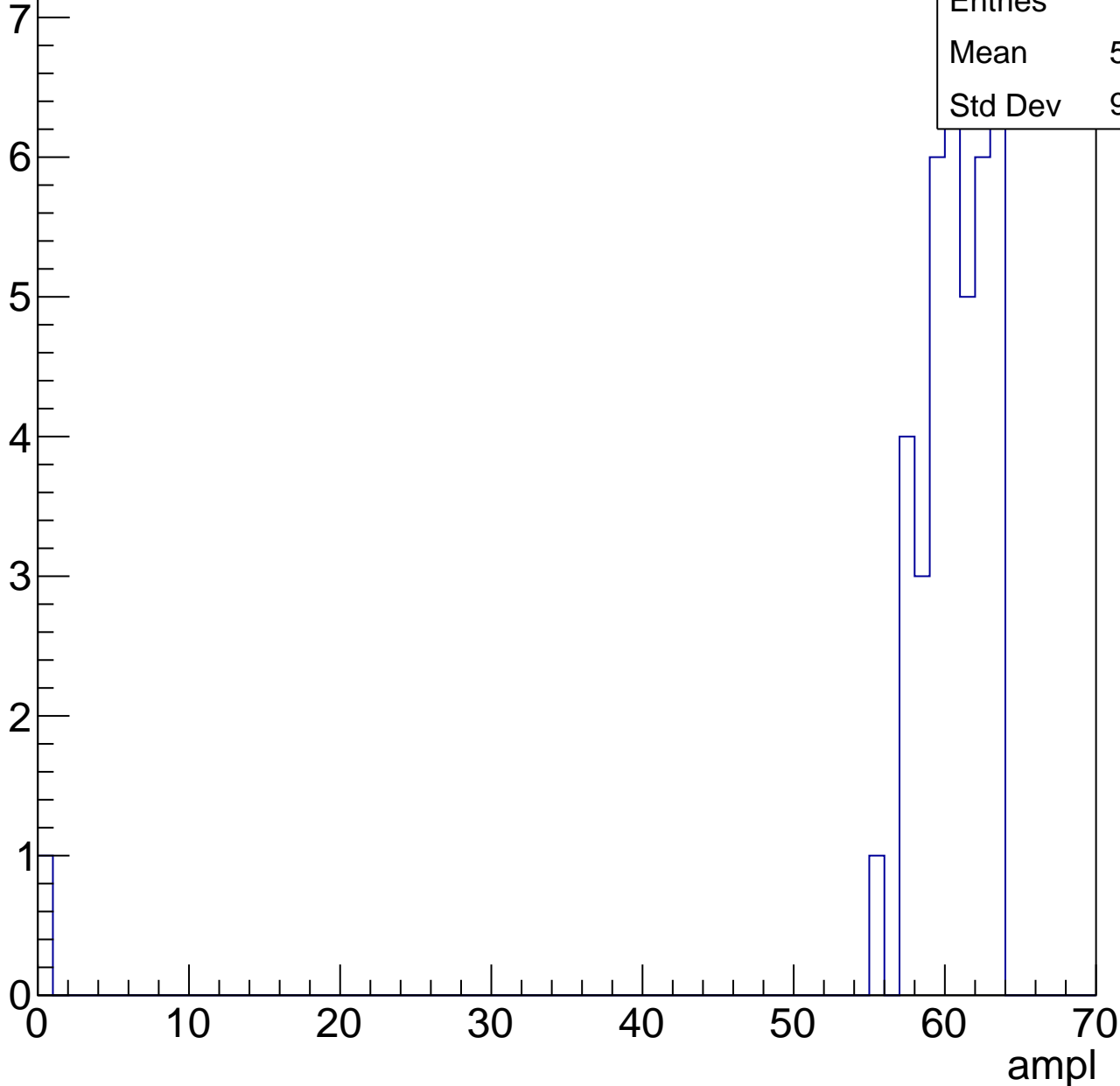


# B1L102S, U24-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

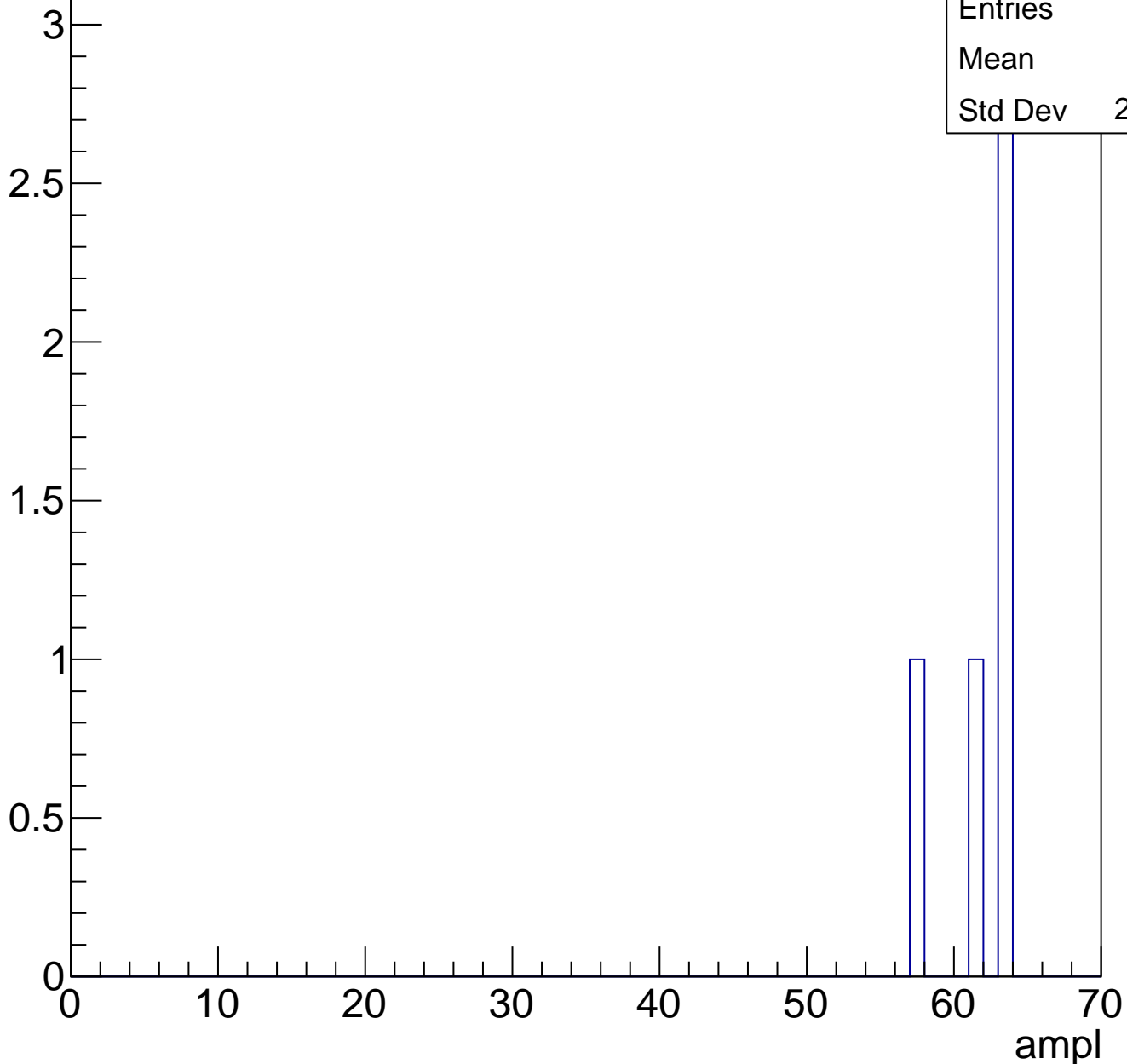
Entries	40
Mean	58.73
Std Dev	9.625



# B1L102S, U24-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U24-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch126, adc0

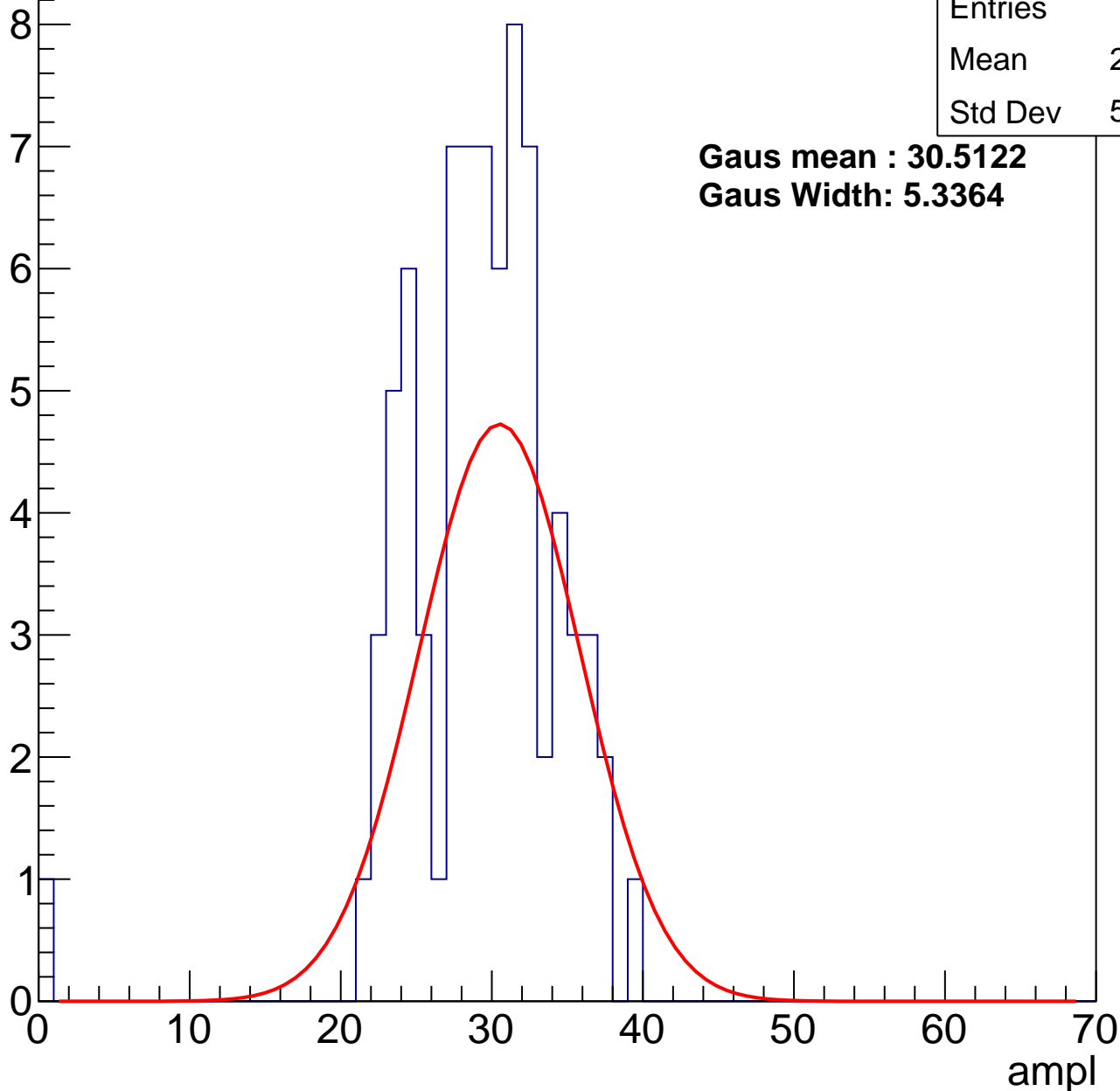
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.77
Std Dev	5.328

**Gaus mean : 30.5122**

**Gaus Width: 5.3364**



# B1L102S, U24-ch126, adc1

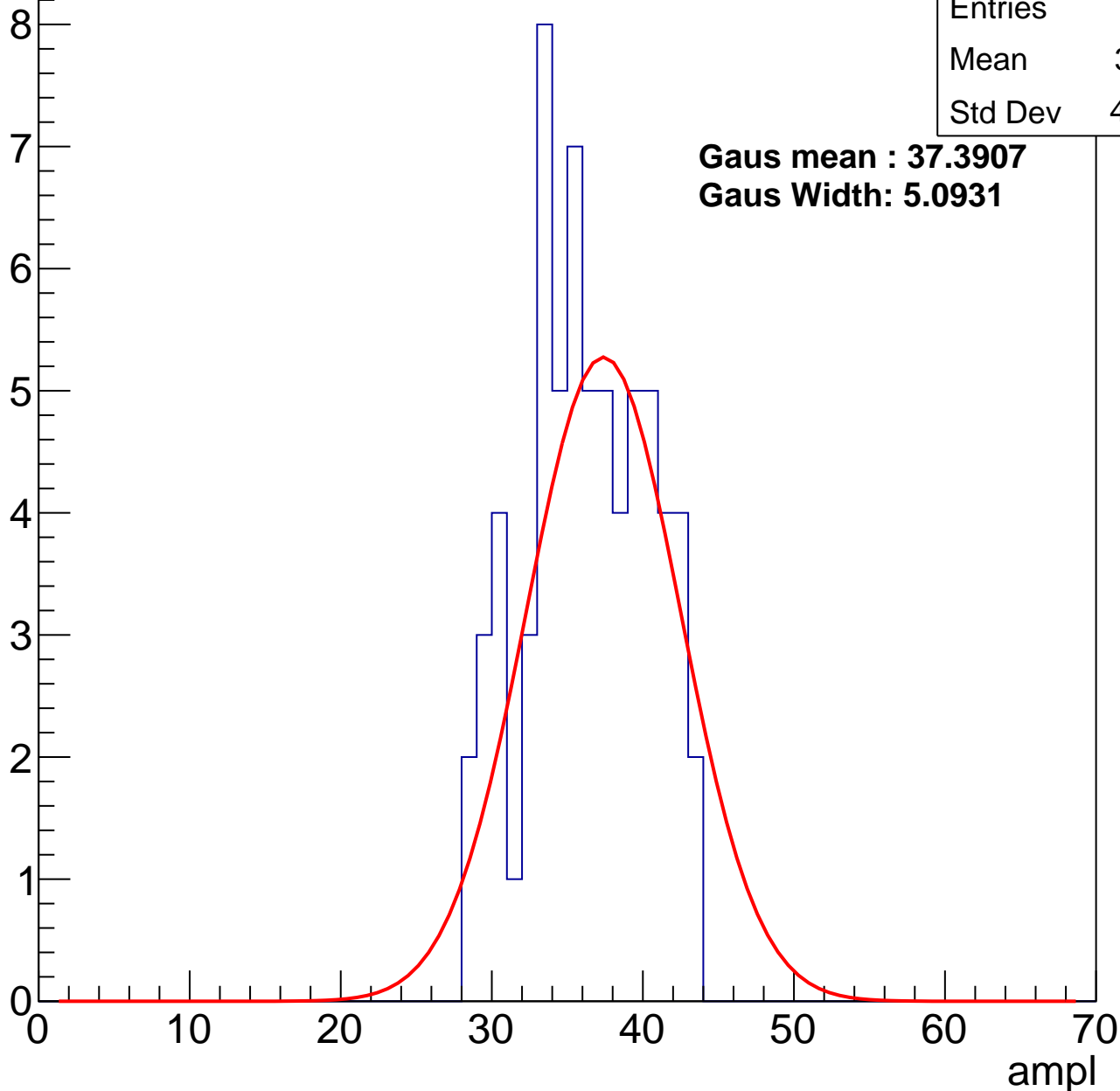
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.81
Std Dev	4.008

**Gaus mean : 37.3907**

**Gaus Width: 5.0931**



# B1L102S, U24-ch126, adc2

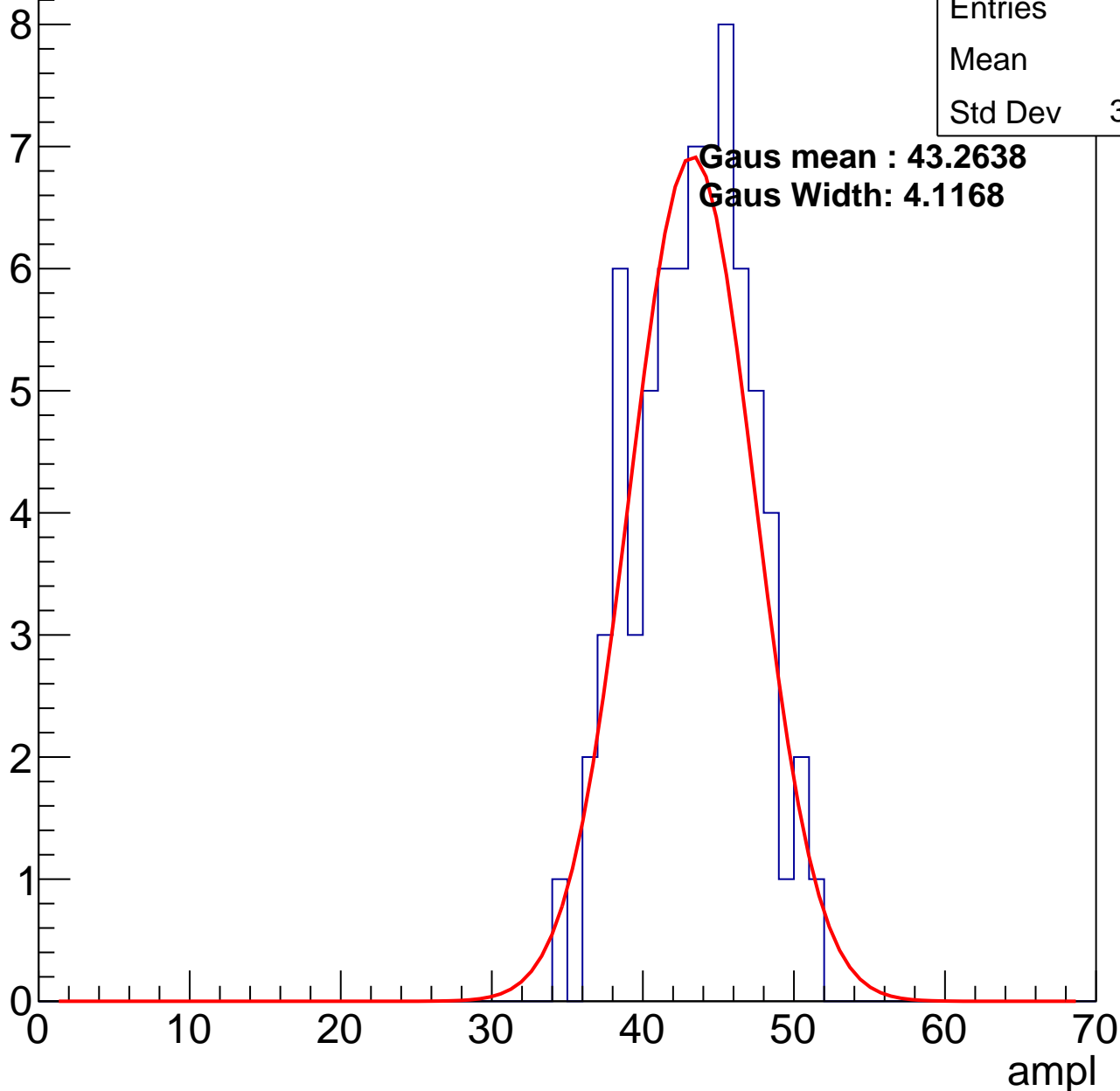
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.9
Std Dev	3.757

**Gaus mean : 43.2638**

**Gaus Width: 4.1168**

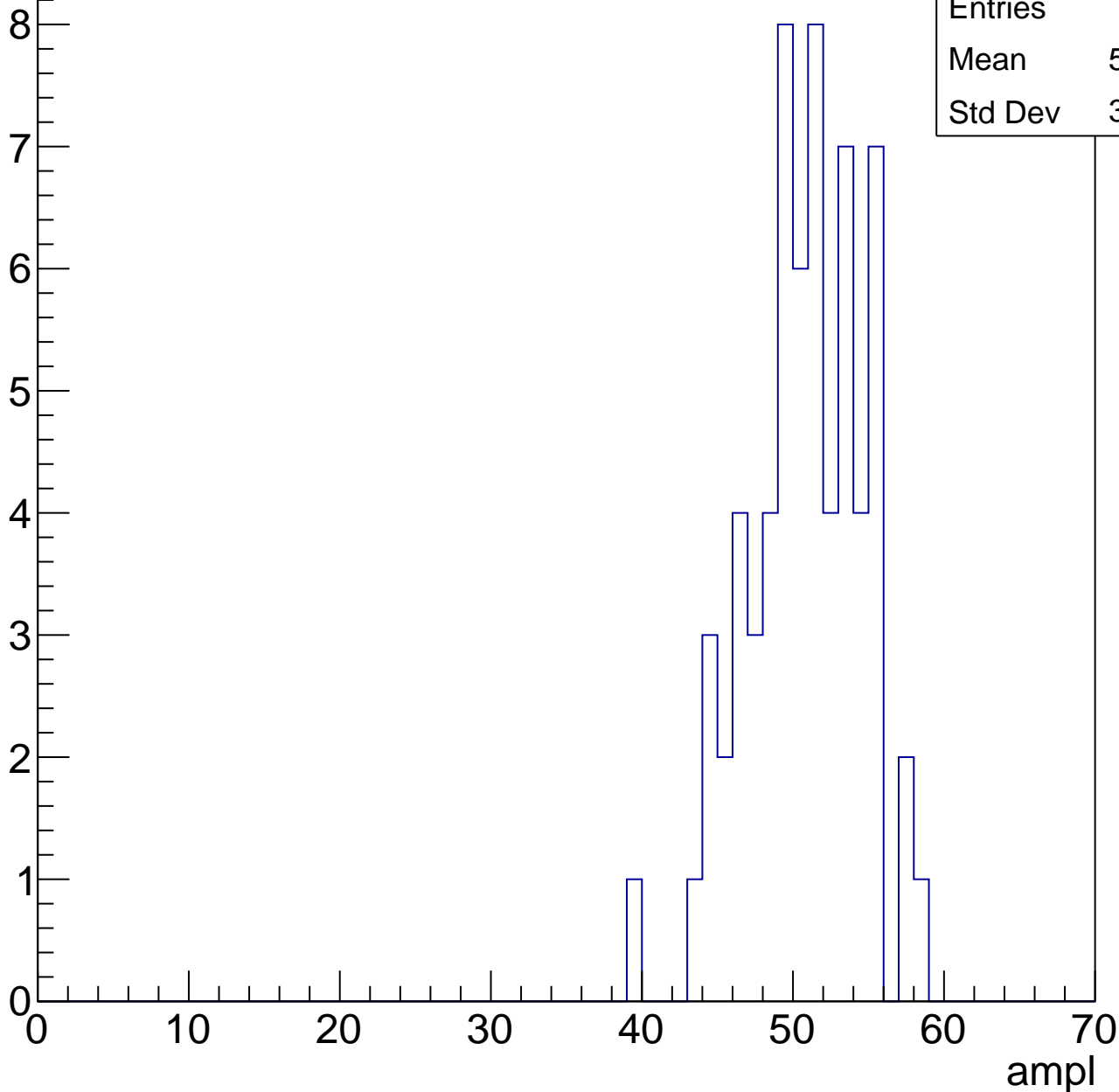


# B1L102S, U24-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	50.35
Std Dev	3.768

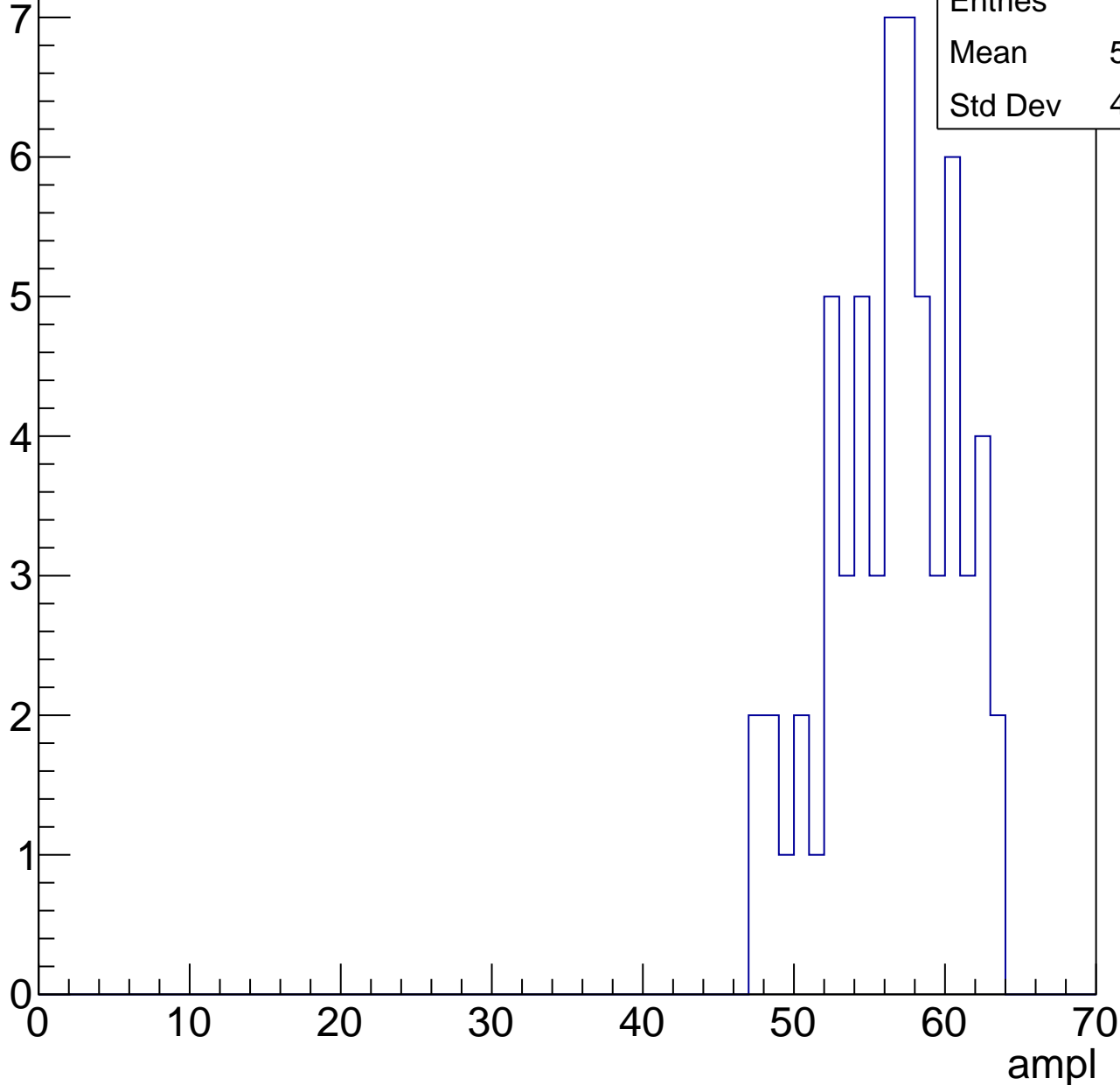


# B1L102S, U24-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.05
Std Dev	4.115

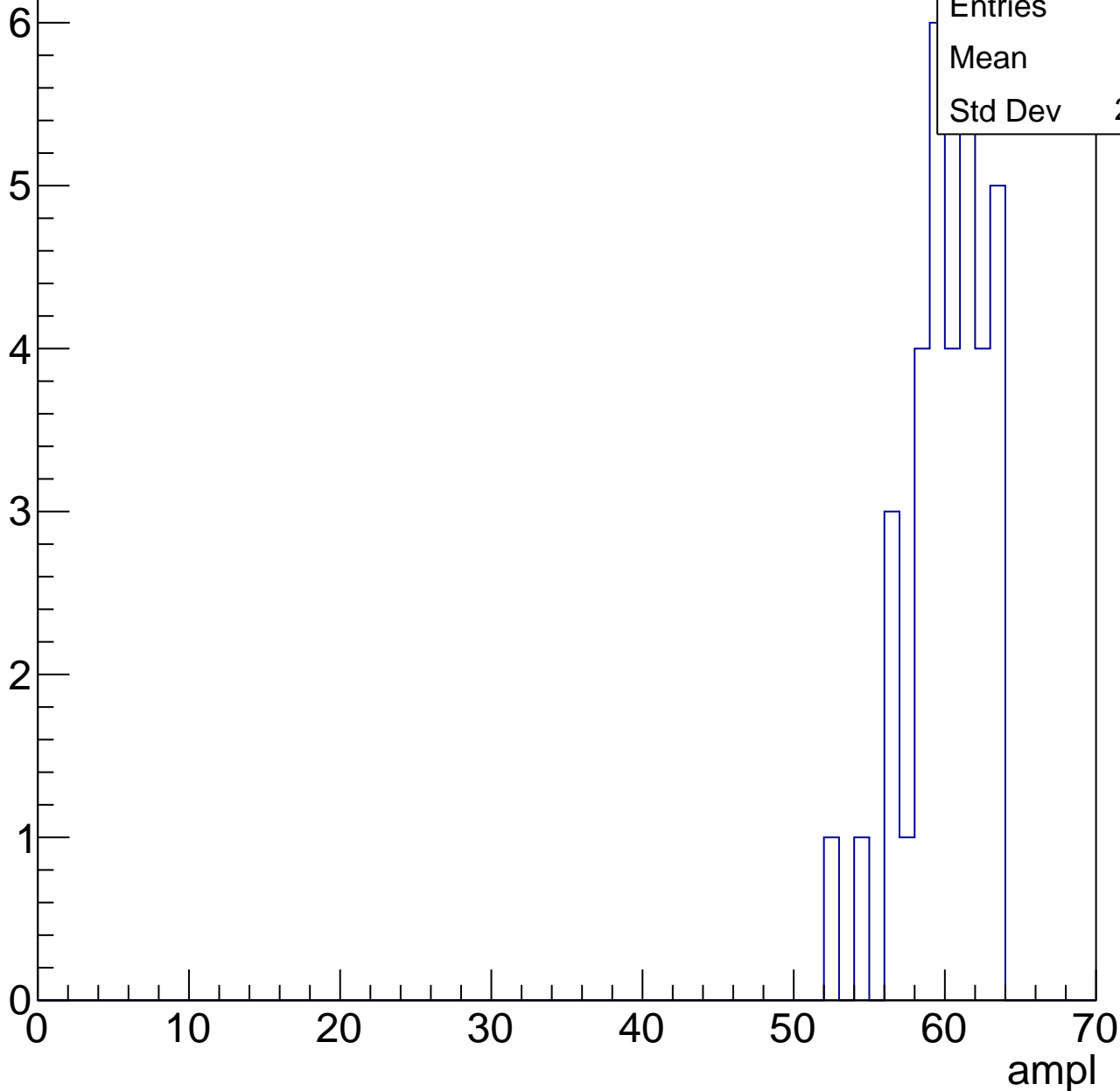


# B1L102S, U24-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.6
Std Dev	2.621



# B1L102S, U24-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

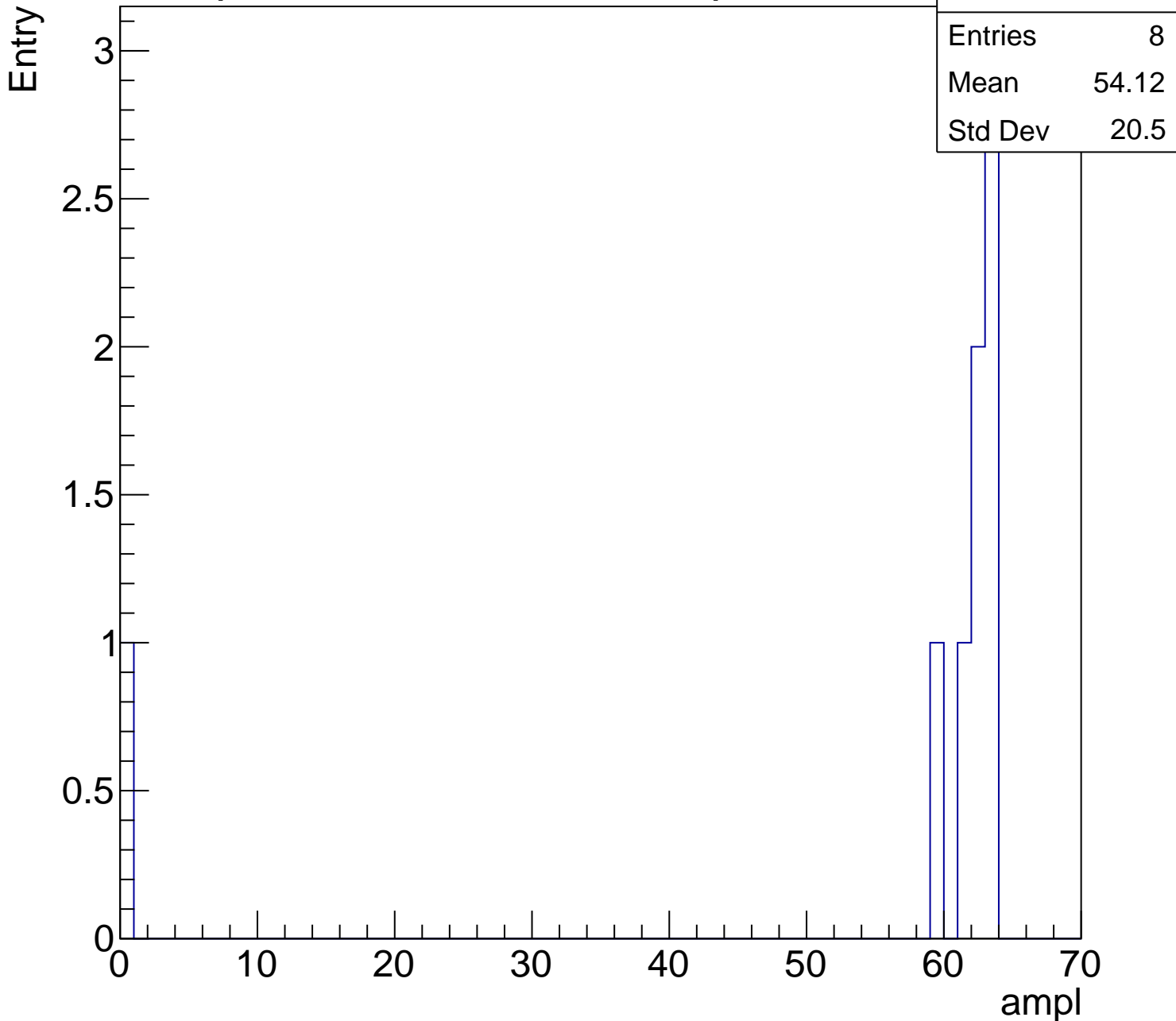
8

Mean

54.12

Std Dev

20.5





# B1L102S, U24-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U24-ch127, adc0

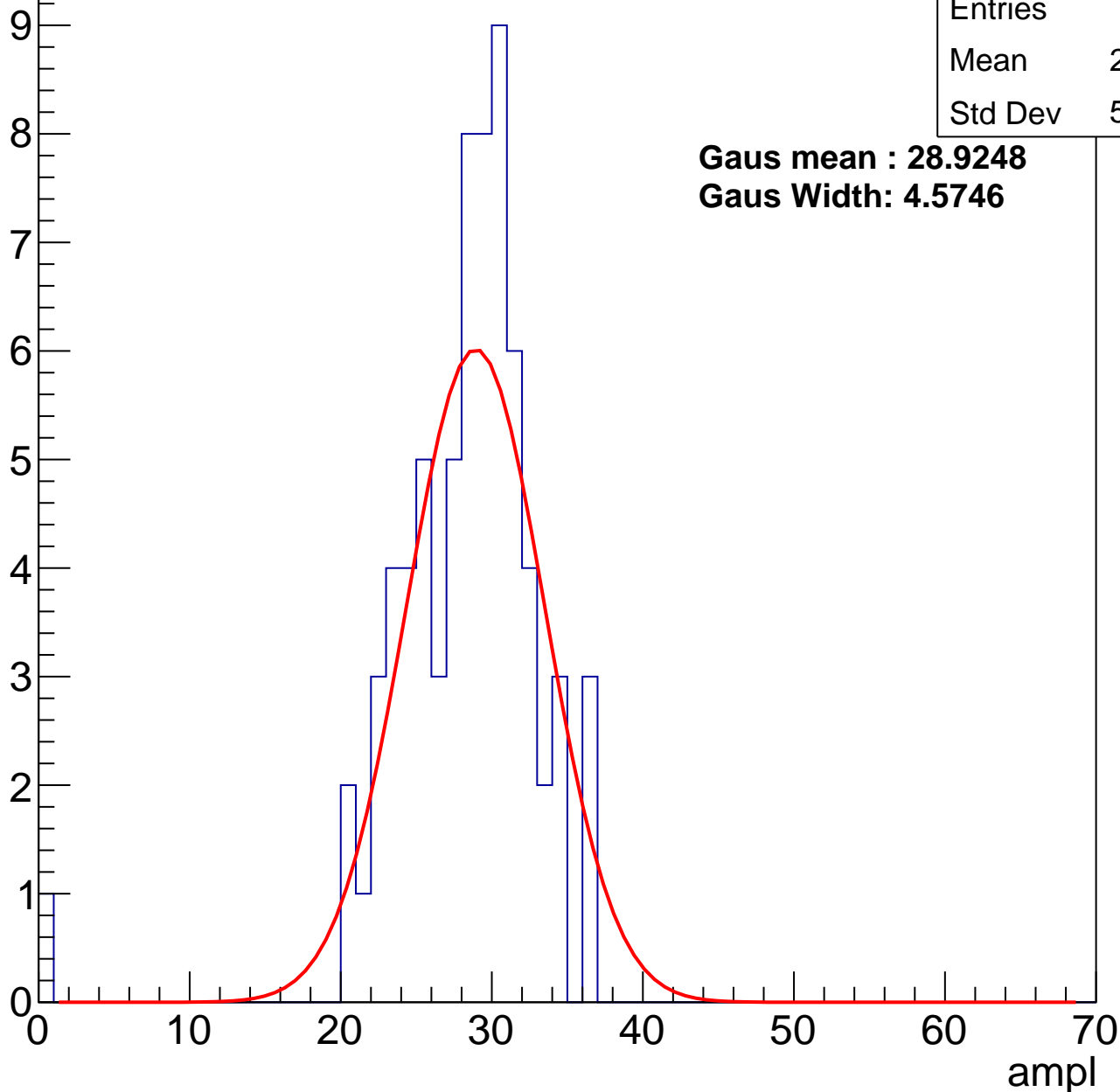
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.73
Std Dev	5.038

**Gaus mean : 28.9248**

**Gaus Width: 4.5746**



# B1L102S, U24-ch127, adc1

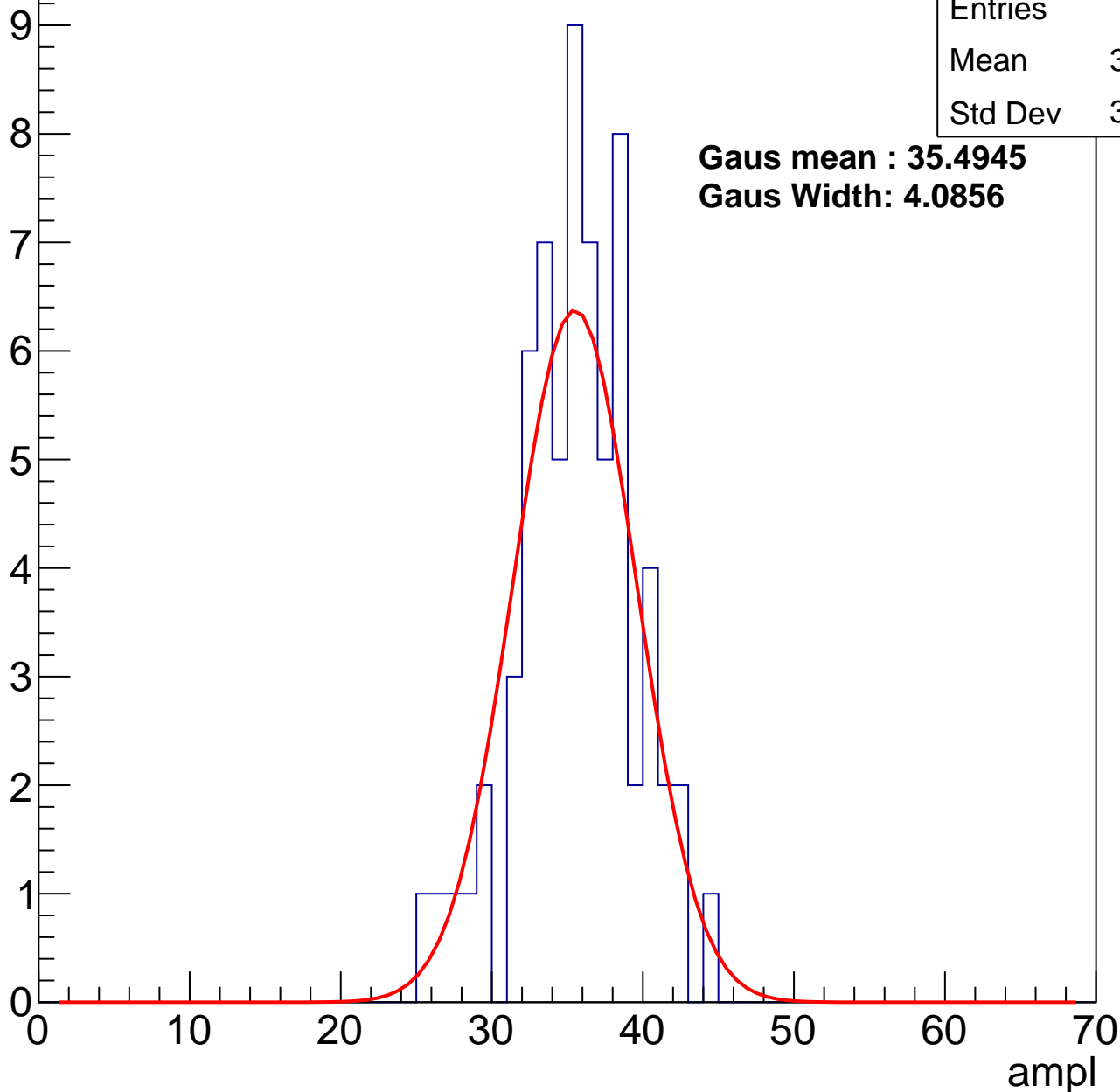
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.13
Std Dev	3.828

**Gaus mean : 35.4945**

**Gaus Width: 4.0856**



# B1L102S, U24-ch127, adc2

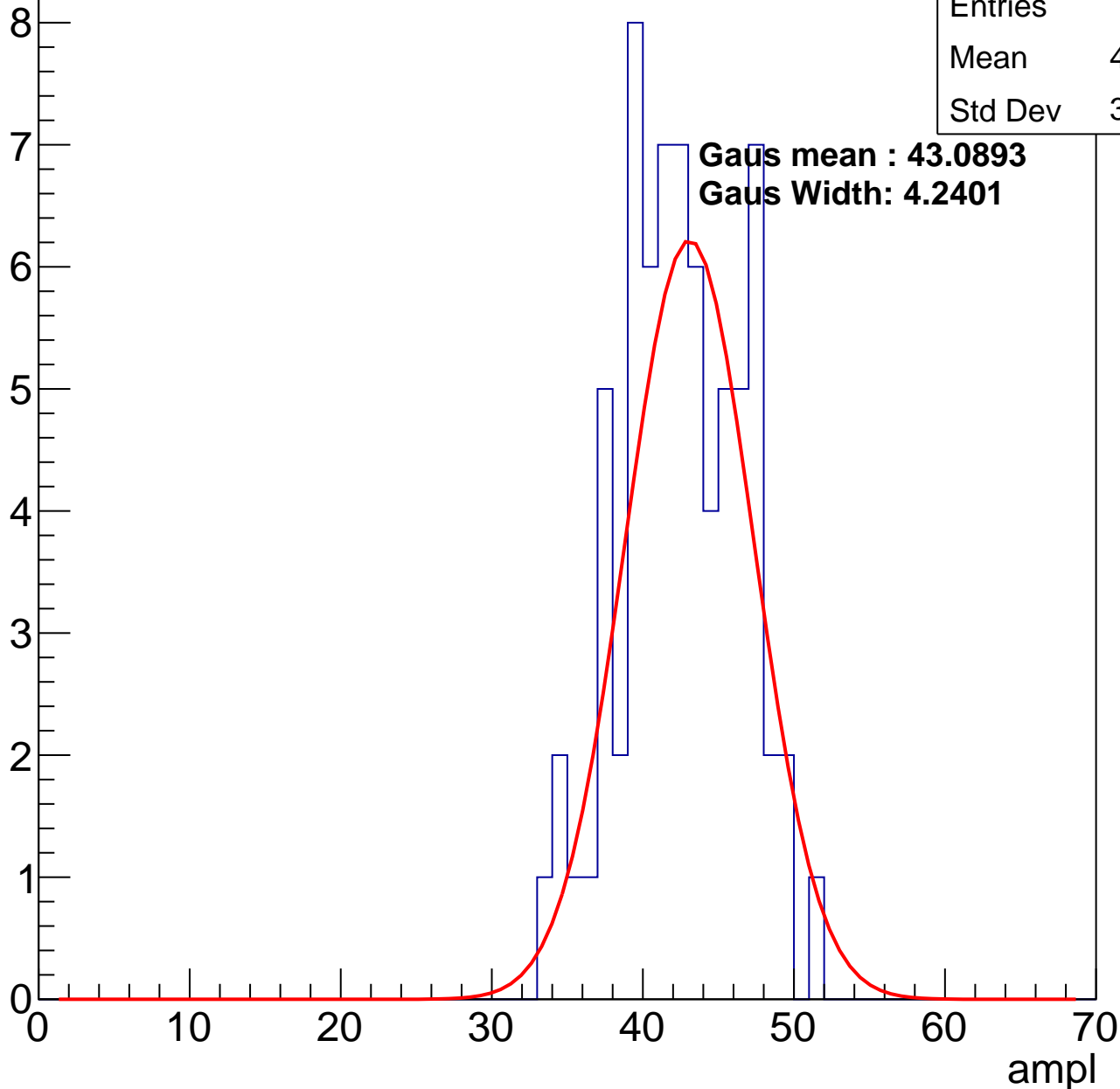
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.07
Std Dev	3.956

**Gaus mean : 43.0893**

**Gaus Width: 4.2401**



# B1L102S, U24-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

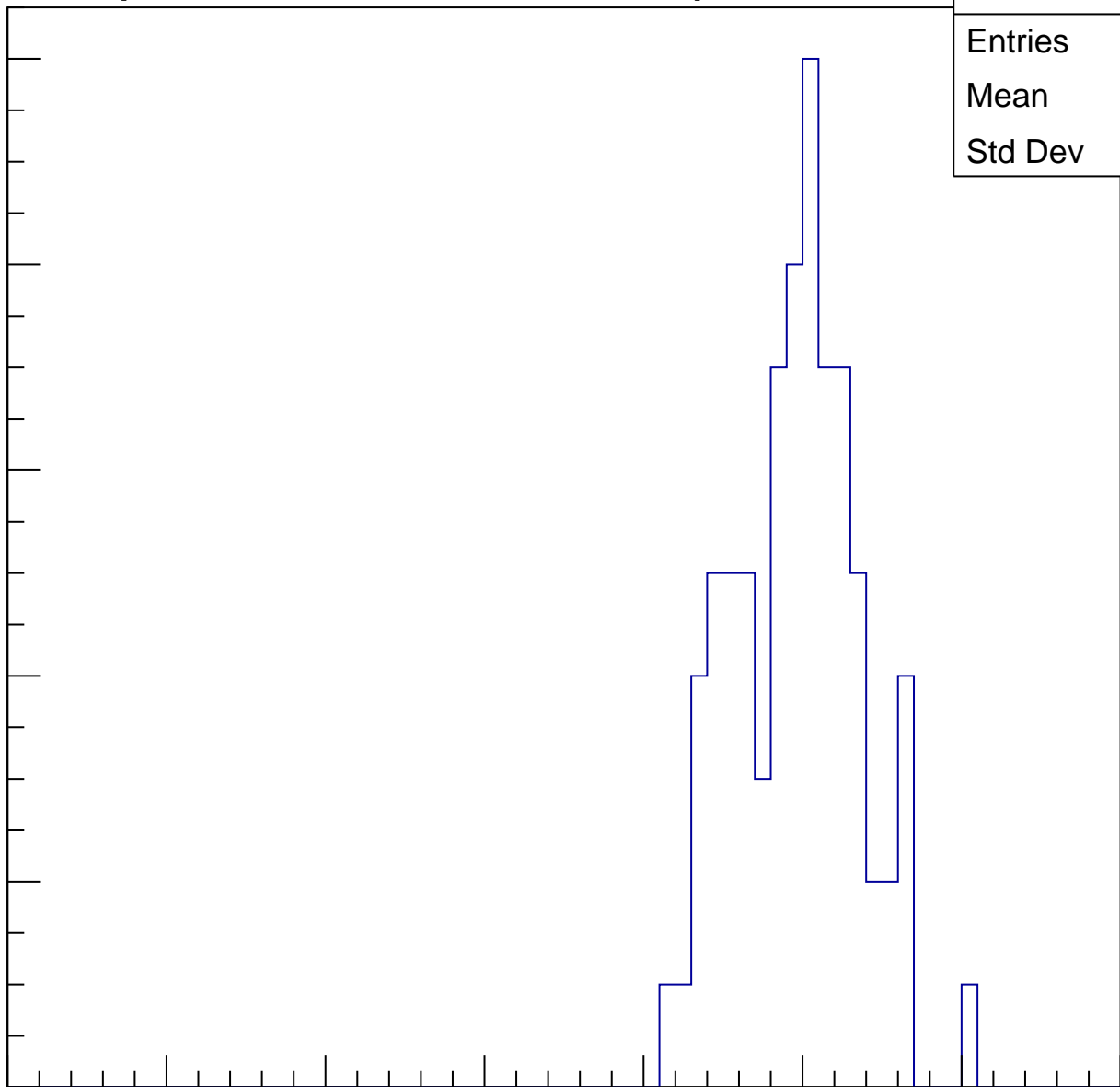
Entries	77
Mean	49.18
Std Dev	3.854

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

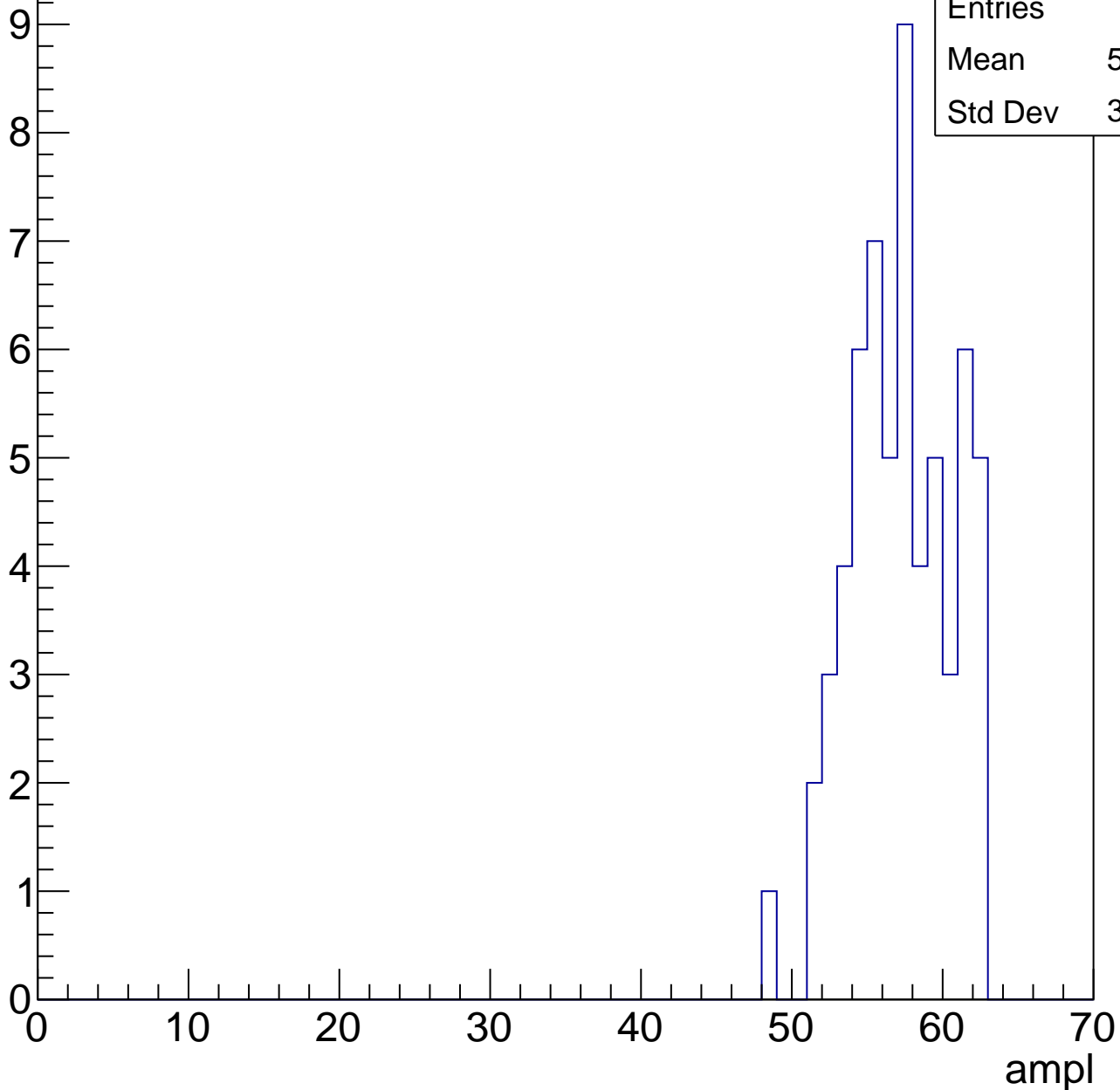


# B1L102S, U24-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.72
Std Dev	3.282

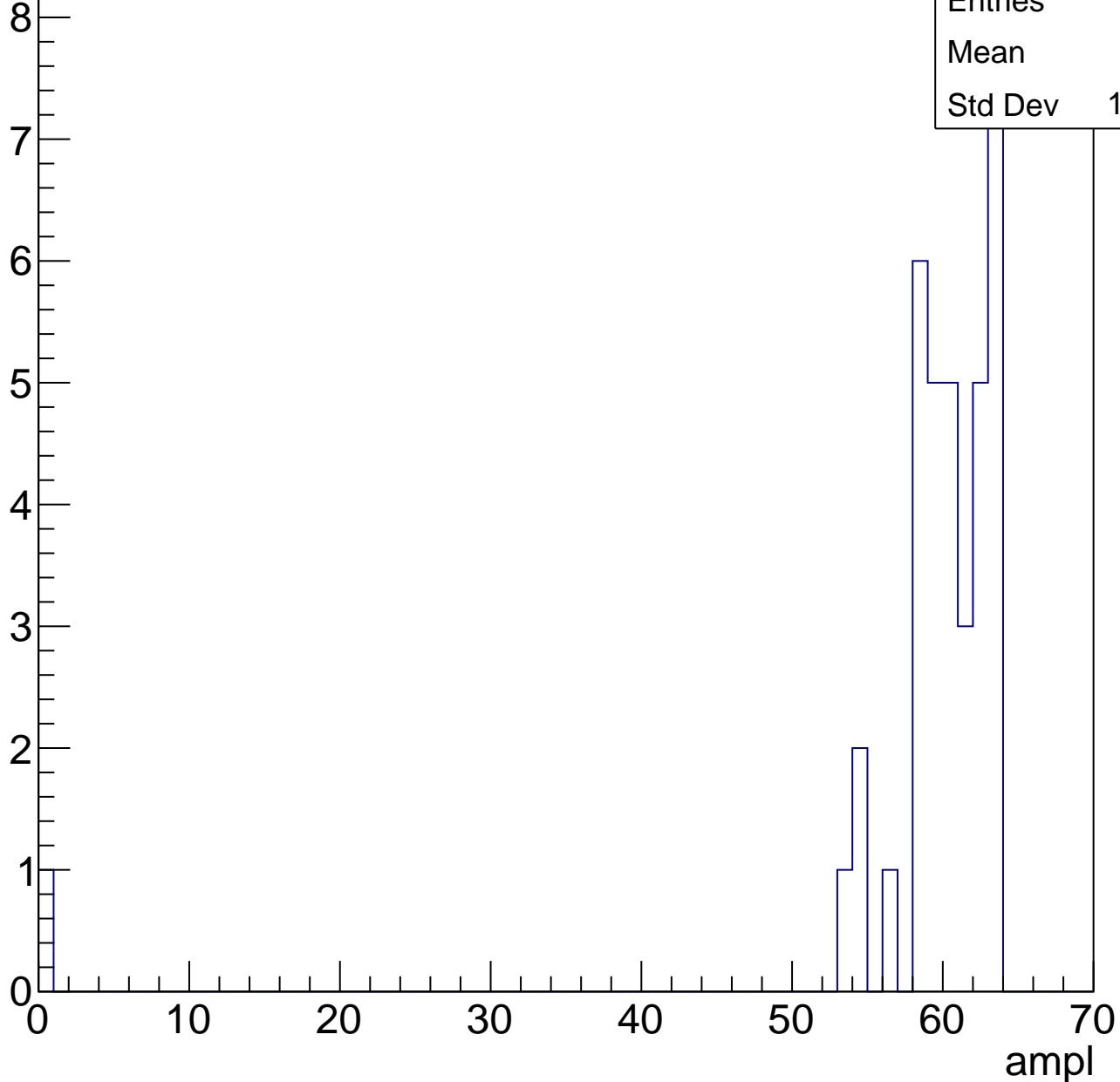


# B1L102S, U24-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.3
Std Dev	10.07



# B1L102S, U24-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

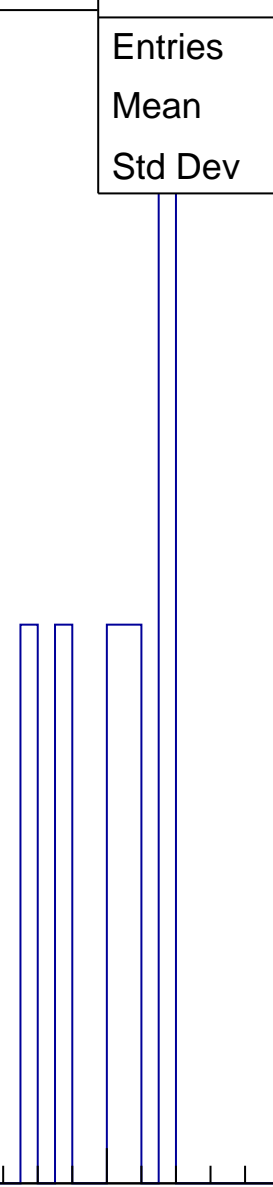
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	59.83
Std Dev	2.967

0 10 20 30 40 50 60 70

ampl

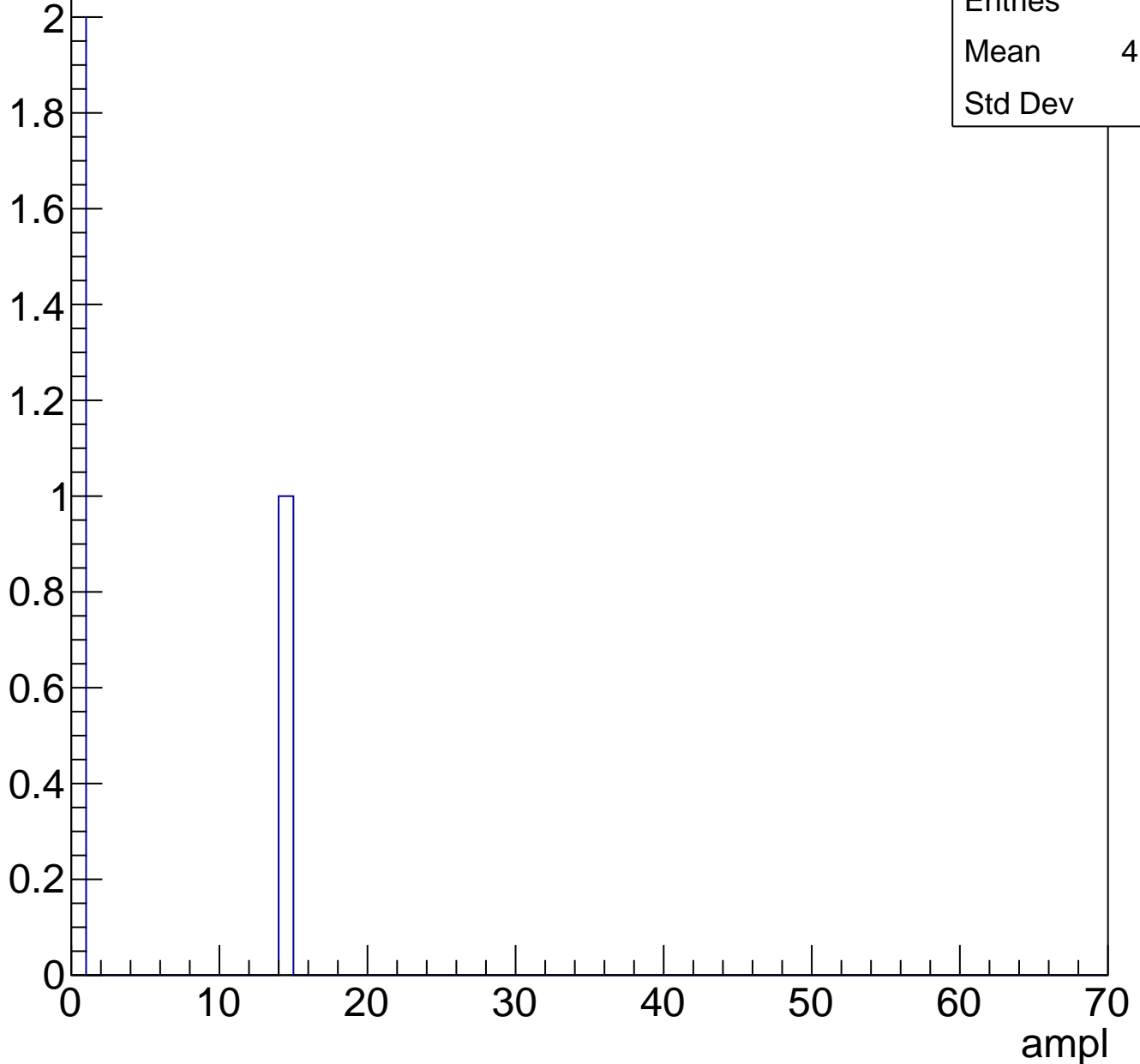




# B1L102S, U24-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

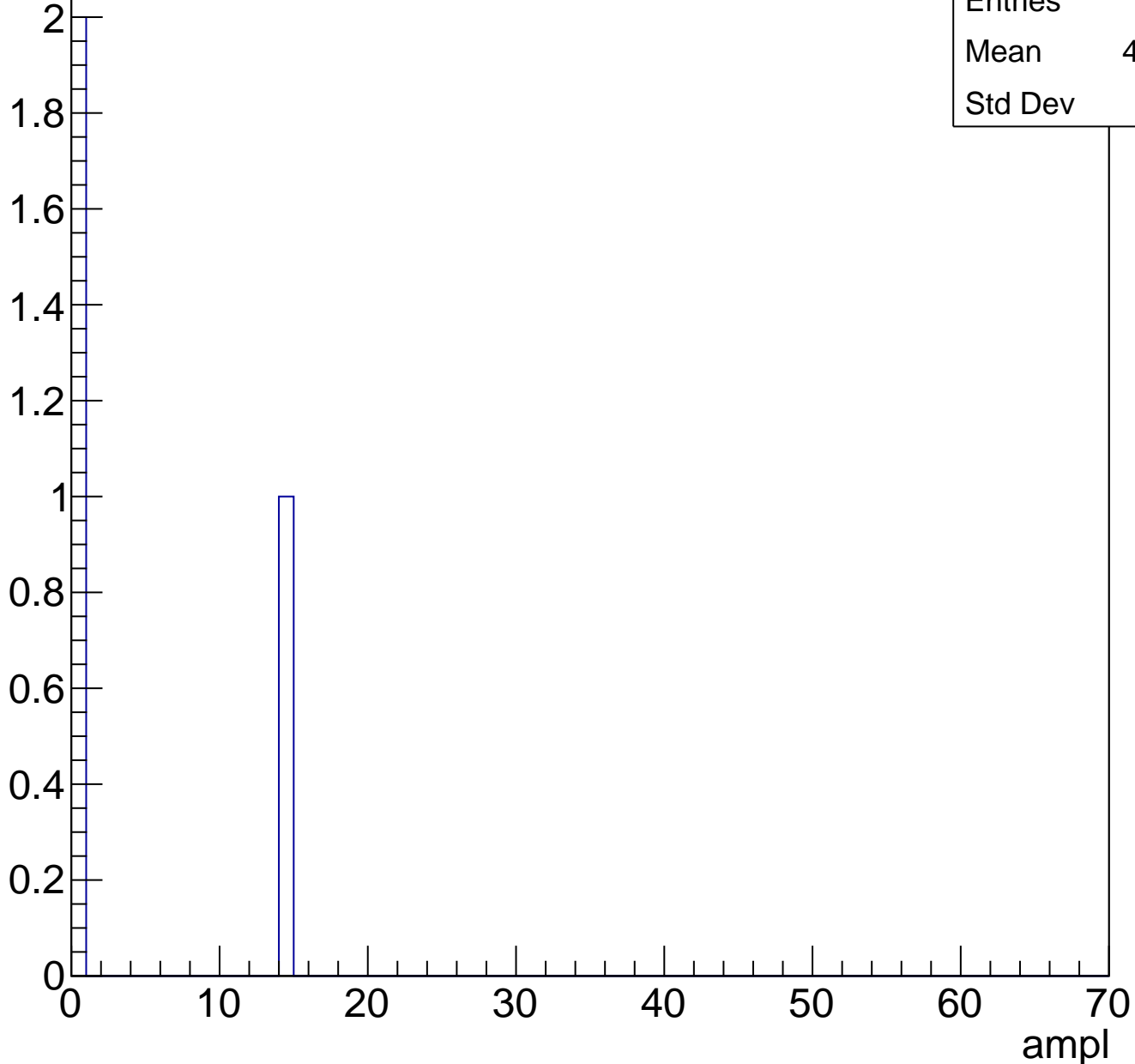
Entry



# B1L102S, U24-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	4.667
Std Dev	6.6