



# B1L102S, U25-ch0, adc0

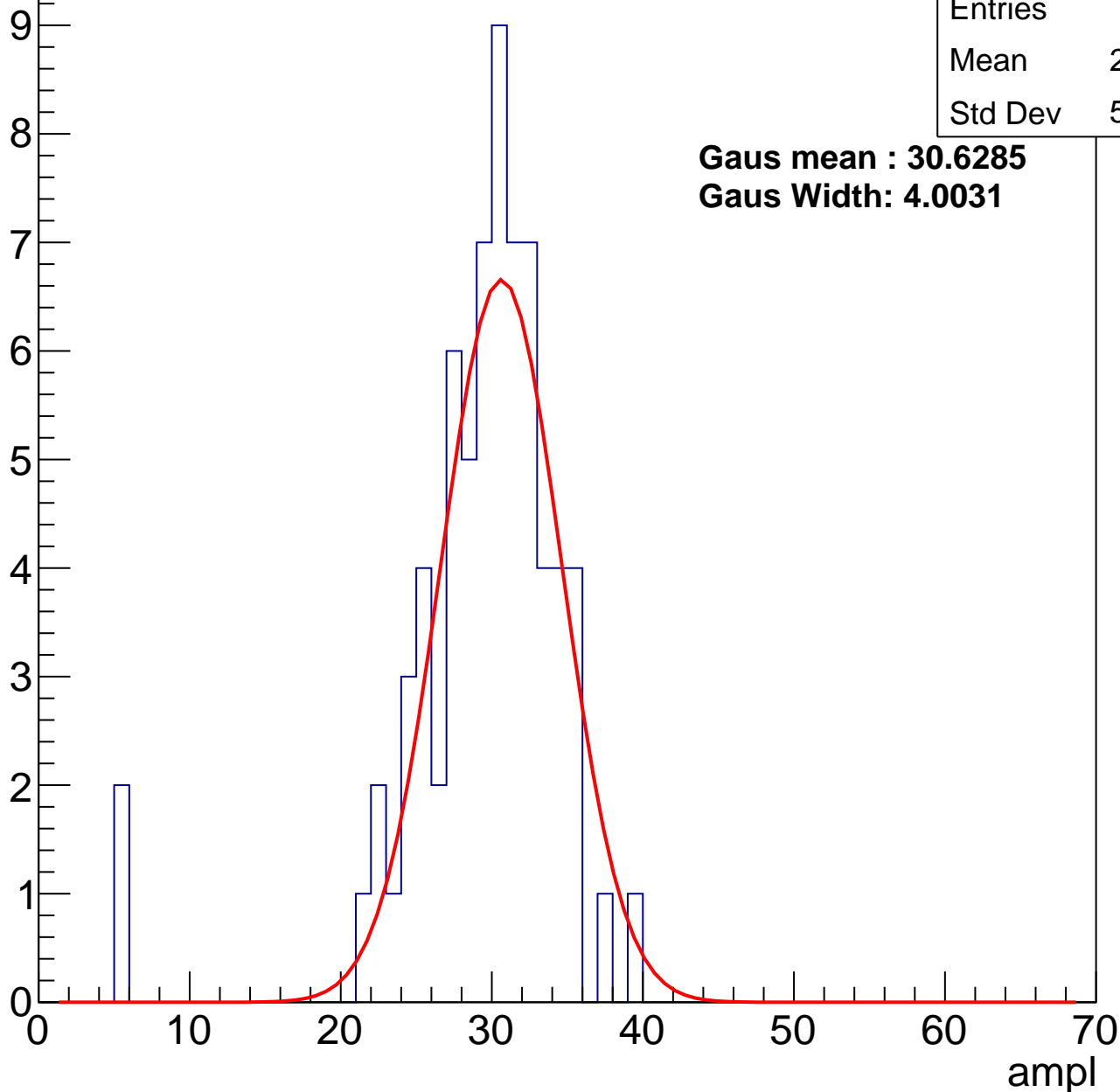
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.89
Std Dev	5.486

**Gaus mean : 30.6285**

**Gaus Width: 4.0031**



# B1L102S, U25-ch0, adc1

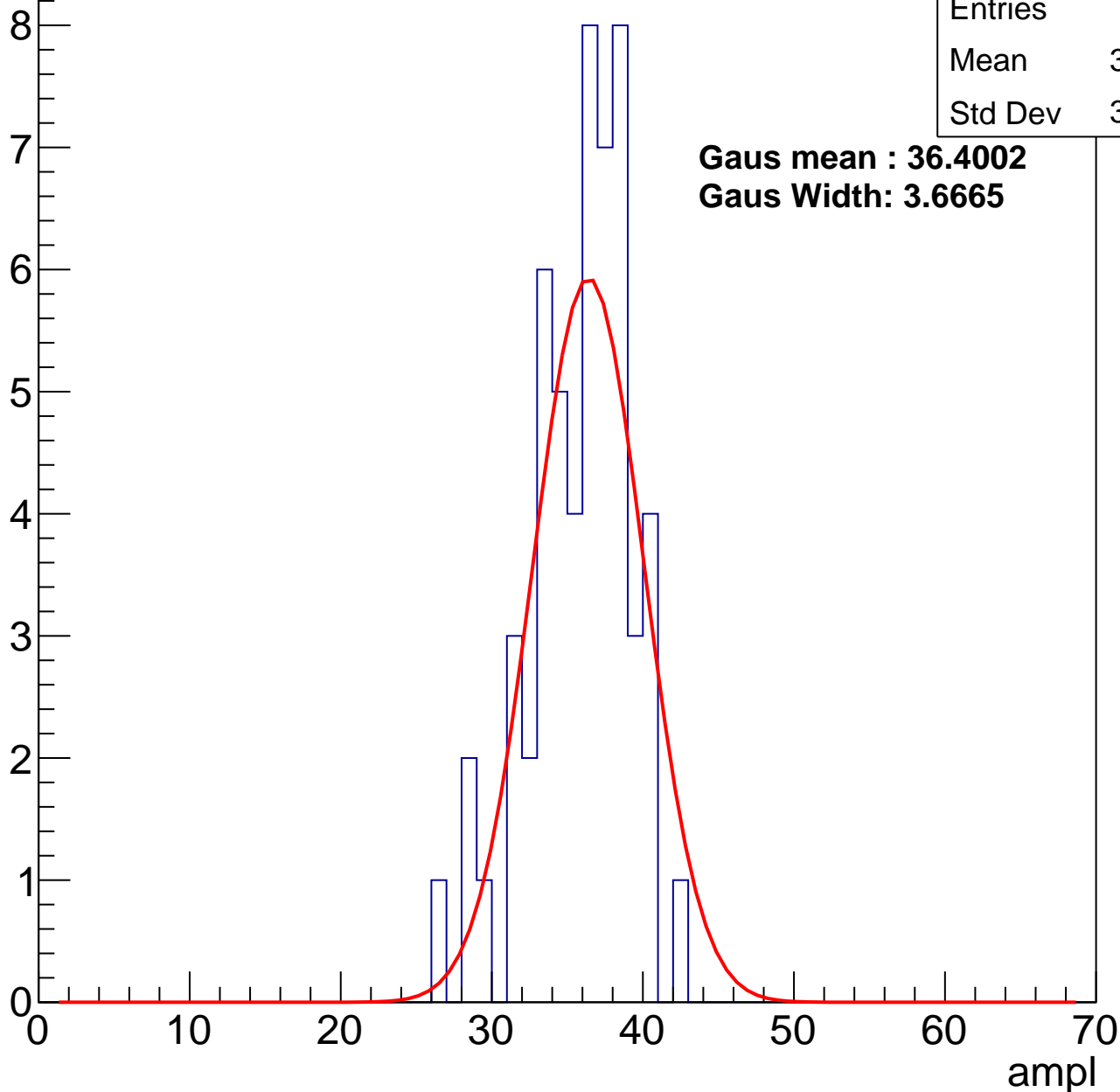
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	35.38
Std Dev	3.322

**Gaus mean : 36.4002**

**Gaus Width: 3.6665**



# B1L102S, U25-ch0, adc2

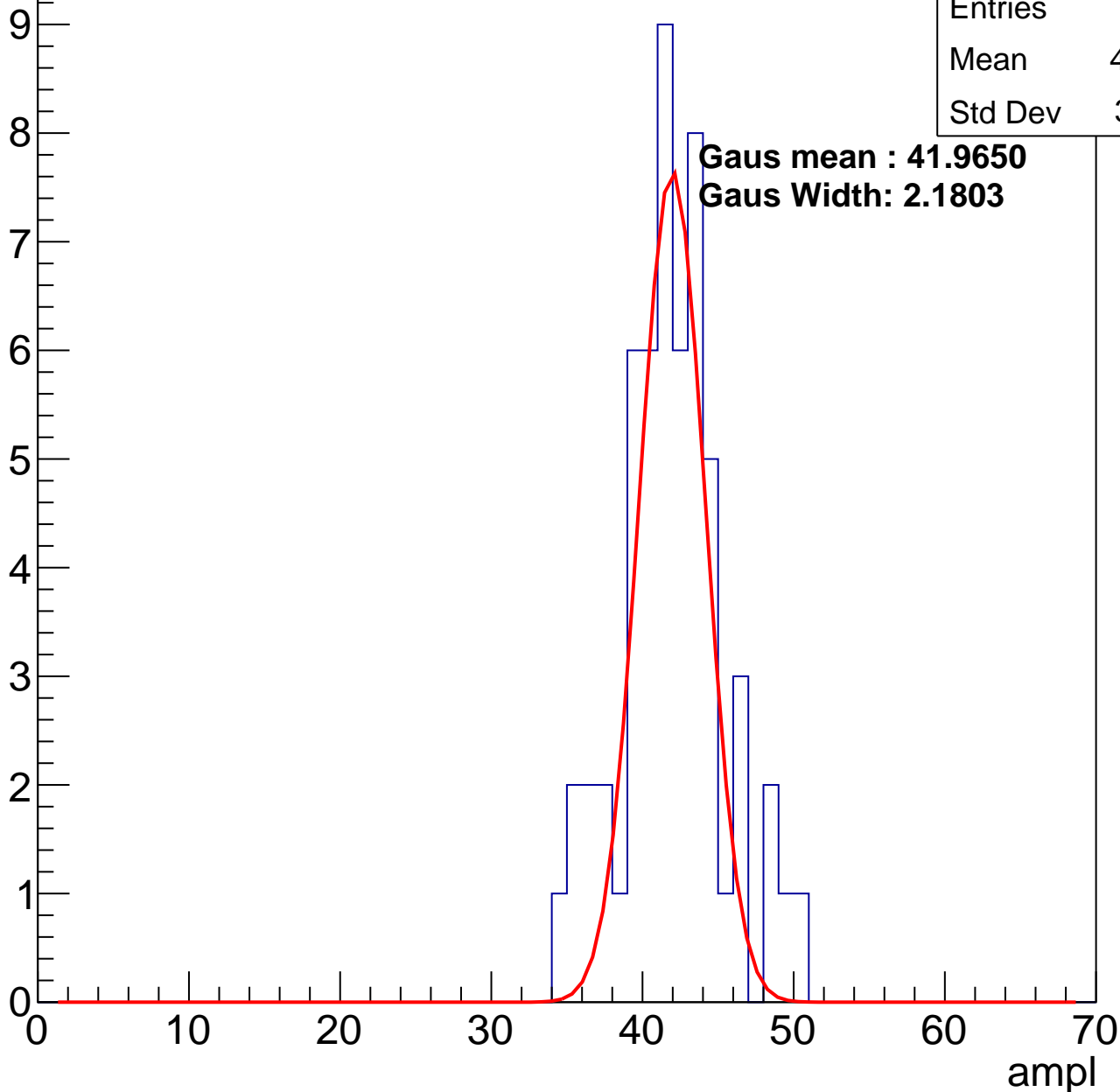
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.52
Std Dev	3.391

**Gaus mean : 41.9650**

**Gaus Width: 2.1803**

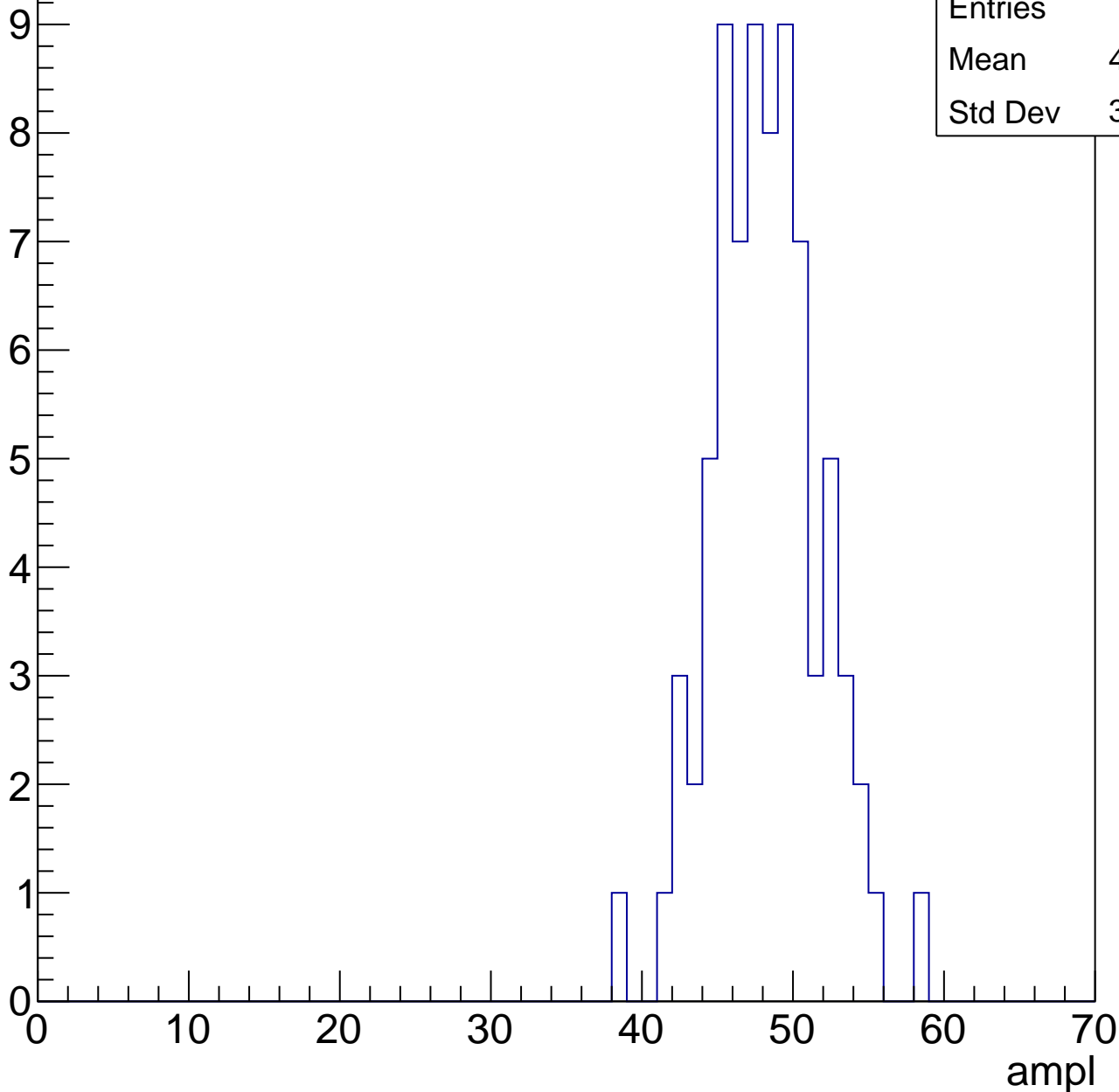


# B1L102S, U25-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	47.75
Std Dev	3.517

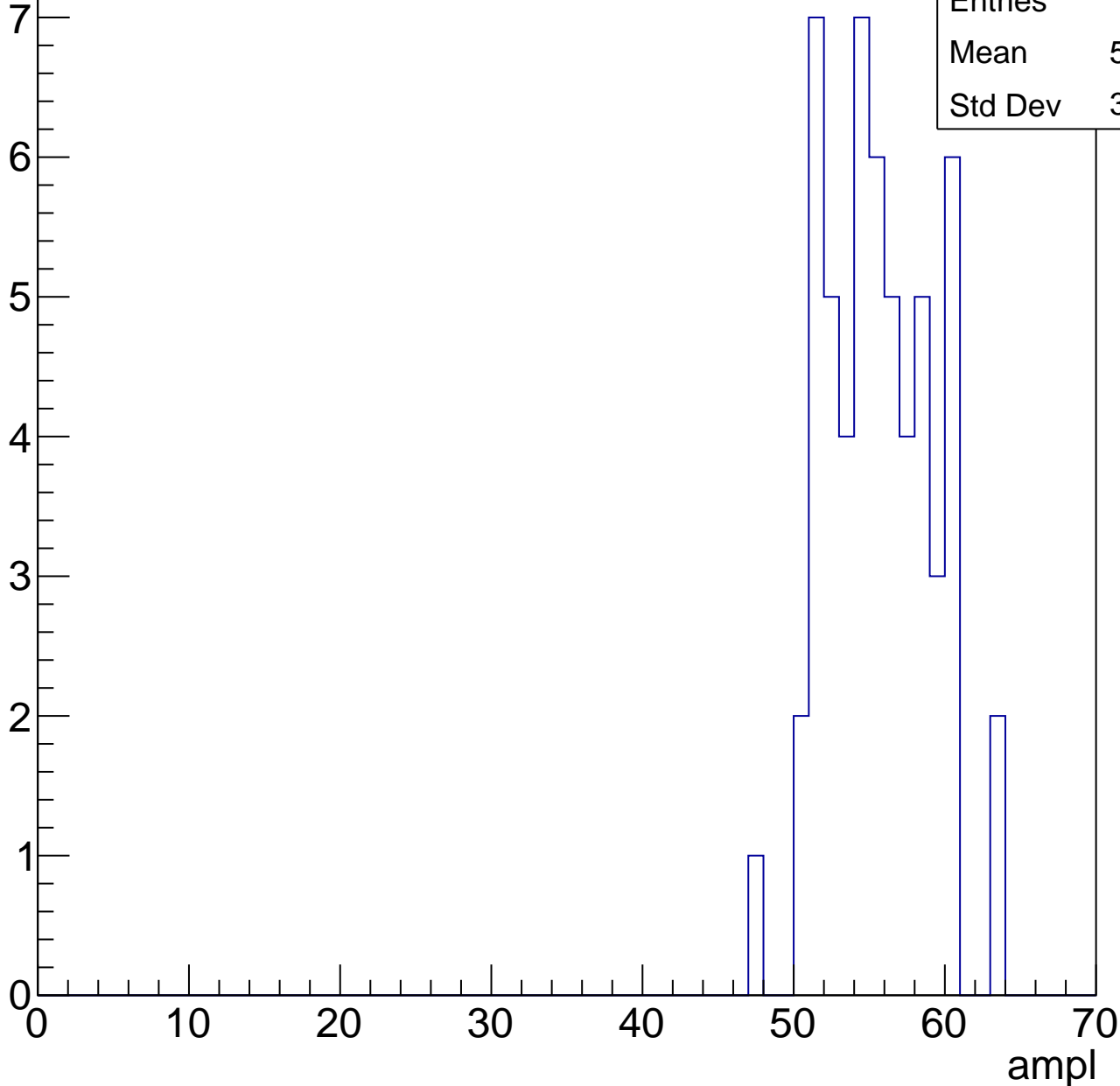


# B1L102S, U25-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.18
Std Dev	3.465

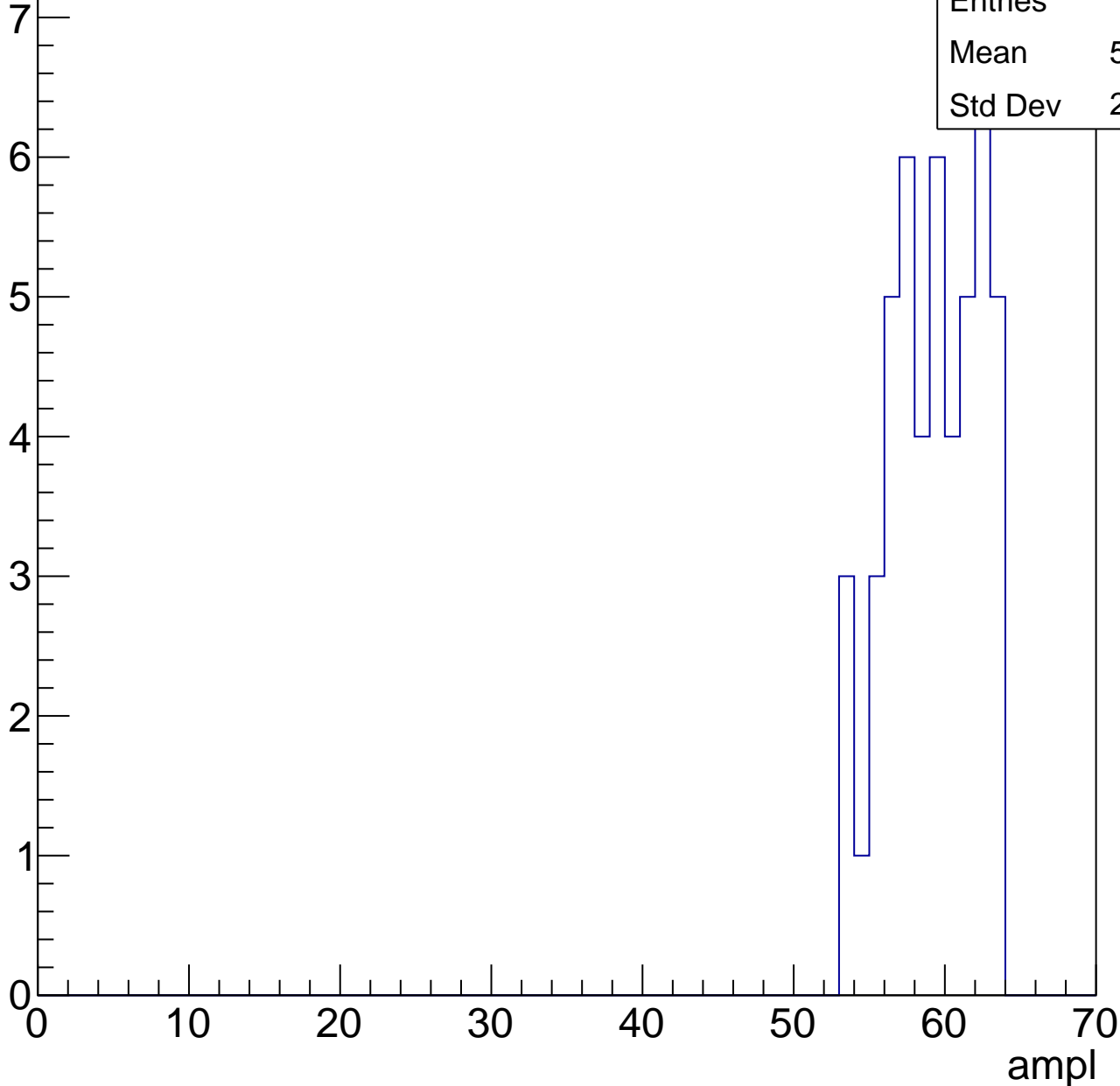


# B1L102S, U25-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.78
Std Dev	2.923

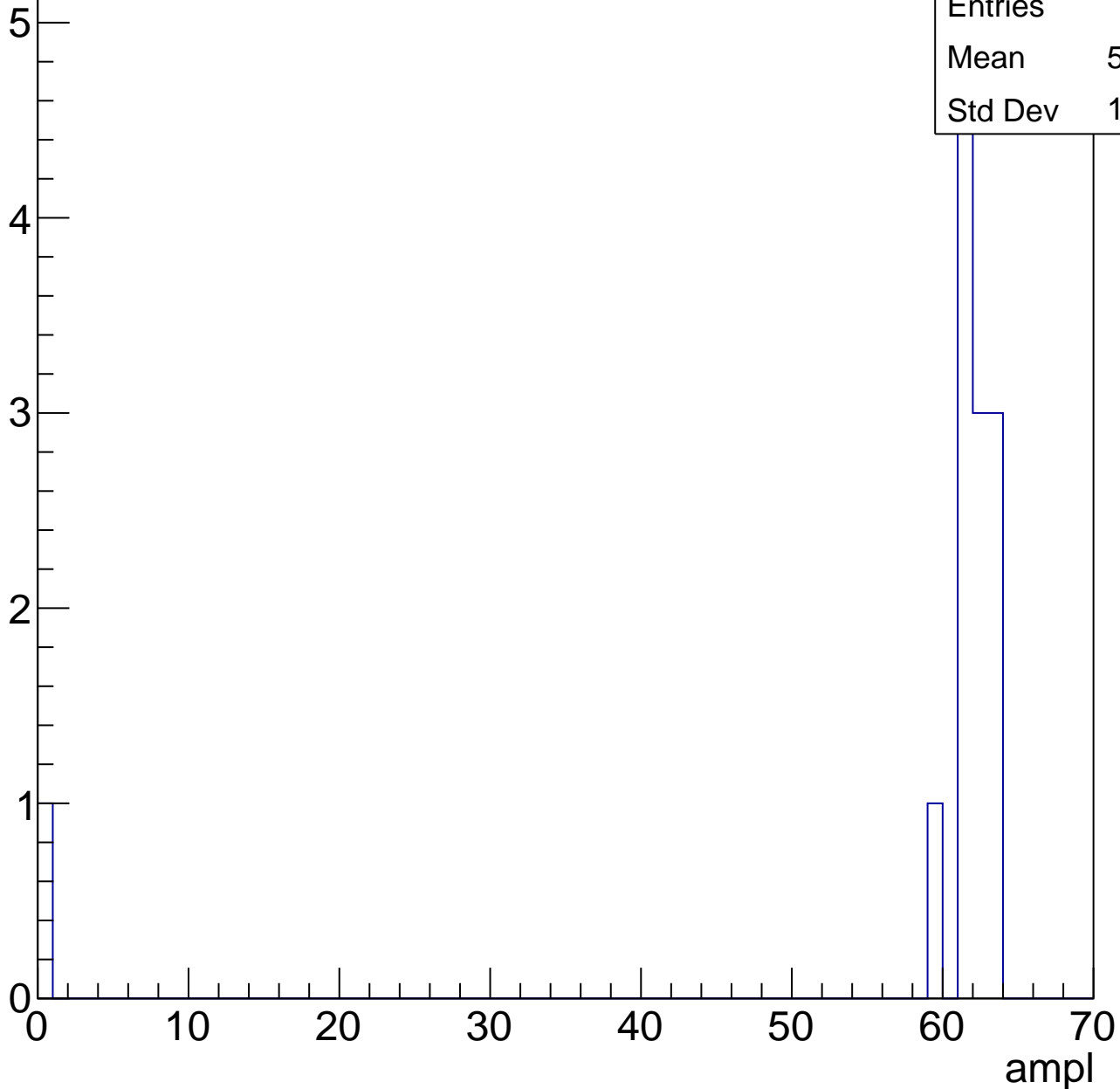


# B1L102S, U25-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.85
Std Dev	16.44





# B1L102S, U25-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch1, adc0

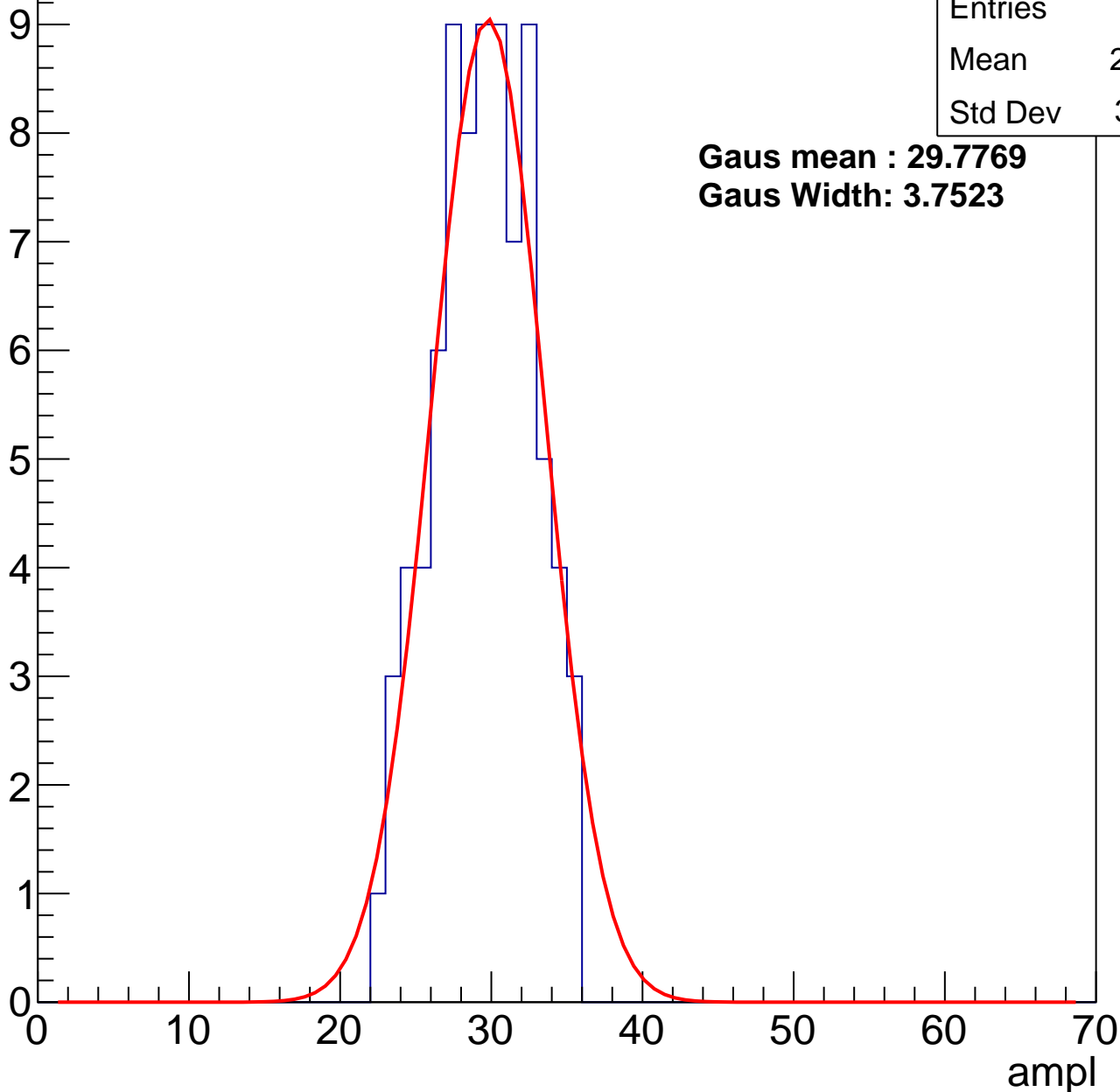
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.04
Std Dev	3.191

**Gaus mean : 29.7769**

**Gaus Width: 3.7523**



# B1L102S, U25-ch1, adc1

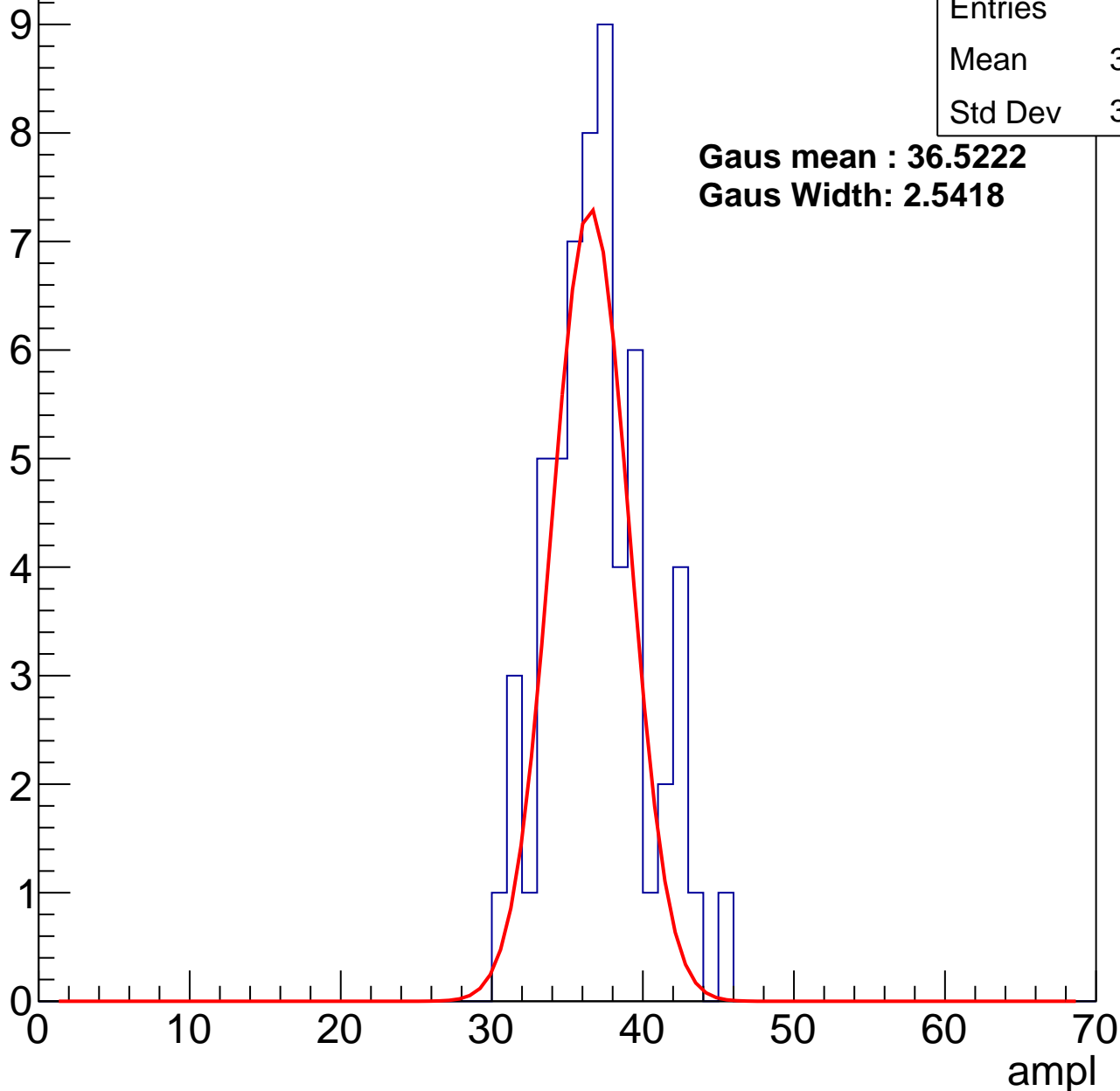
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.55
Std Dev	3.217

**Gaus mean : 36.5222**

**Gaus Width: 2.5418**



# B1L102S, U25-ch1, adc2

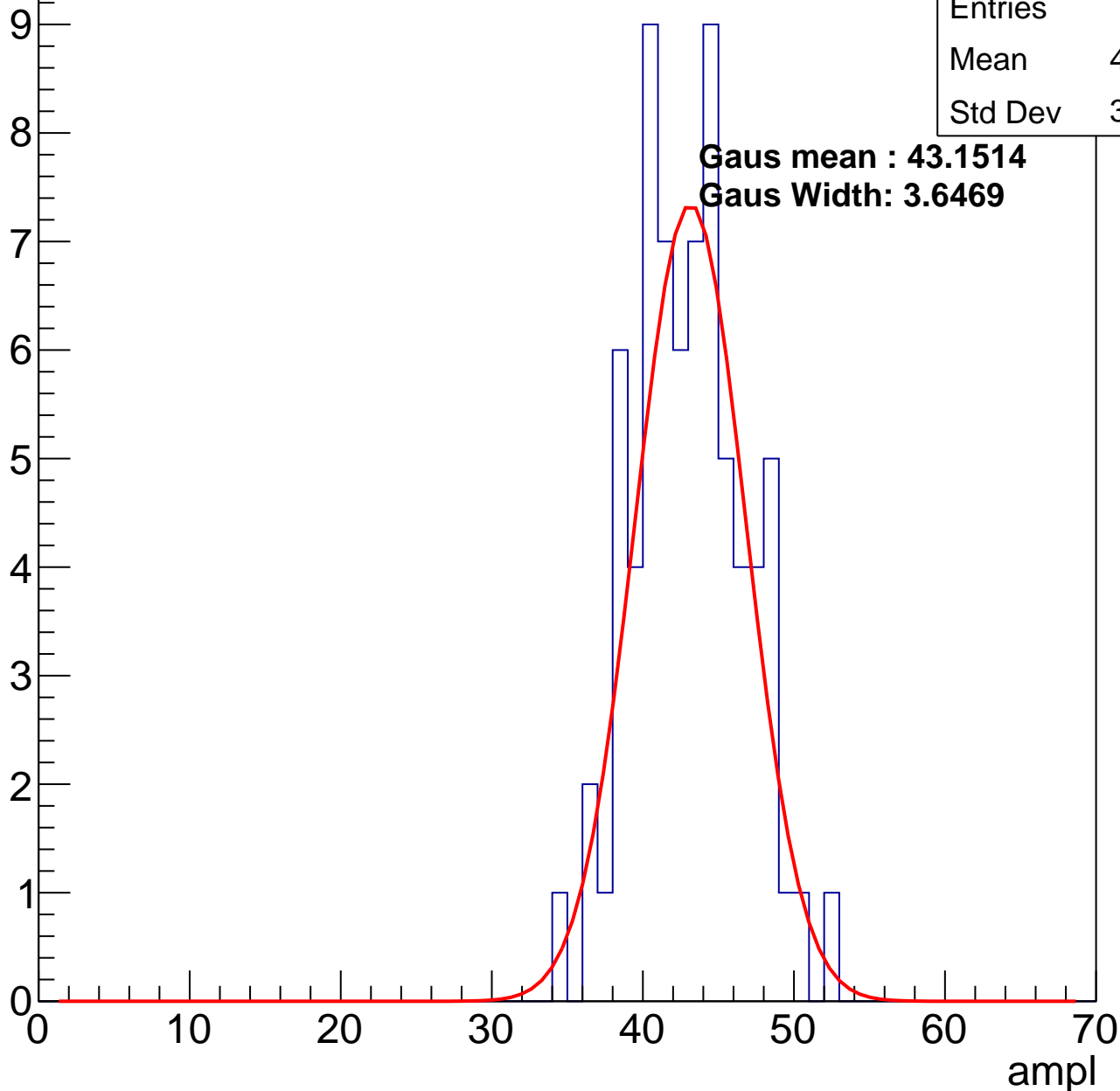
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.62
Std Dev	3.614

**Gaus mean : 43.1514**

**Gaus Width: 3.6469**

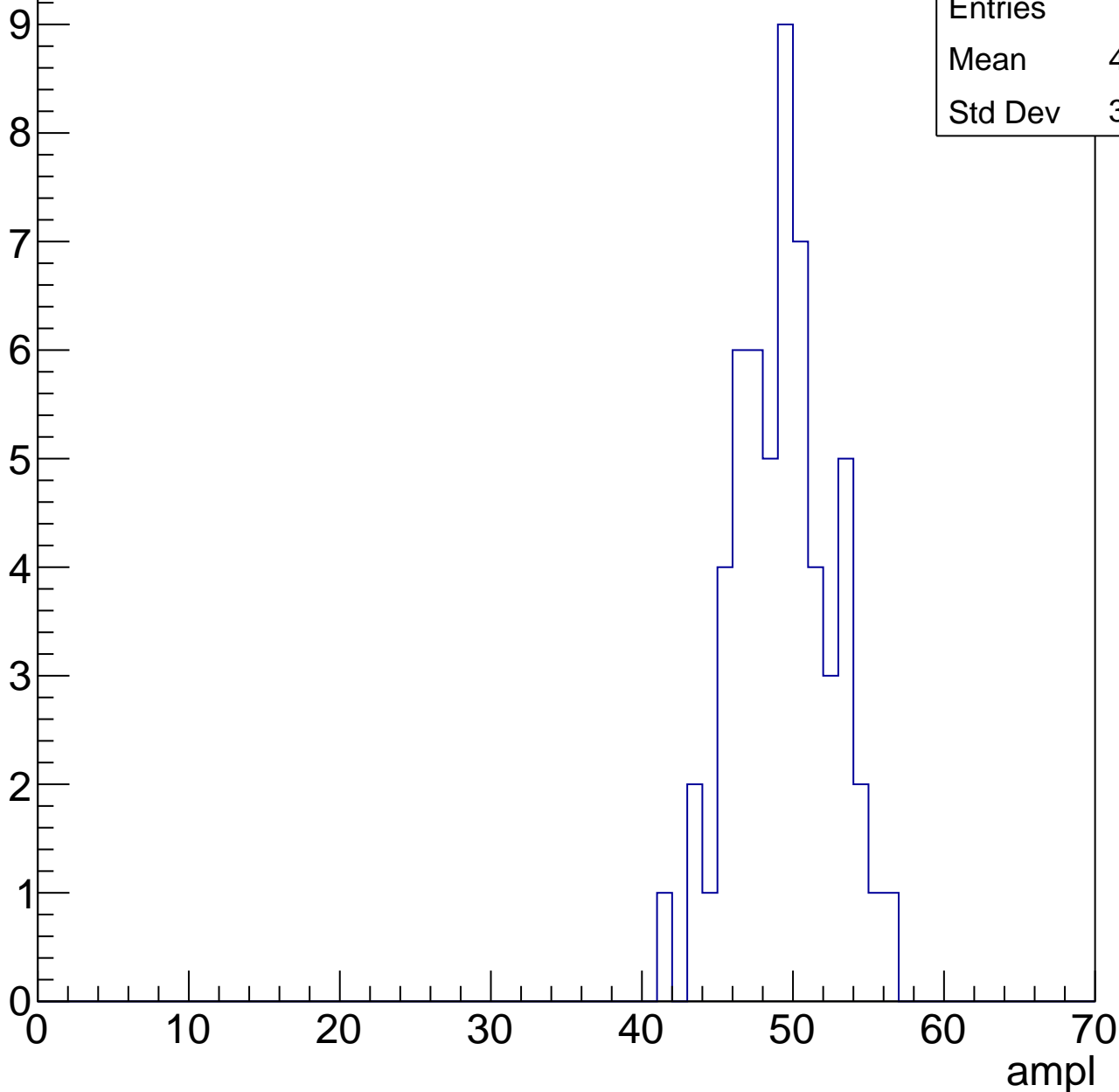


# B1L102S, U25-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.84
Std Dev	3.167

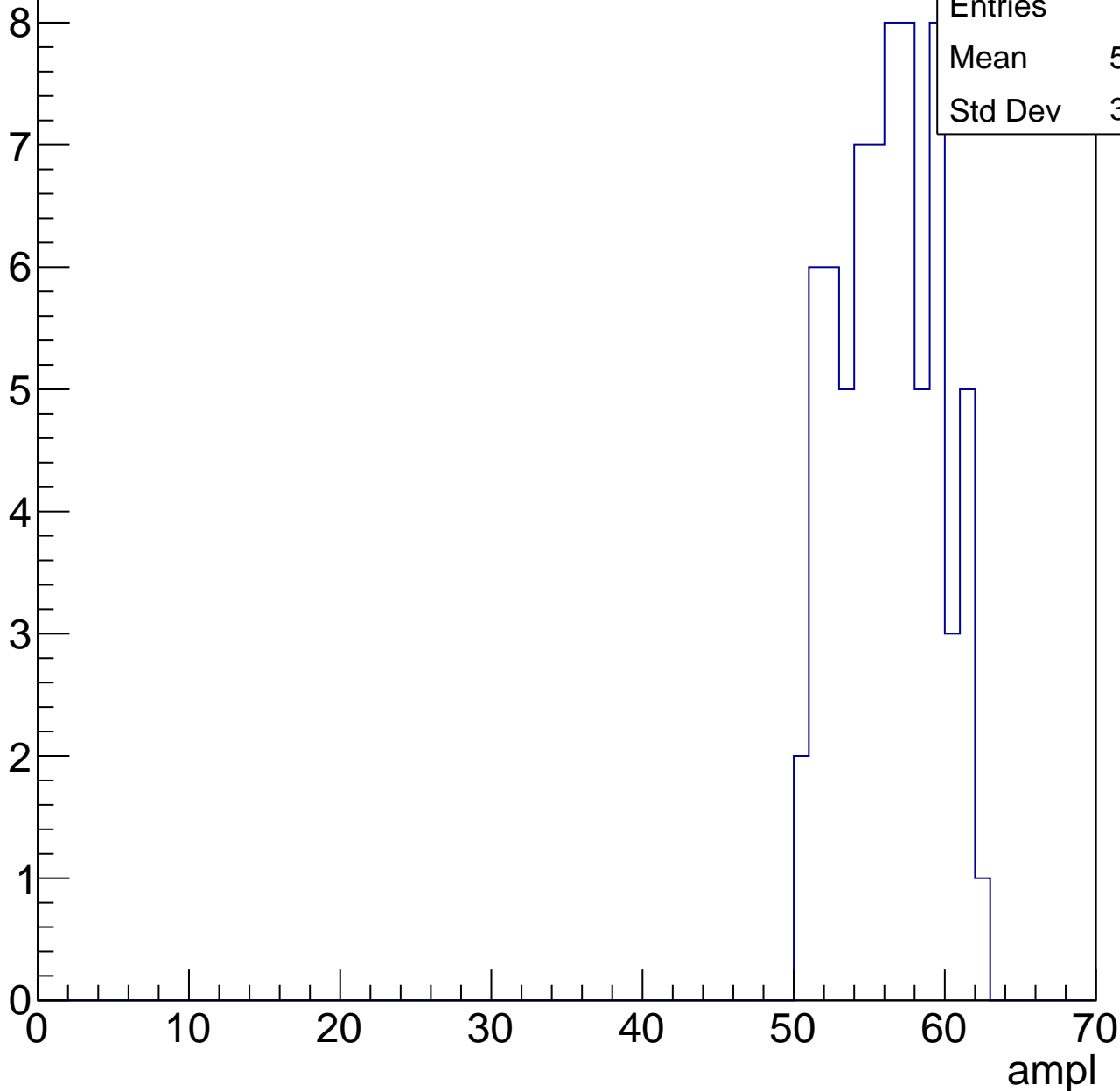


# B1L102S, U25-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55.76
Std Dev	3.146

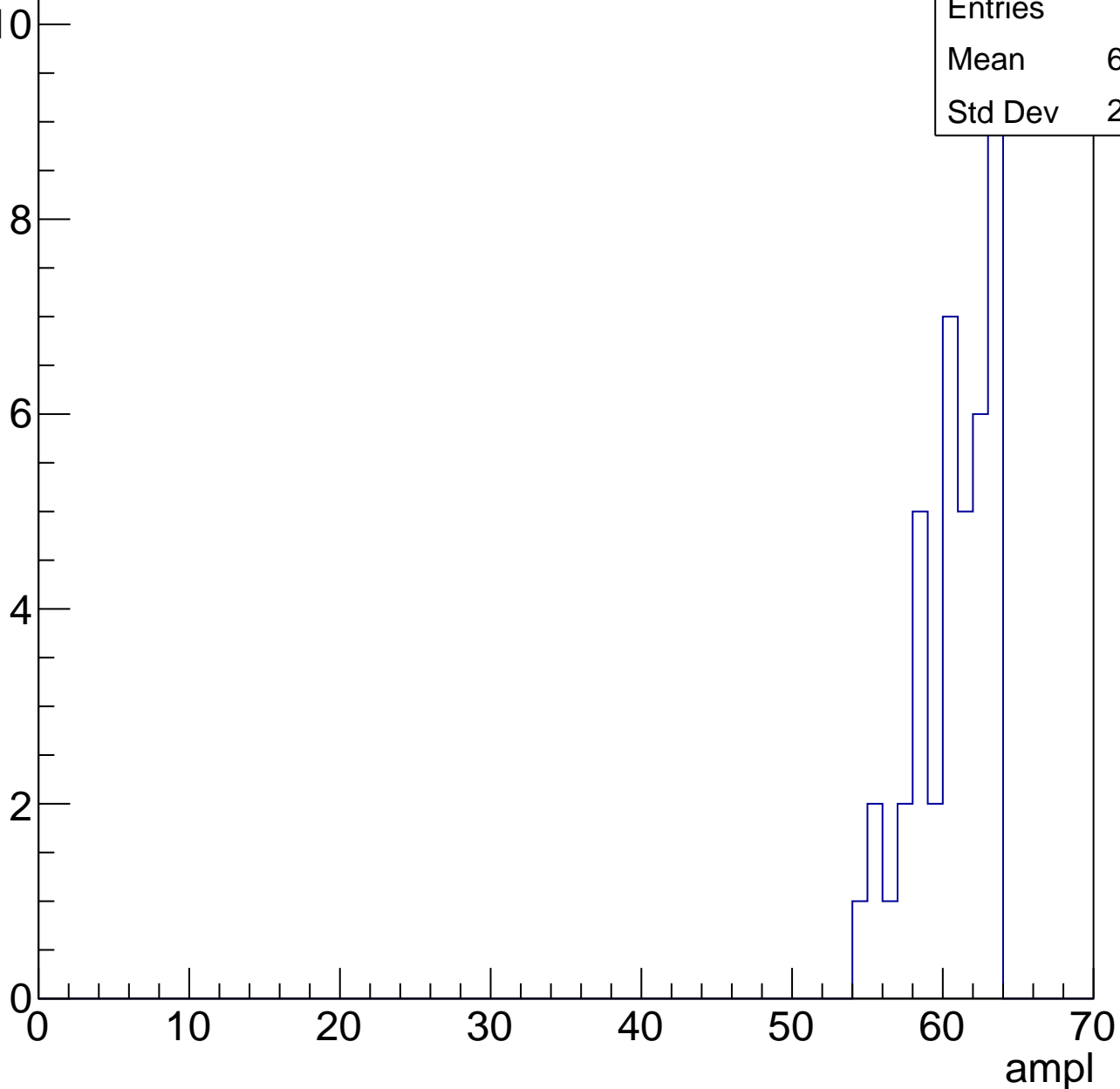


# B1L102S, U25-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

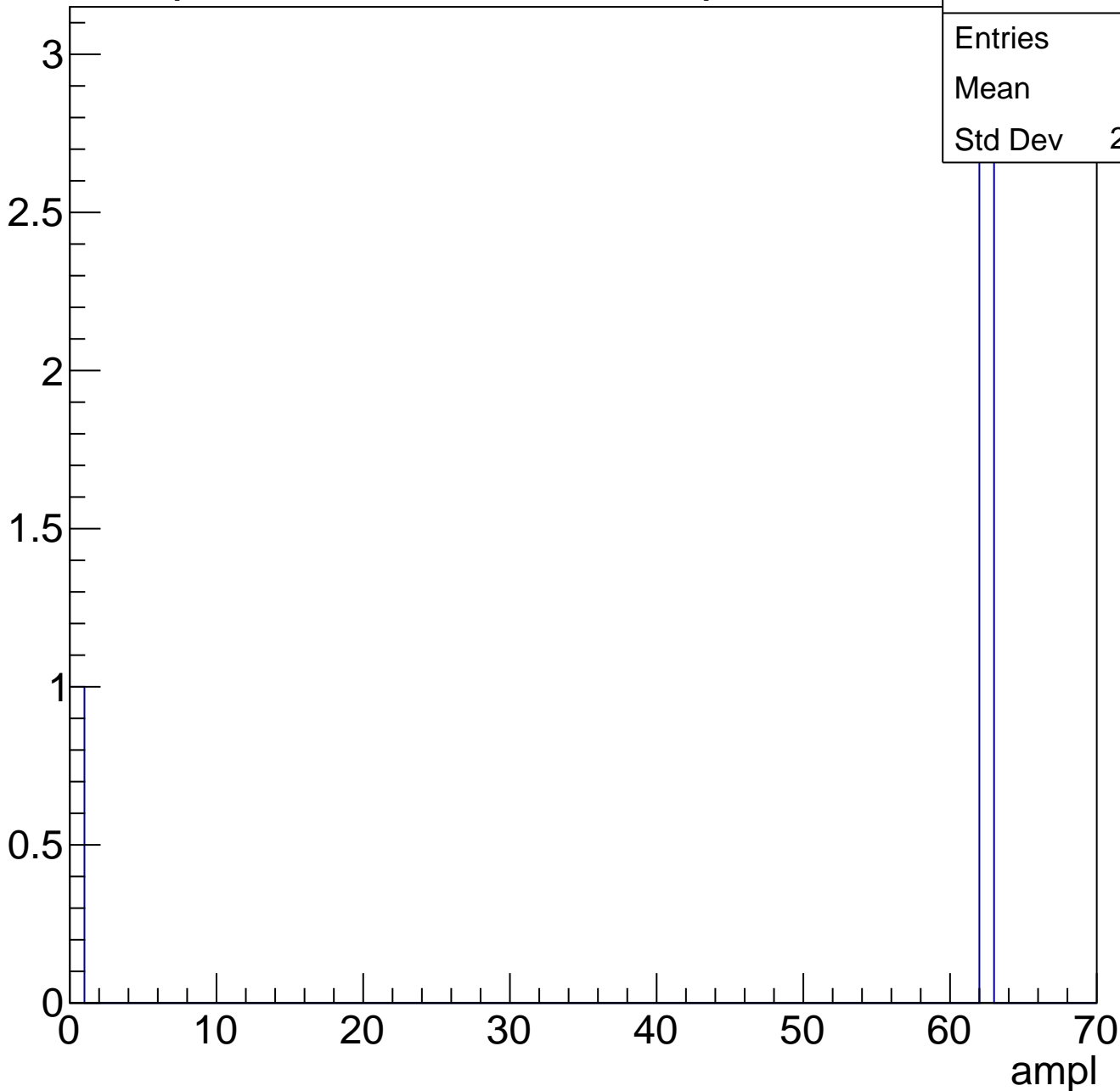
Entries	41
Mean	60.22
Std Dev	2.513



# B1L102S, U25-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch2, adc0

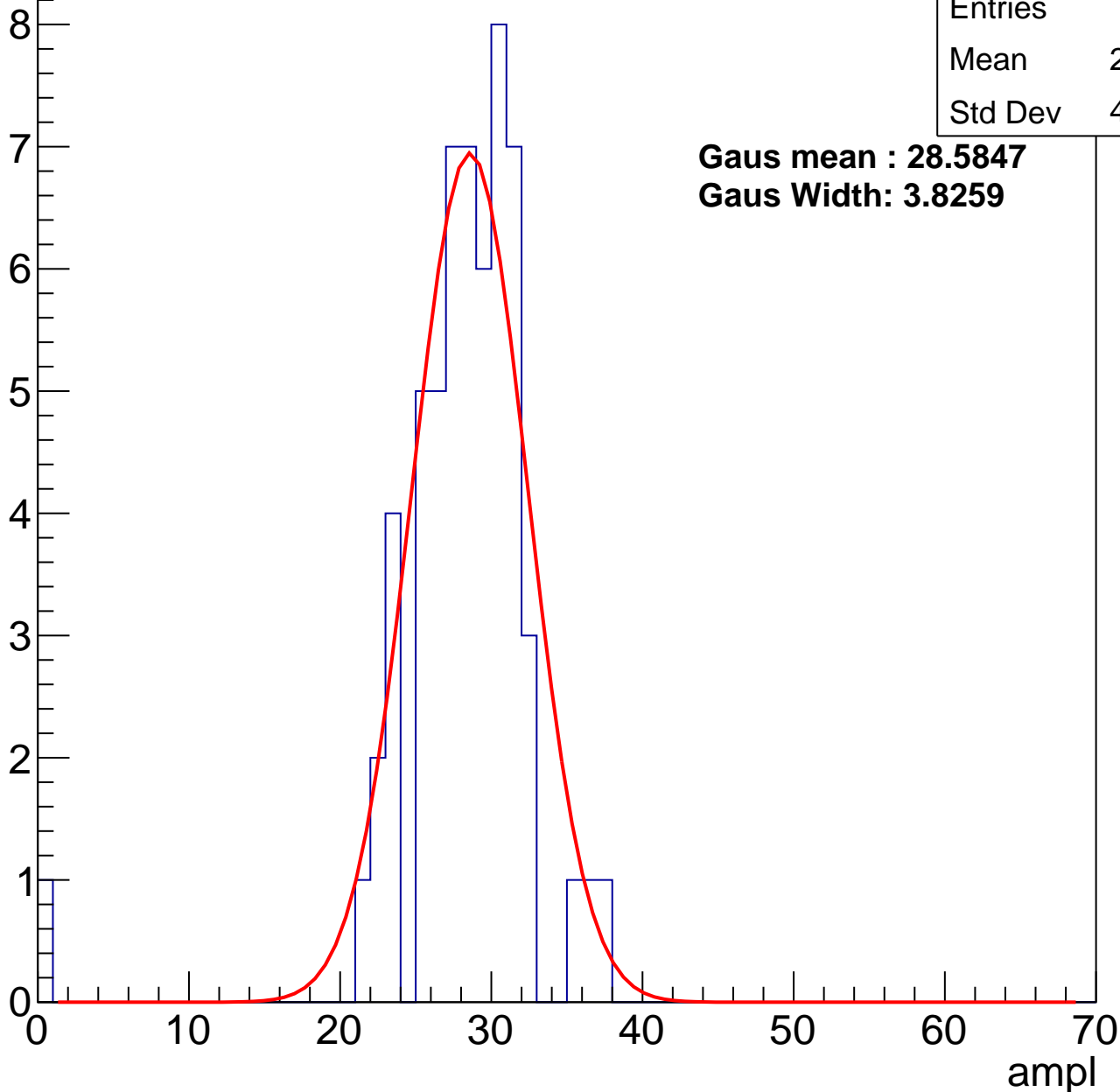
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.66
Std Dev	4.894

**Gaus mean : 28.5847**

**Gaus Width: 3.8259**



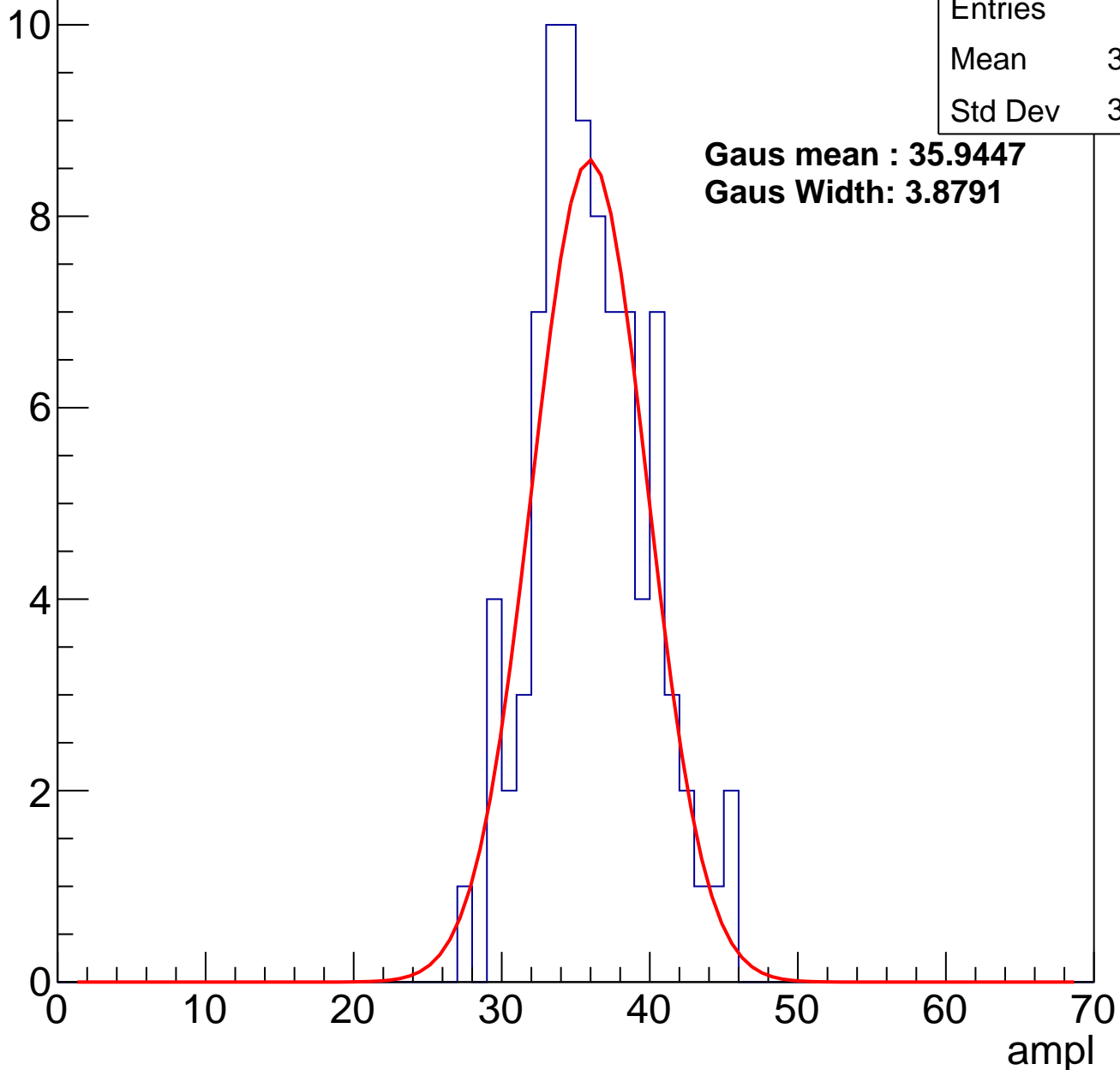
# B1L102S, U25-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	88
Mean	35.66
Std Dev	3.799

**Gaus mean : 35.9447**  
**Gaus Width: 3.8791**

Entry



# B1L102S, U25-ch2, adc2

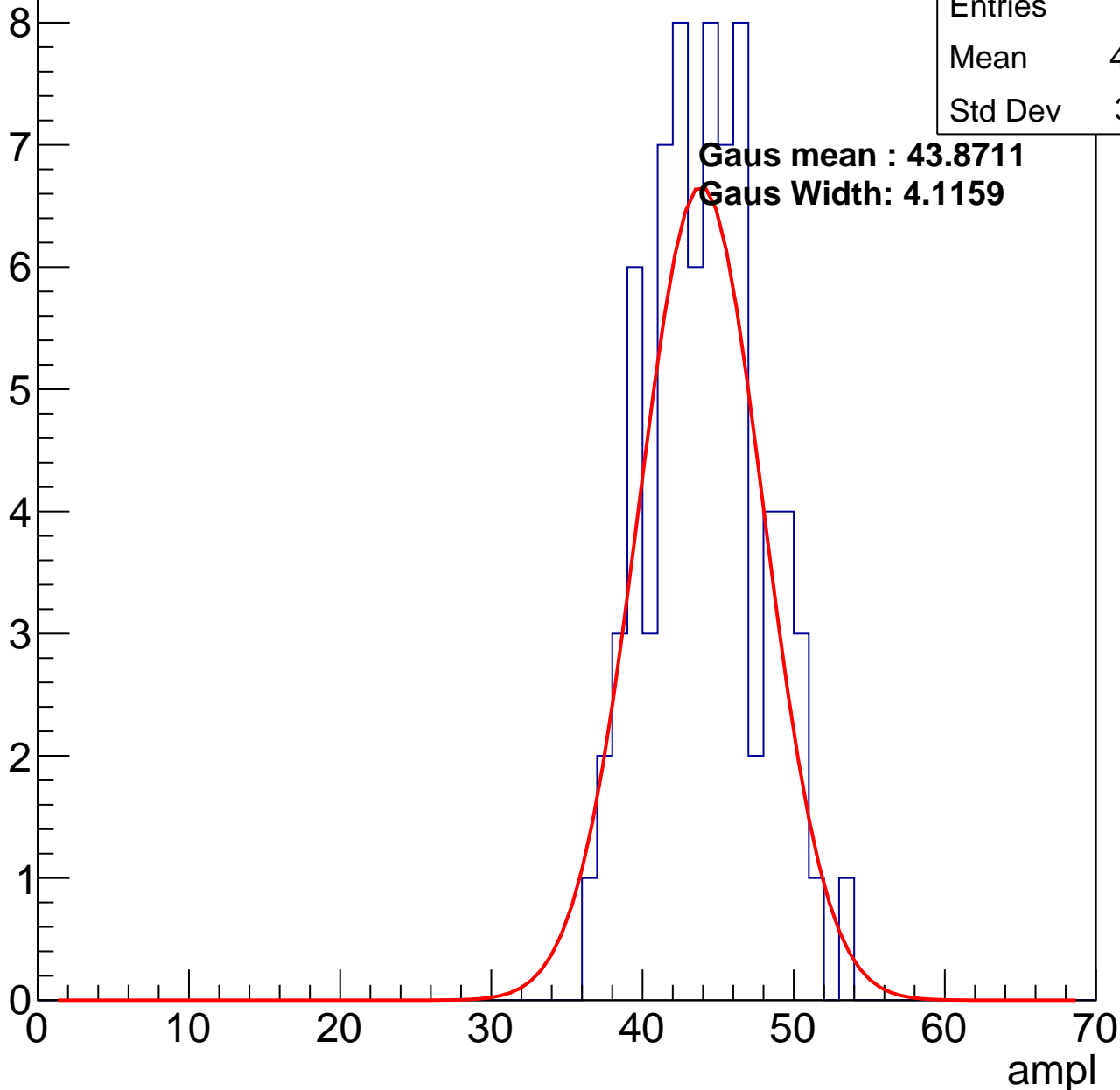
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.65
Std Dev	3.711

**Gaus mean : 43.8711**

**Gaus Width: 4.1159**

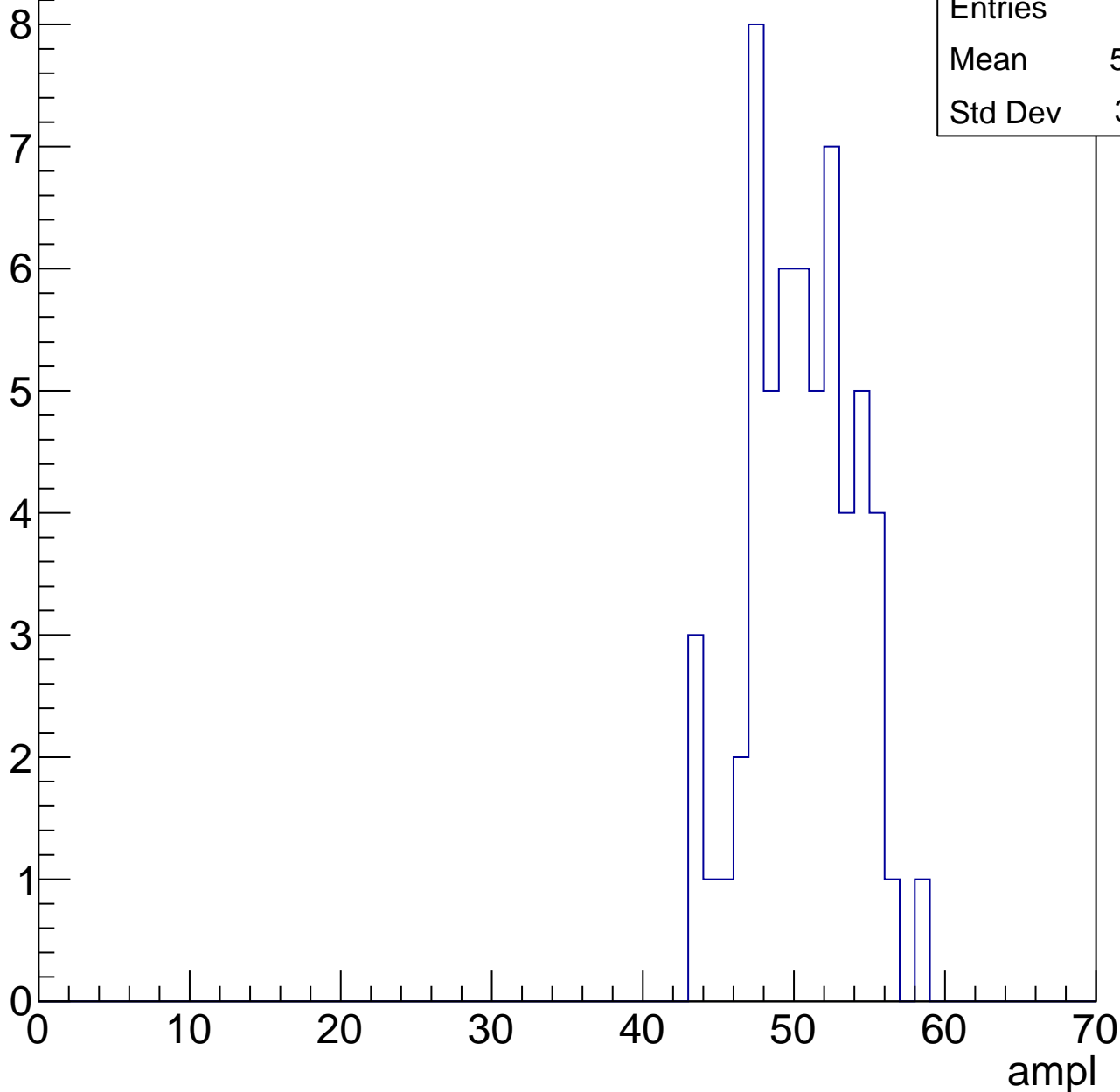


# B1L102S, U25-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

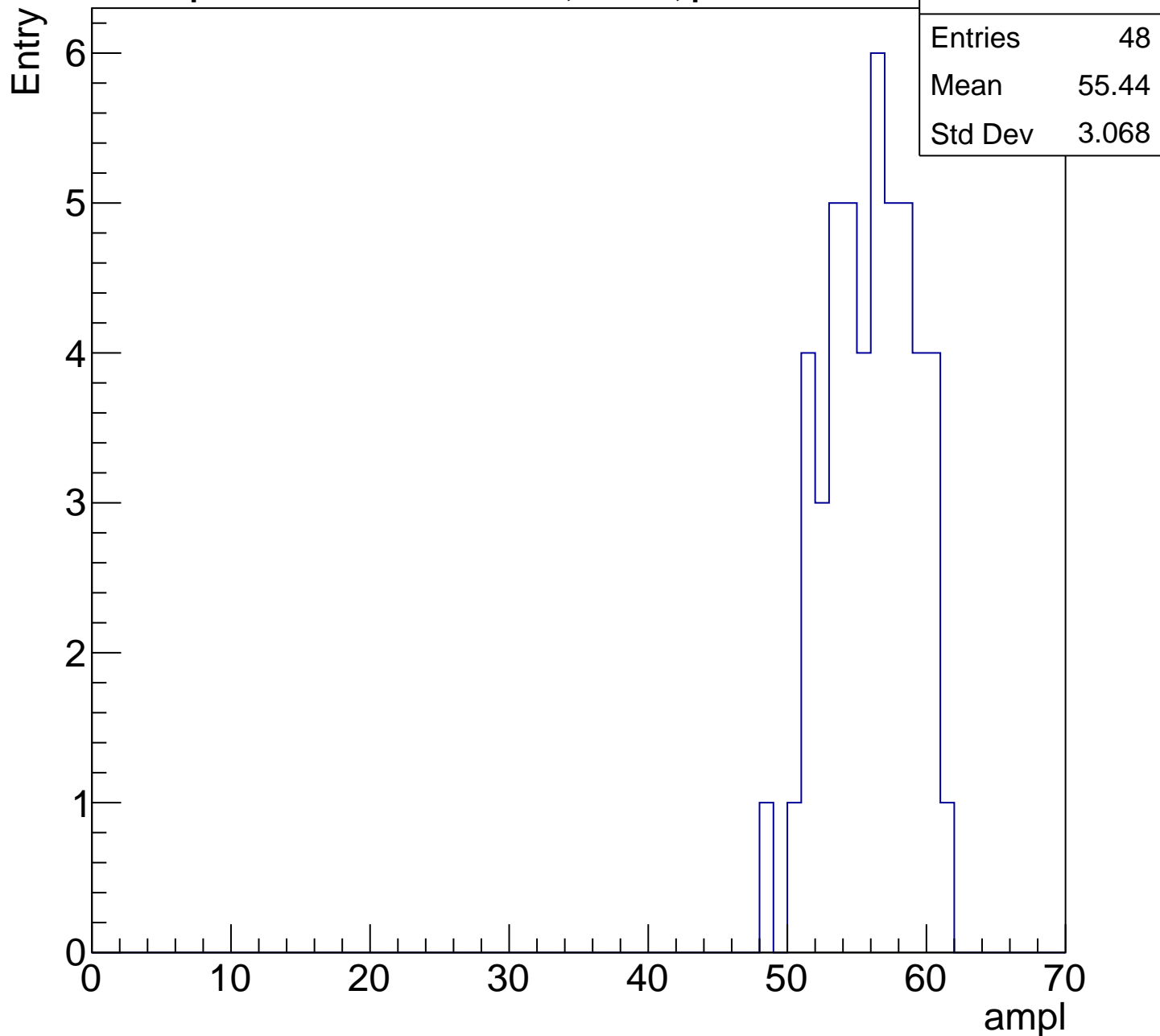
Entry

Entries	59
Mean	50.08
Std Dev	3.411



# B1L102S, U25-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2



# B1L102S, U25-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	58.56
Std Dev	8.846

ampl

0

10

20

30

40

50

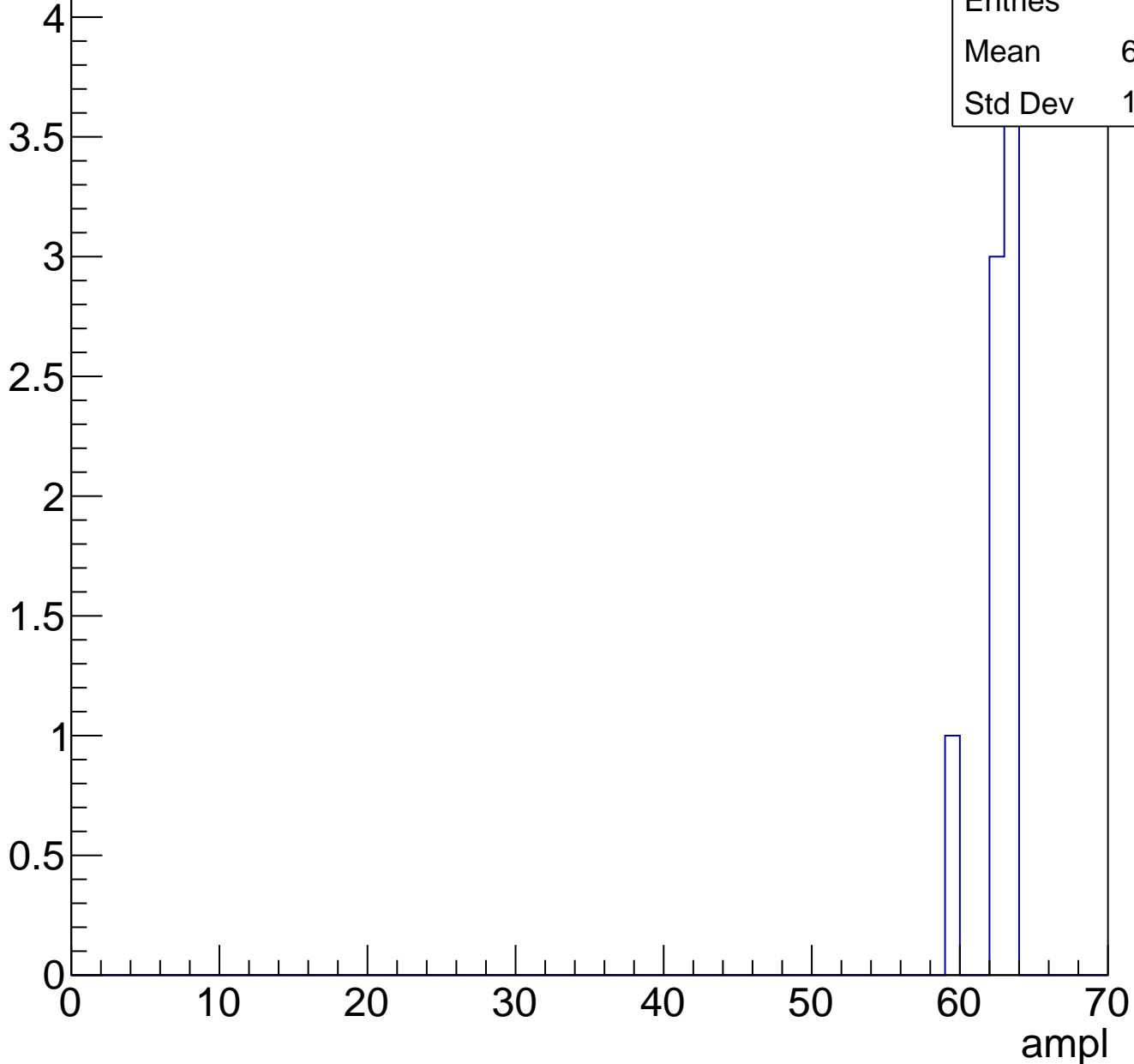
60

70

# B1L102S, U25-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch3, adc0

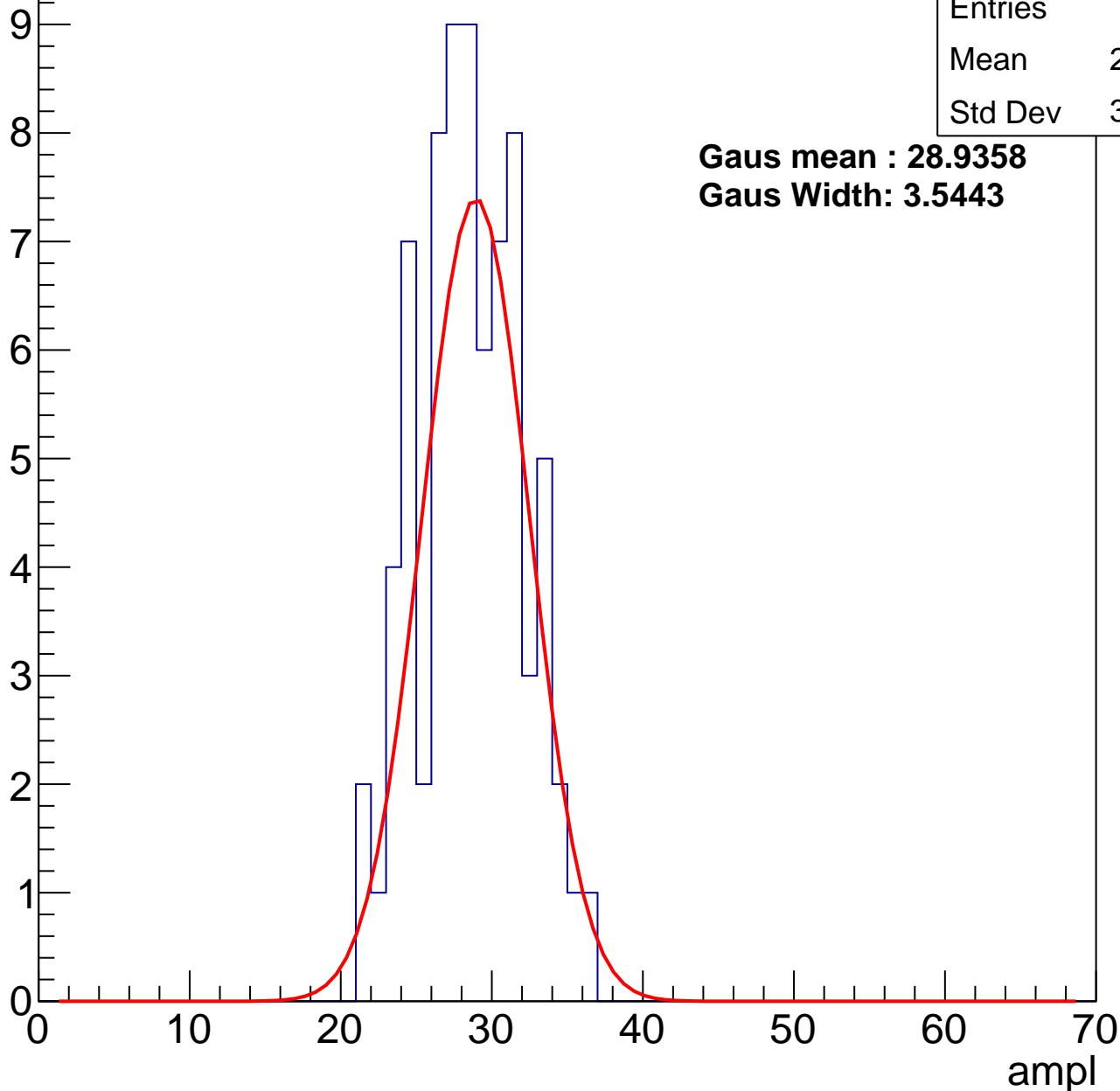
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.12
Std Dev	3.402

**Gaus mean : 28.9358**

**Gaus Width: 3.5443**



# B1L102S, U25-ch3, adc1

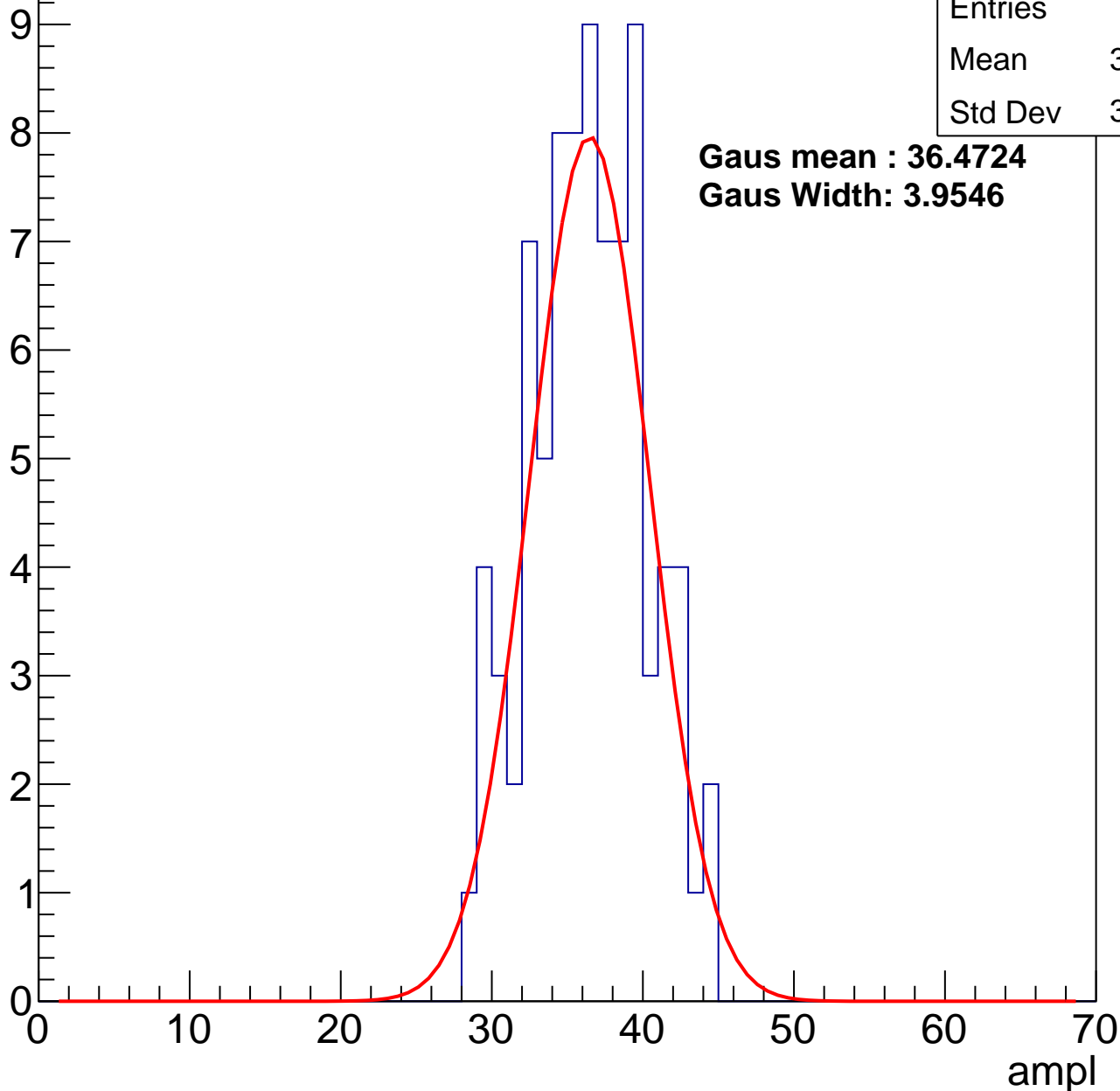
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	35.95
Std Dev	3.779

**Gaus mean : 36.4724**

**Gaus Width: 3.9546**



# B1L102S, U25-ch3, adc2

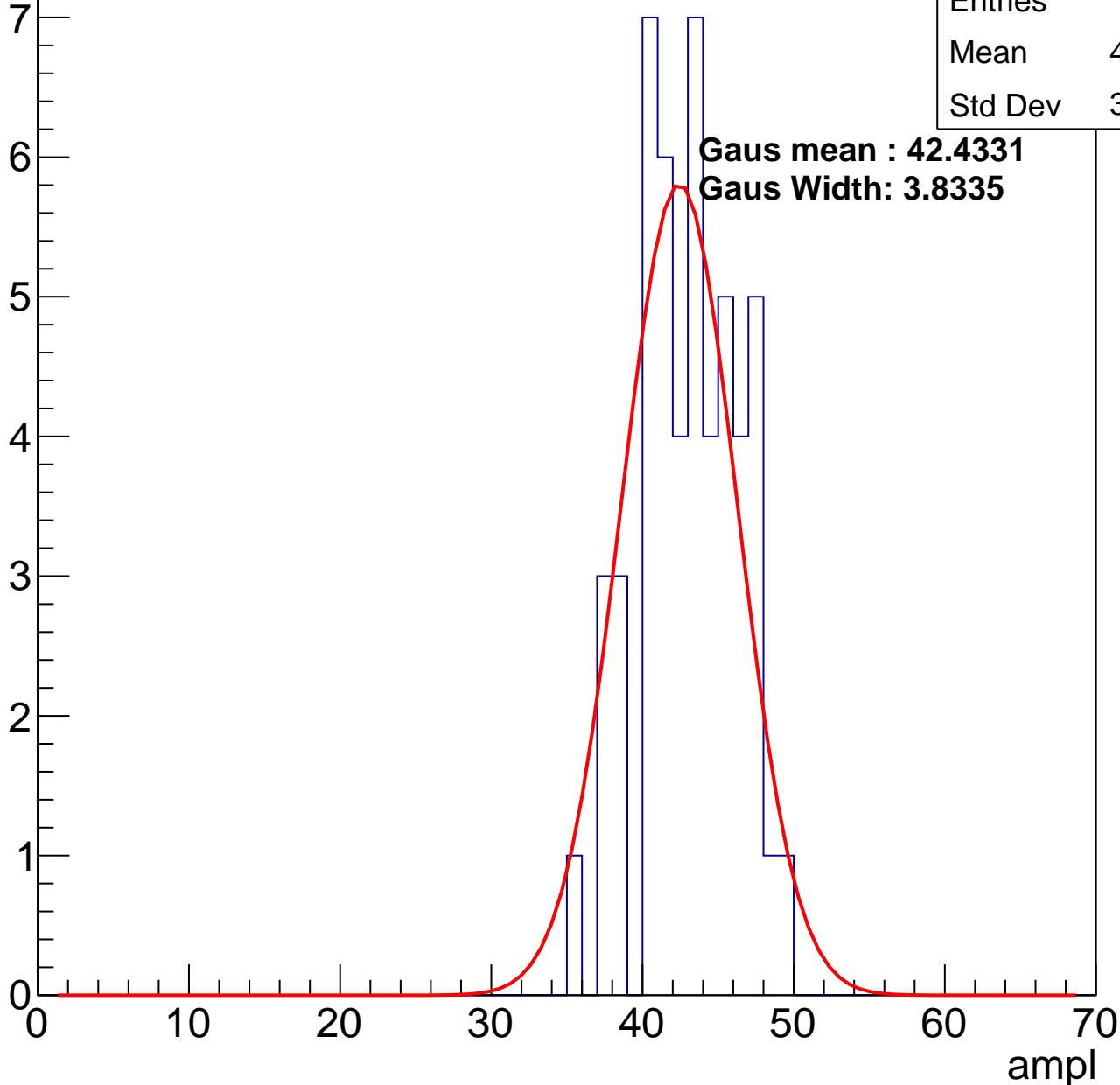
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.59
Std Dev	3.225

**Gaus mean : 42.4331**

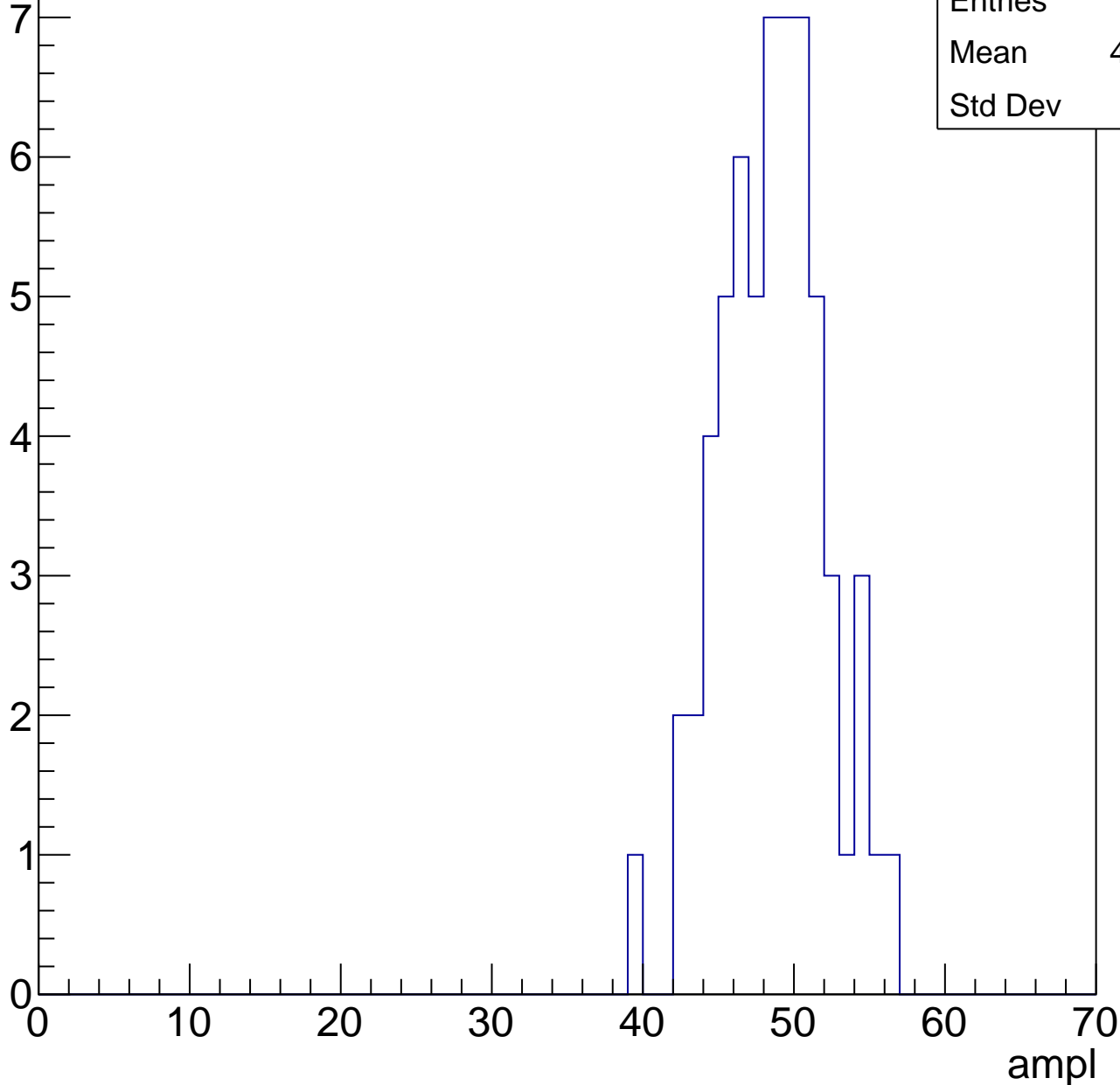
**Gaus Width: 3.8335**



# B1L102S, U25-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



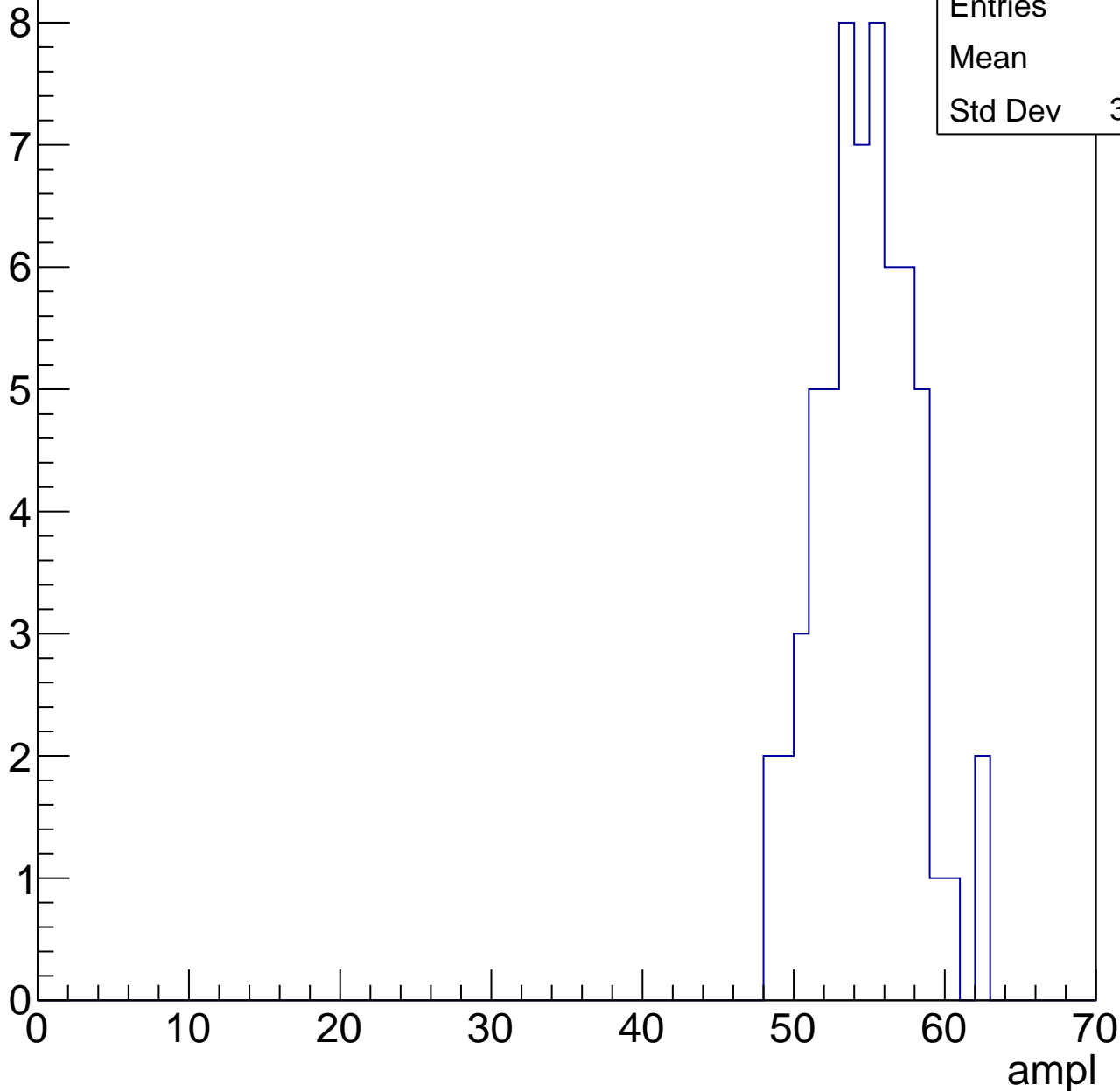
Entries	60
Mean	48.12
Std Dev	3.45

# B1L102S, U25-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.3
Std Dev	3.117

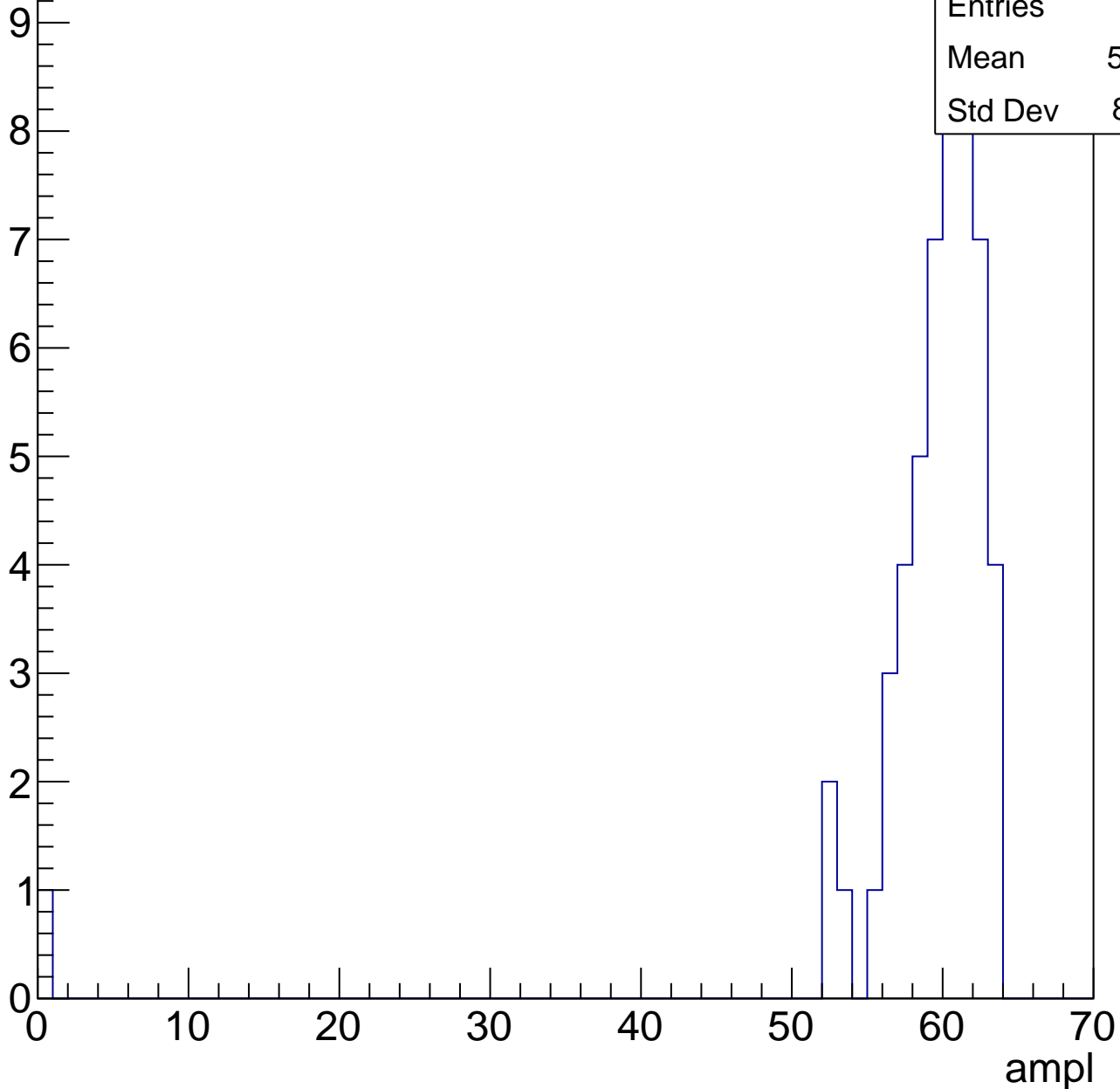


# B1L102S, U25-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.17
Std Dev	8.561

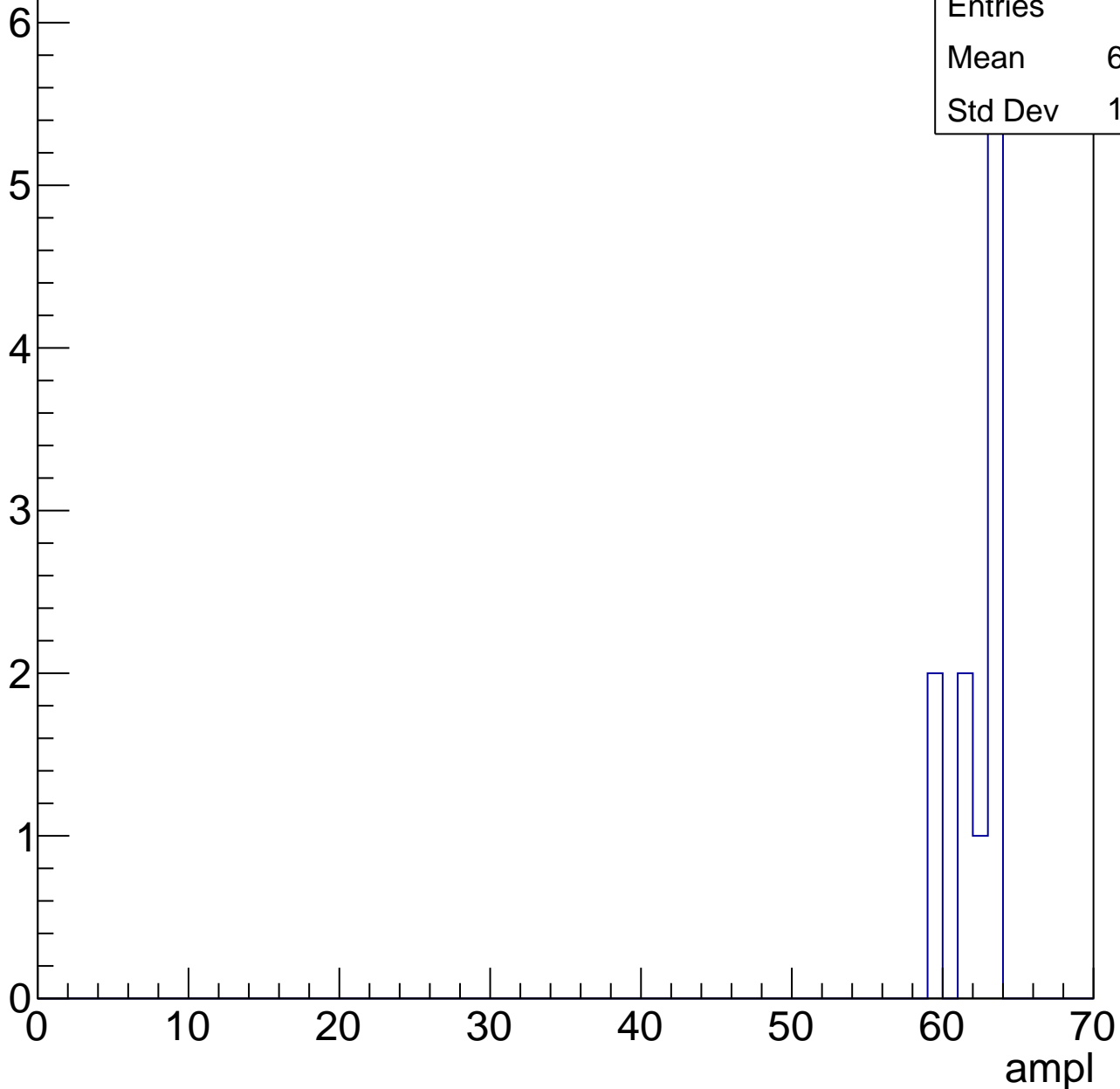


# B1L102S, U25-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.82
Std Dev	1.527





# B1L102S, U25-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch4, adc0

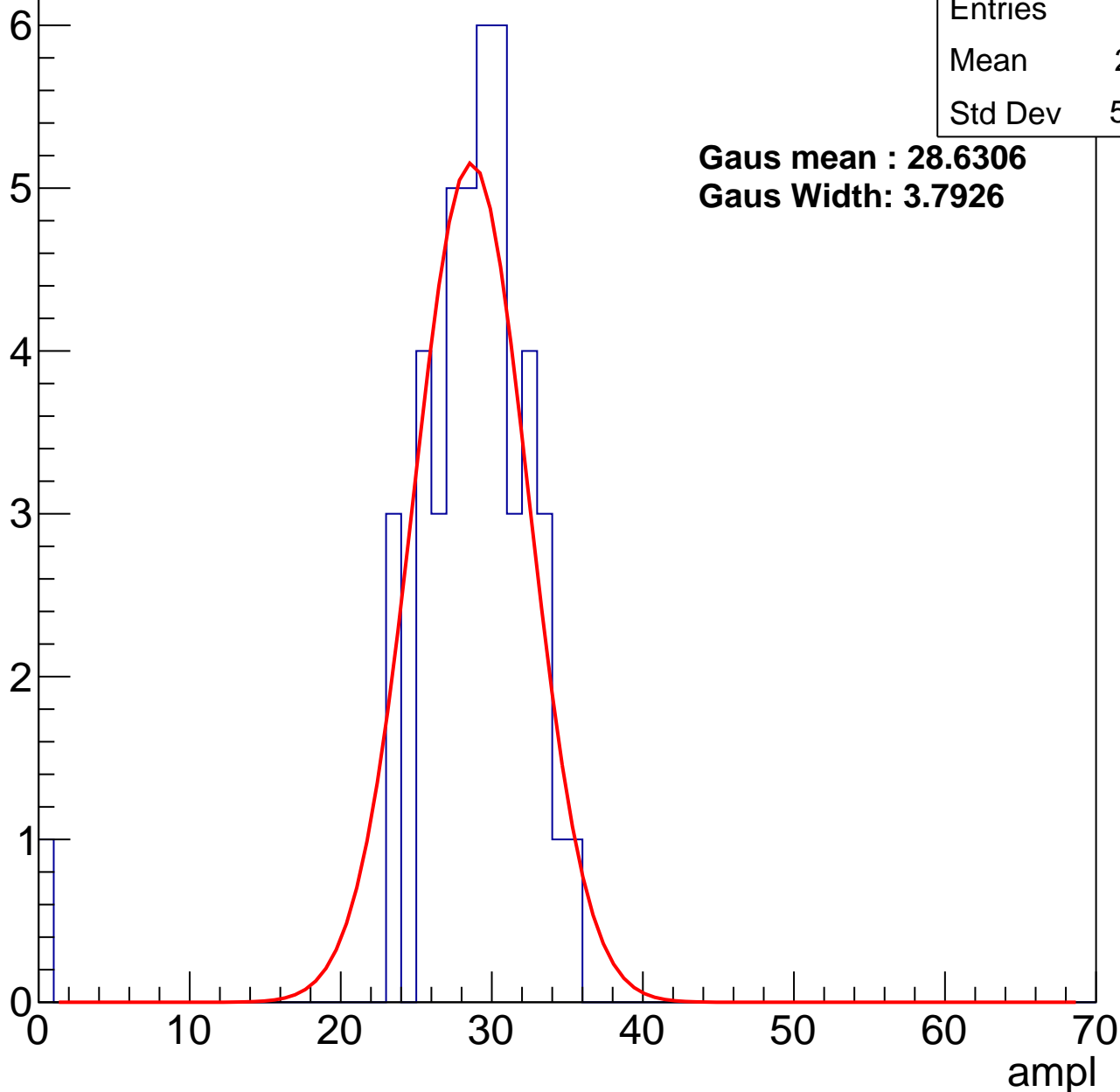
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	28.11
Std Dev	5.148

**Gaus mean : 28.6306**

**Gaus Width: 3.7926**



# B1L102S, U25-ch4, adc1

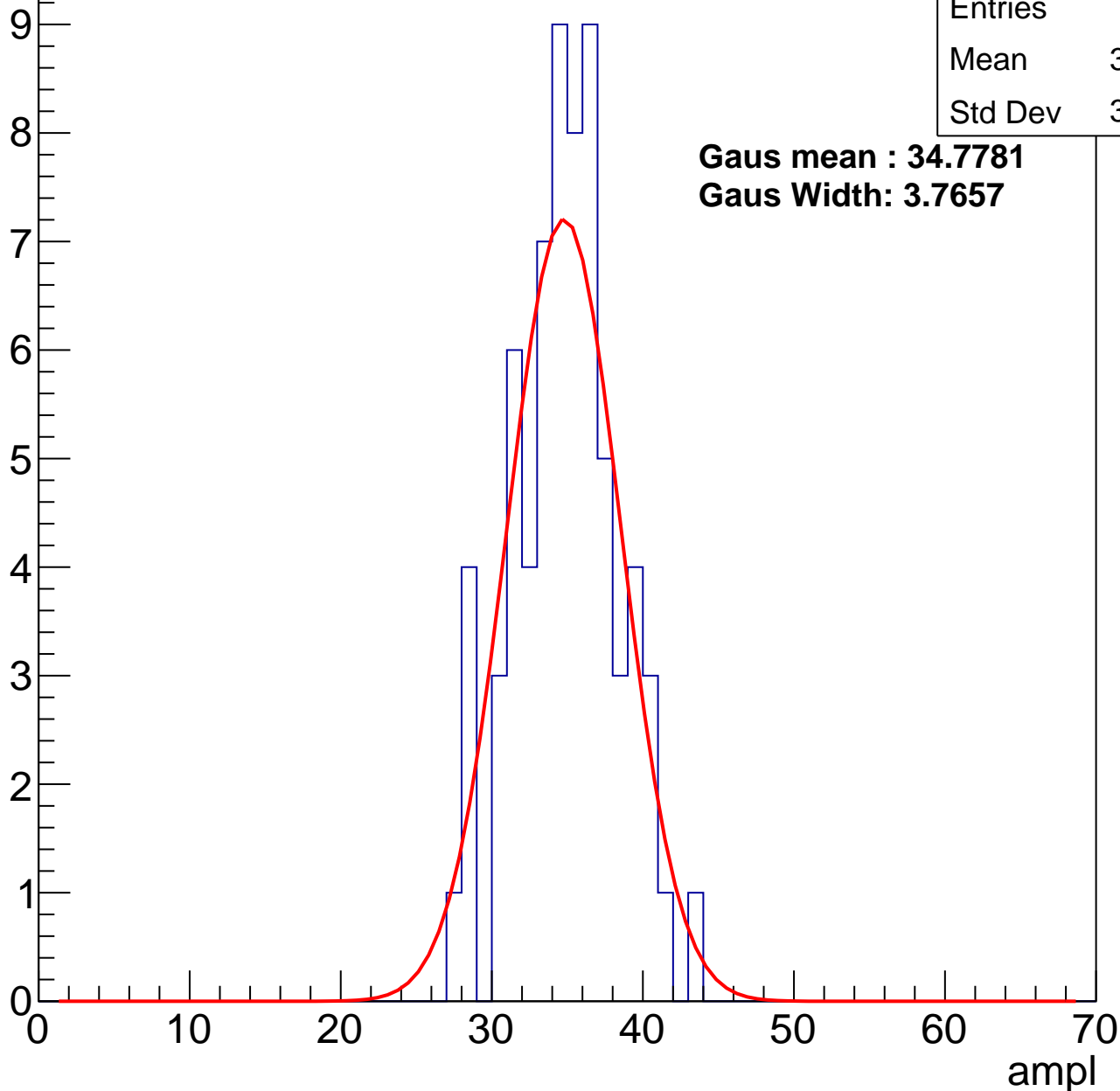
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.46
Std Dev	3.384

**Gaus mean : 34.7781**

**Gaus Width: 3.7657**



# B1L102S, U25-ch4, adc2

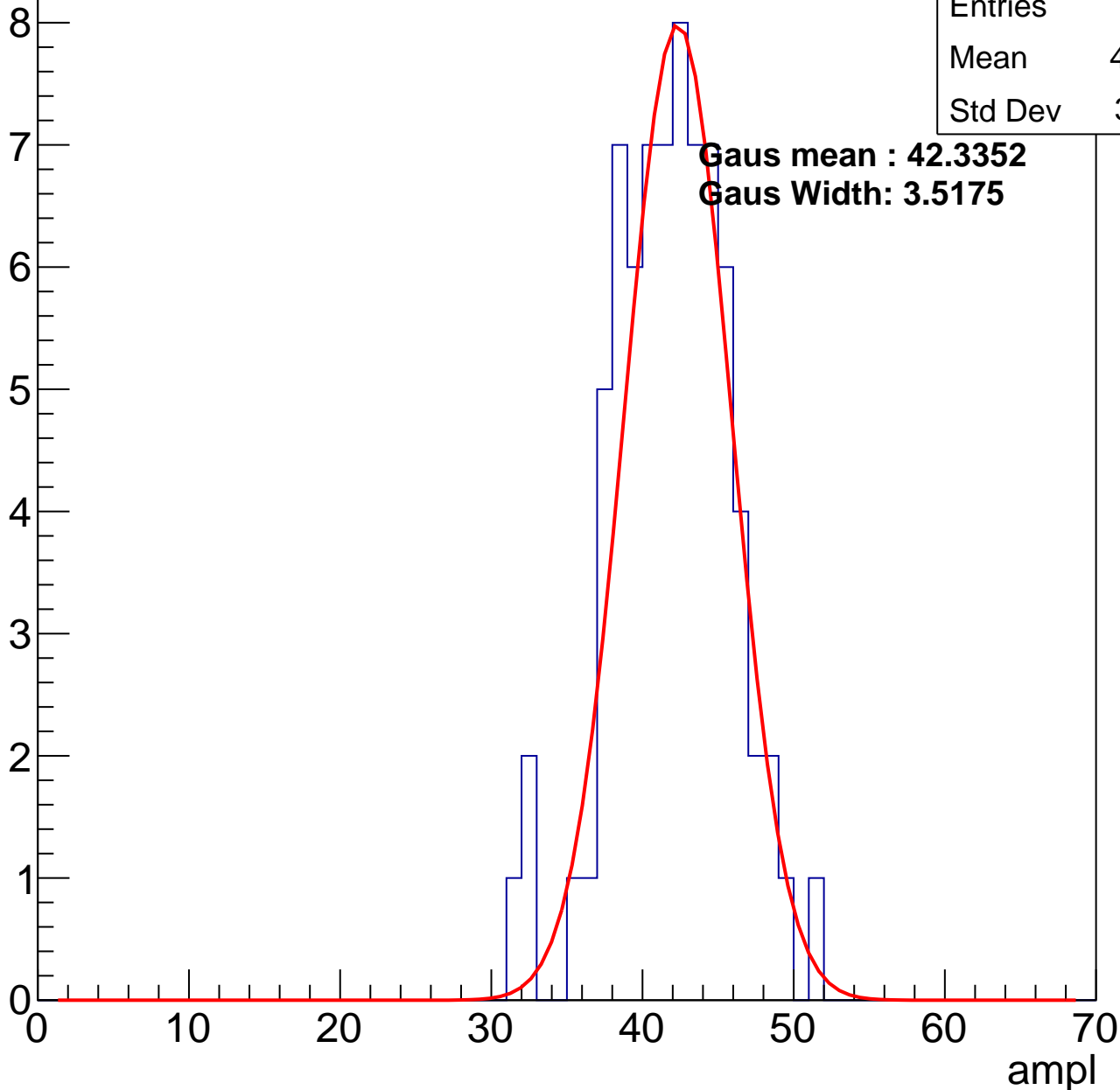
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.43
Std Dev	3.851

**Gaus mean : 42.3352**

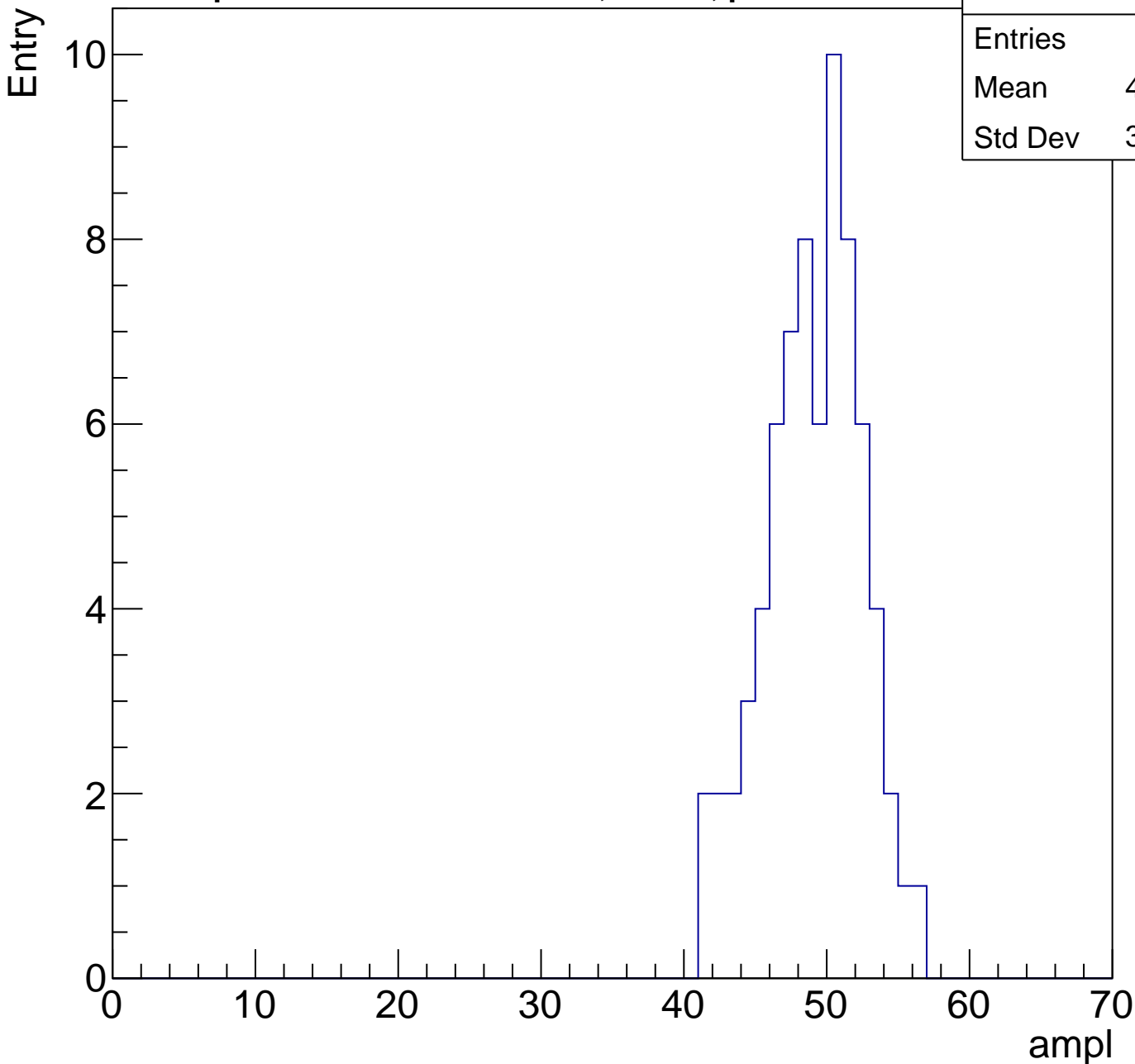
**Gaus Width: 3.5175**



# B1L102S, U25-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

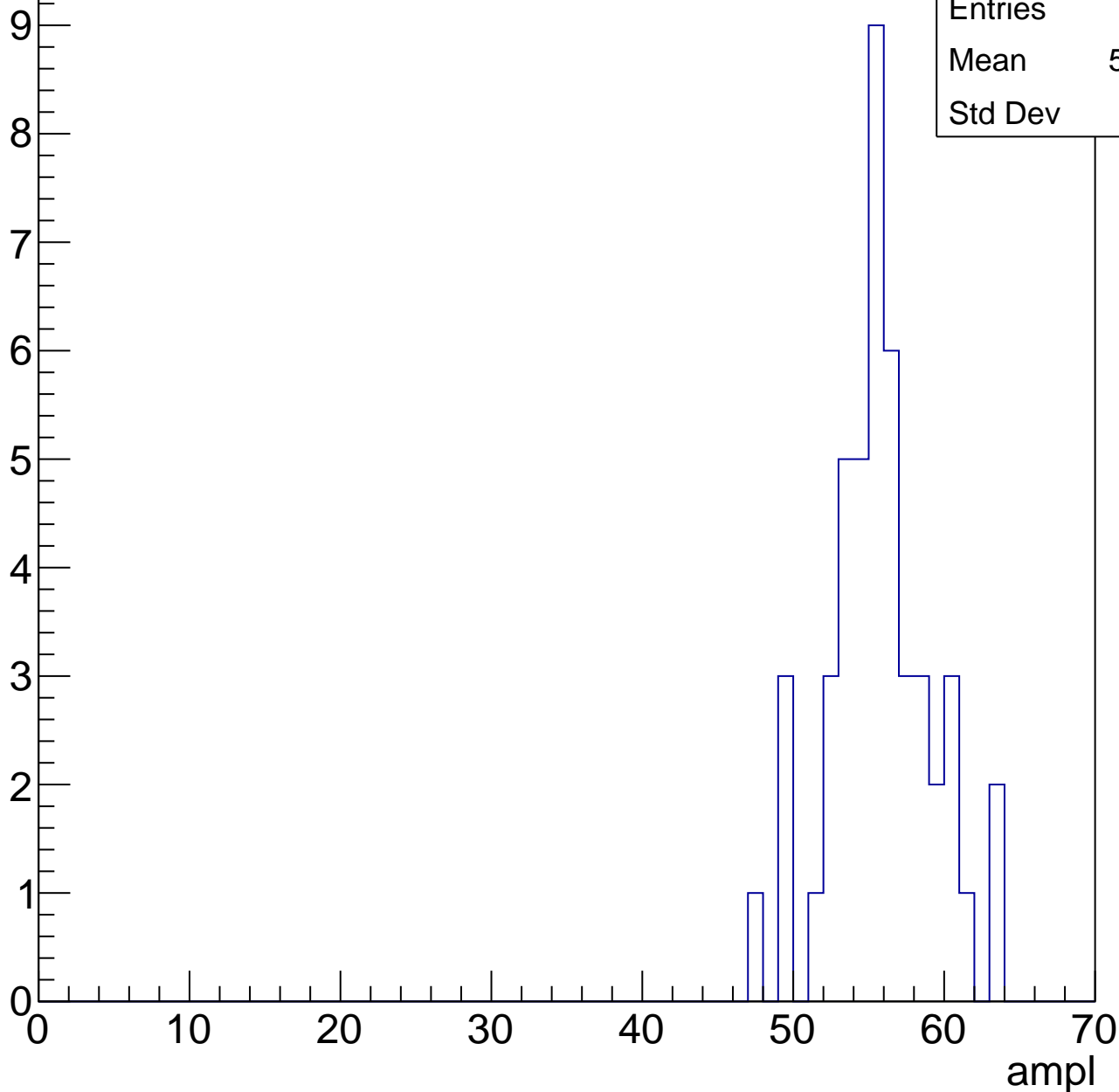
Entries	72
Mean	48.58
Std Dev	3.353



# B1L102S, U25-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

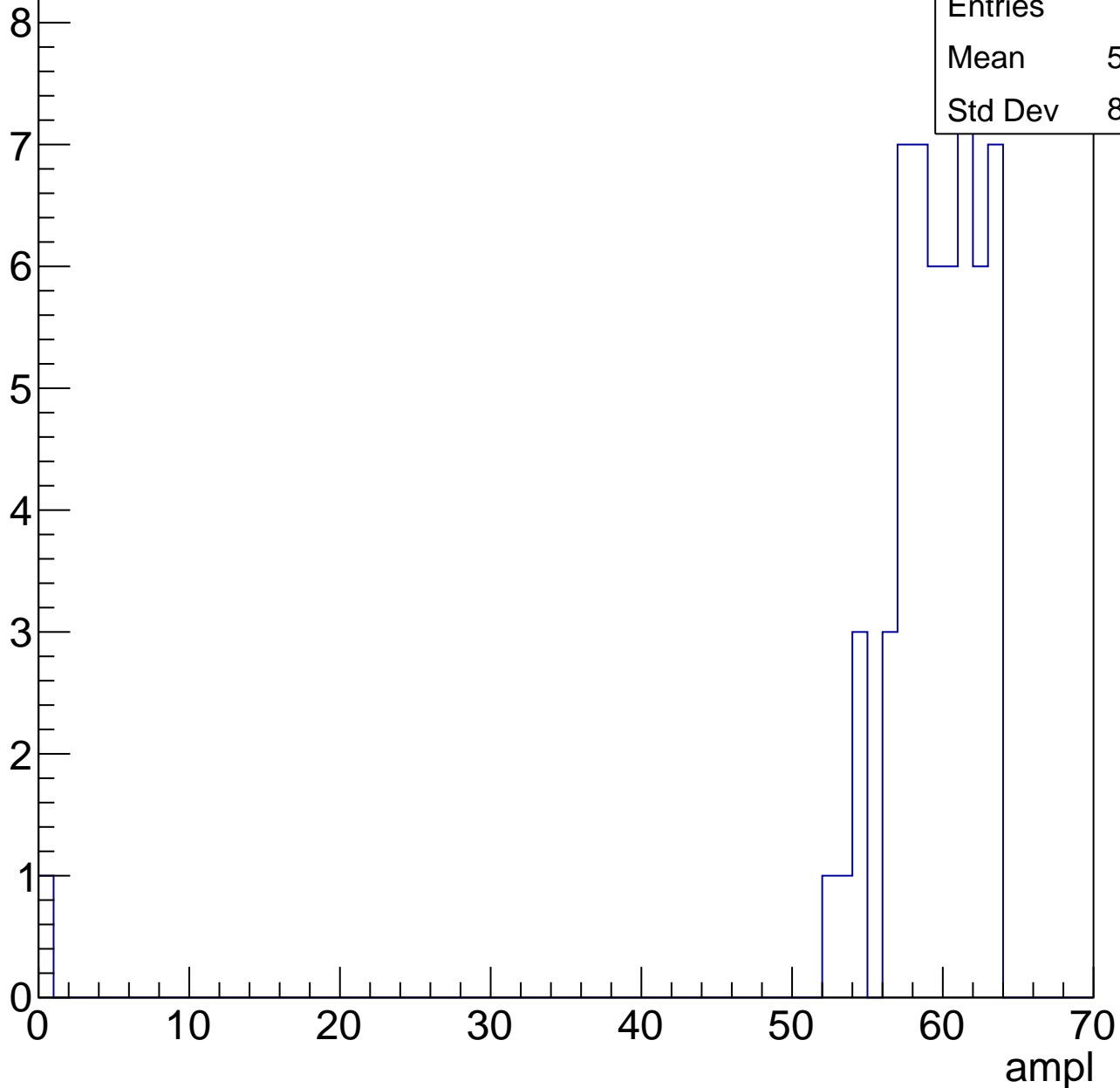


# B1L102S, U25-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

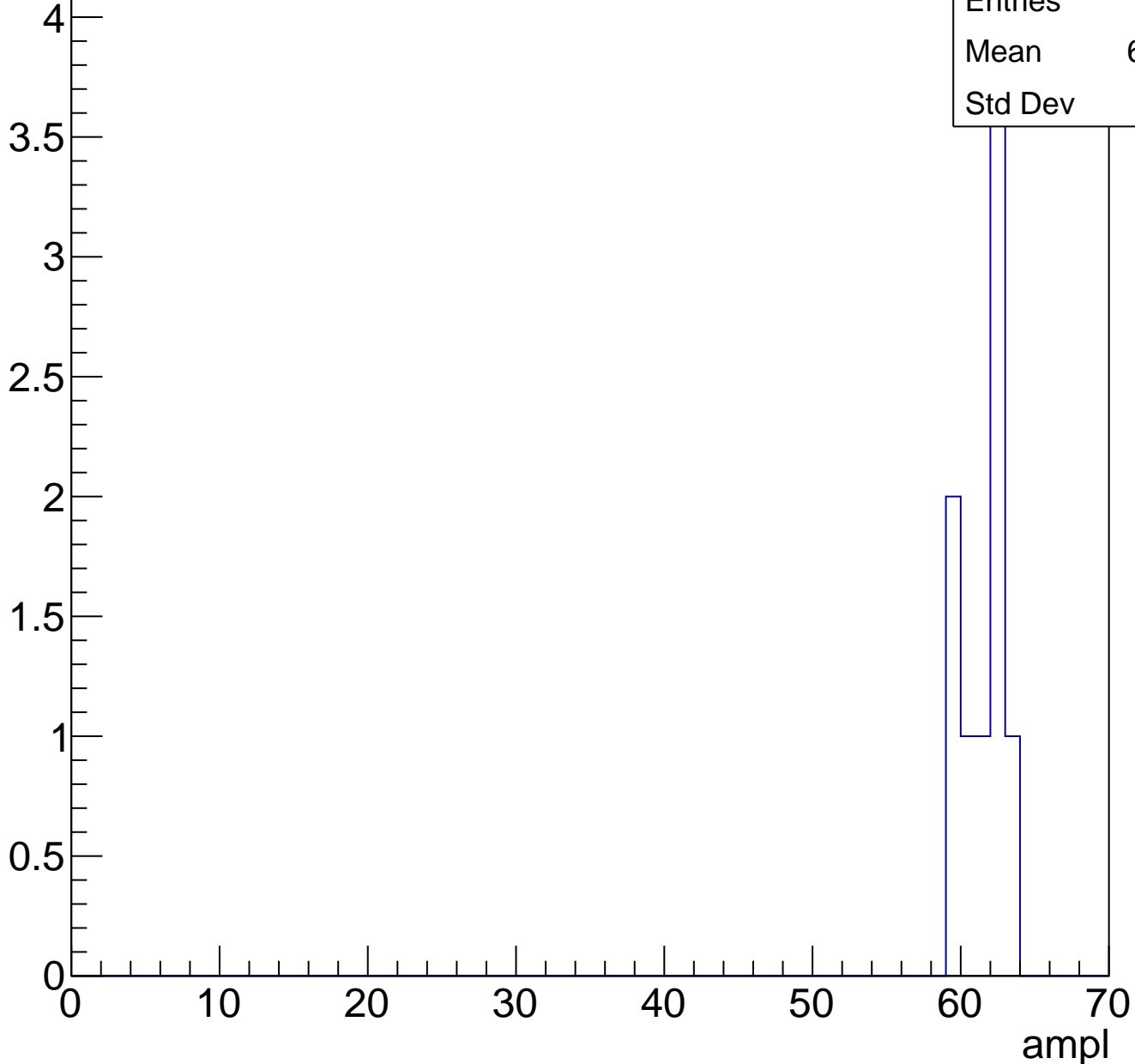
Entries	56
Mean	58.12
Std Dev	8.307



# B1L102S, U25-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch5, adc0

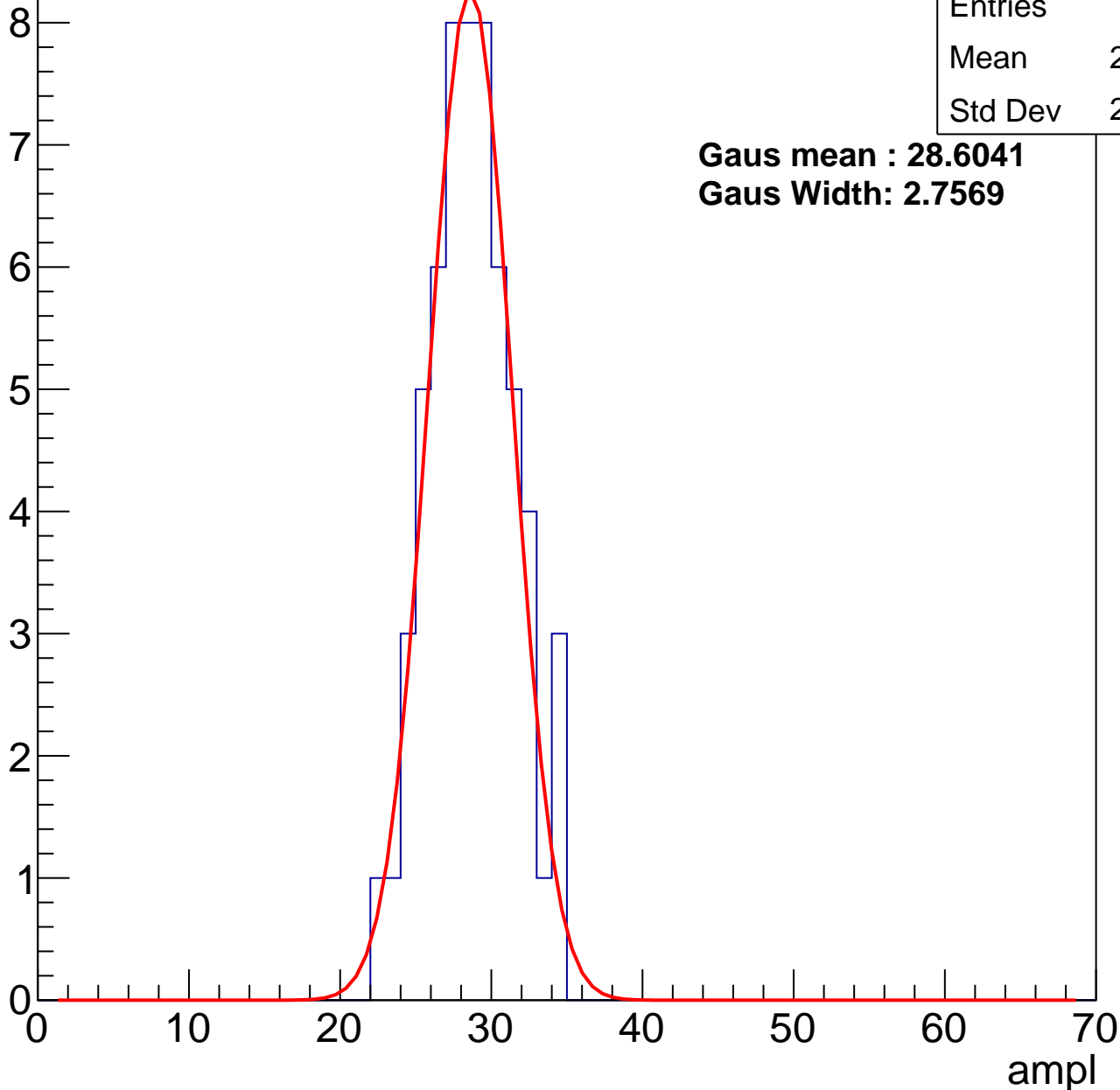
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.27
Std Dev	2.779

**Gaus mean : 28.6041**

**Gaus Width: 2.7569**



# B1L102S, U25-ch5, adc1

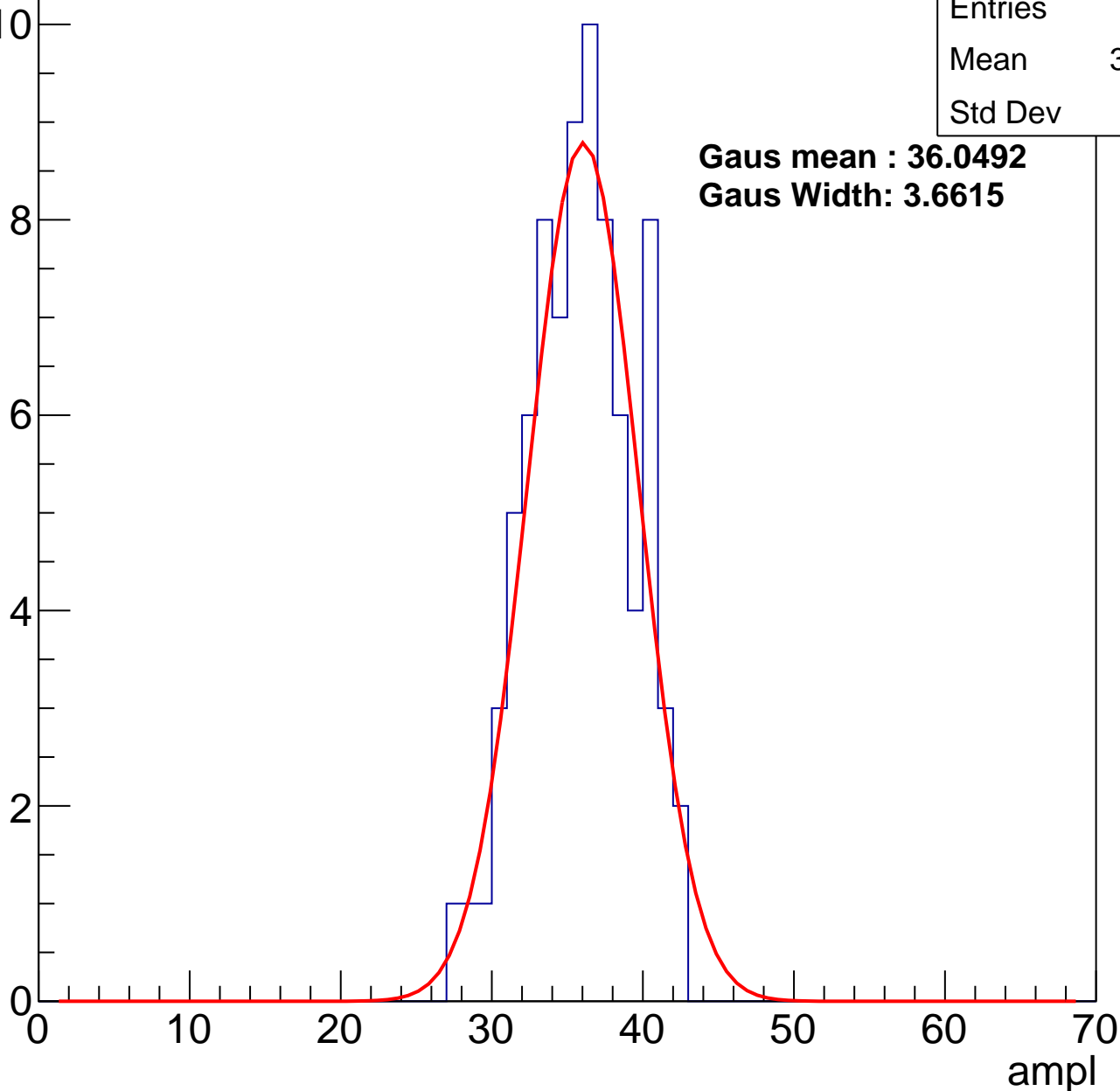
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.43
Std Dev	3.4

**Gaus mean : 36.0492**

**Gaus Width: 3.6615**



# B1L102S, U25-ch5, adc2

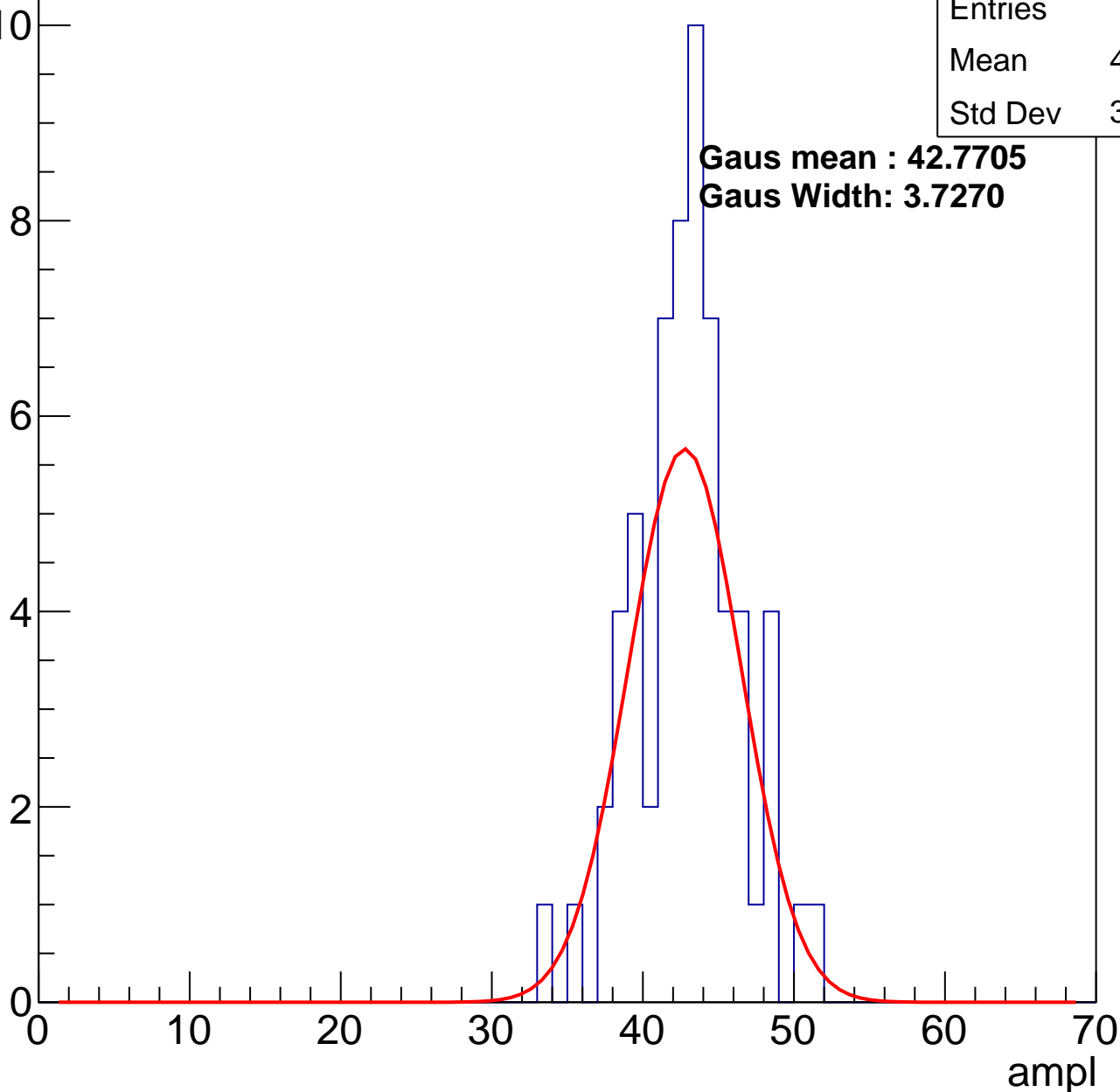
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.48
Std Dev	3.458

**Gaus mean : 42.7705**

**Gaus Width: 3.7270**

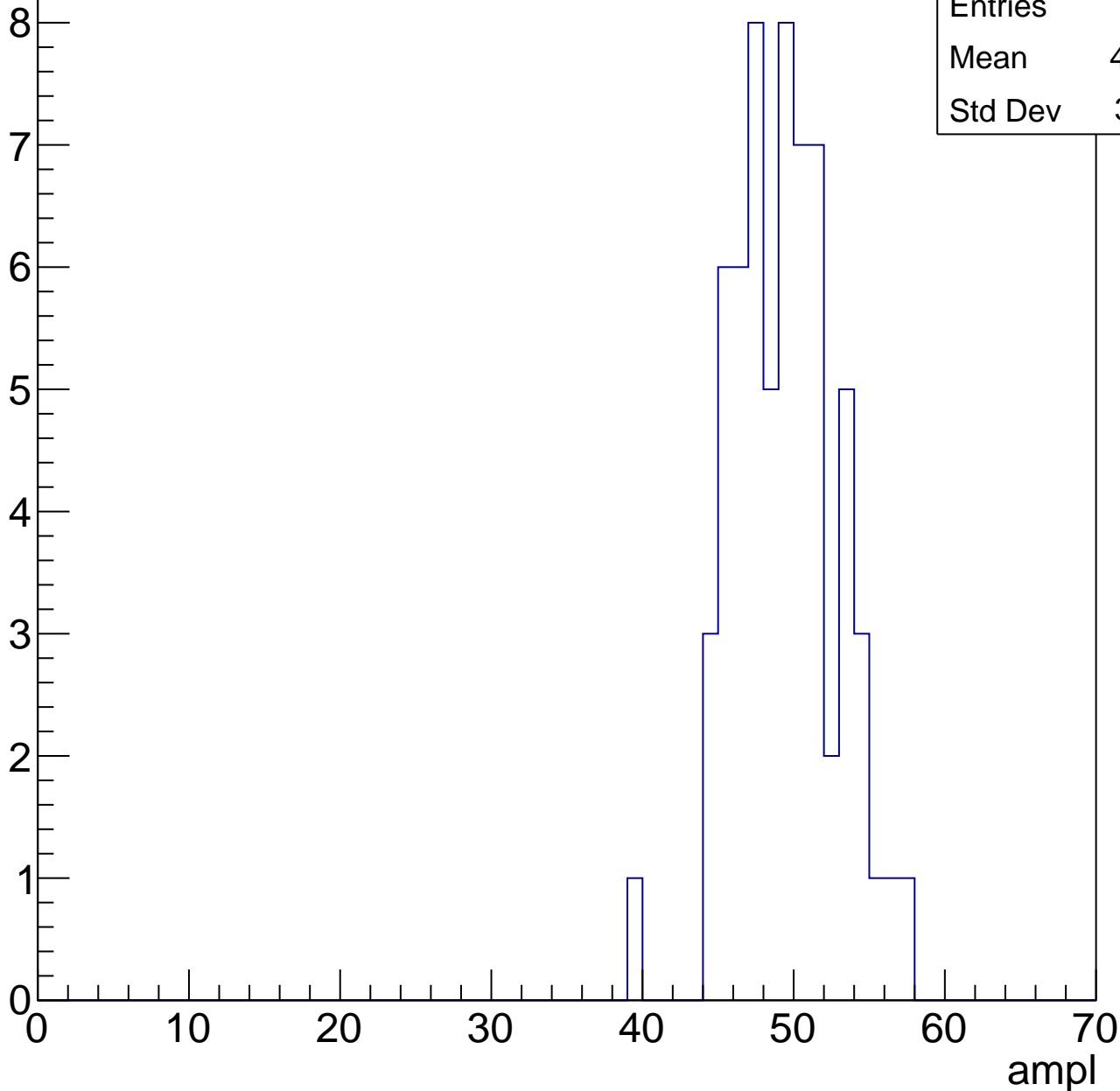


# B1L102S, U25-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.92
Std Dev	3.351

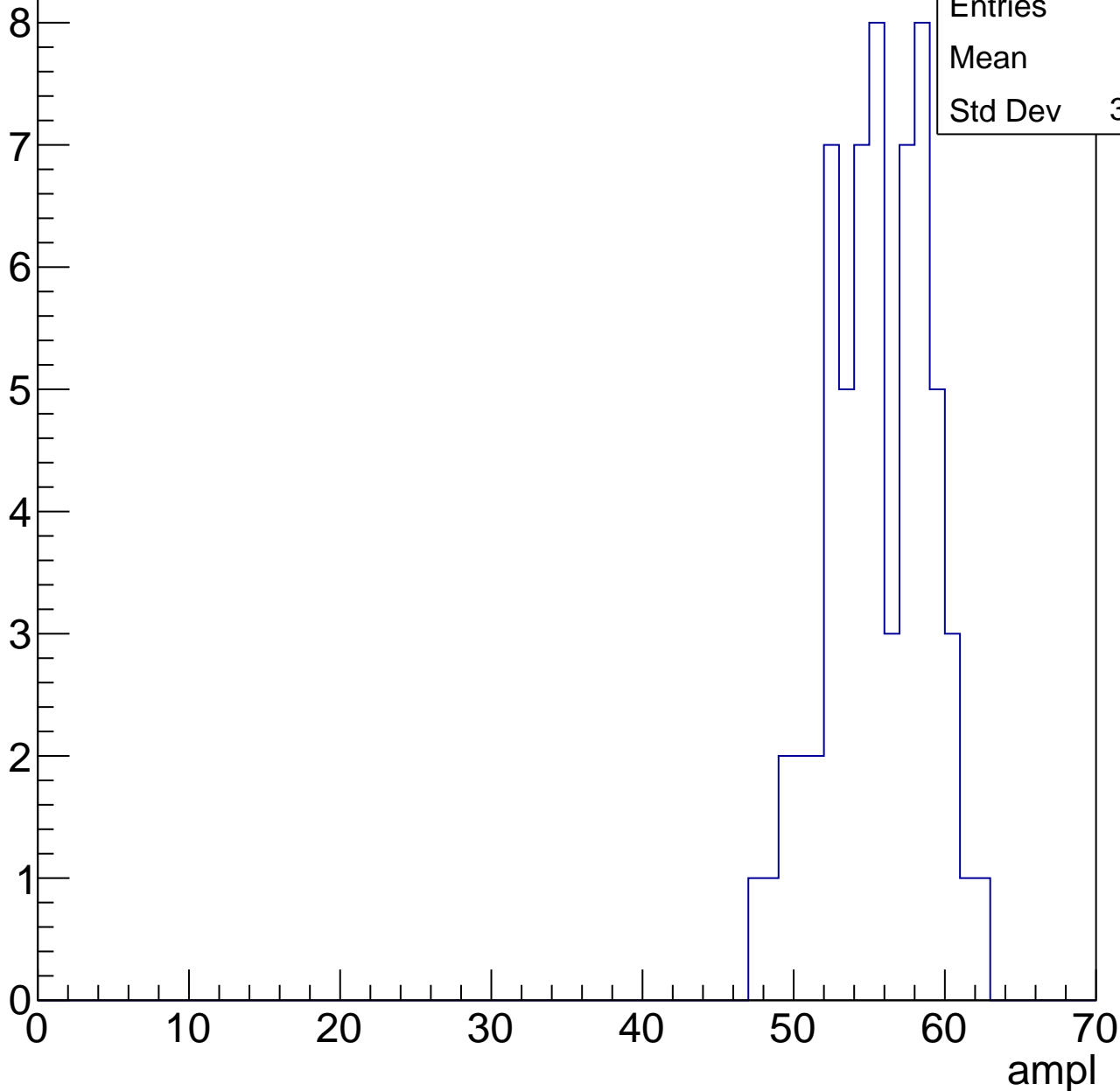


# B1L102S, U25-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.1
Std Dev	3.332

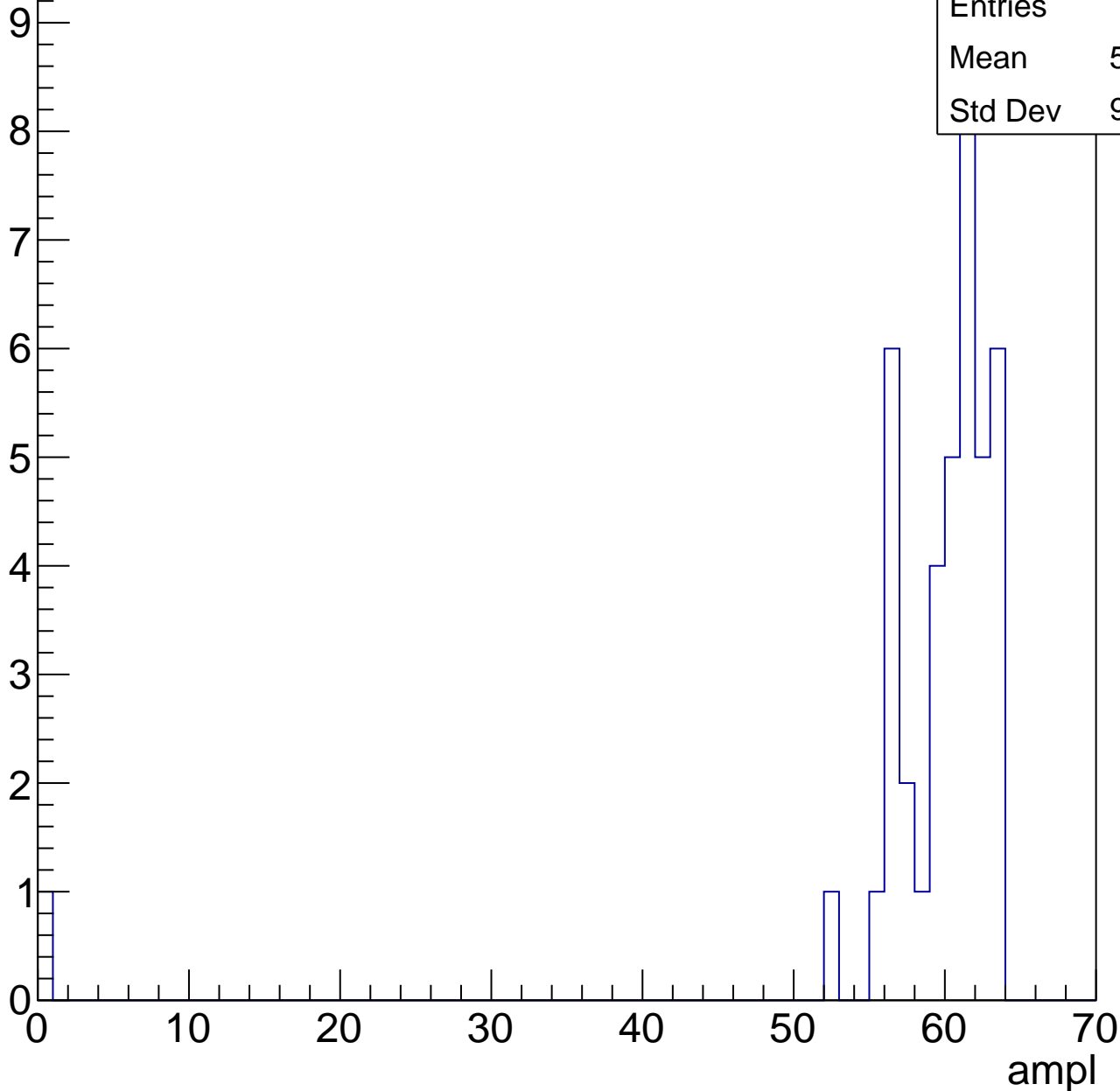


# B1L102S, U25-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

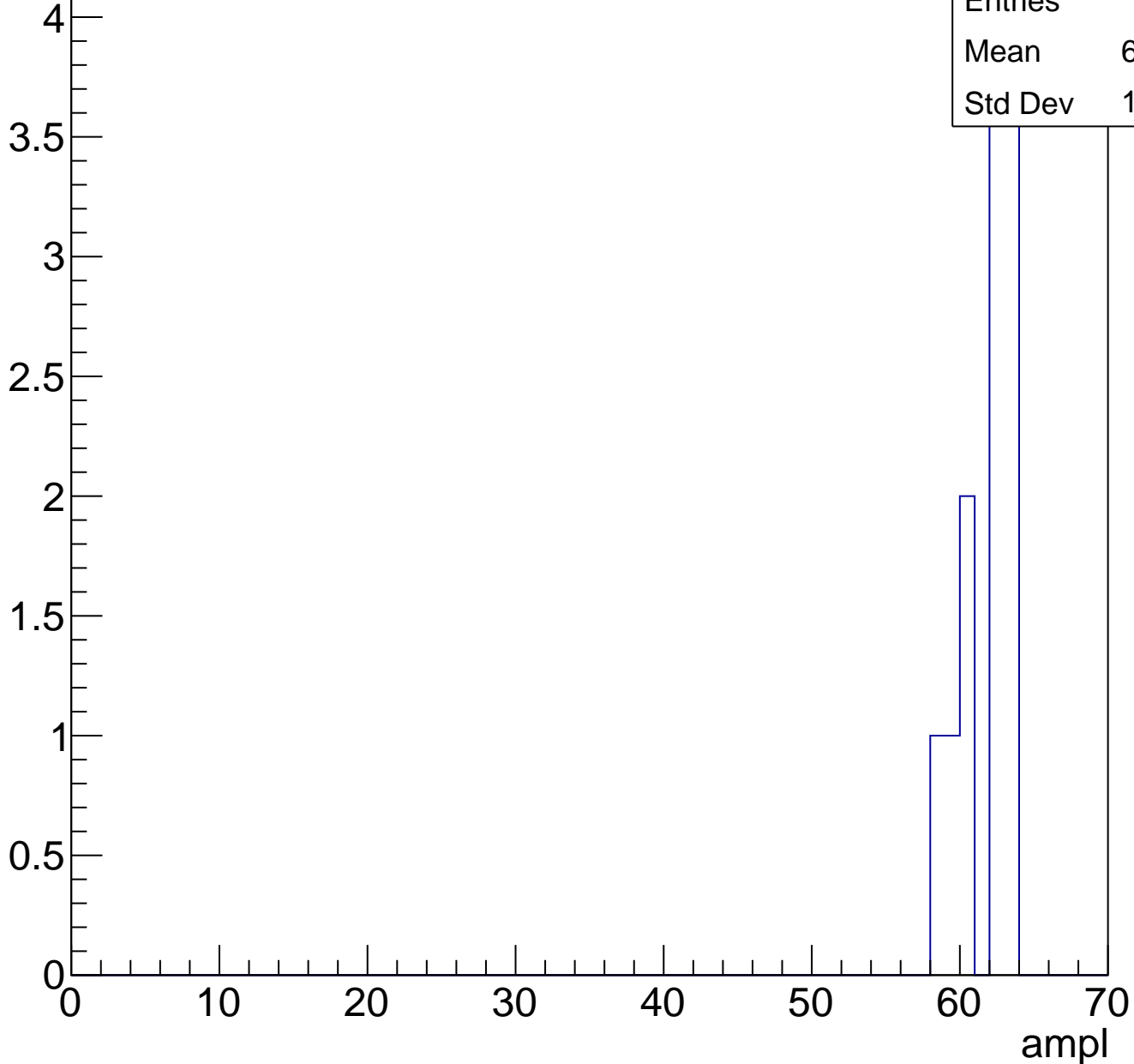
Entries	41
Mean	58.24
Std Dev	9.586



# B1L102S, U25-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U25-ch6, adc0

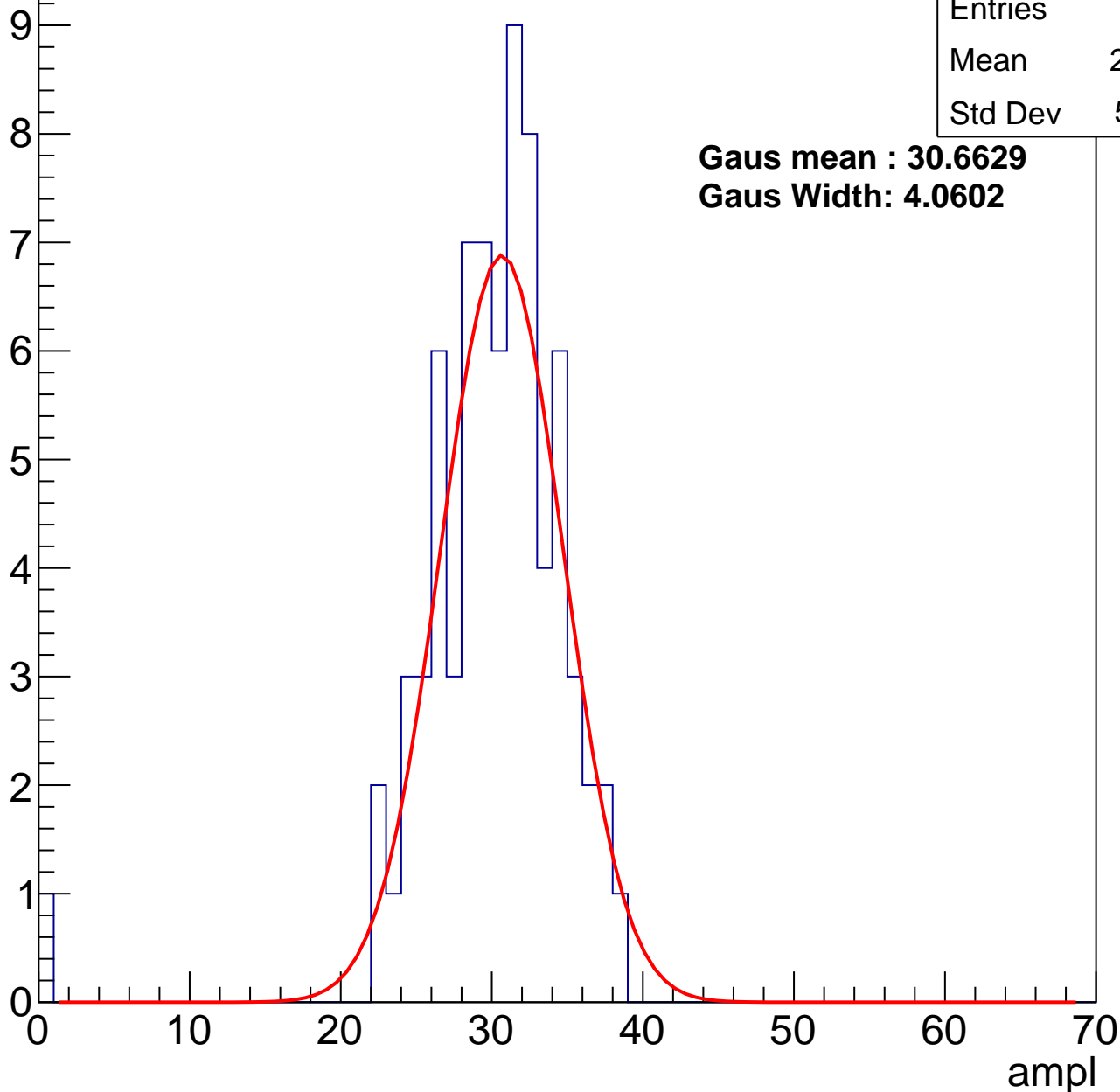
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.59
Std Dev	5.051

**Gaus mean : 30.6629**

**Gaus Width: 4.0602**



# B1L102S, U25-ch6, adc1

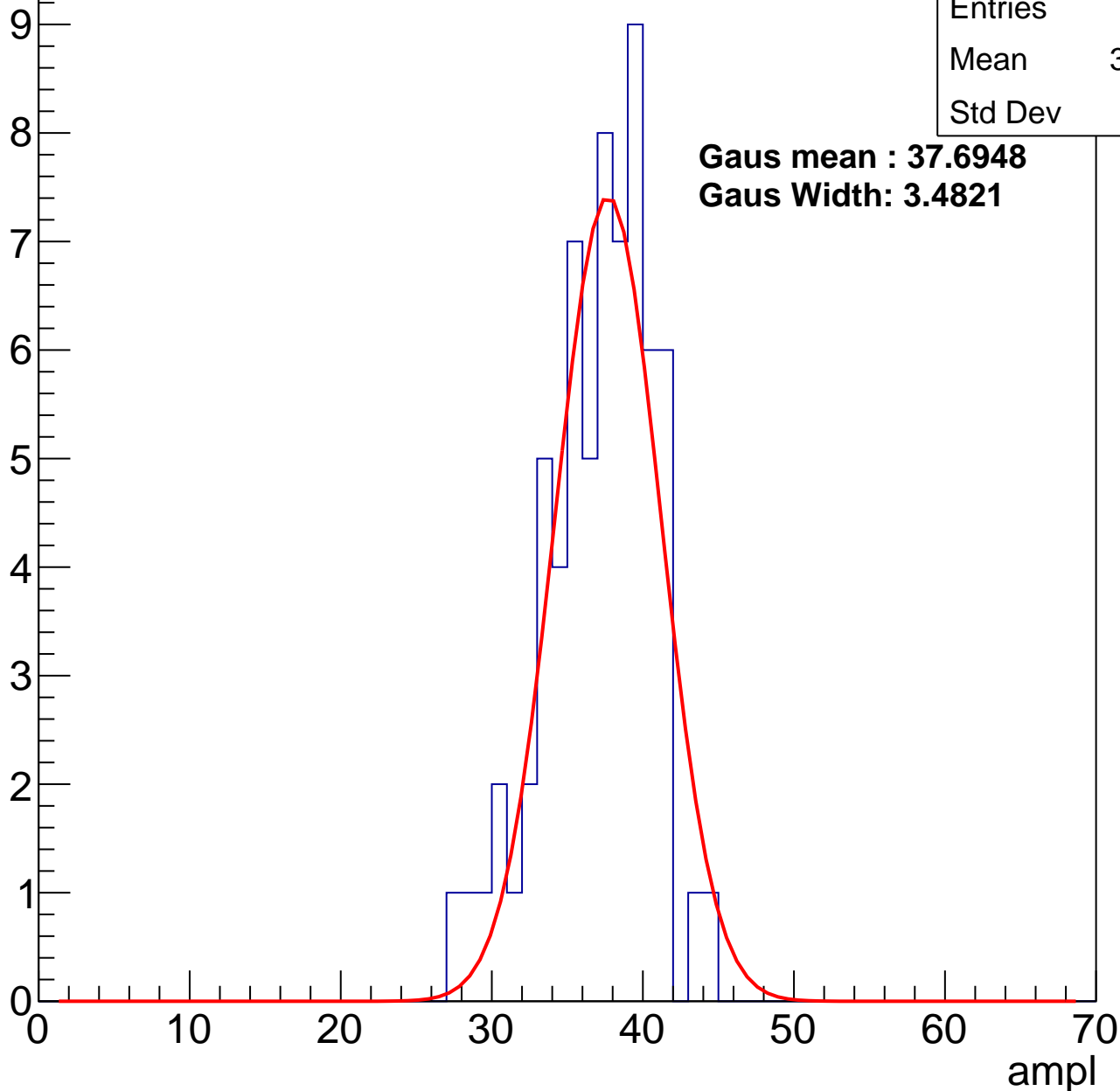
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.58
Std Dev	3.55

**Gaus mean : 37.6948**

**Gaus Width: 3.4821**



# B1L102S, U25-ch6, adc2

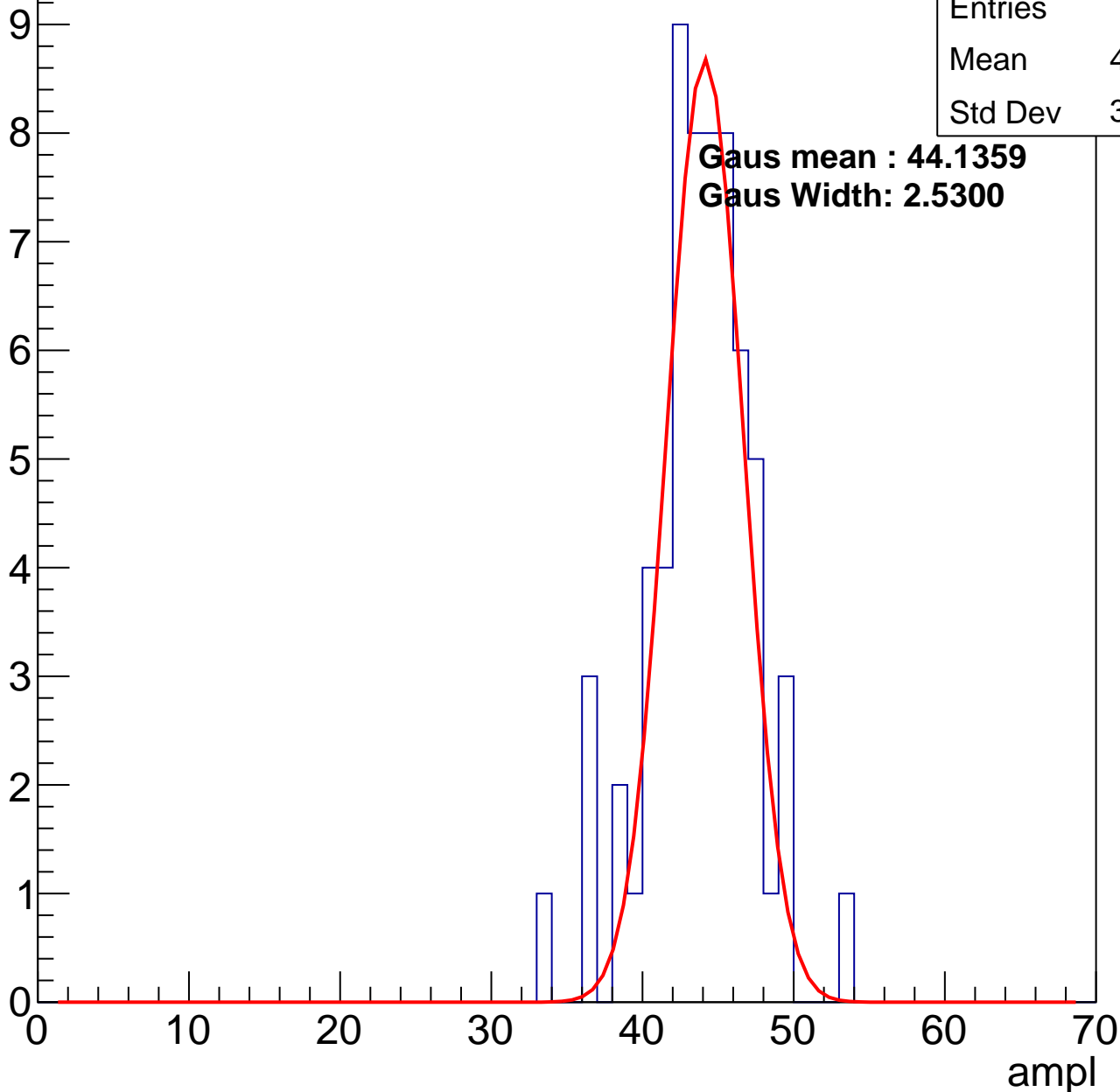
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.33
Std Dev	3.478

**Gaus mean : 44.1359**

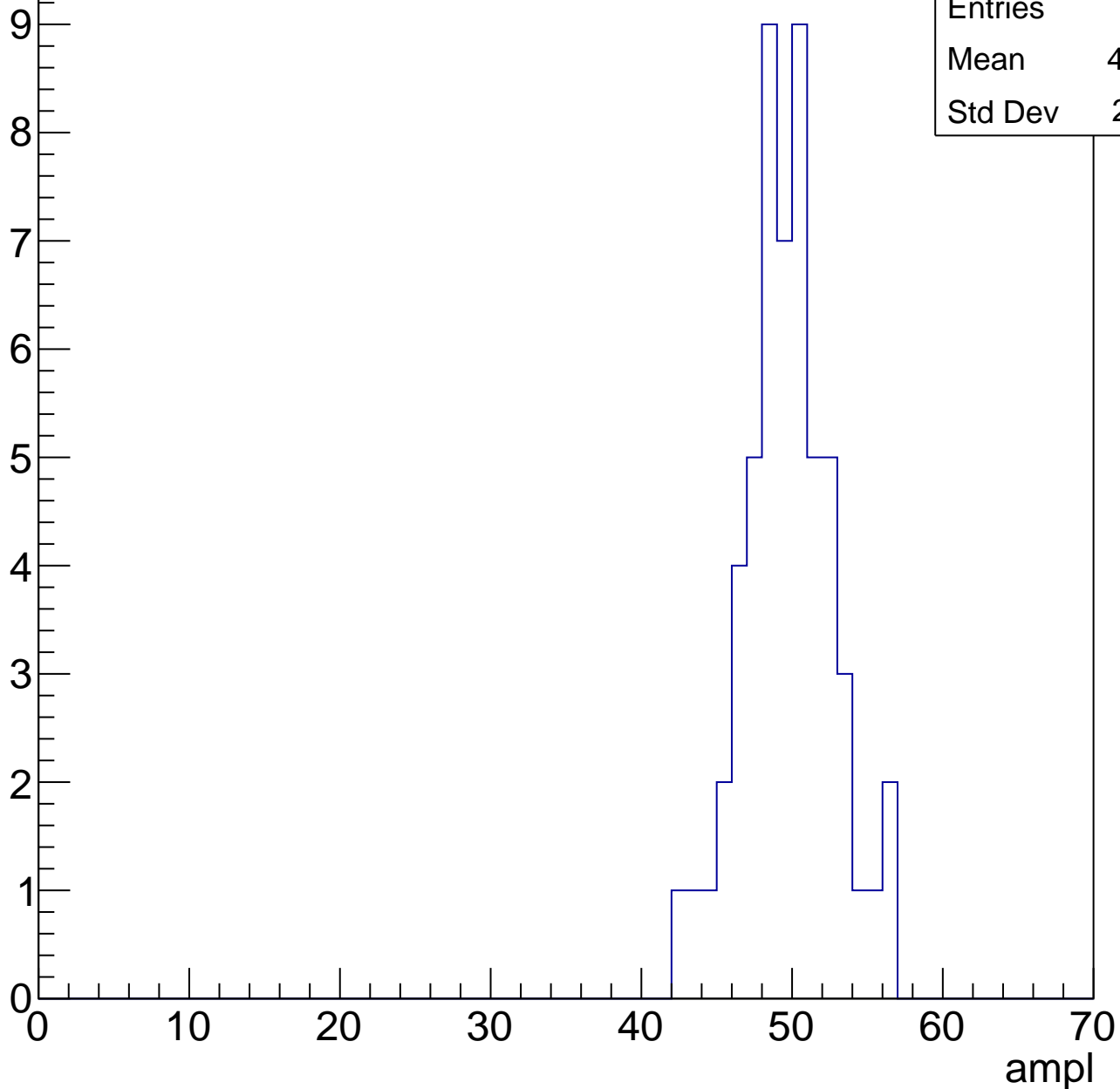
**Gaus Width: 2.5300**



# B1L102S, U25-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

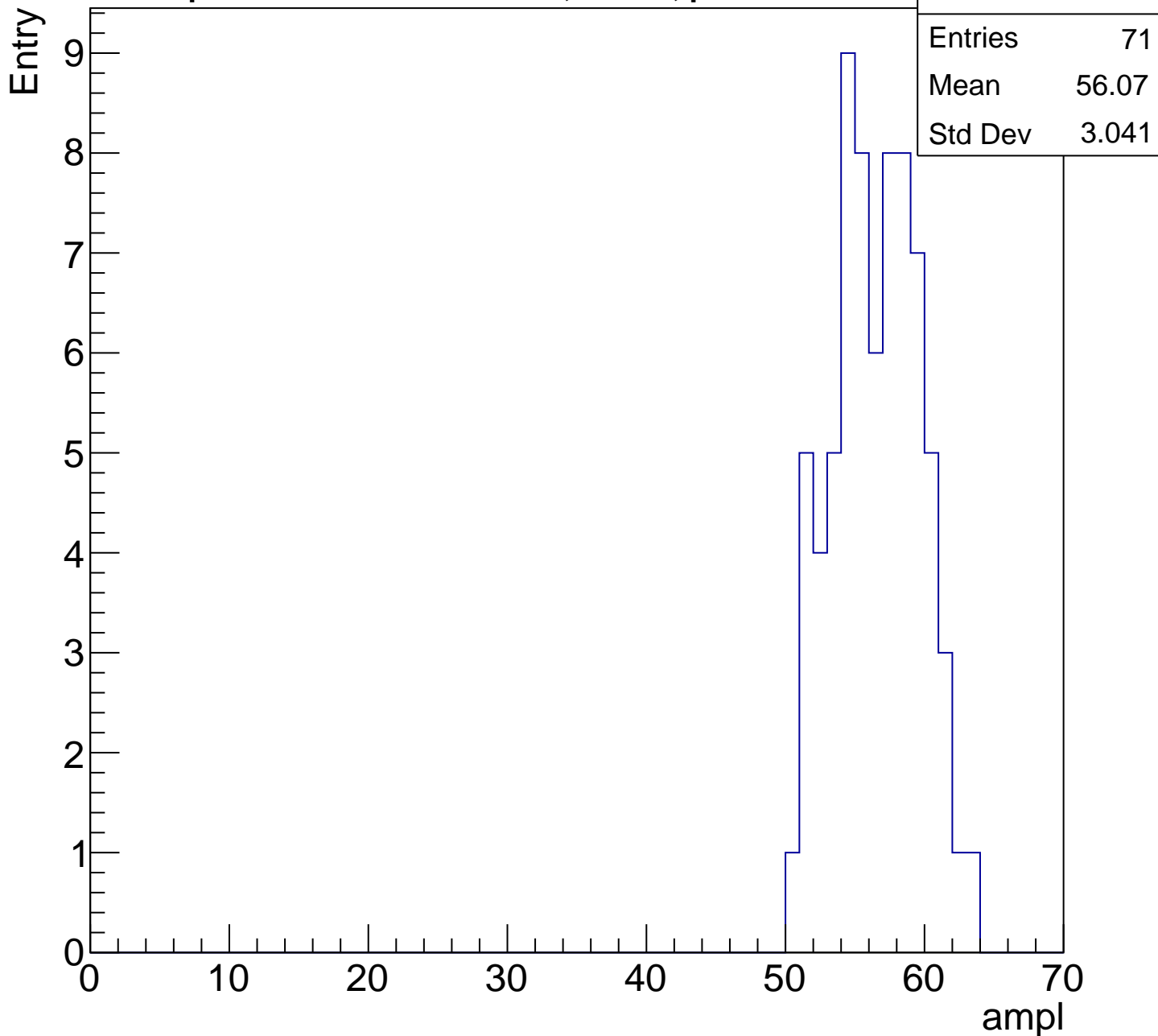
Entry



Entries	56
Mean	49.25
Std Dev	2.941

# B1L102S, U25-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

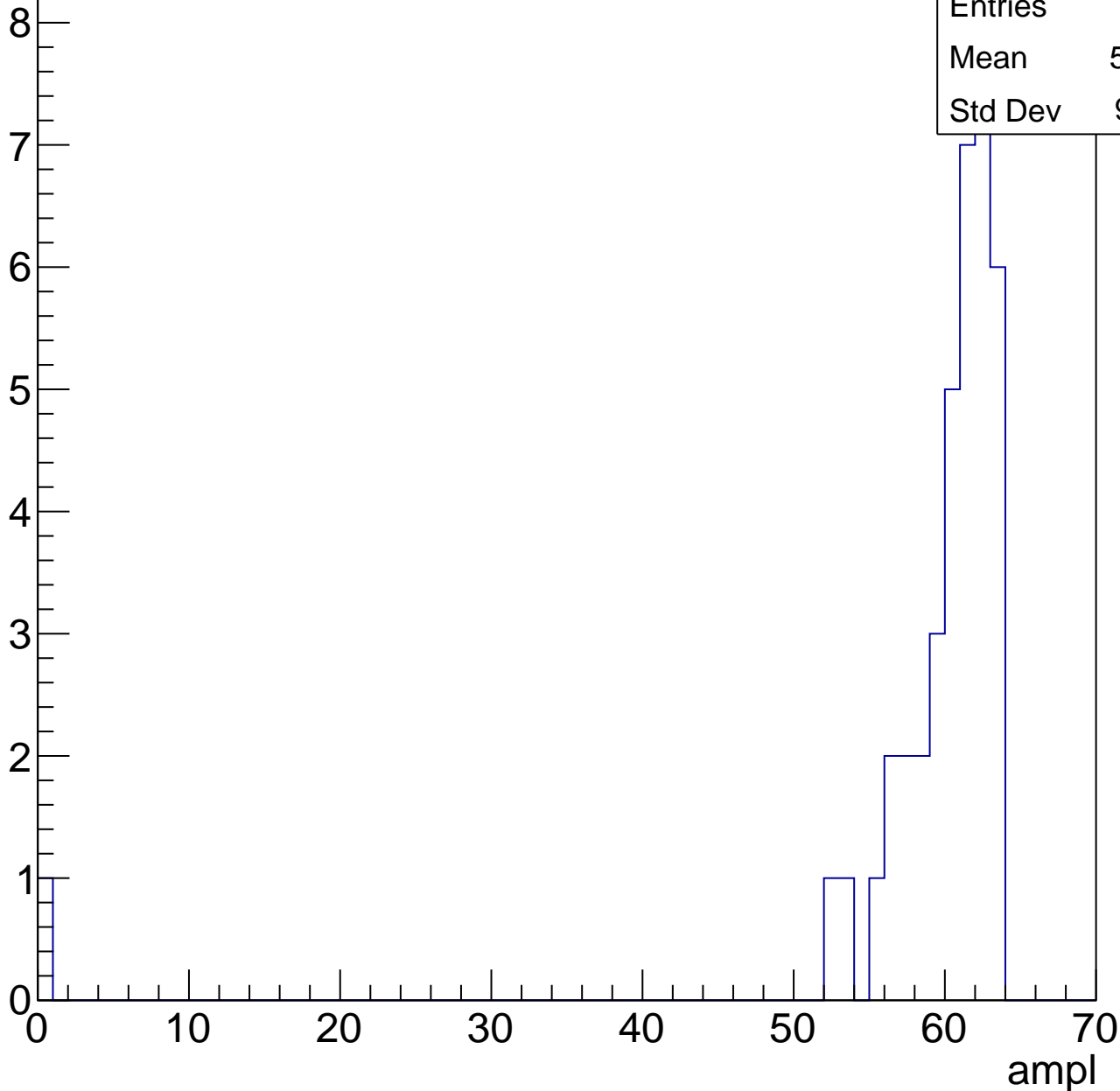


# B1L102S, U25-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

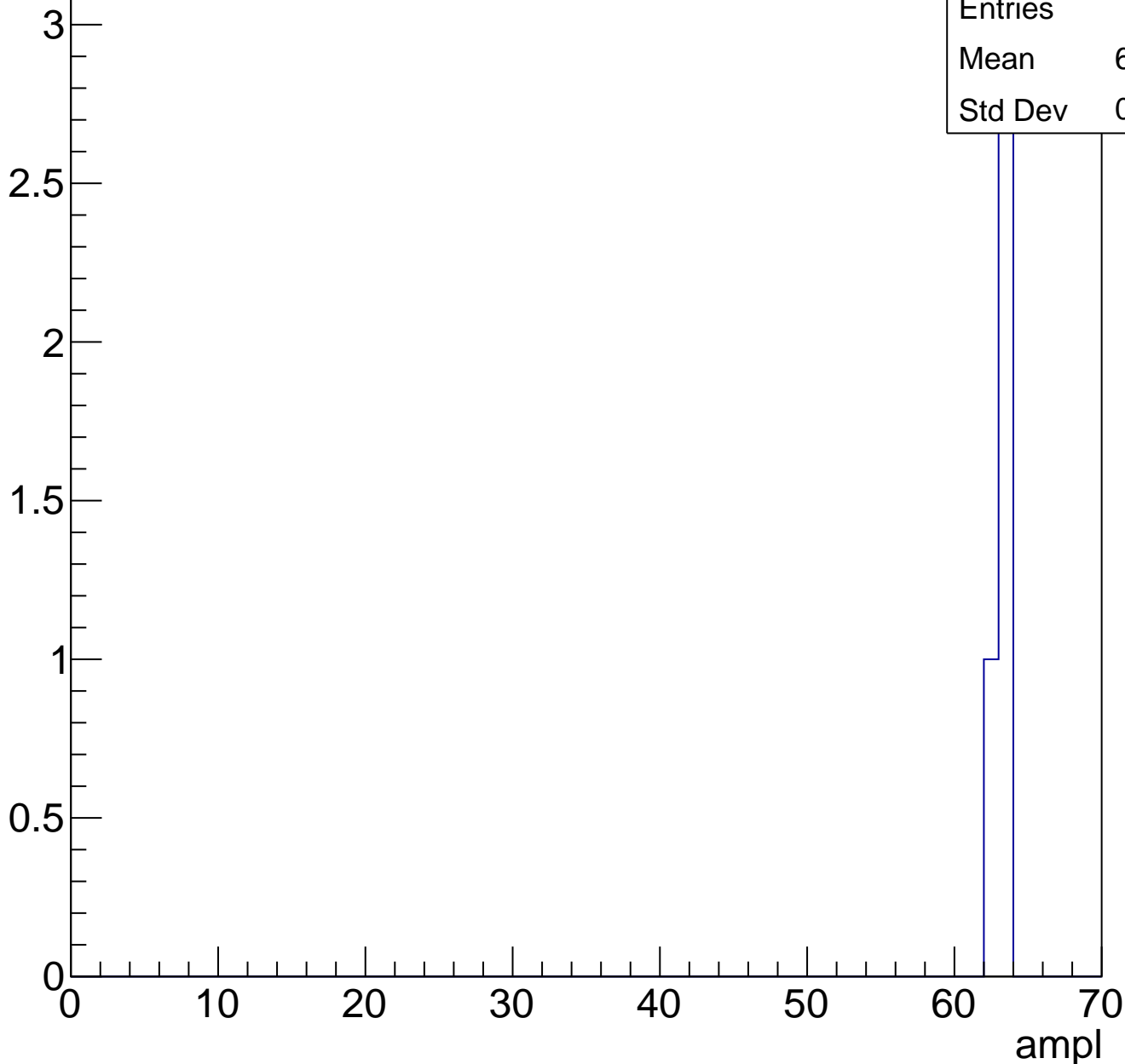
Entries	39
Mean	58.46
Std Dev	9.871



# B1L102S, U25-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch7, adc0

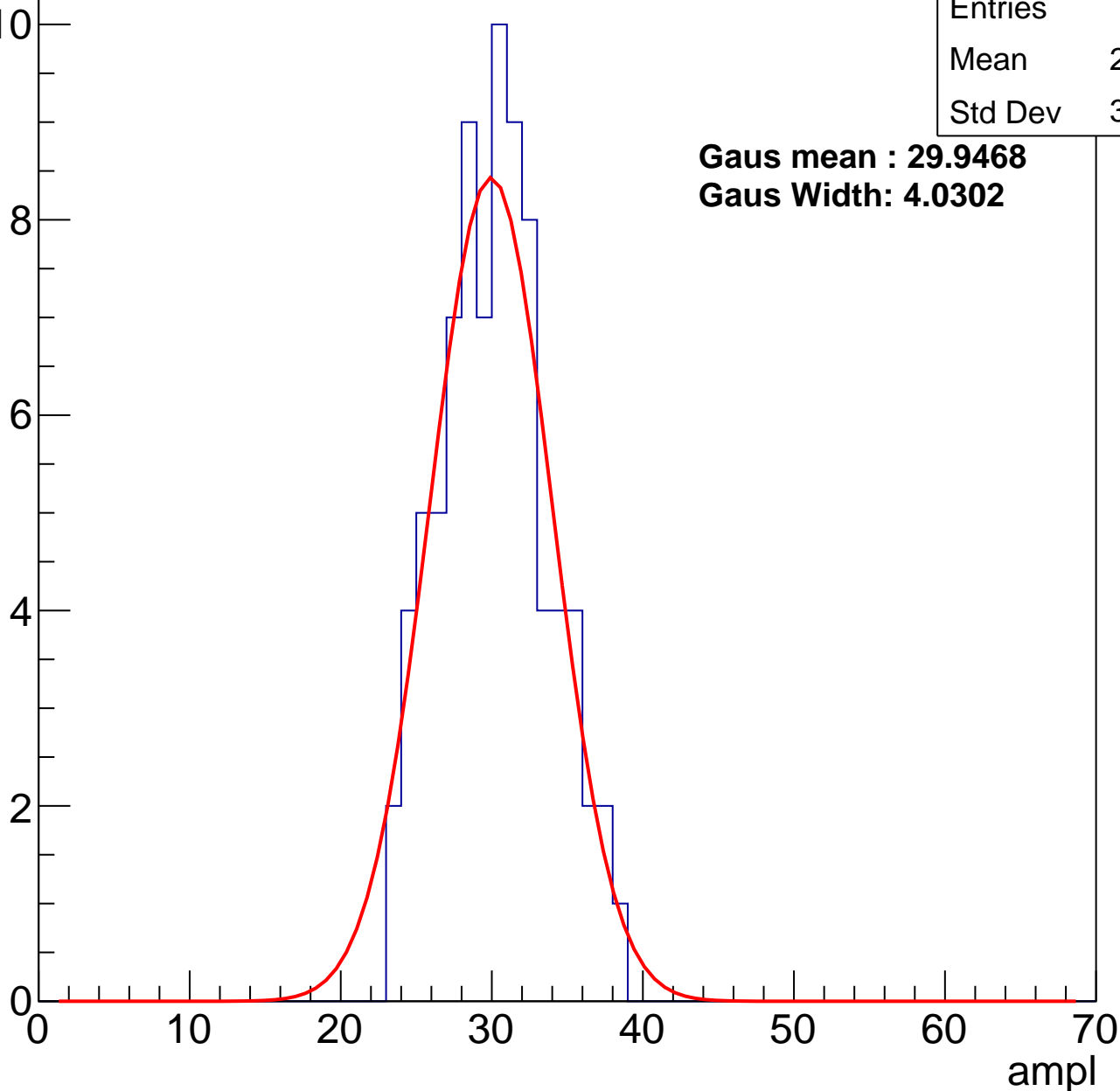
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.73
Std Dev	3.509

**Gaus mean : 29.9468**

**Gaus Width: 4.0302**



# B1L102S, U25-ch7, adc1

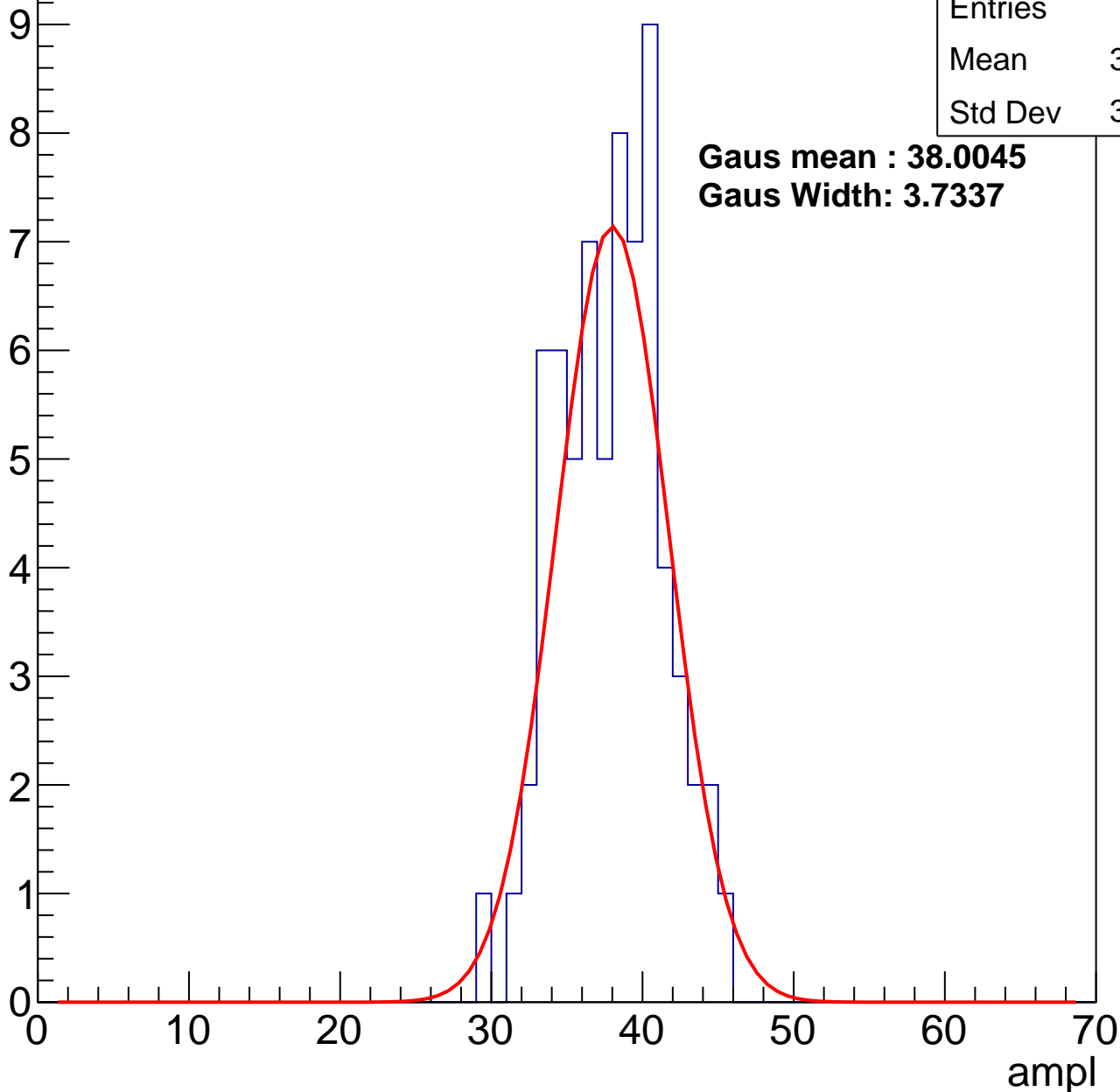
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.45
Std Dev	3.412

**Gaus mean : 38.0045**

**Gaus Width: 3.7337**



# B1L102S, U25-ch7, adc2

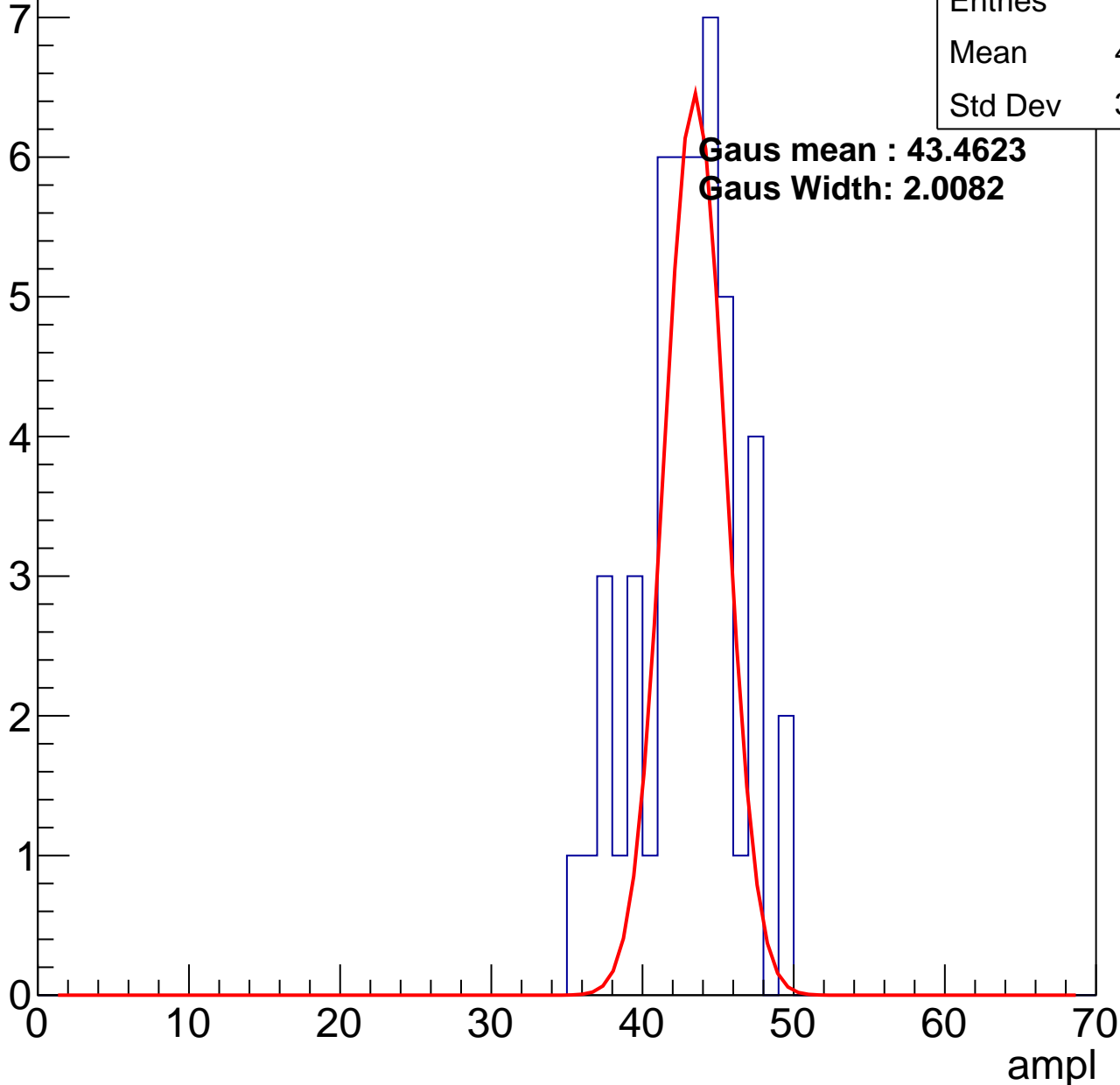
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	42.51
Std Dev	3.241

**Gaus mean : 43.4623**

**Gaus Width: 2.0082**



# B1L102S, U25-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

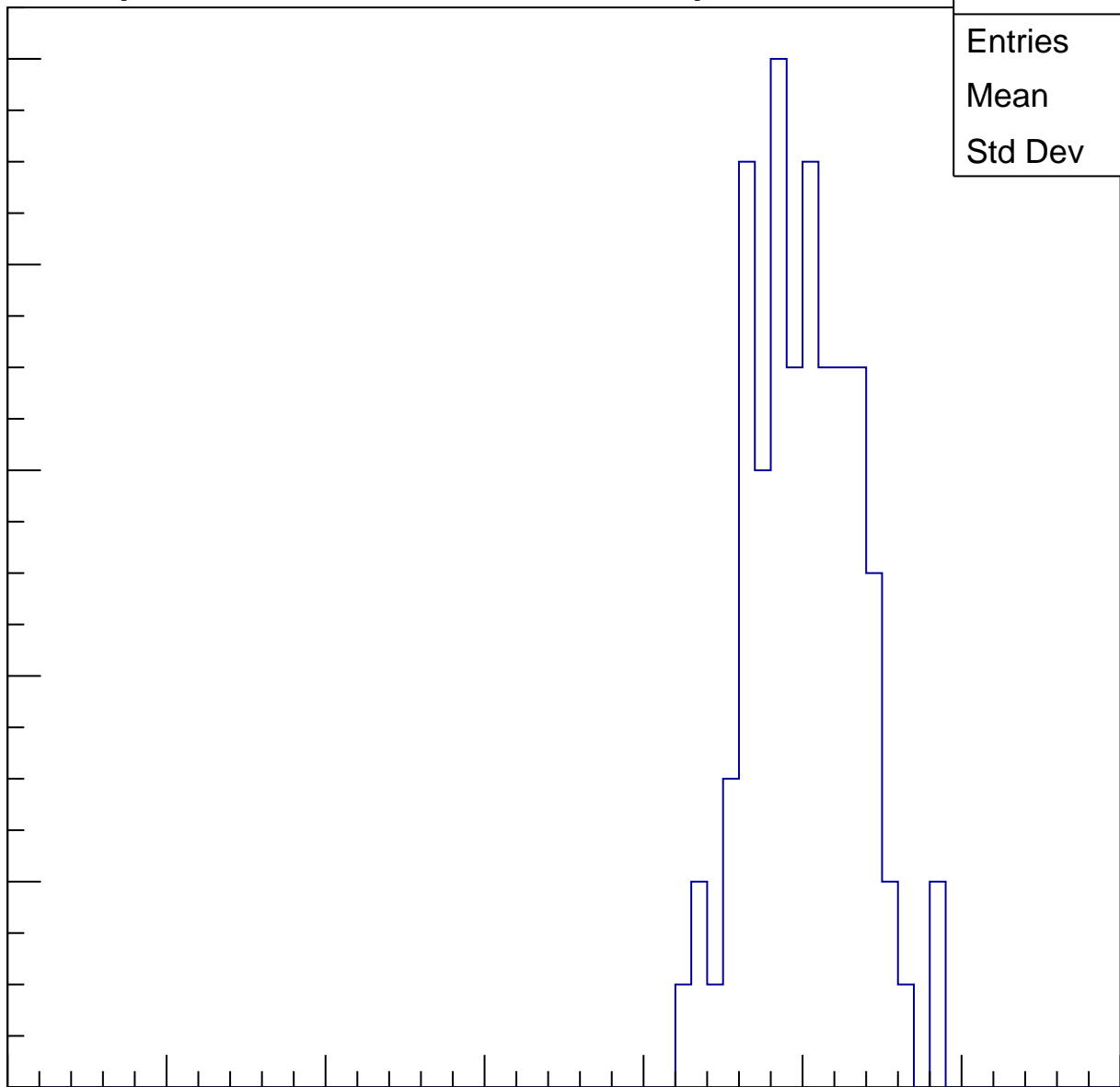
Entries	79
Mean	49.62
Std Dev	3.373

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

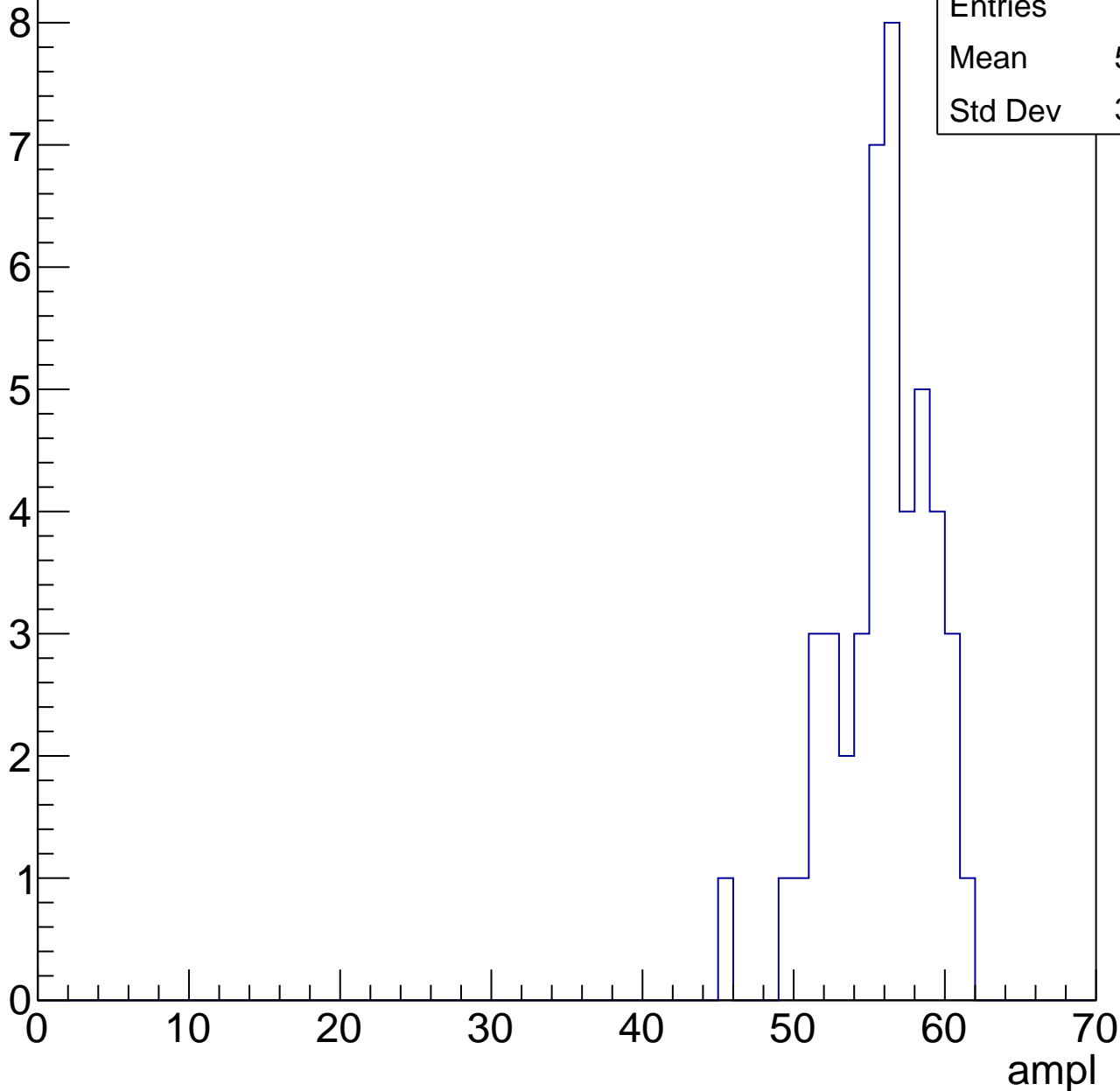


# B1L102S, U25-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.41
Std Dev	3.241

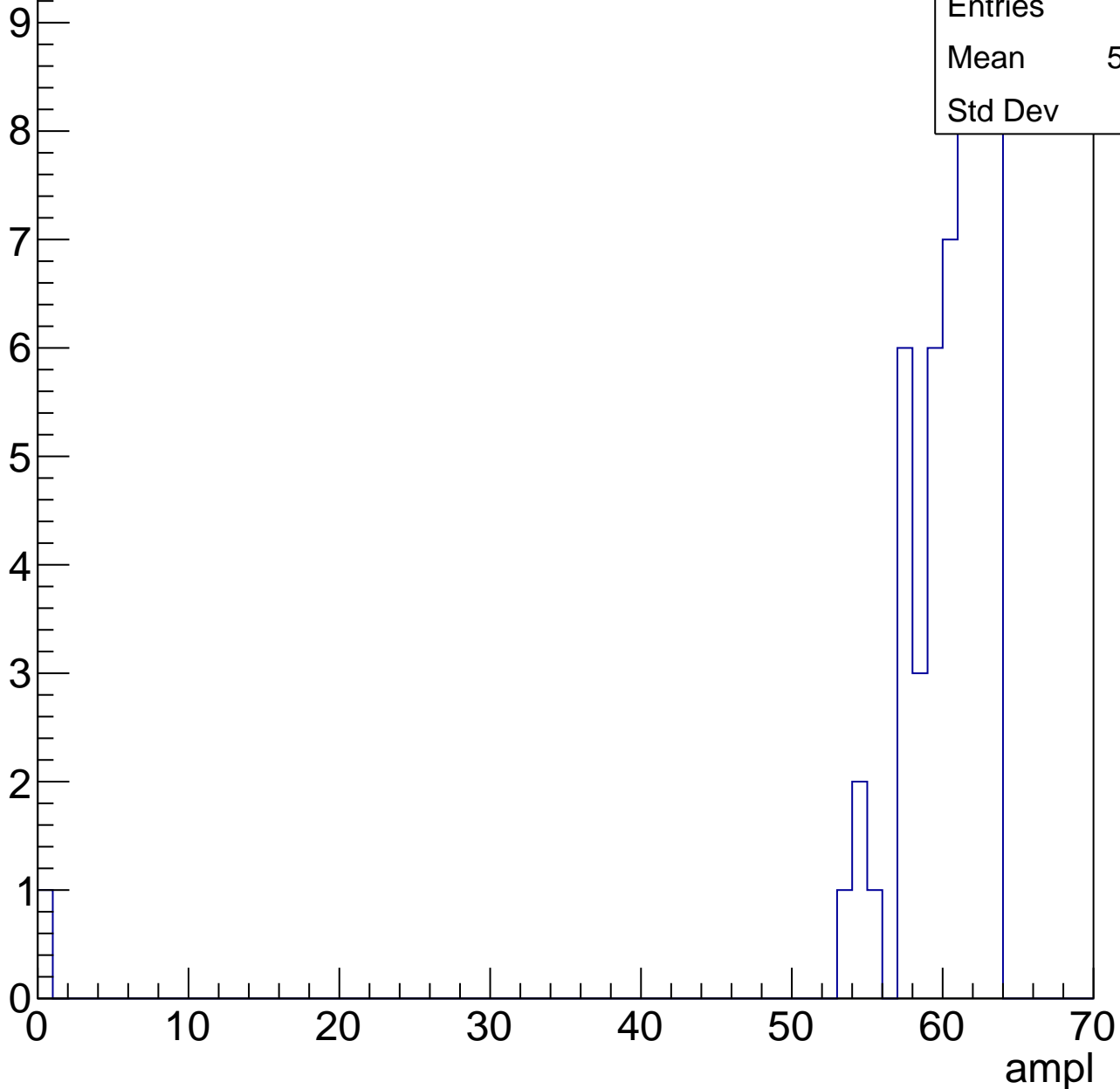


# B1L102S, U25-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.77
Std Dev	8.61



# B1L102S, U25-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

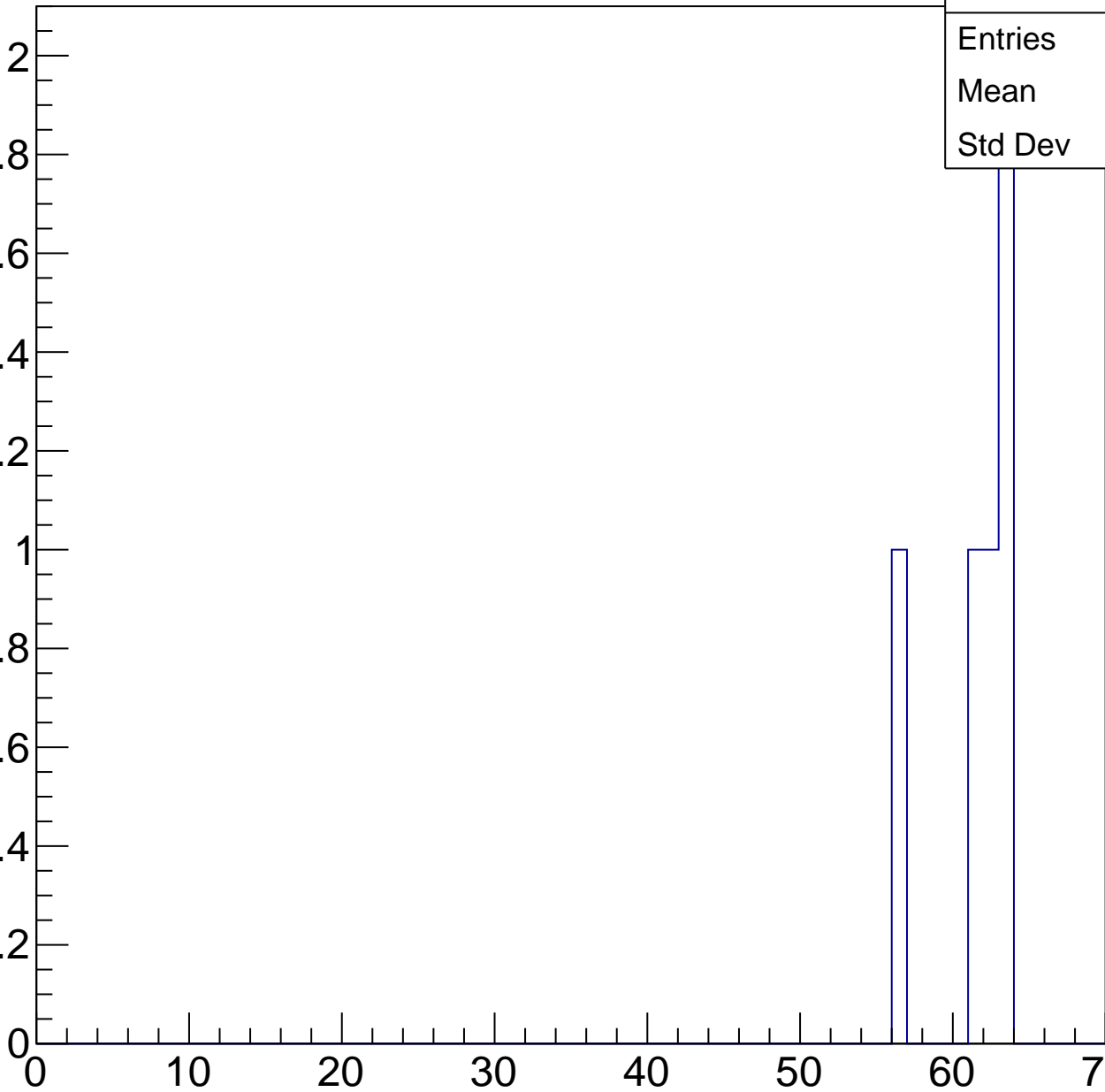
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61
Std Dev	2.608

0 10 20 30 40 50 60 70

ampl





# B1L102S, U25-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



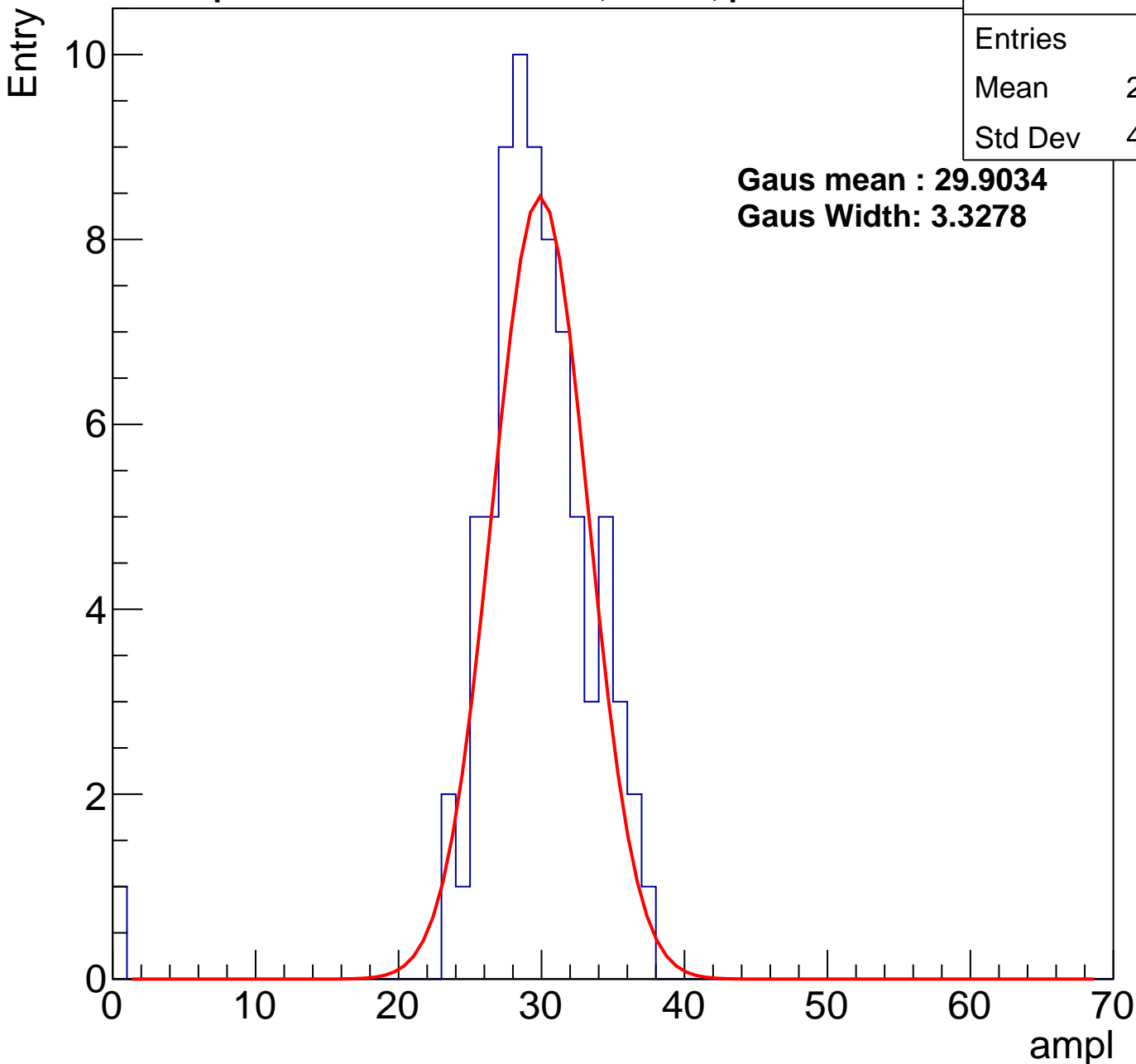
Entries	2
Mean	11
Std Dev	11

# B1L102S, U25-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	76
Mean	29.07
Std Dev	4.635

**Gaus mean : 29.9034**  
**Gaus Width: 3.3278**



# B1L102S, U25-ch8, adc1

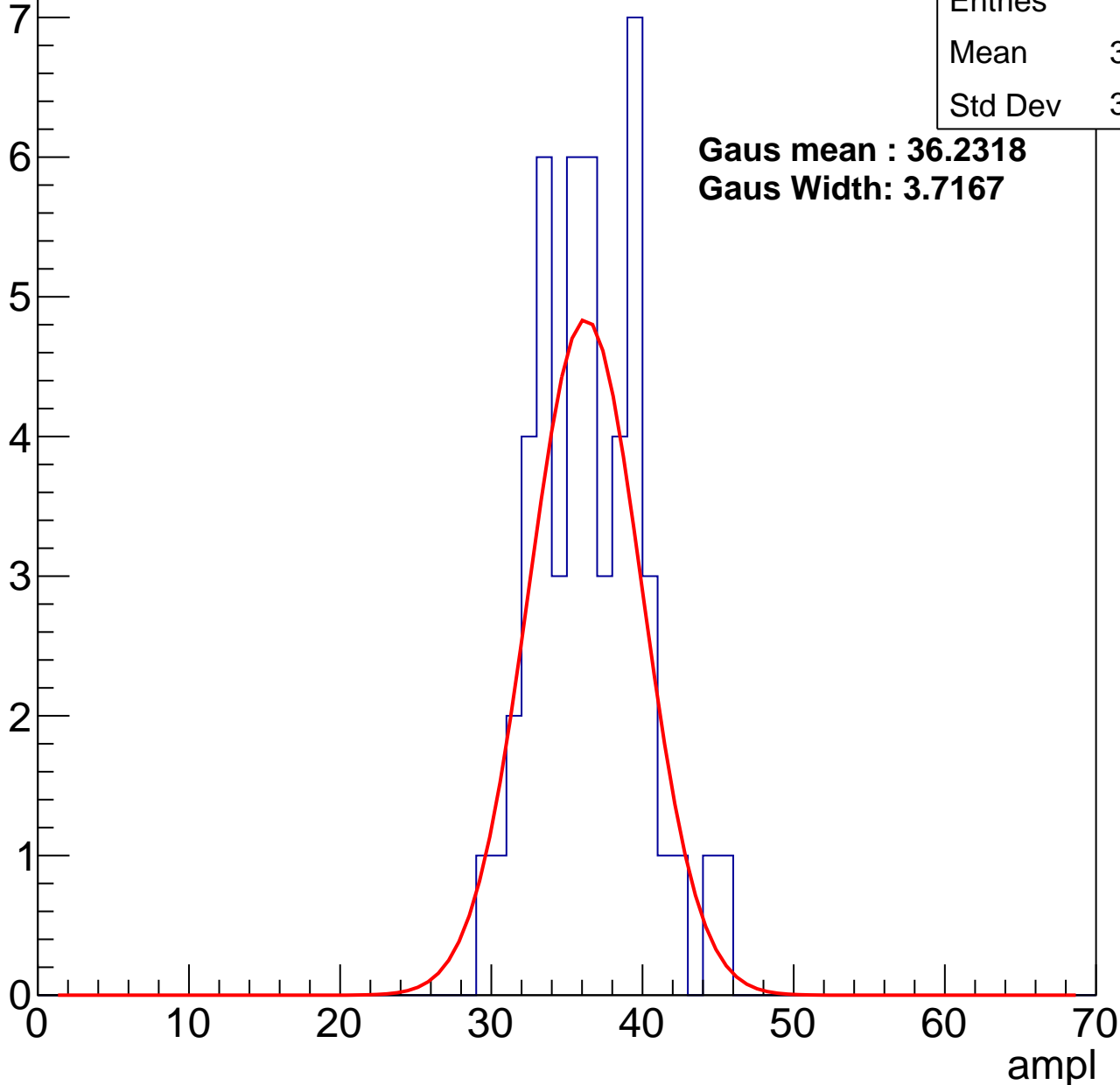
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	36.06
Std Dev	3.484

**Gaus mean : 36.2318**

**Gaus Width: 3.7167**



# B1L102S, U25-ch8, adc2

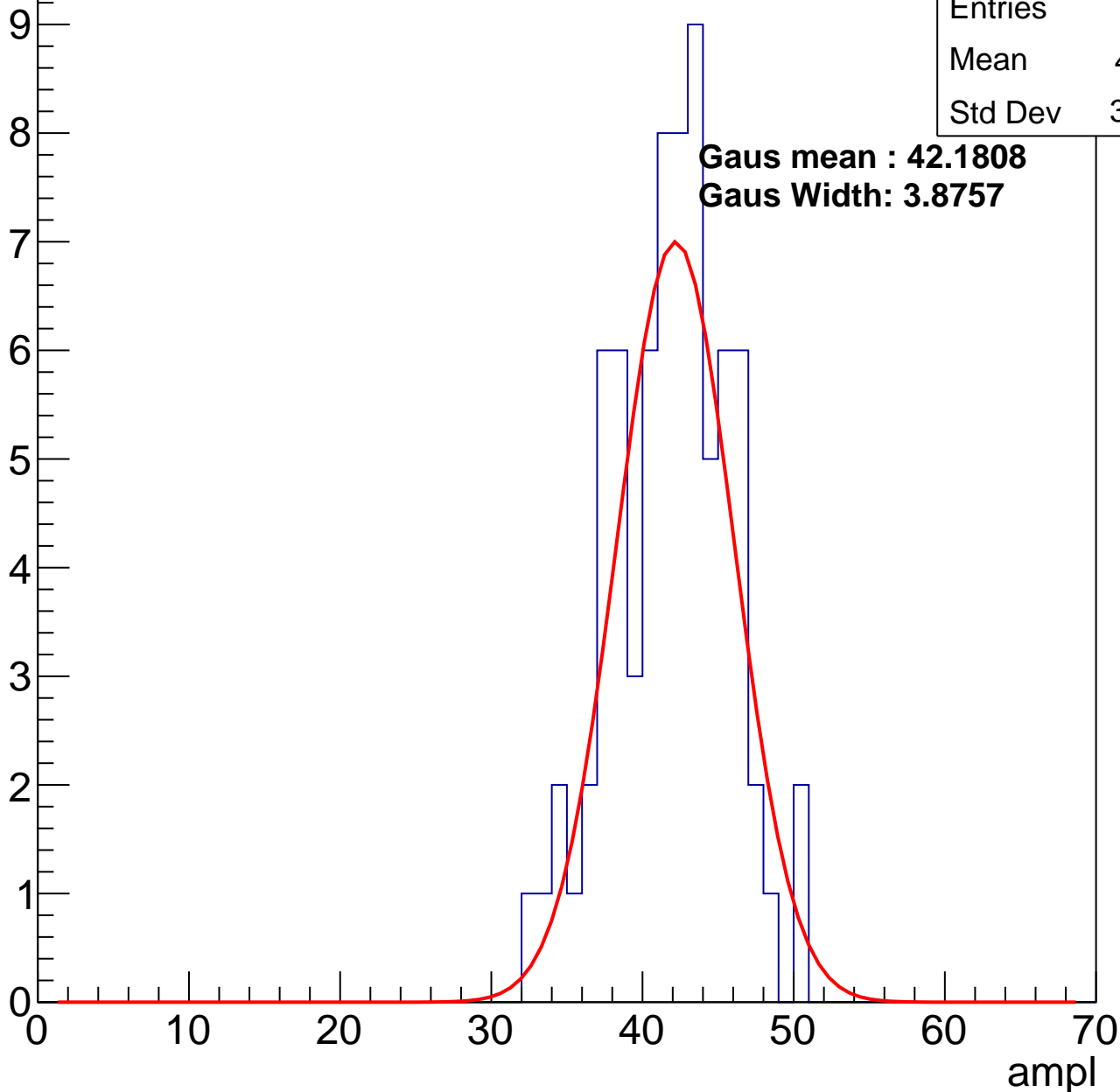
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.41
Std Dev	3.834

**Gaus mean : 42.1808**

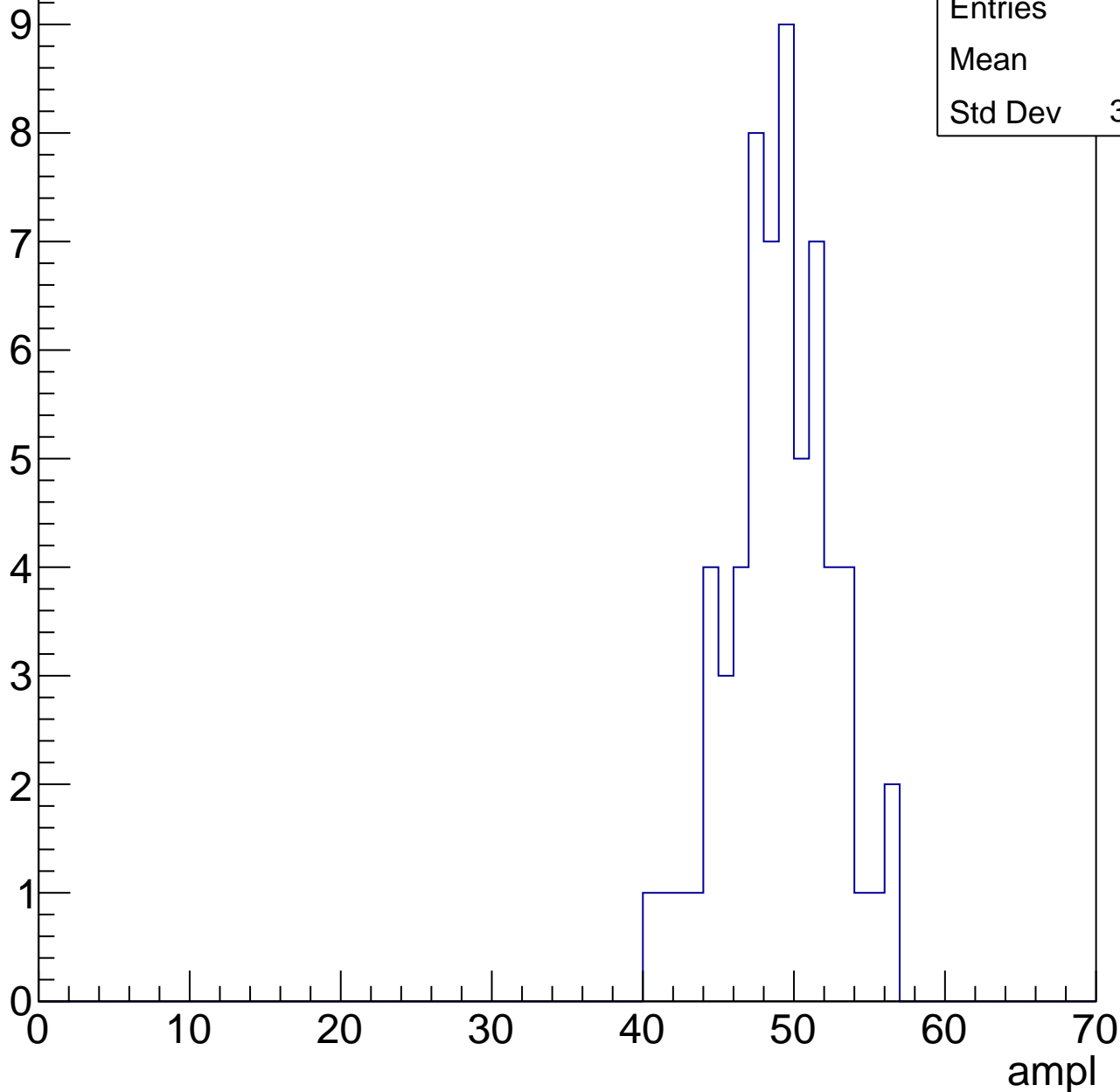
**Gaus Width: 3.8757**



# B1L102S, U25-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

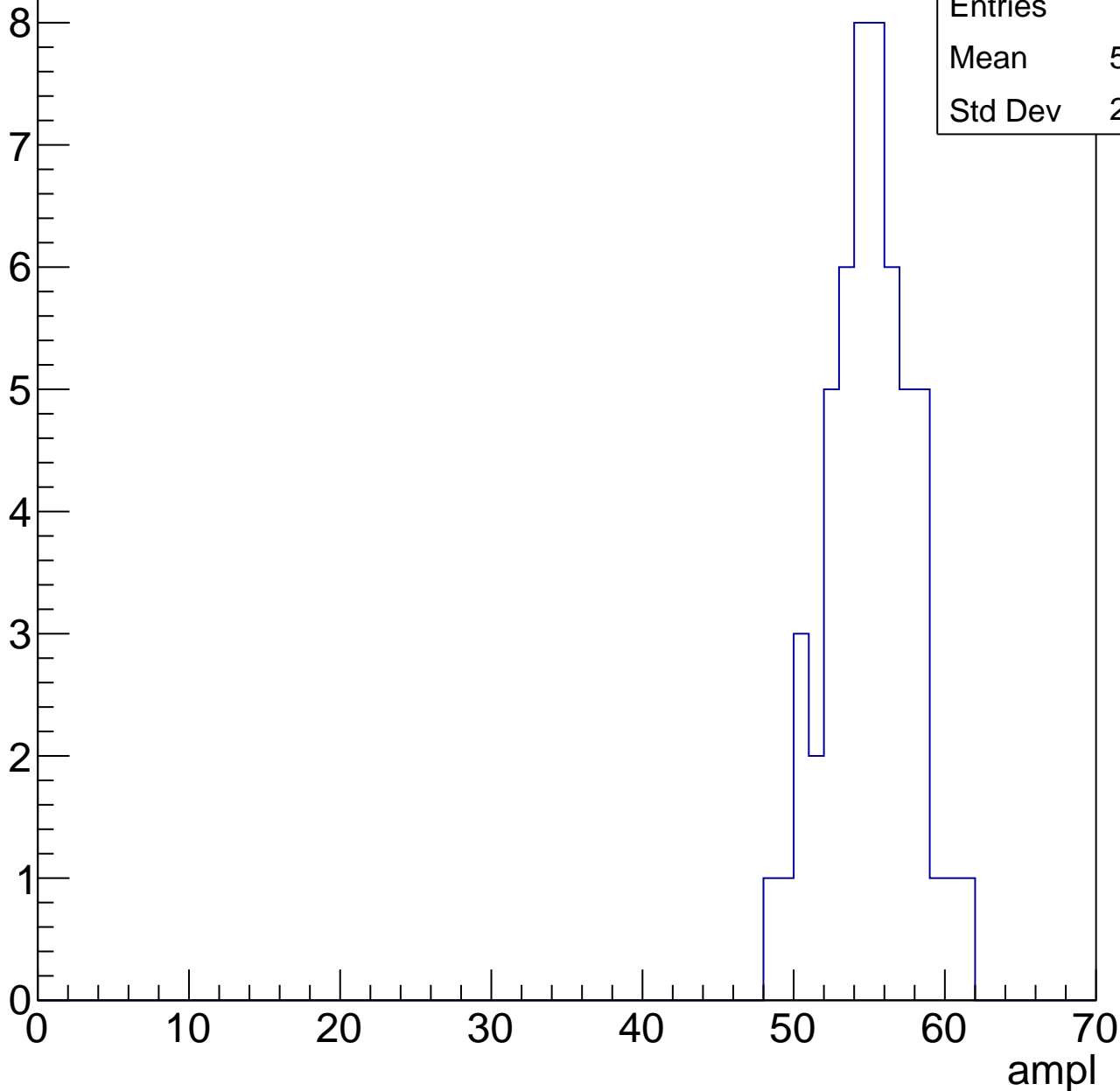


# B1L102S, U25-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.53
Std Dev	2.765

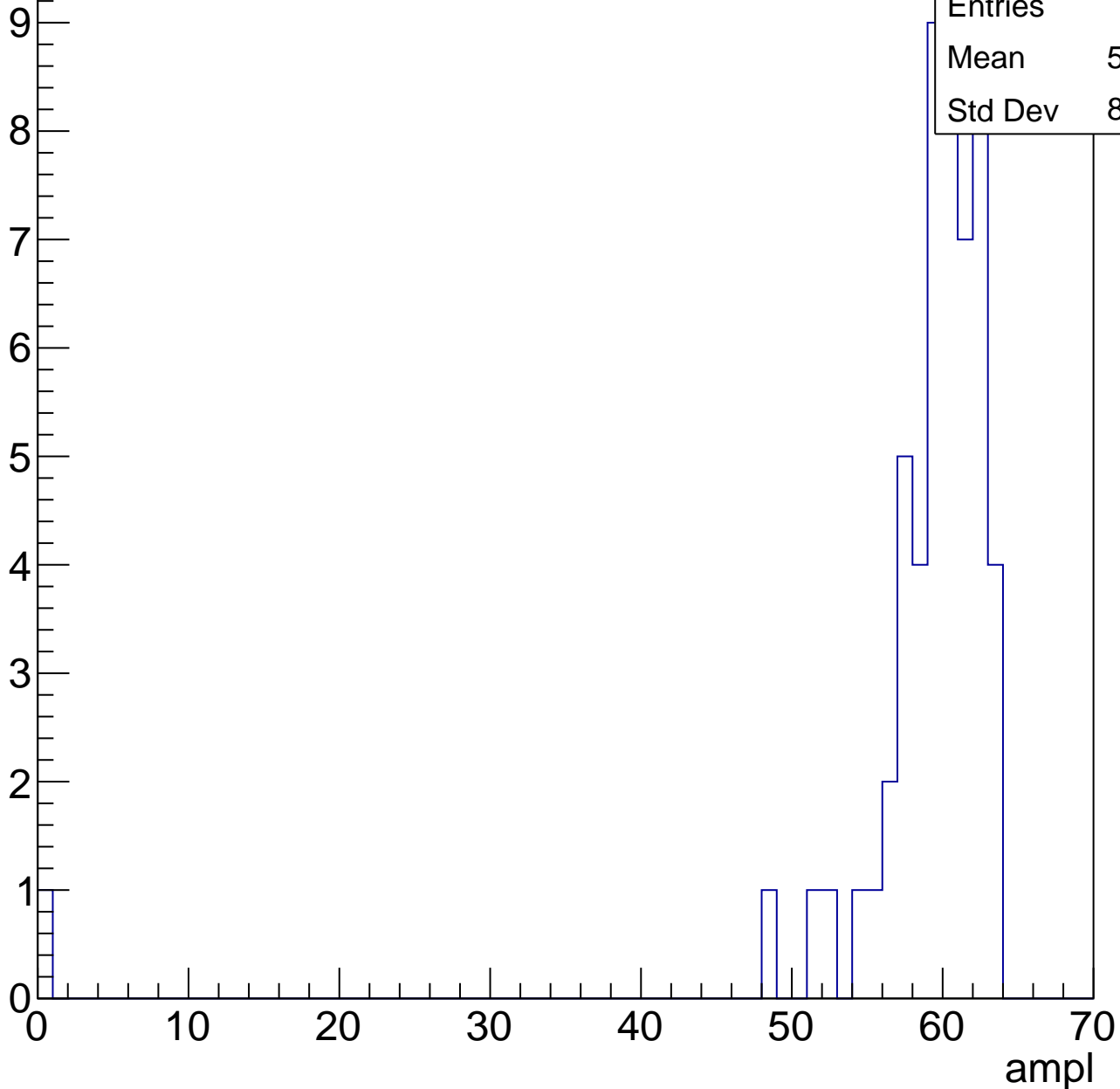


# B1L102S, U25-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.13
Std Dev	8.462

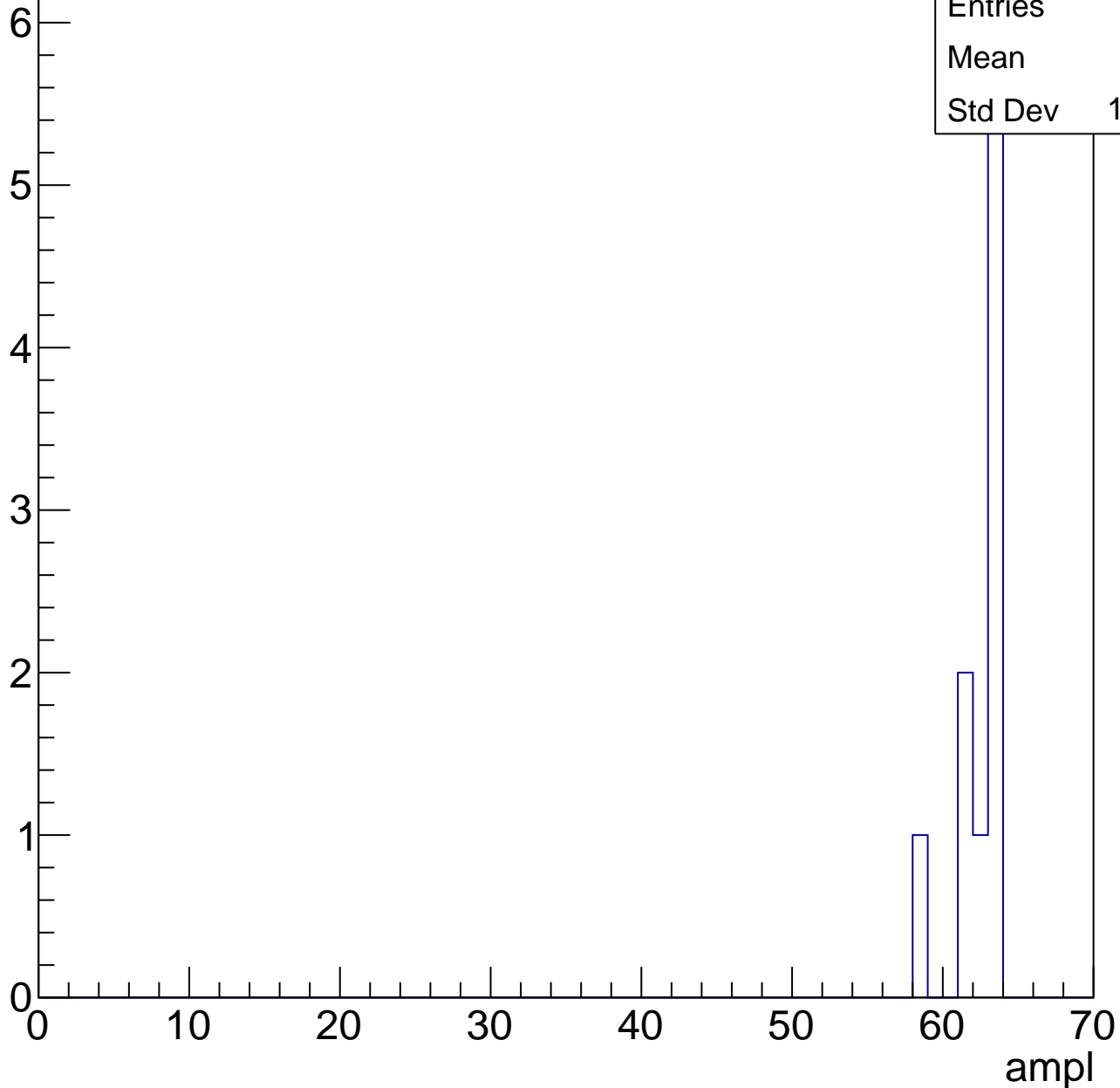


# B1L102S, U25-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62
Std Dev	1.549





# B1L102S, U25-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L102S, U25-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	72
Mean	30.61
Std Dev	3.319

**Gaus mean : 31.2937**

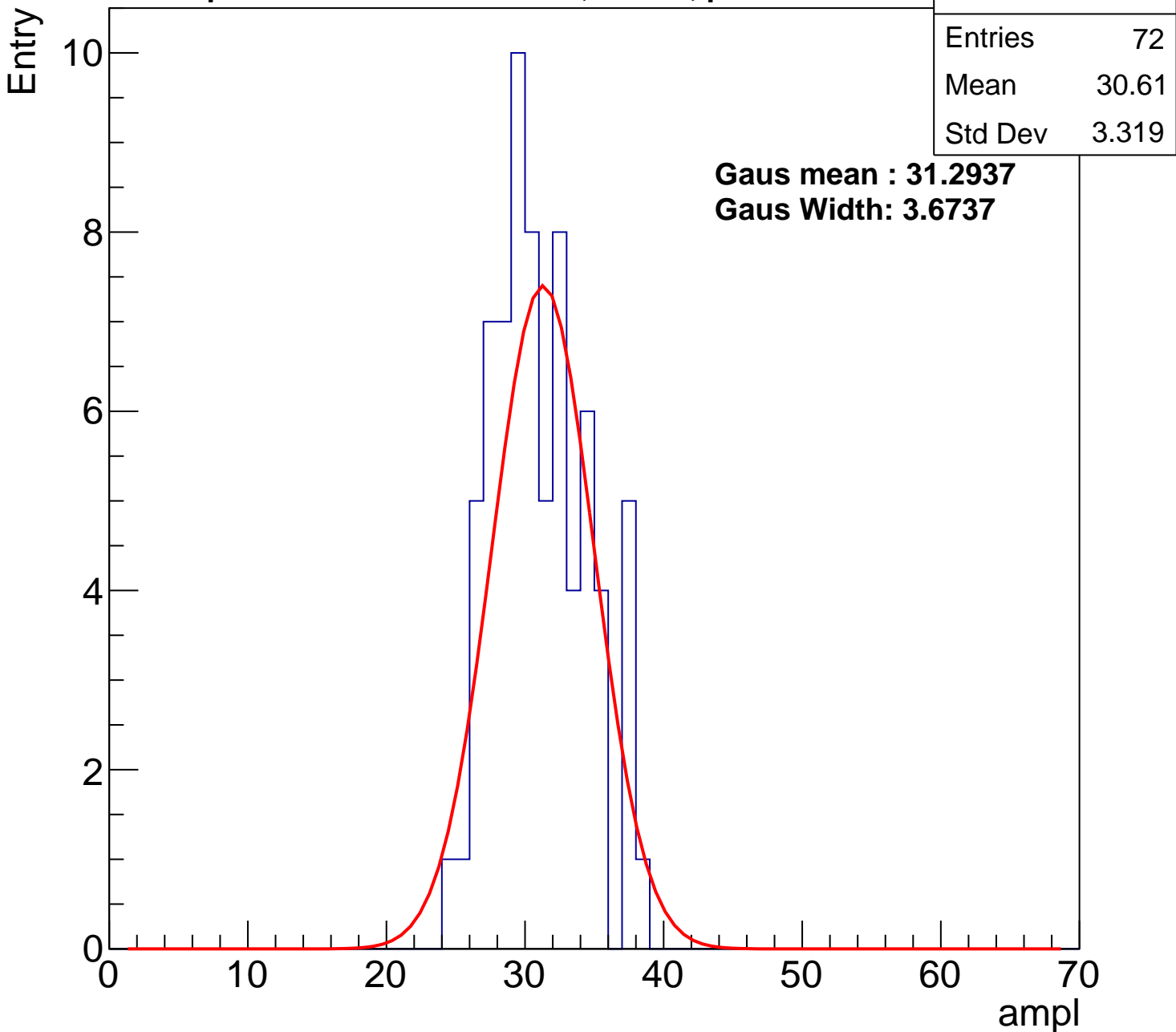
**Gaus Width: 3.6737**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch9, adc1

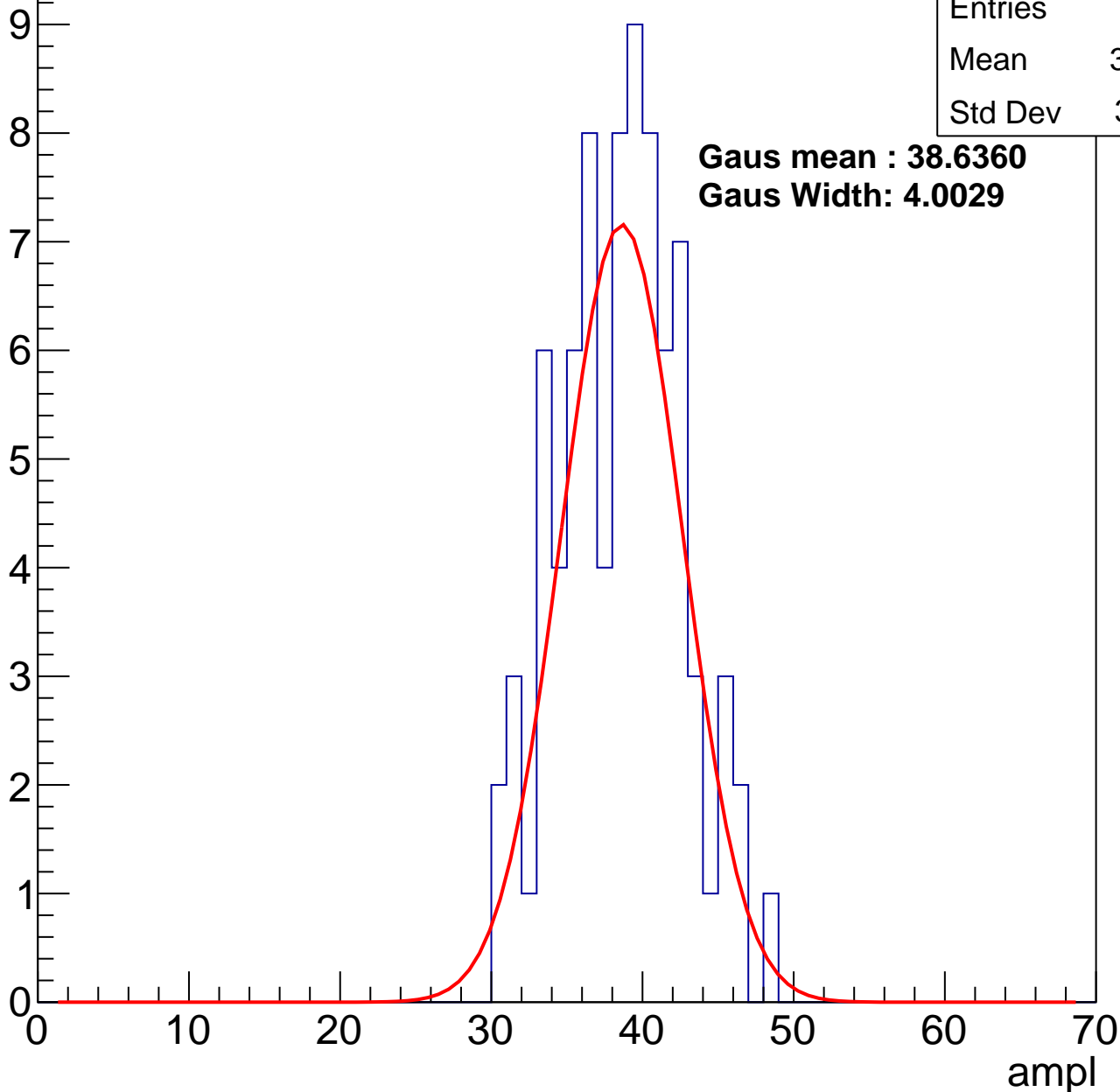
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	38.15
Std Dev	3.991

**Gaus mean : 38.6360**

**Gaus Width: 4.0029**



# B1L102S, U25-ch9, adc2

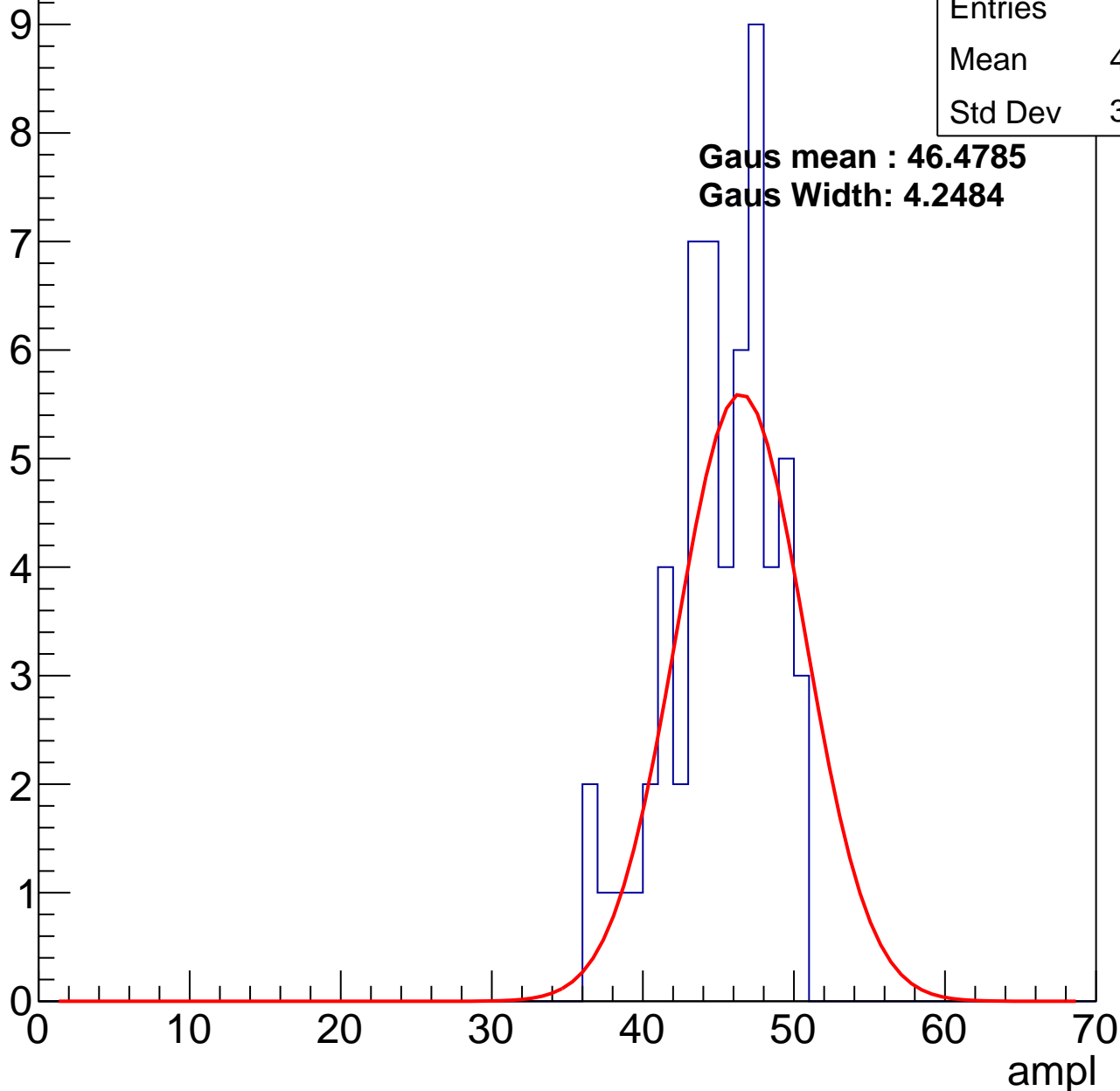
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	44.64
Std Dev	3.482

**Gaus mean : 46.4785**

**Gaus Width: 4.2484**

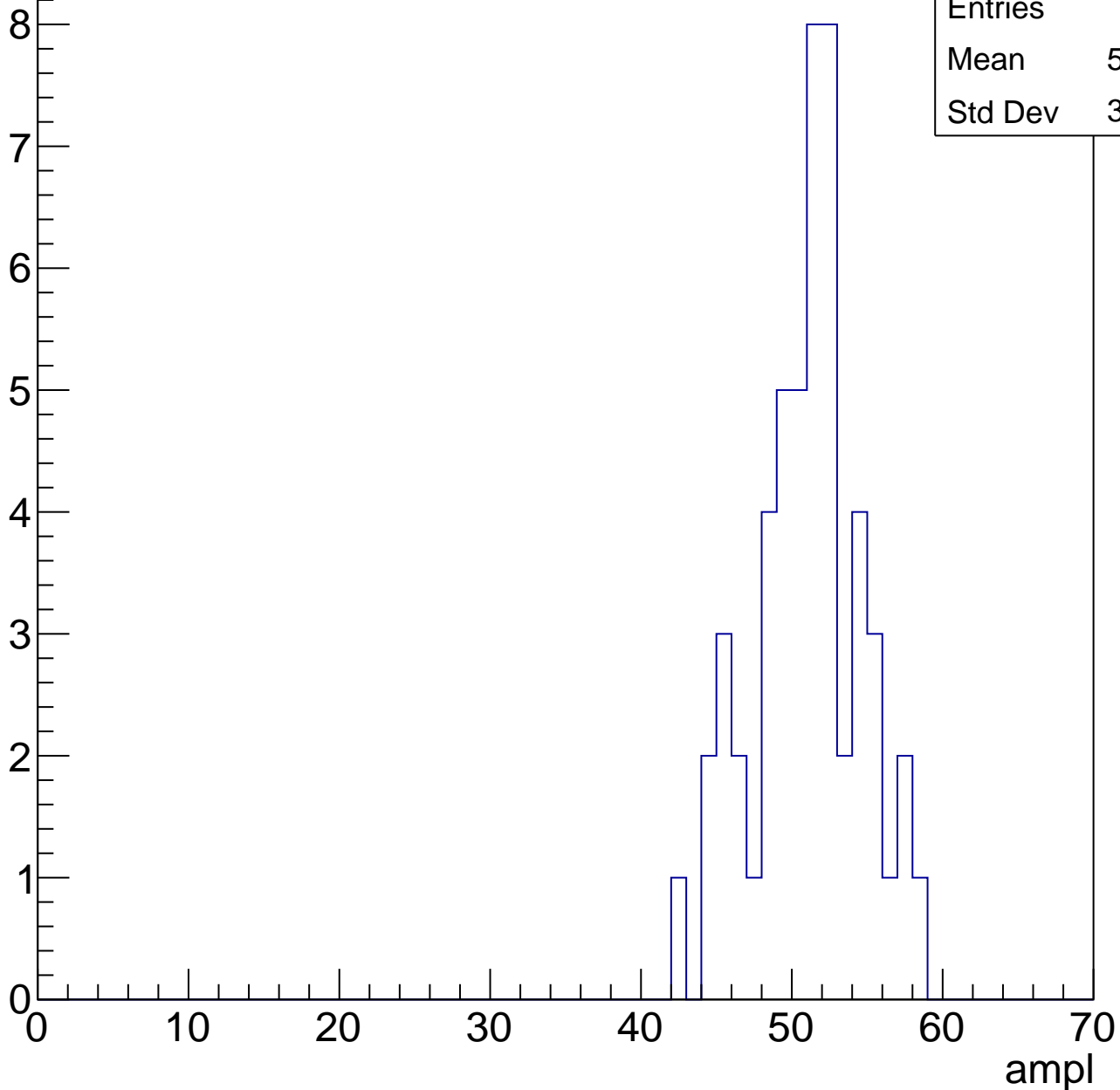


# B1L102S, U25-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	50.58
Std Dev	3.537

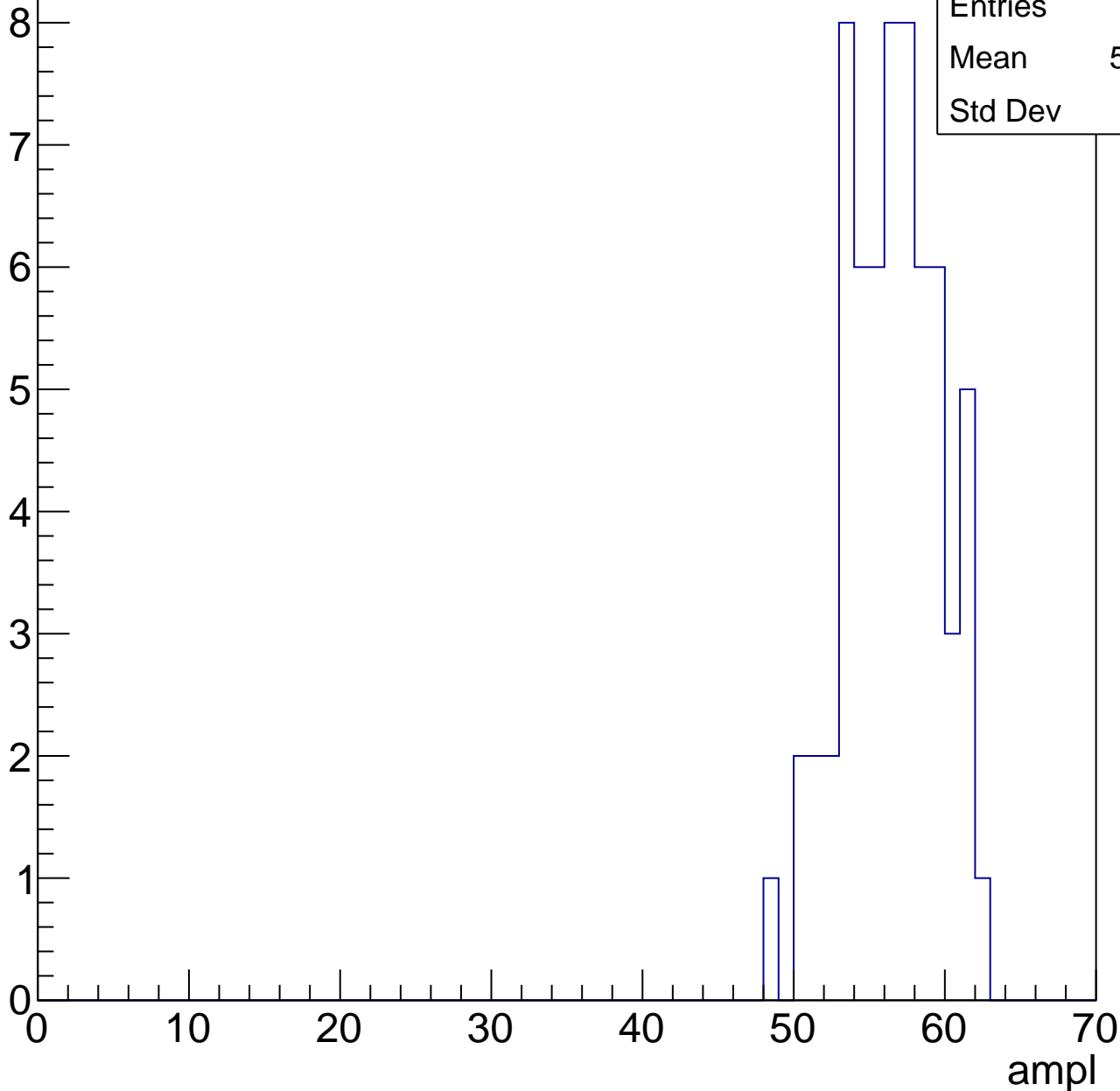


# B1L102S, U25-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	56.02
Std Dev	3.1

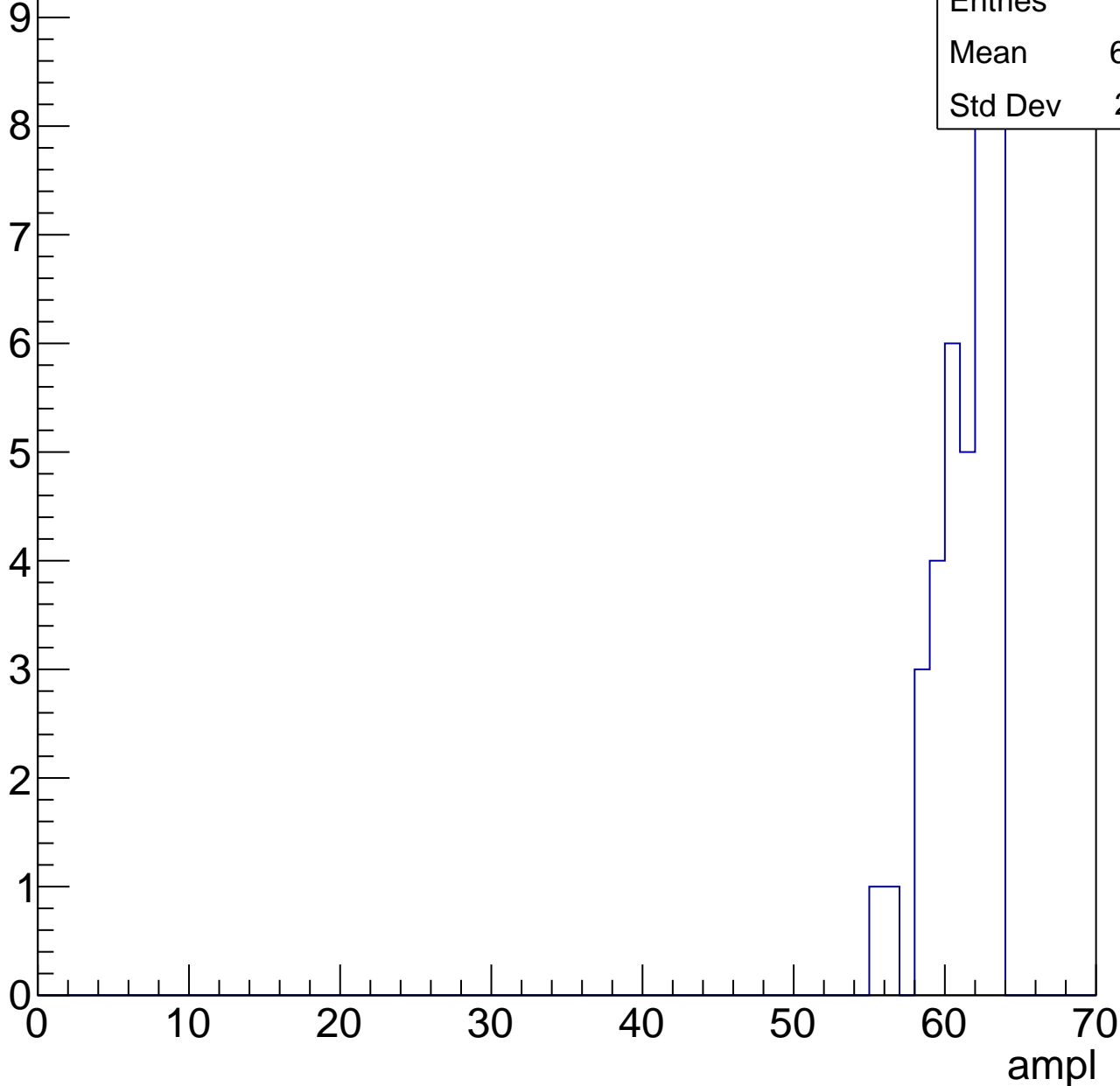


# B1L102S, U25-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

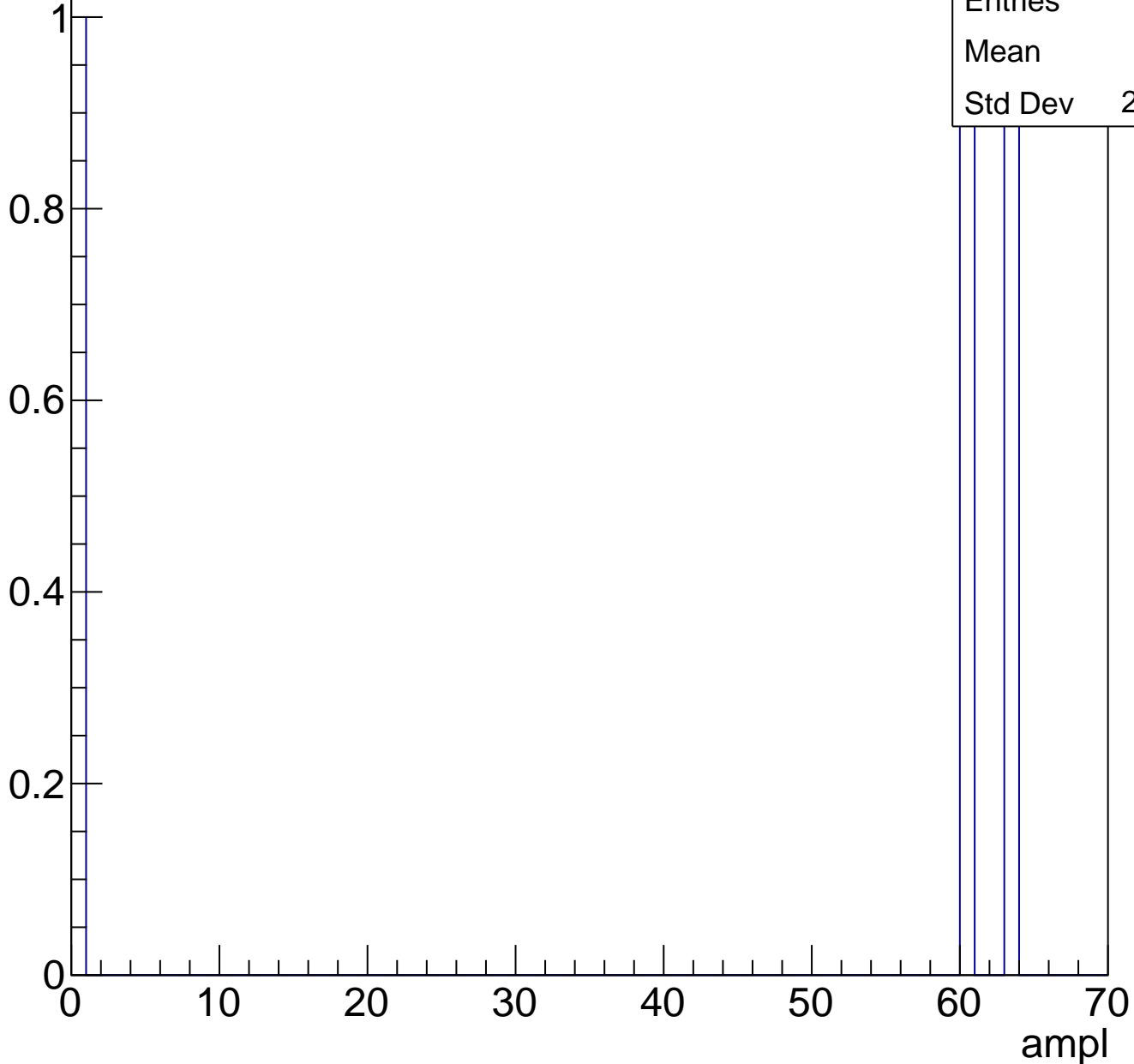
Entries	38
Mean	60.82
Std Dev	2.011



# B1L102S, U25-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

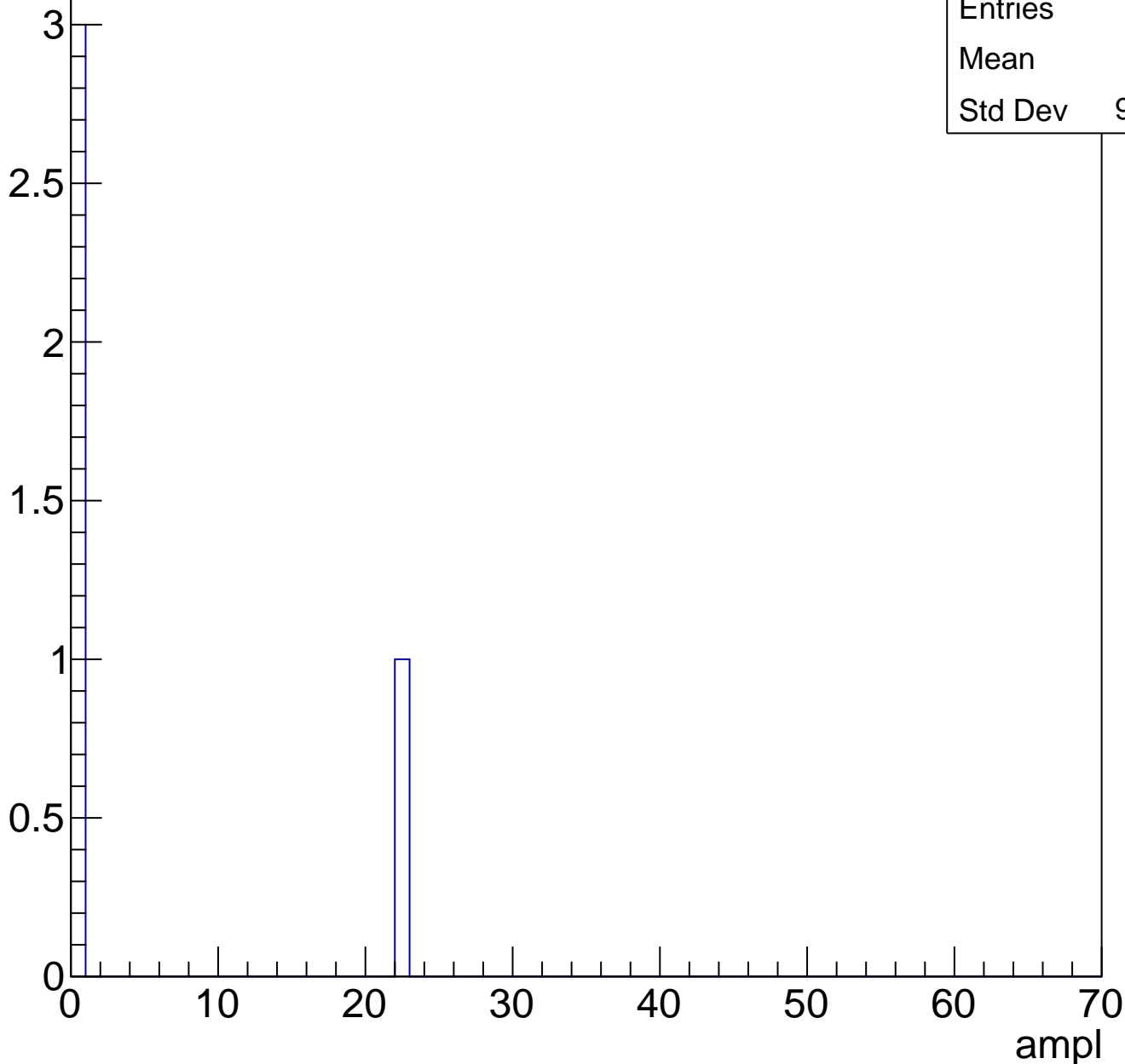




# B1L102S, U25-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch10, adc0

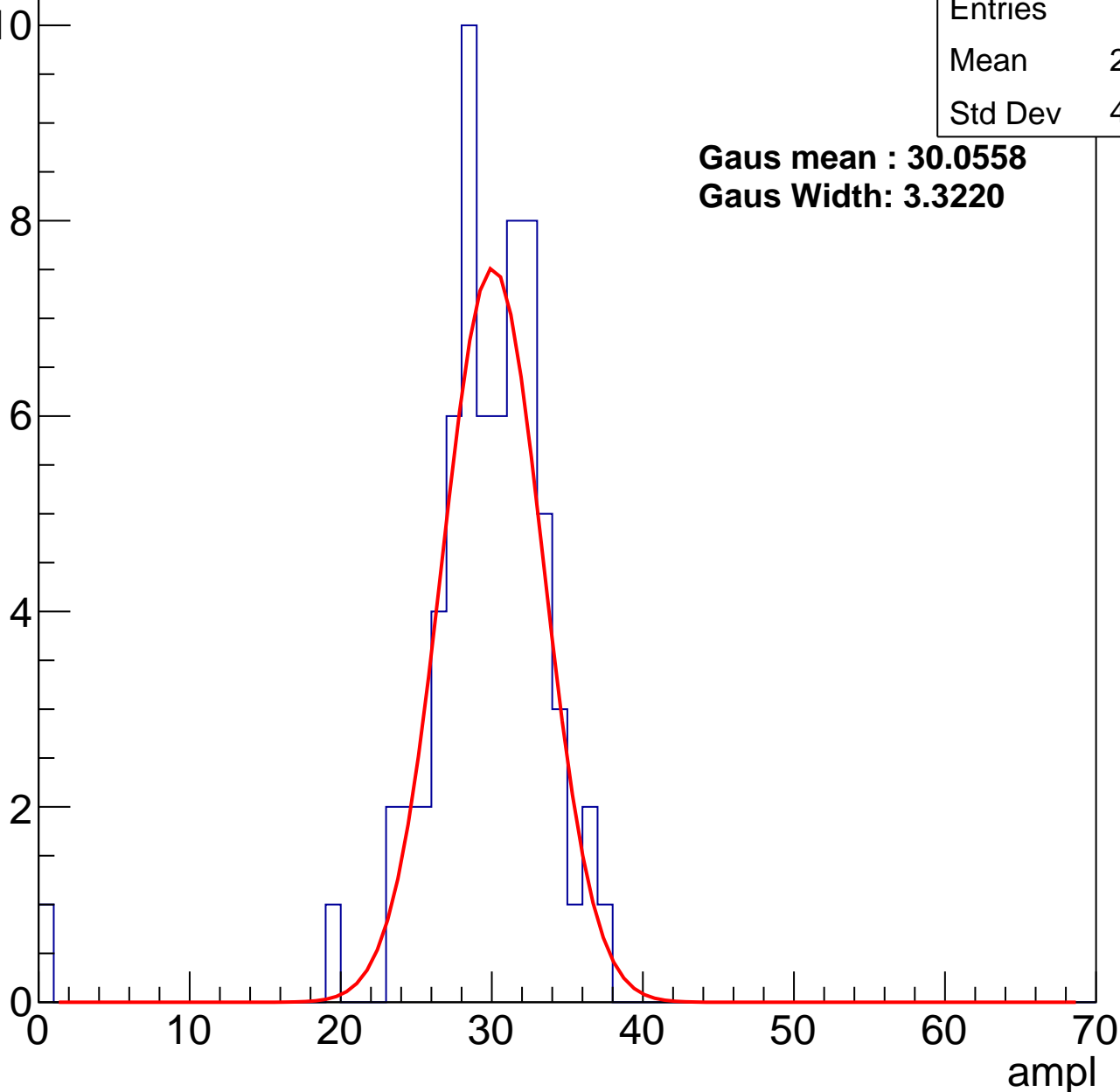
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.09
Std Dev	4.892

**Gaus mean : 30.0558**

**Gaus Width: 3.3220**



# B1L102S, U25-ch10, adc1

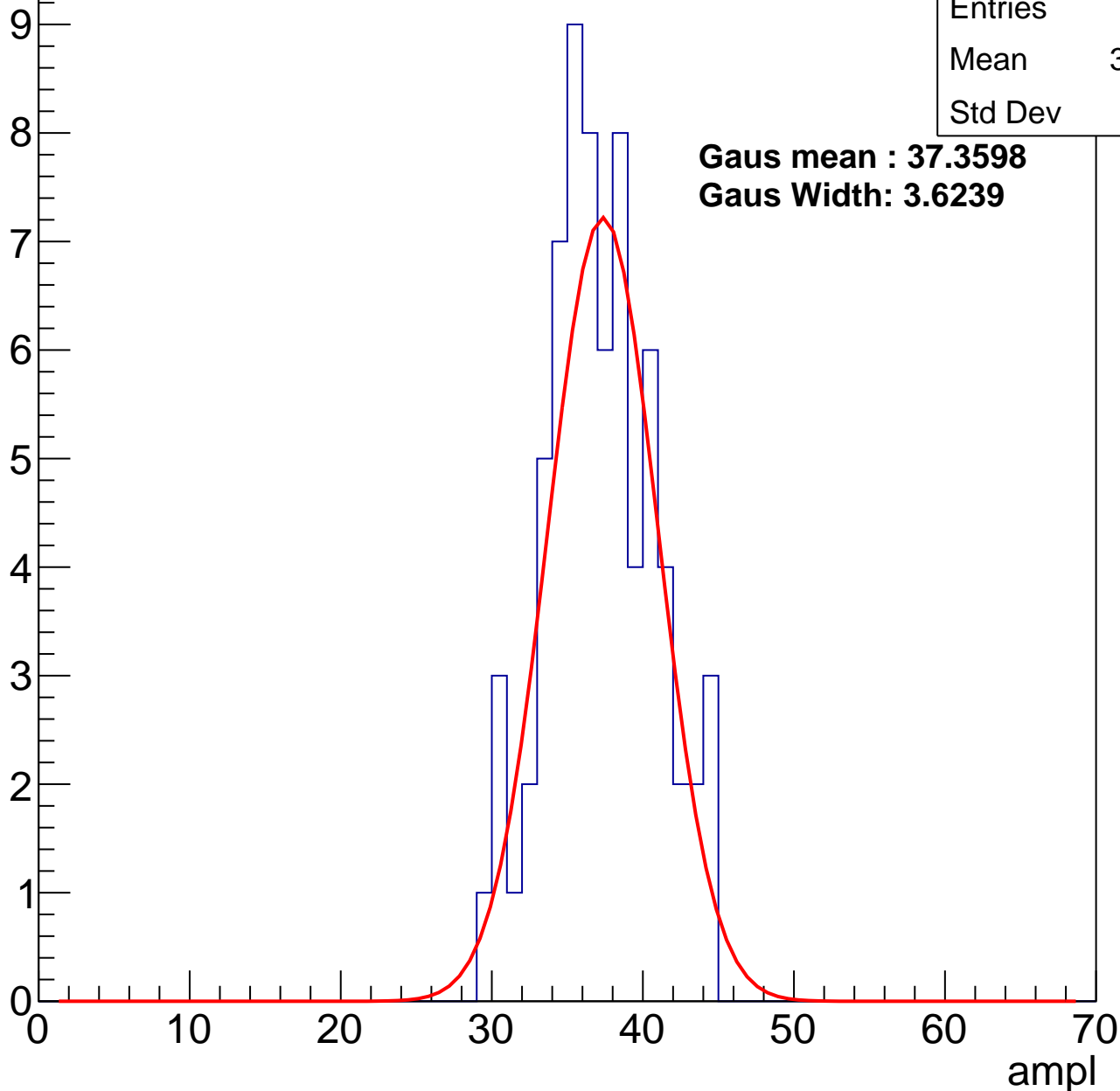
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.73
Std Dev	3.54

**Gaus mean : 37.3598**

**Gaus Width: 3.6239**



# B1L102S, U25-ch10, adc2

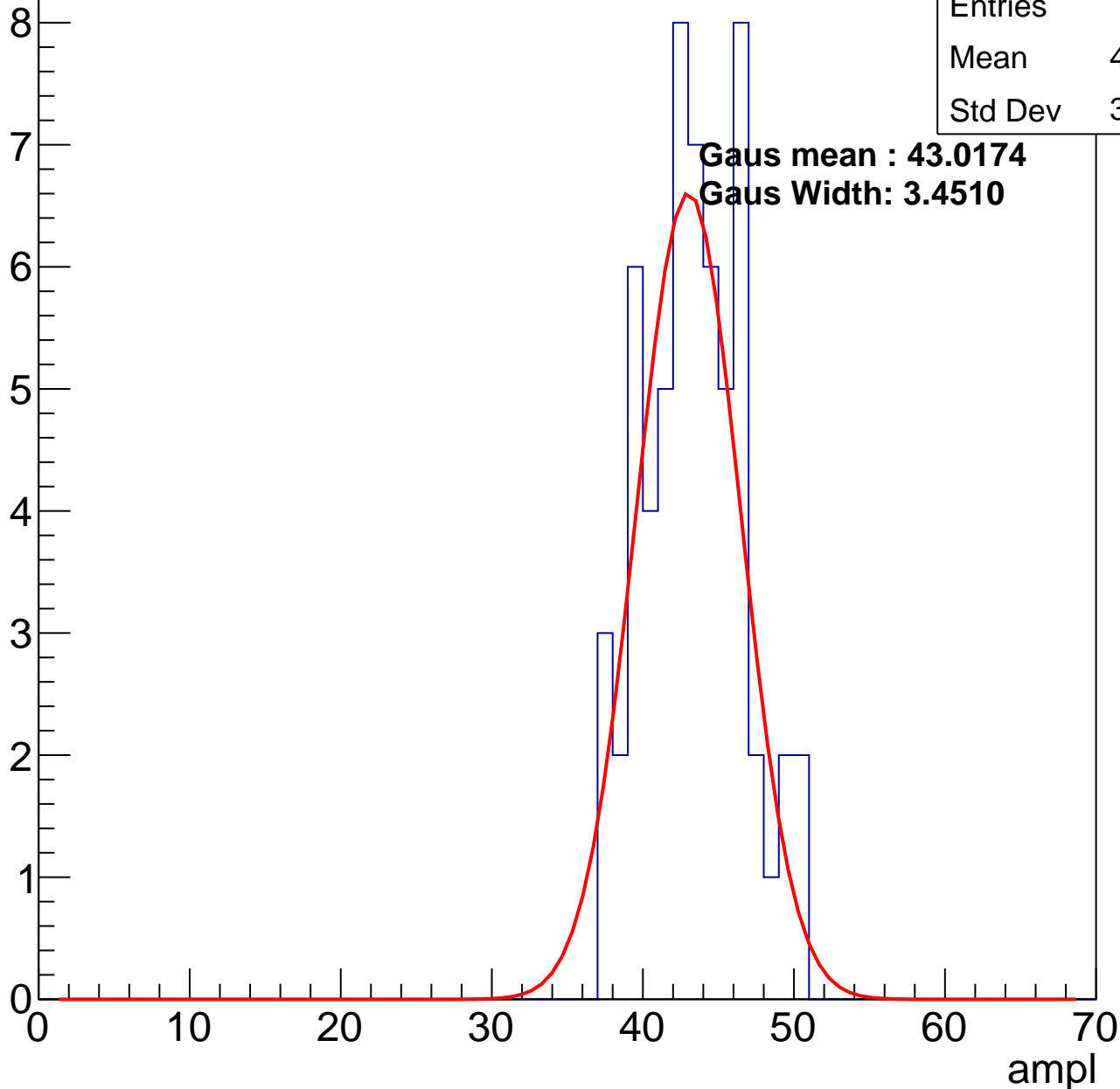
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.95
Std Dev	3.246

**Gaus mean : 43.0174**

**Gaus Width: 3.4510**

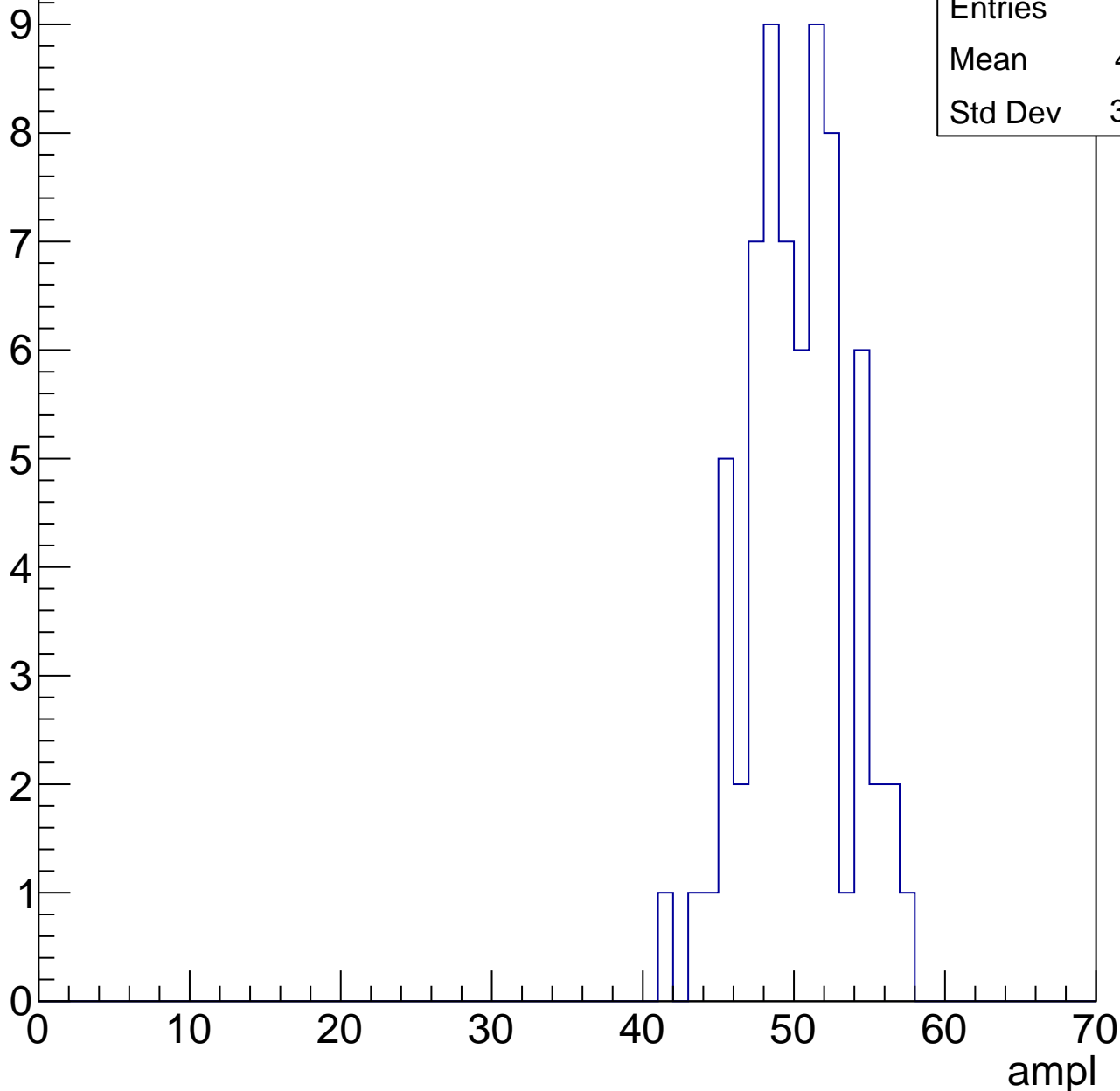


# B1L102S, U25-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.71
Std Dev	3.299

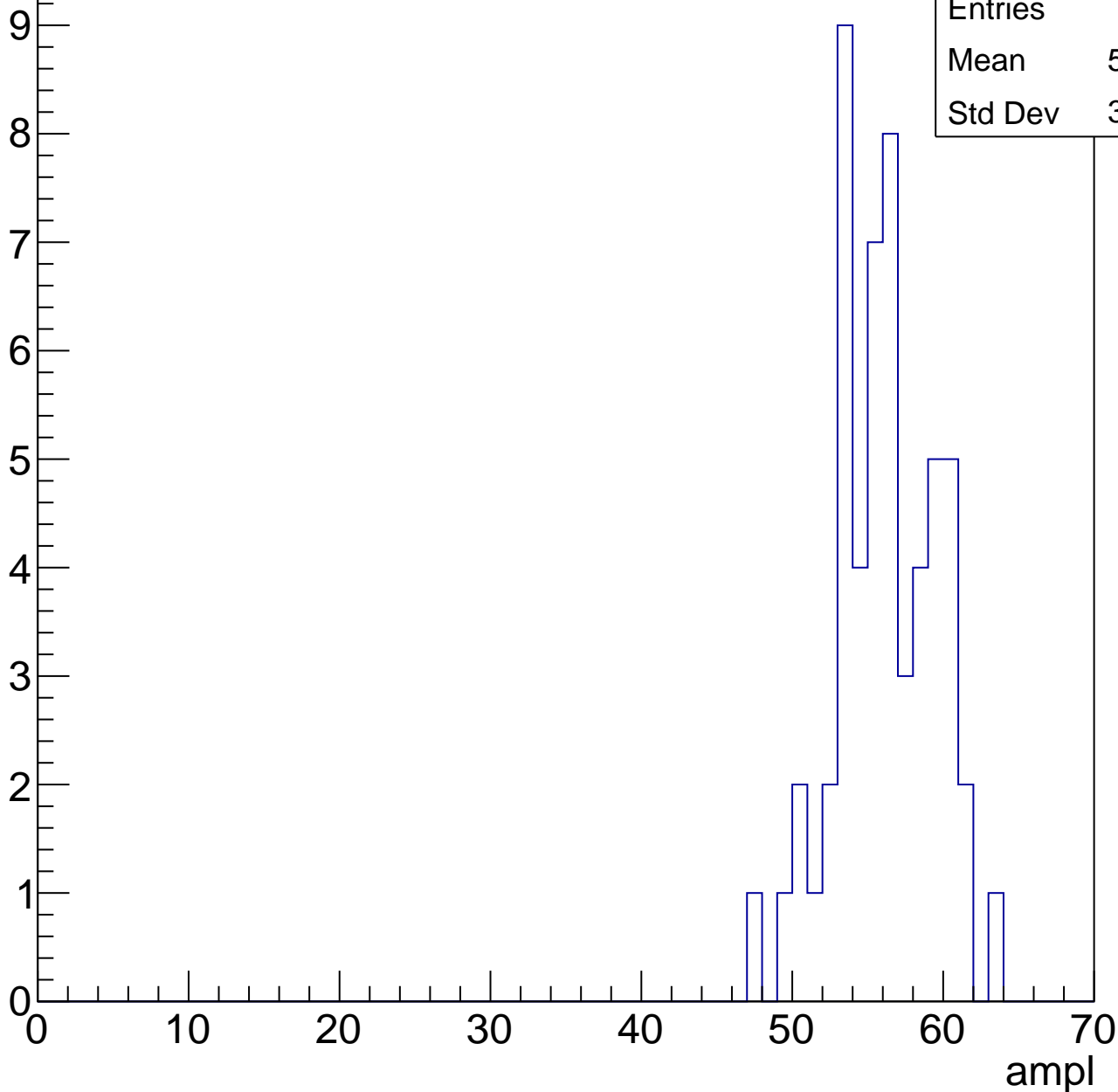


# B1L102S, U25-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.64
Std Dev	3.299

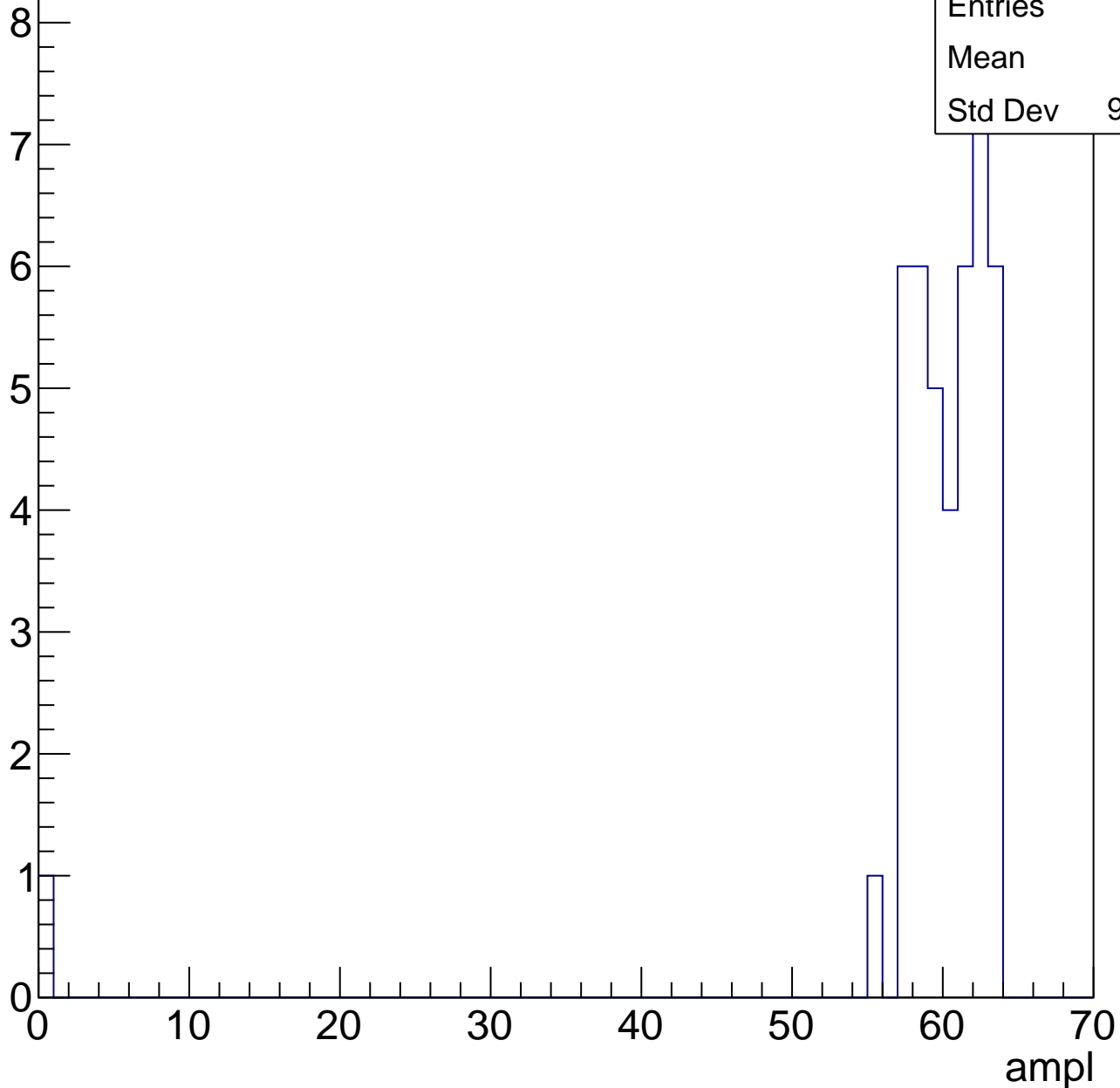


# B1L102S, U25-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.6
Std Dev	9.297



# B1L102S, U25-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch11, adc0

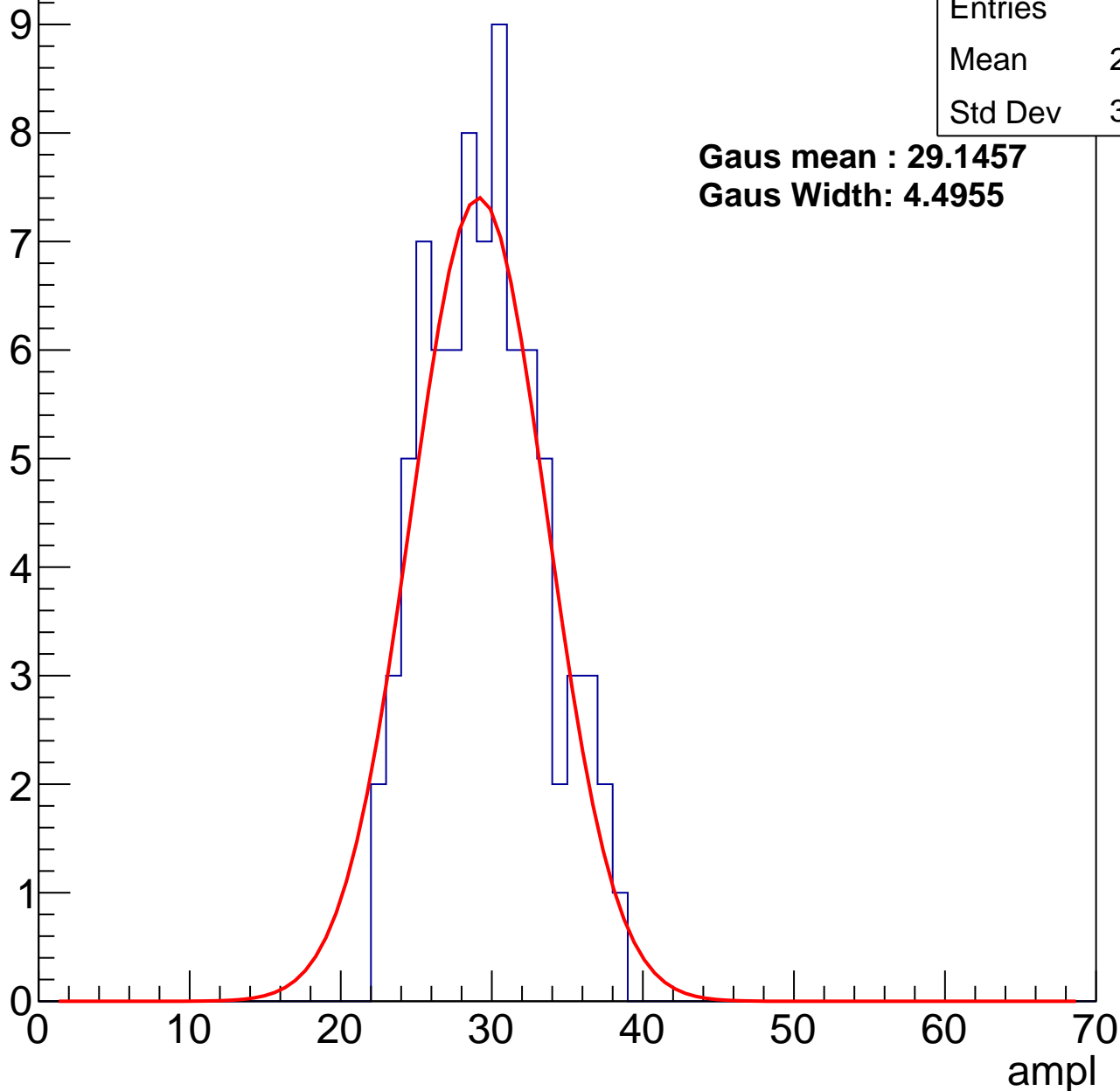
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	29.12
Std Dev	3.863

**Gaus mean : 29.1457**

**Gaus Width: 4.4955**



# B1L102S, U25-ch11, adc1

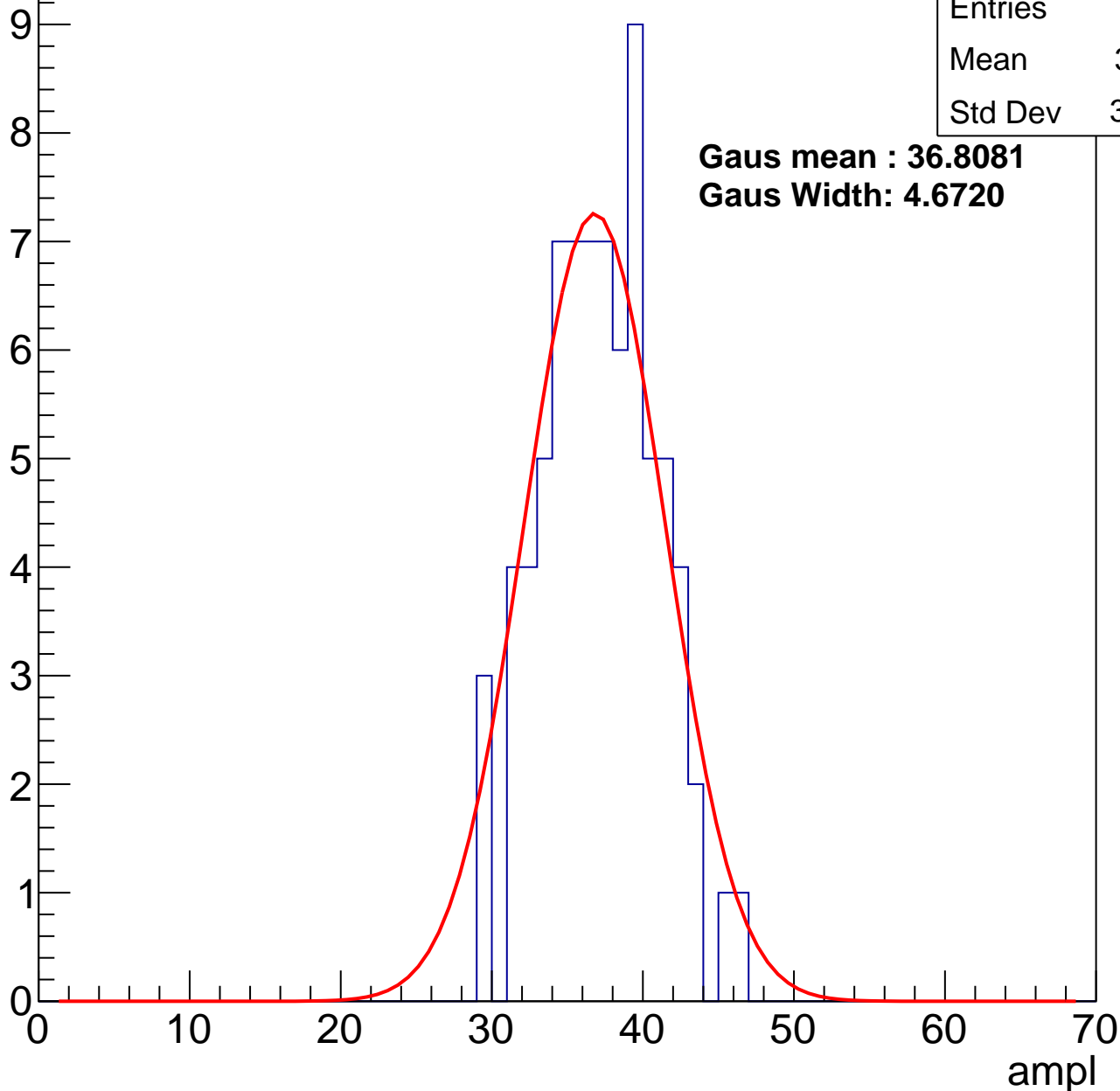
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.71
Std Dev	3.765

**Gaus mean : 36.8081**

**Gaus Width: 4.6720**



# B1L102S, U25-ch11, adc2

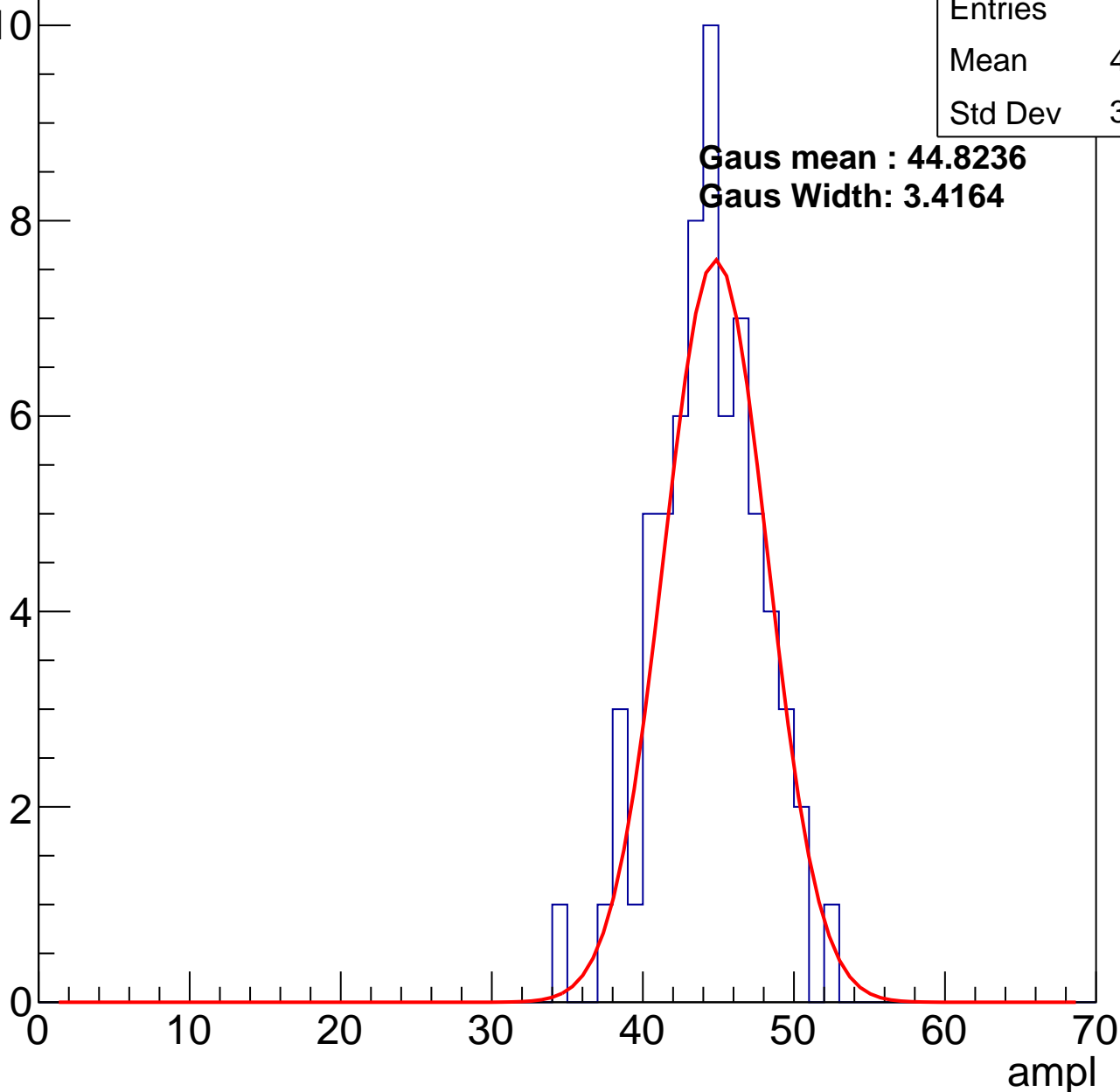
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.87
Std Dev	3.412

**Gaus mean : 44.8236**

**Gaus Width: 3.4164**

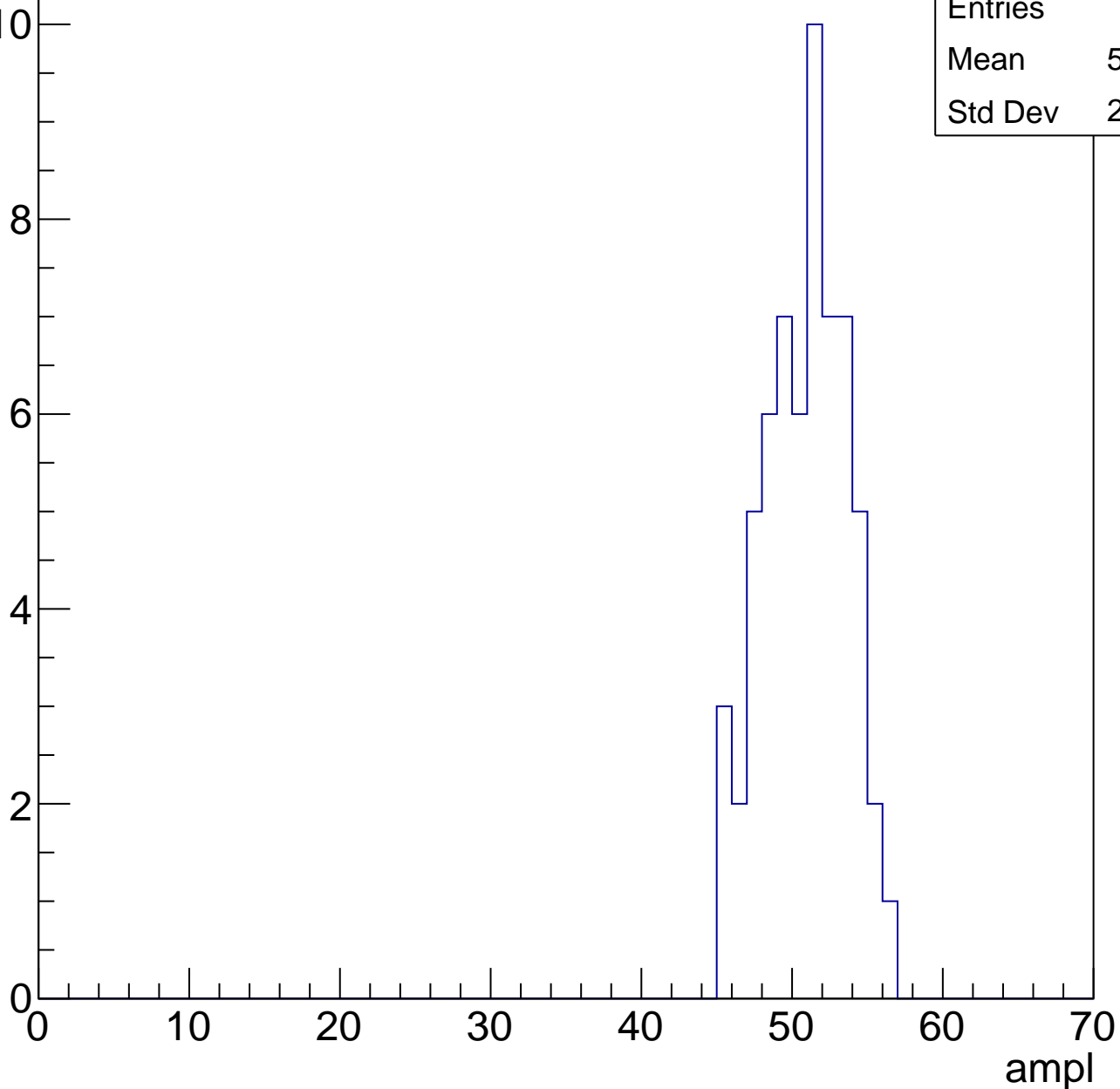


# B1L102S, U25-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.39
Std Dev	2.687

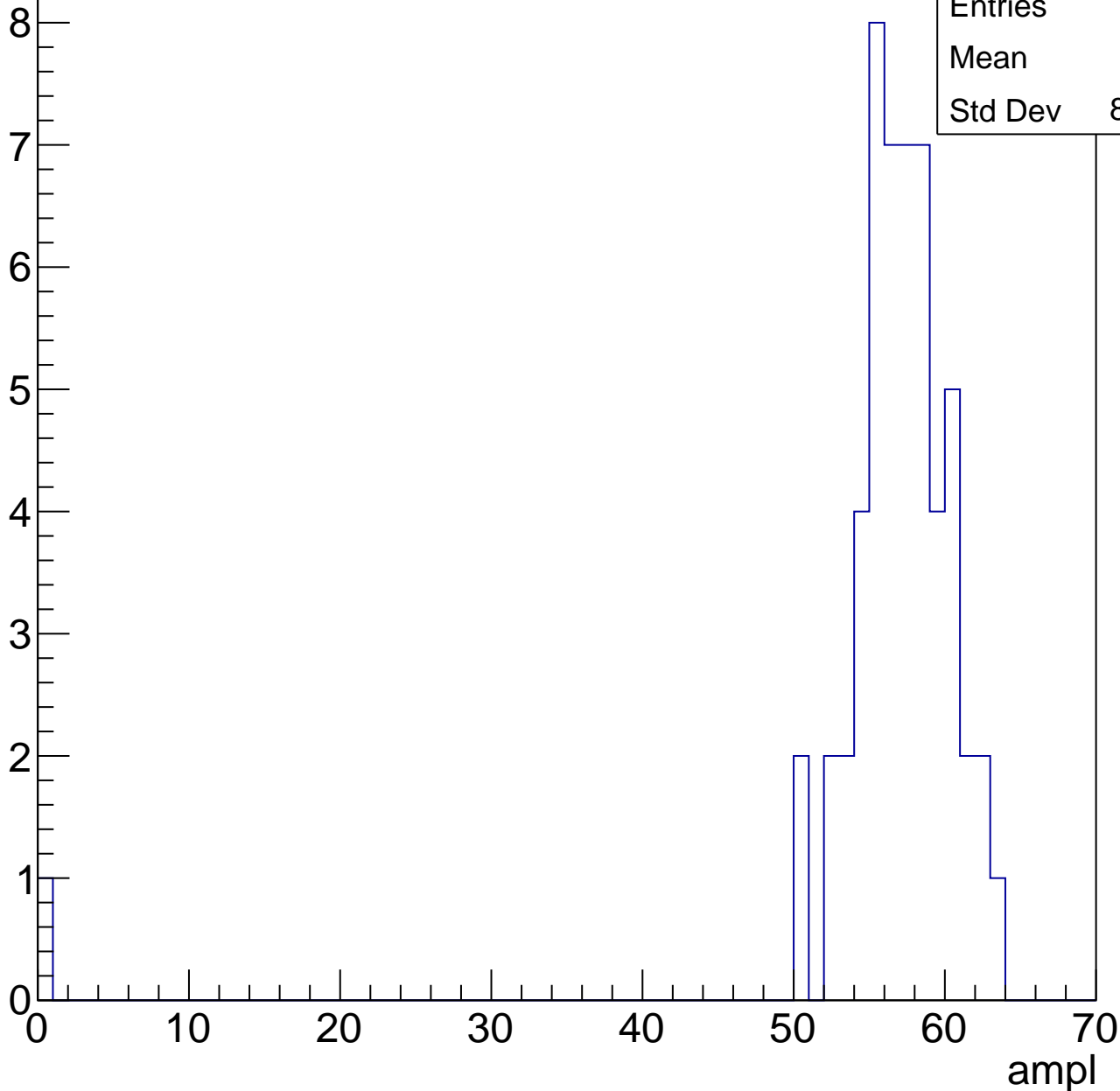


# B1L102S, U25-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.7
Std Dev	8.164

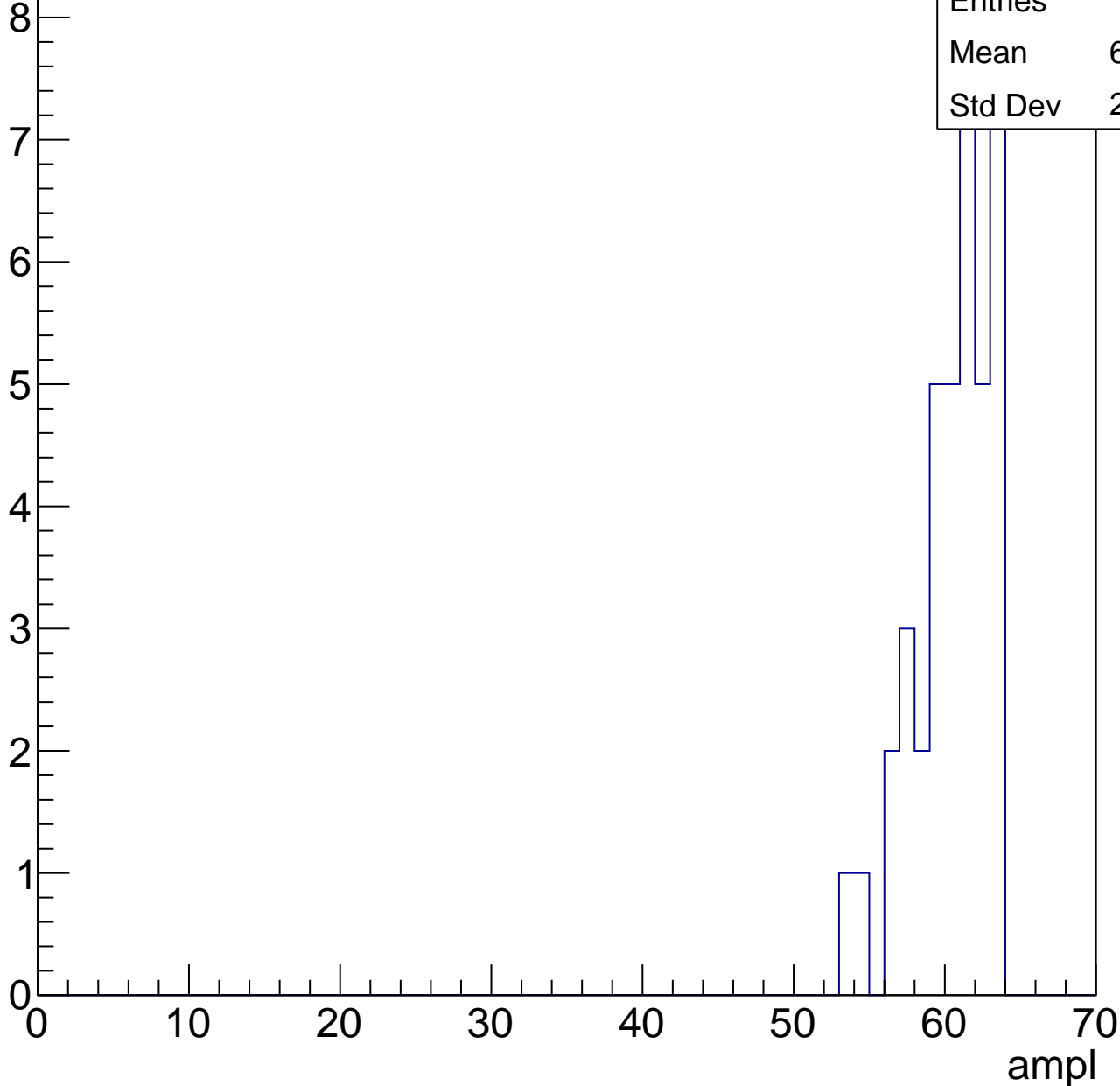


# B1L102S, U25-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

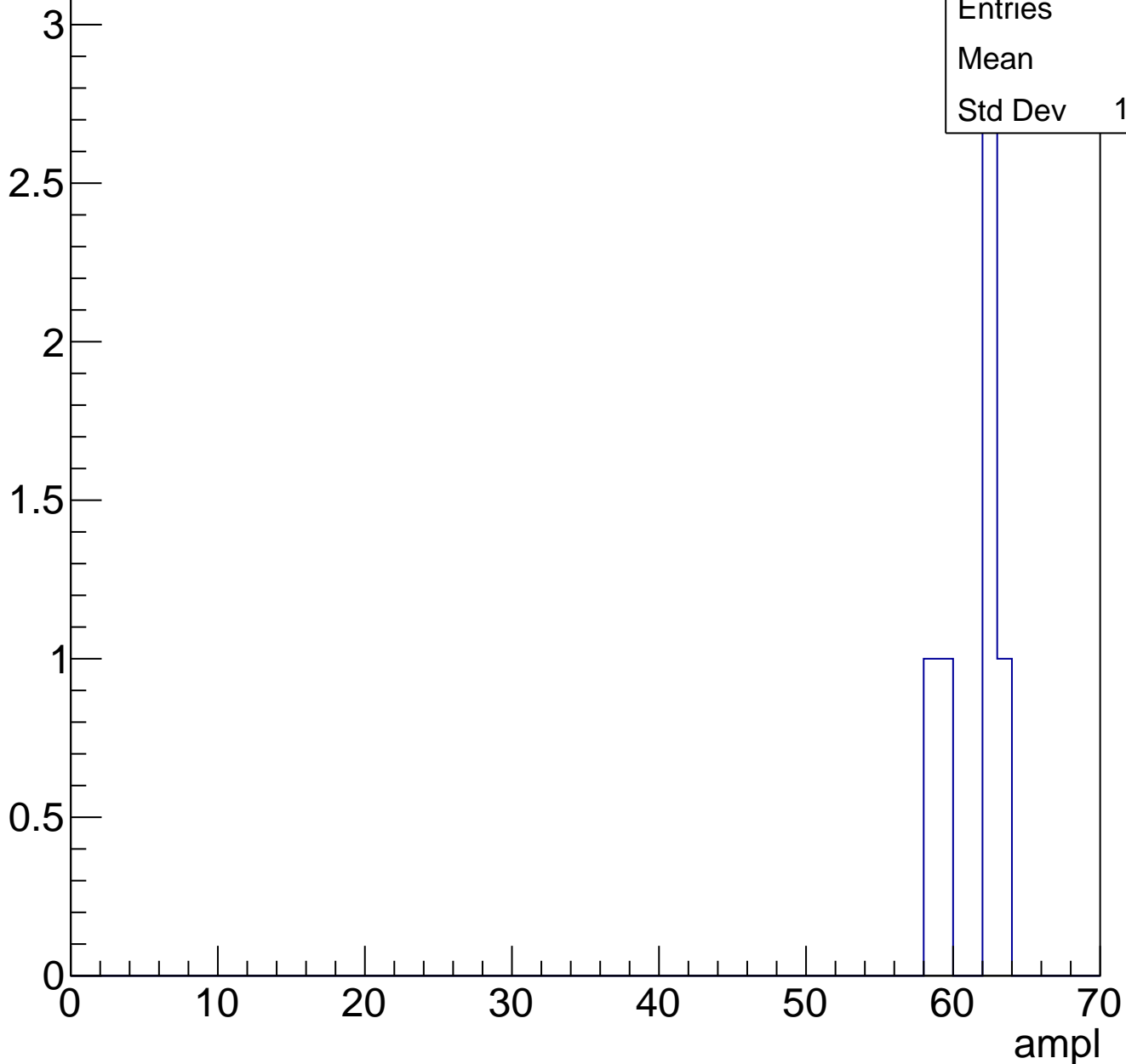
Entries	40
Mean	60.08
Std Dev	2.534



# B1L102S, U25-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch12, adc0

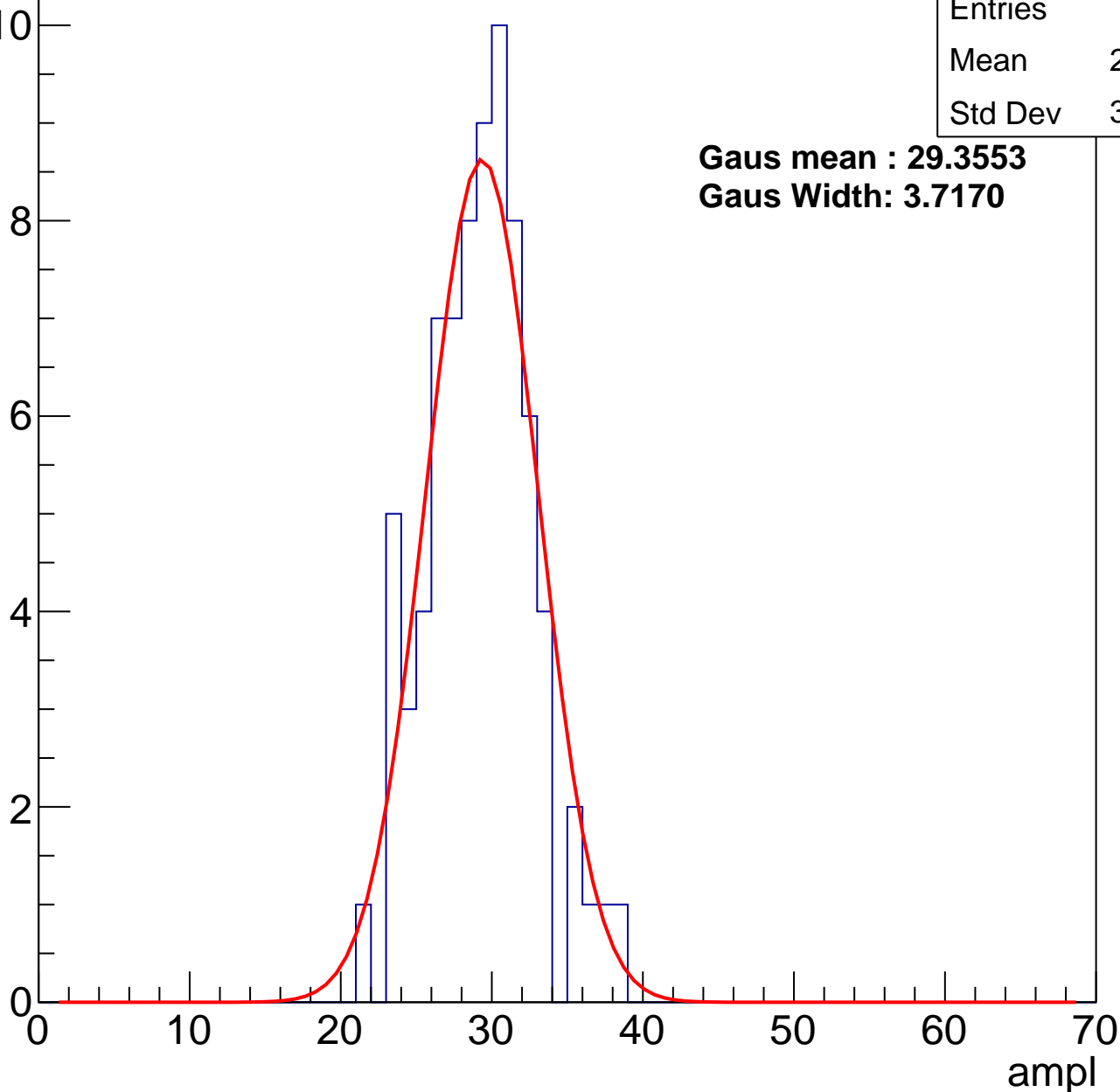
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.79
Std Dev	3.428

**Gaus mean : 29.3553**

**Gaus Width: 3.7170**



# B1L102S, U25-ch12, adc1

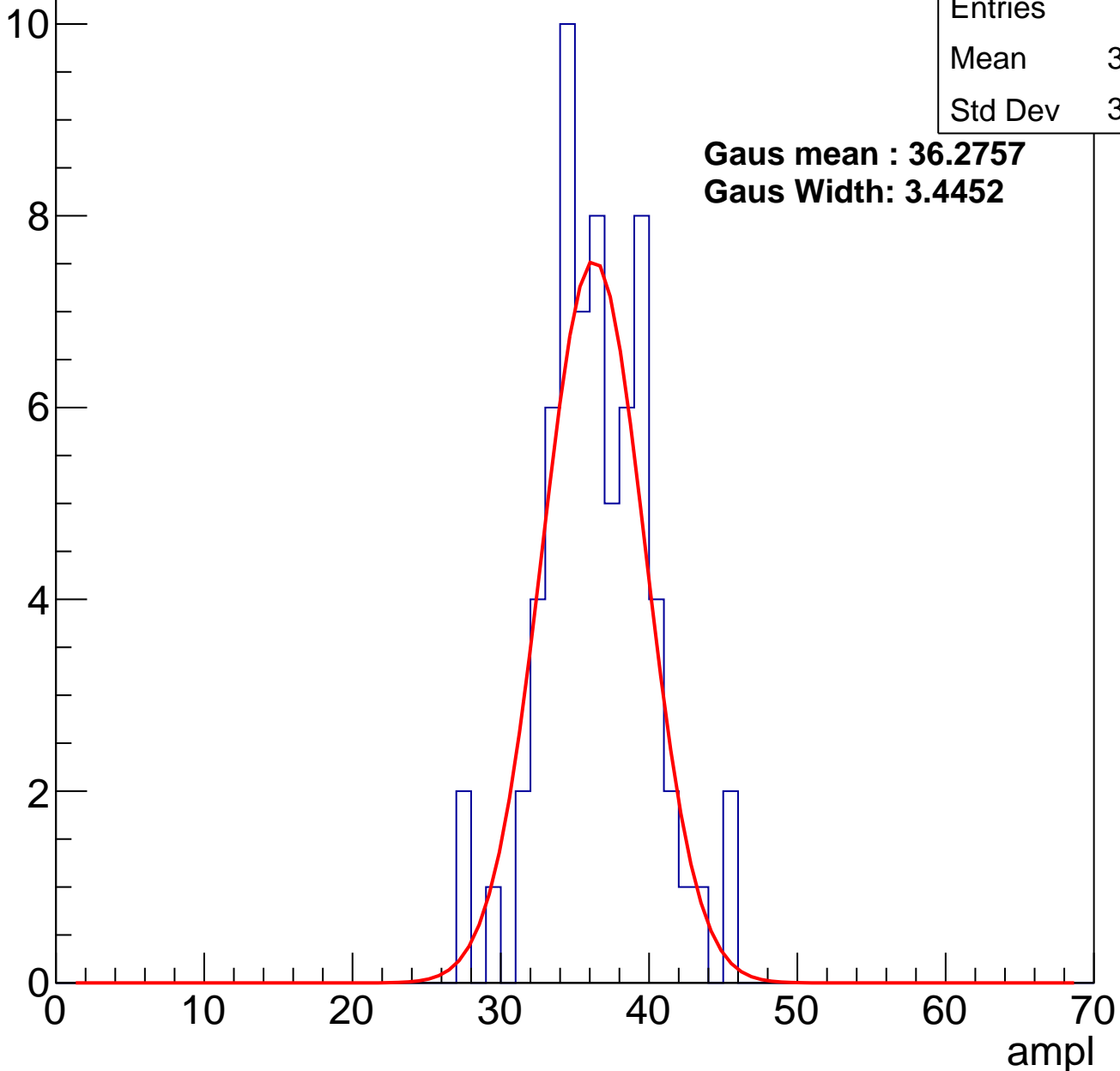
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	69
Mean	36.03
Std Dev	3.587

**Gaus mean : 36.2757**

**Gaus Width: 3.4452**

Entry



# B1L102S, U25-ch12, adc2

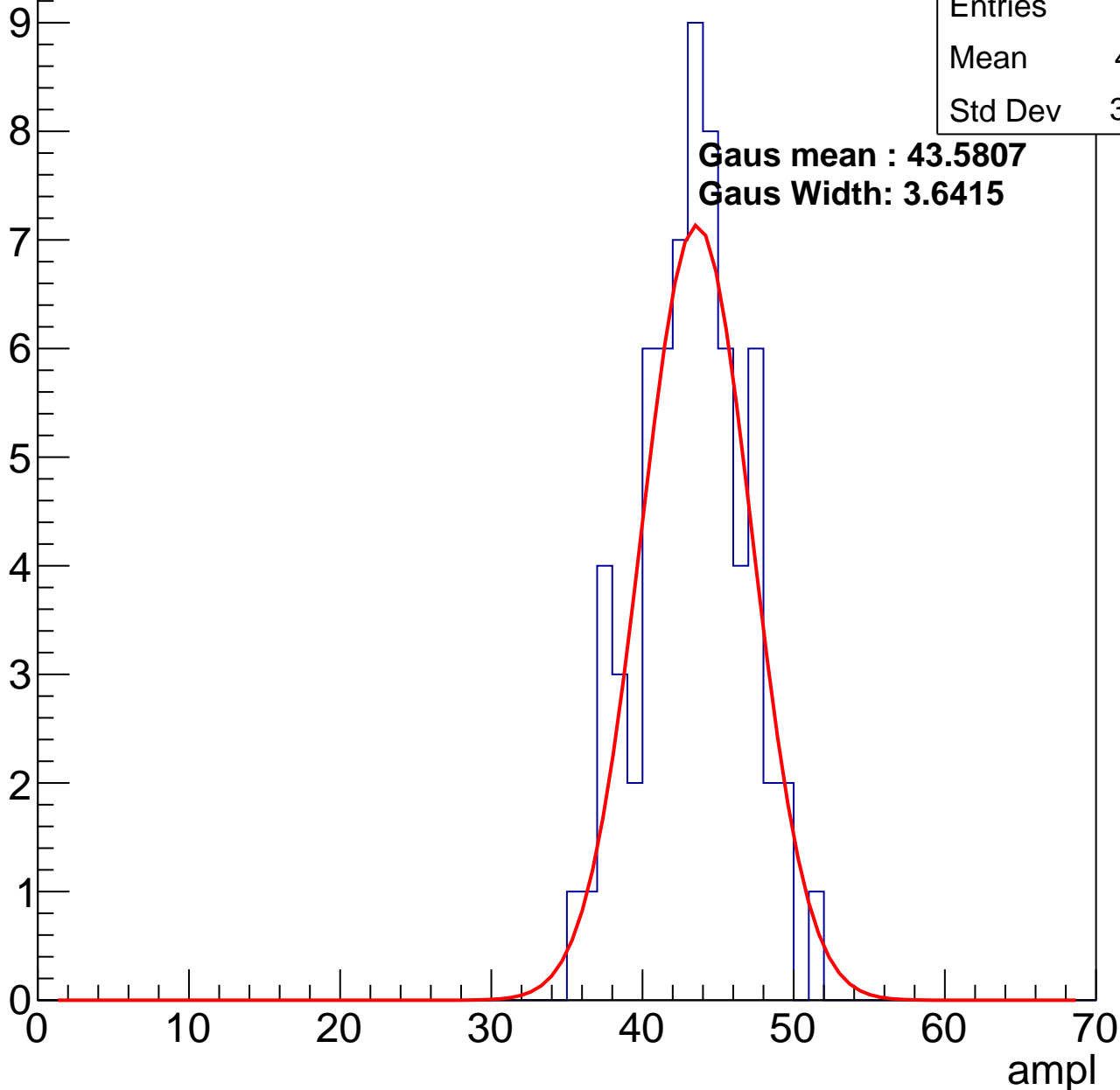
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.81
Std Dev	3.422

**Gaus mean : 43.5807**

**Gaus Width: 3.6415**



# B1L102S, U25-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

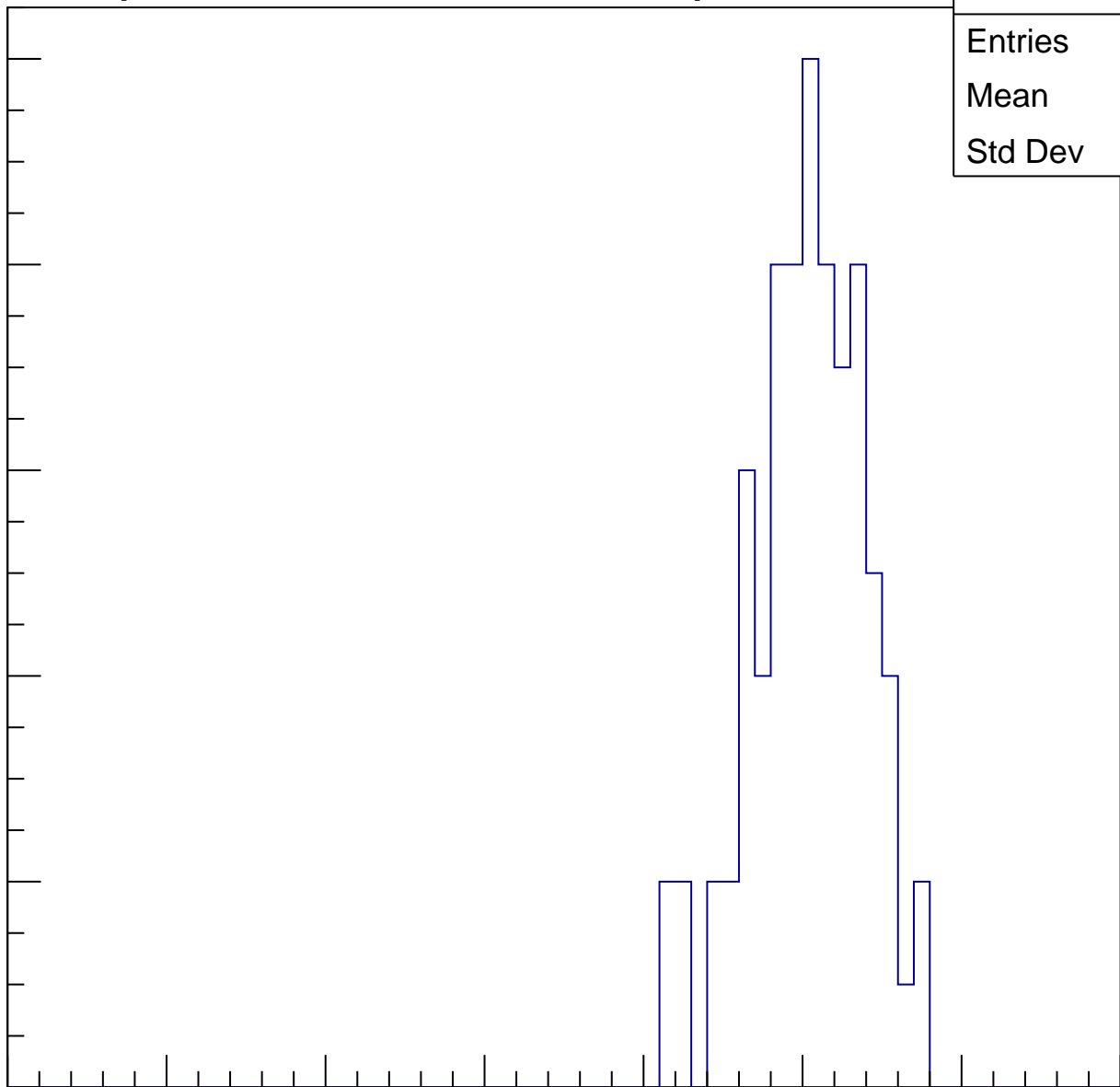
Entries	79
Mean	49.87
Std Dev	3.566

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

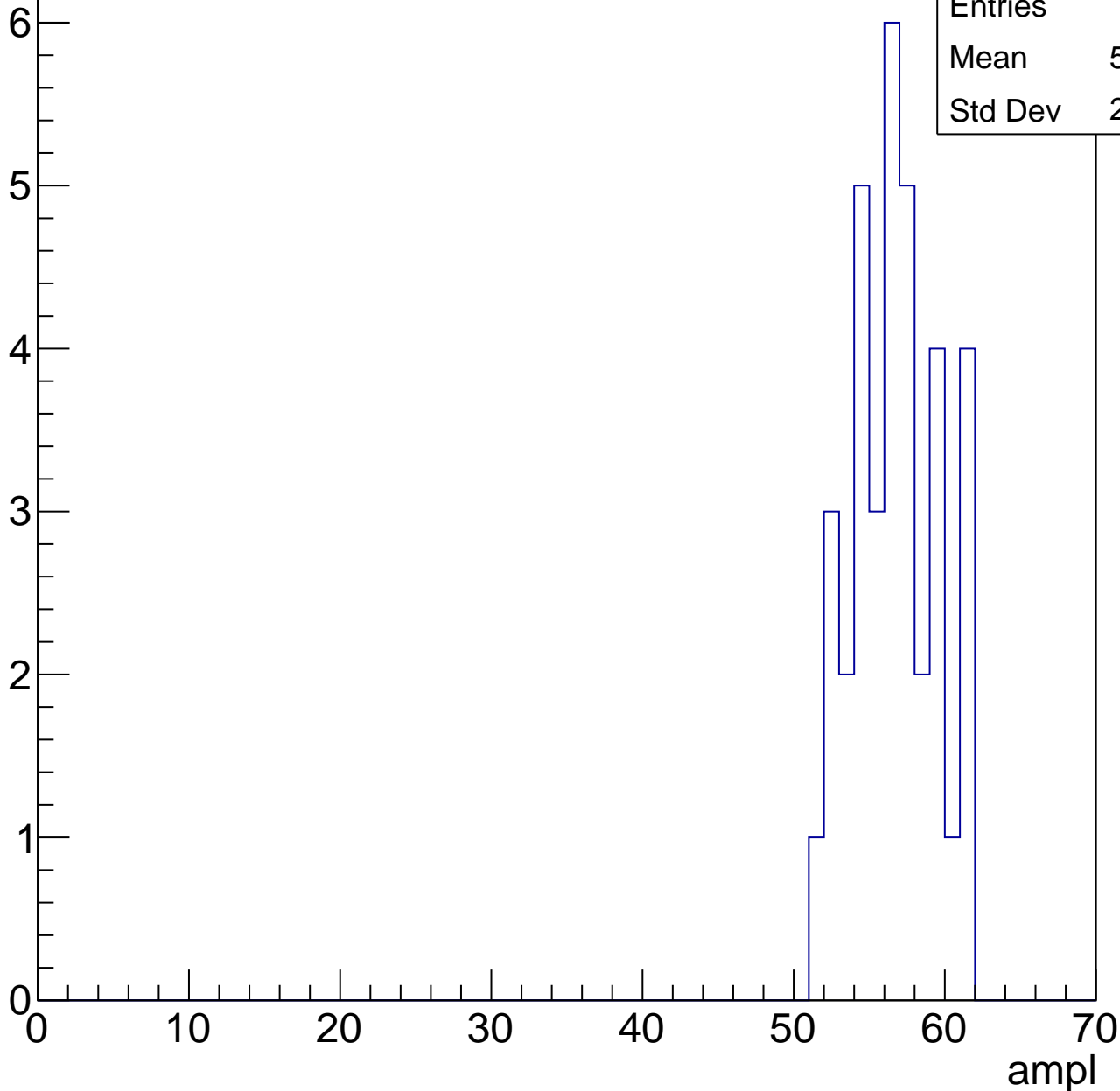


# B1L102S, U25-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	56.25
Std Dev	2.773



# B1L102S, U25-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

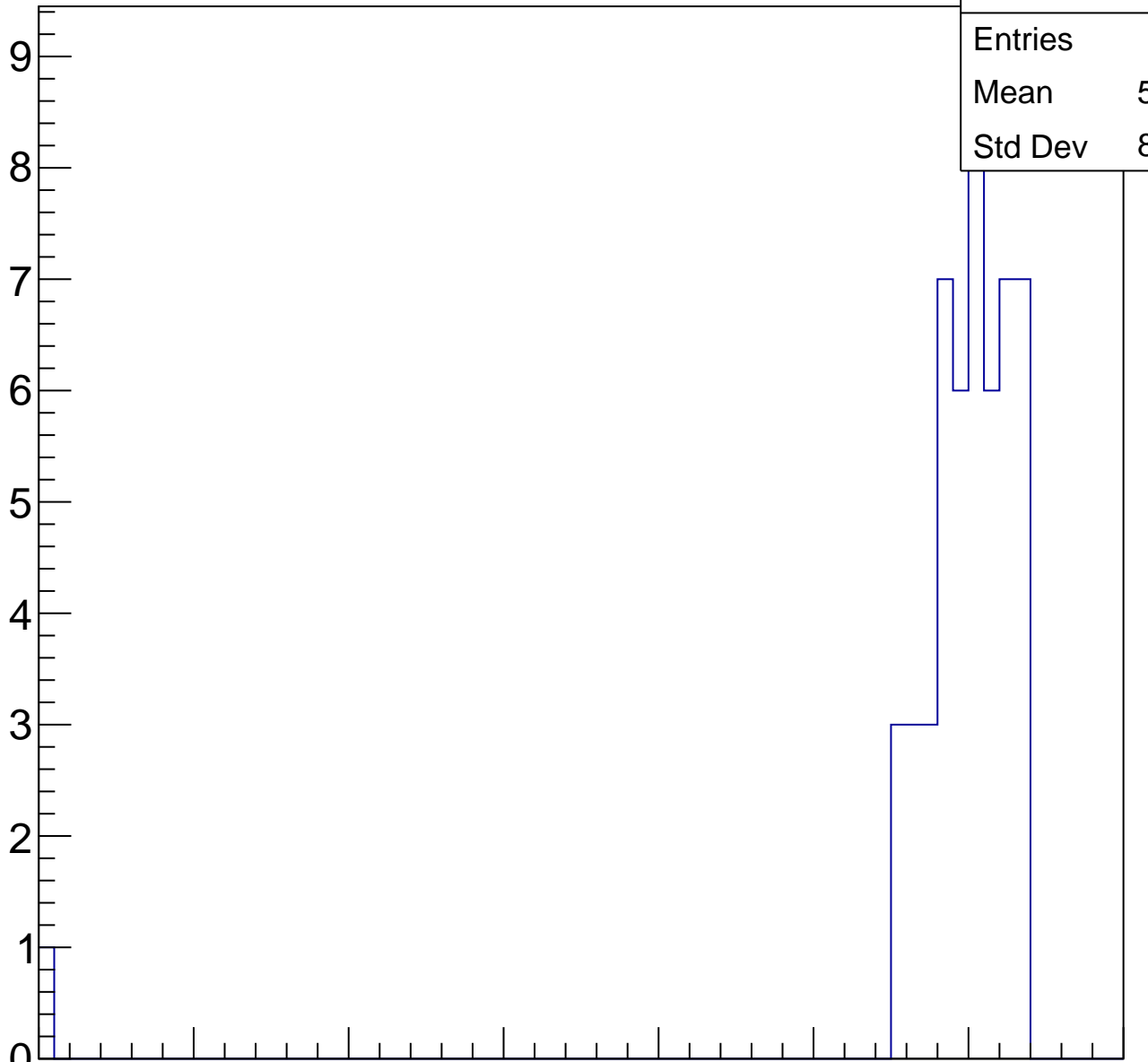
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.56
Std Dev	8.518

ampl

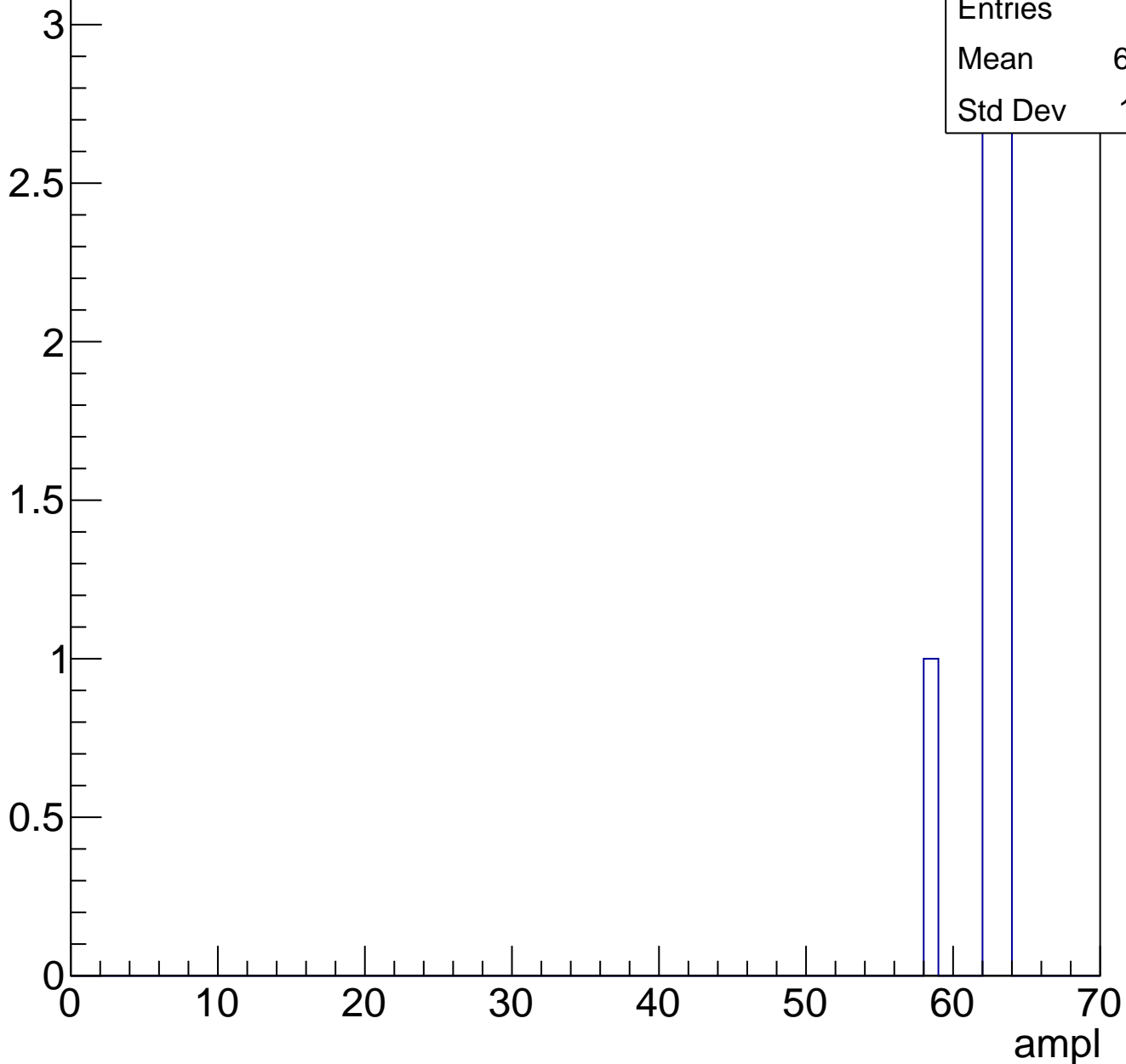
0 10 20 30 40 50 60 70



# B1L102S, U25-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch13, adc0

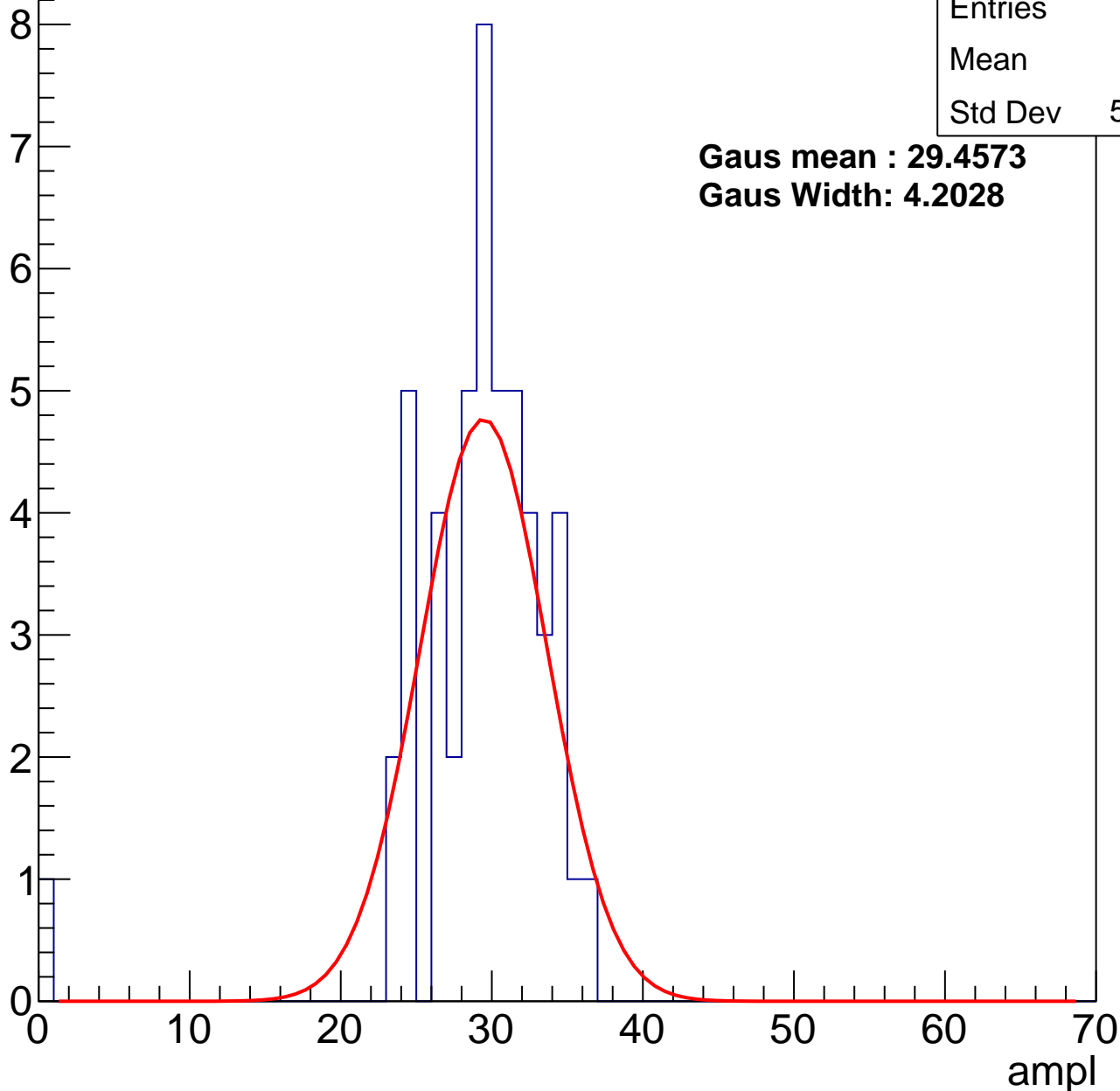
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	28.7
Std Dev	5.247

**Gaus mean : 29.4573**

**Gaus Width: 4.2028**



# B1L102S, U25-ch13, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

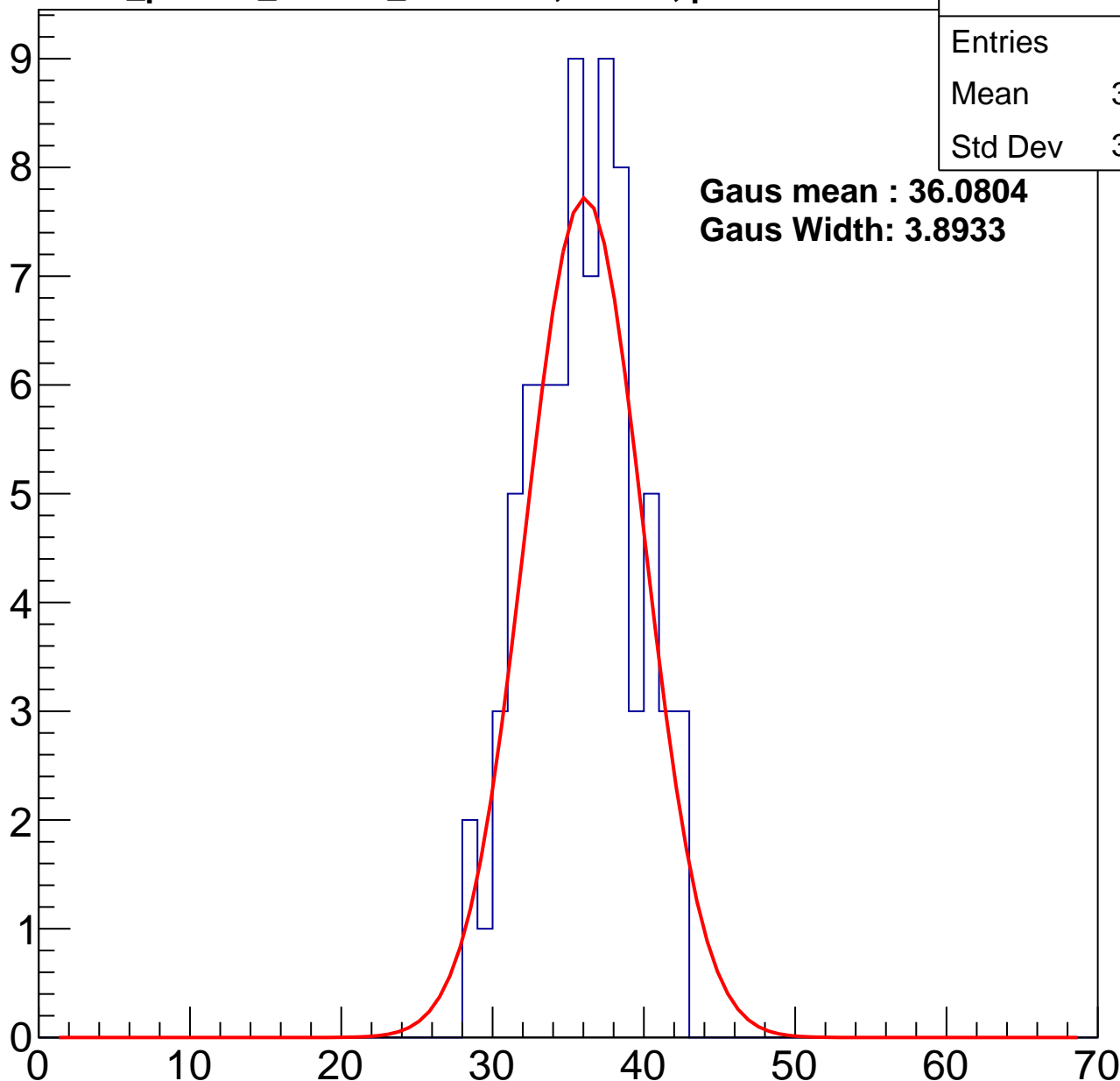
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	76
Mean	35.45
Std Dev	3.443

**Gaus mean : 36.0804**

**Gaus Width: 3.8933**

ampl



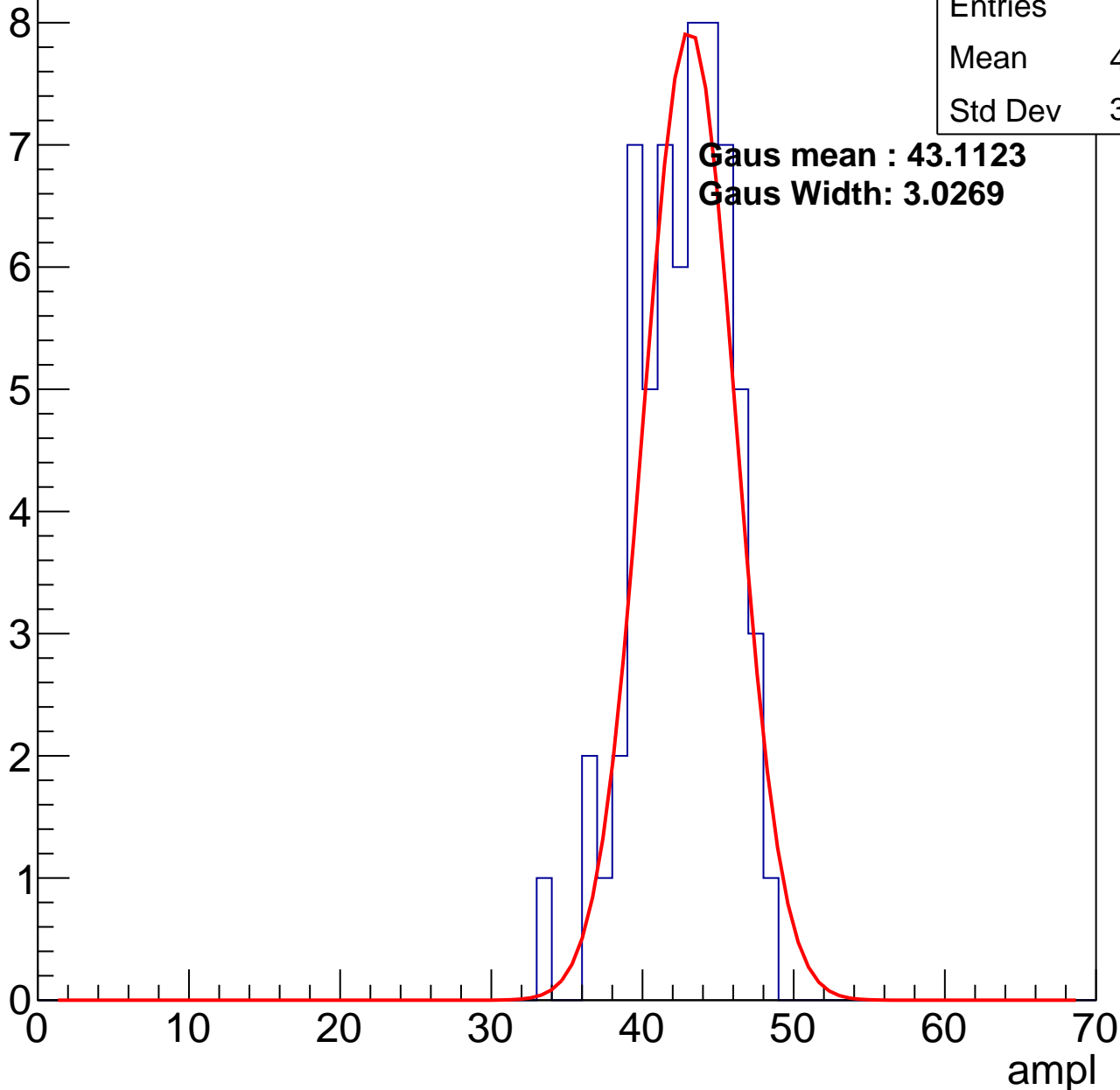
# B1L102S, U25-ch13, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.22
Std Dev	3.063

**Gaus mean : 43.1123**  
**Gaus Width: 3.0269**



# B1L102S, U25-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

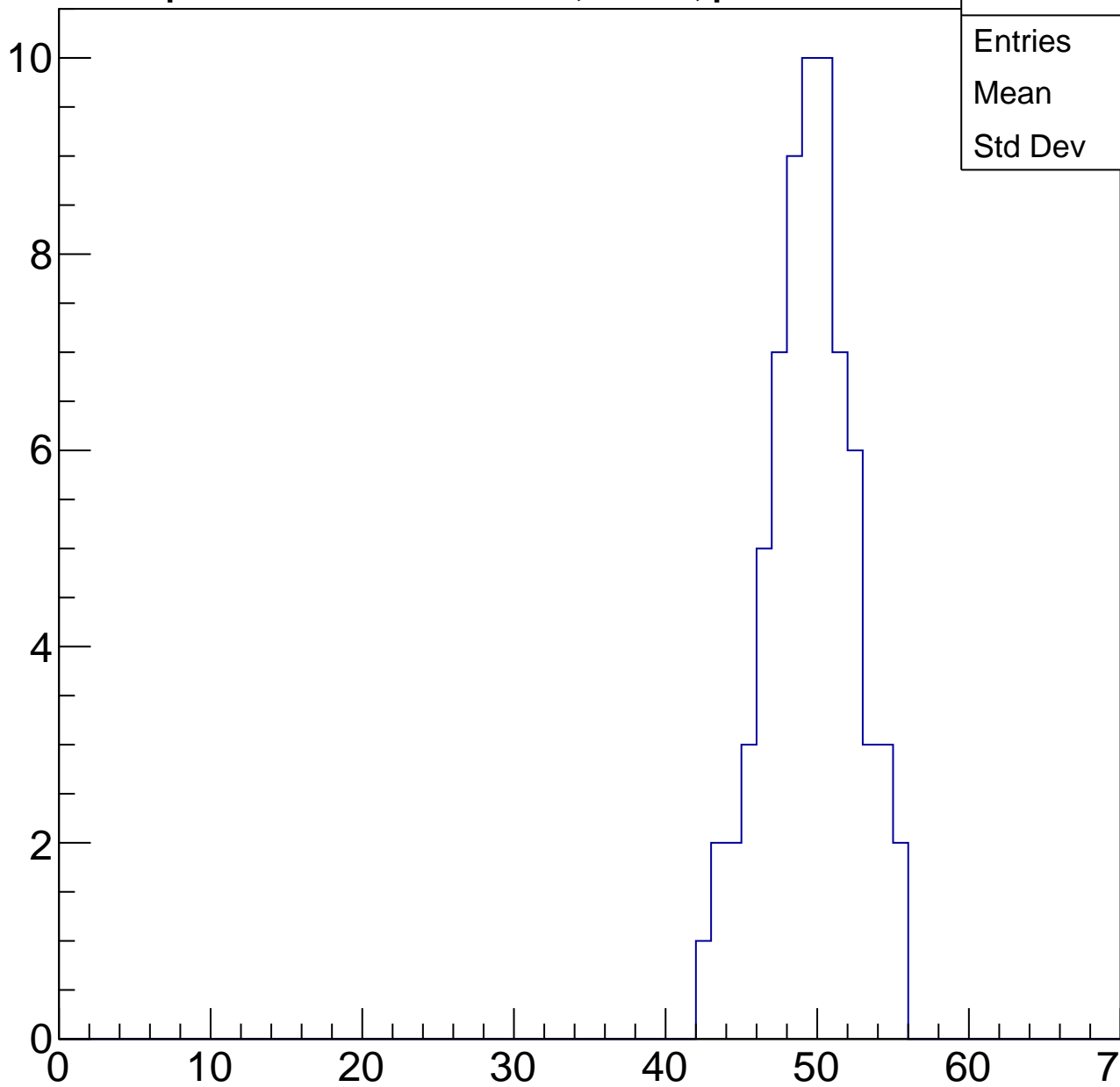
Entries	70
Mean	49.03
Std Dev	2.898

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

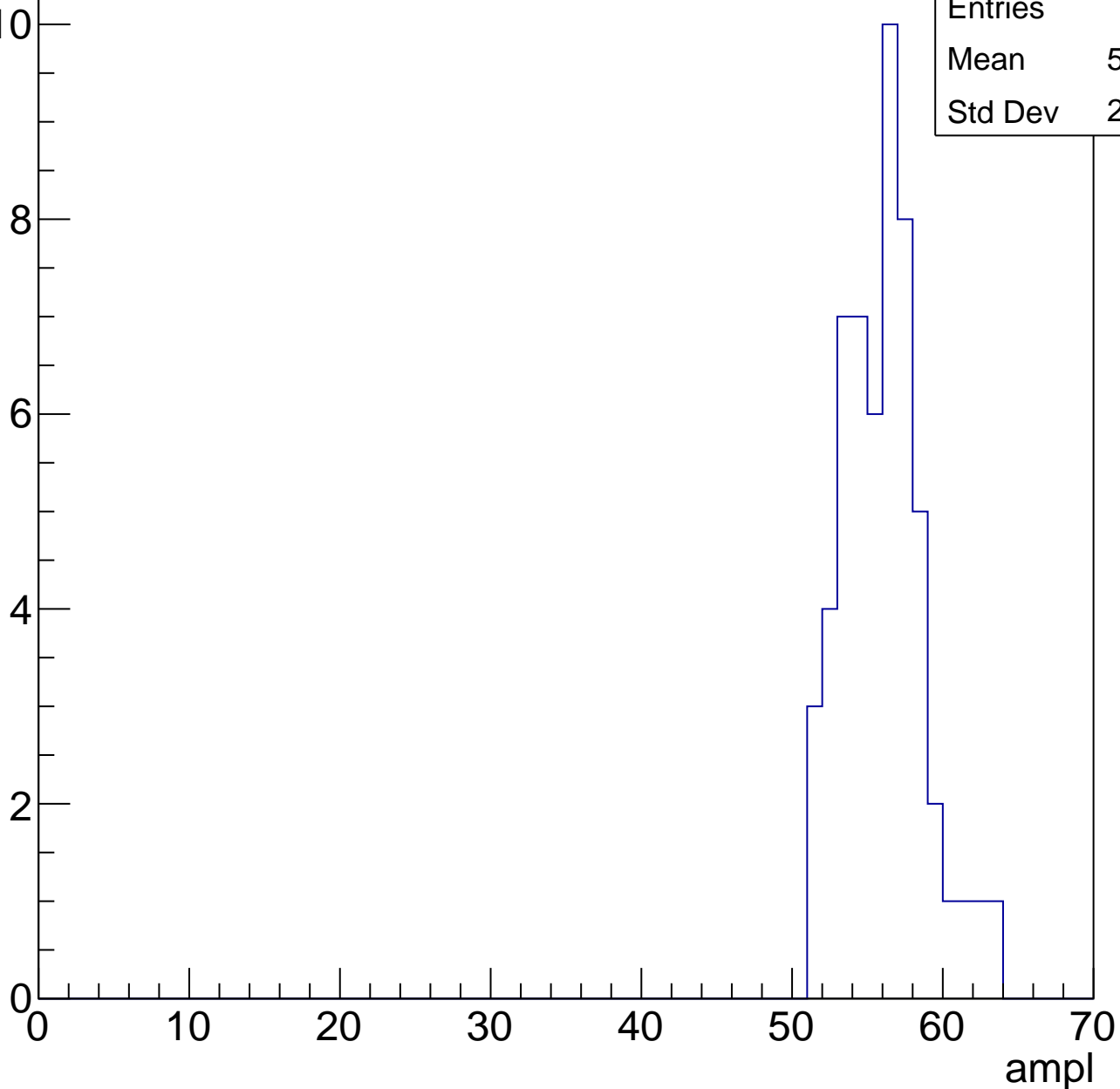


# B1L102S, U25-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.54
Std Dev	2.659



# B1L102S, U25-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	60.29
Std Dev	2.003

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

# B1L102S, U25-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	90
Mean	28.24
Std Dev	4.764

**Gaus mean : 29.0411**

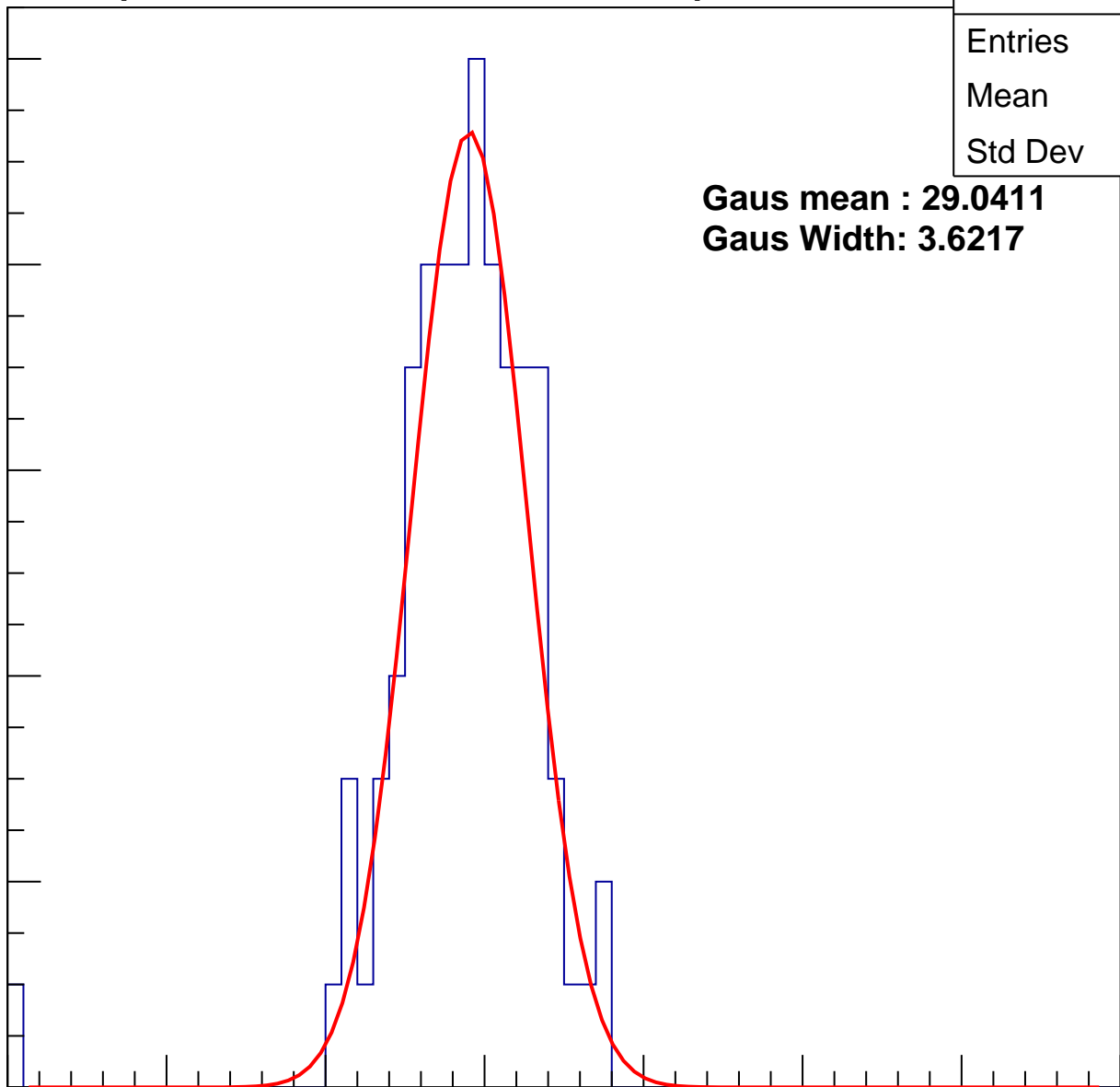
**Gaus Width: 3.6217**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch14, adc1

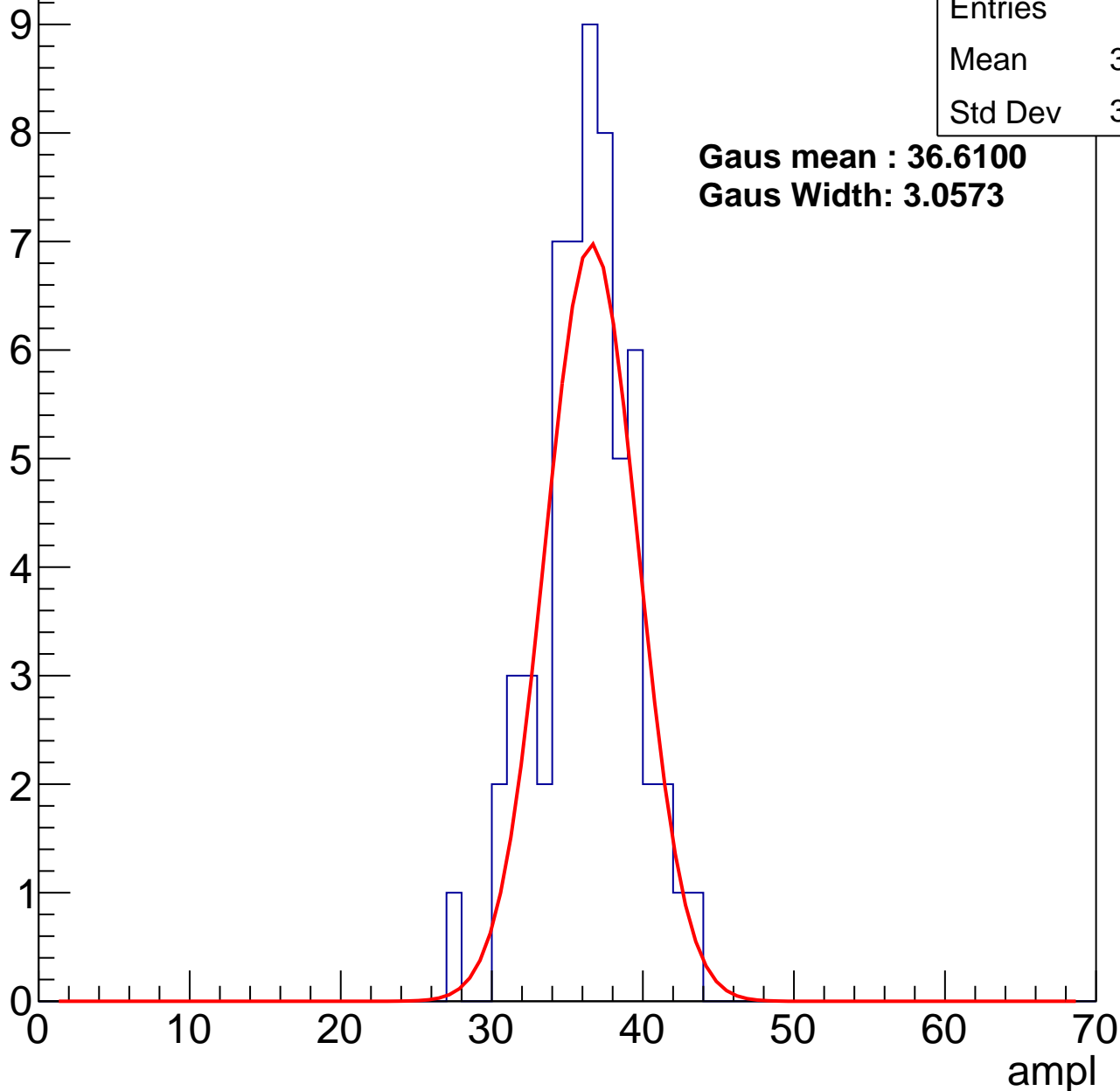
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.86
Std Dev	3.127

**Gaus mean : 36.6100**

**Gaus Width: 3.0573**



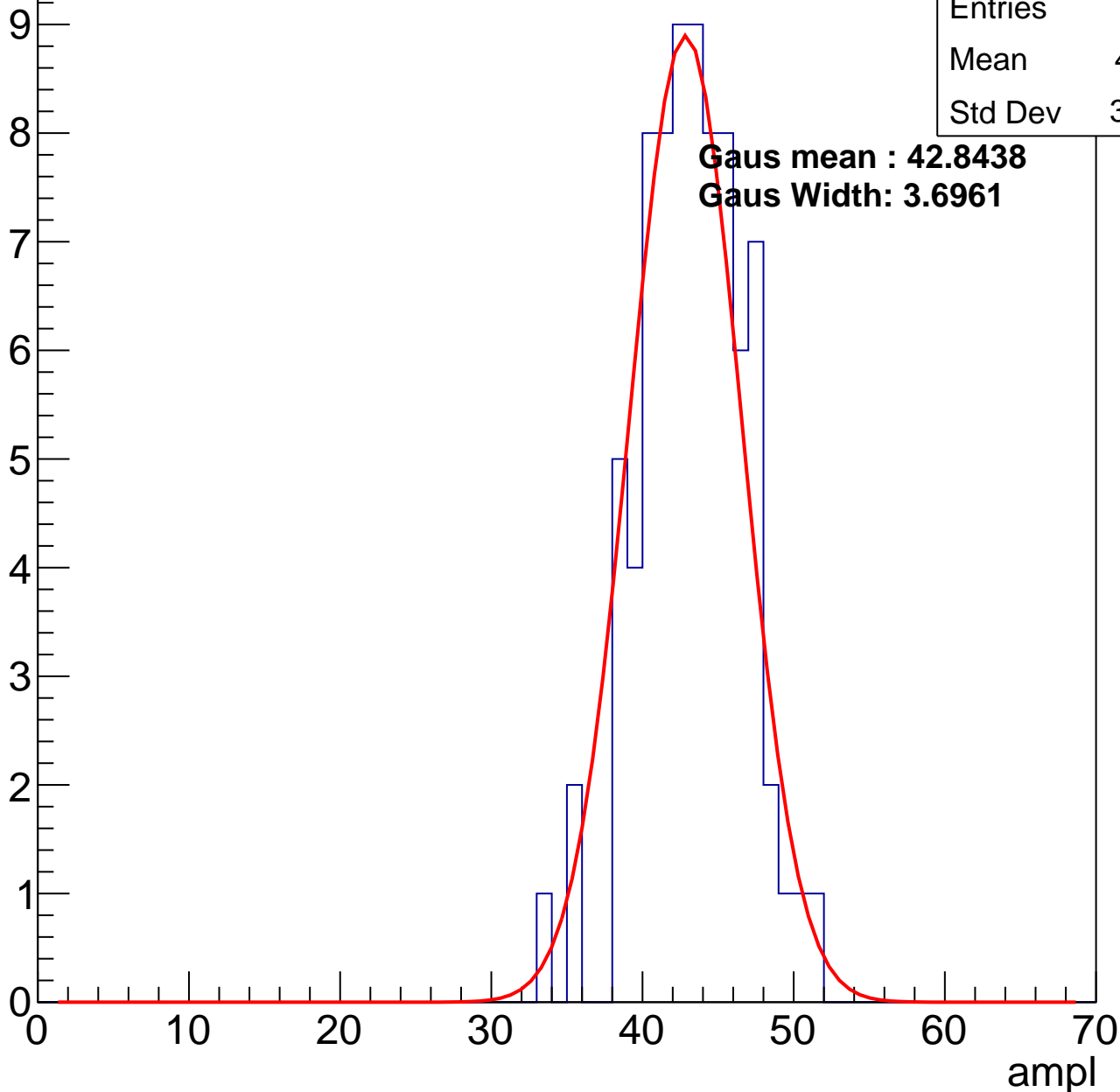
# B1L102S, U25-ch14, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	42.81
Std Dev	3.414

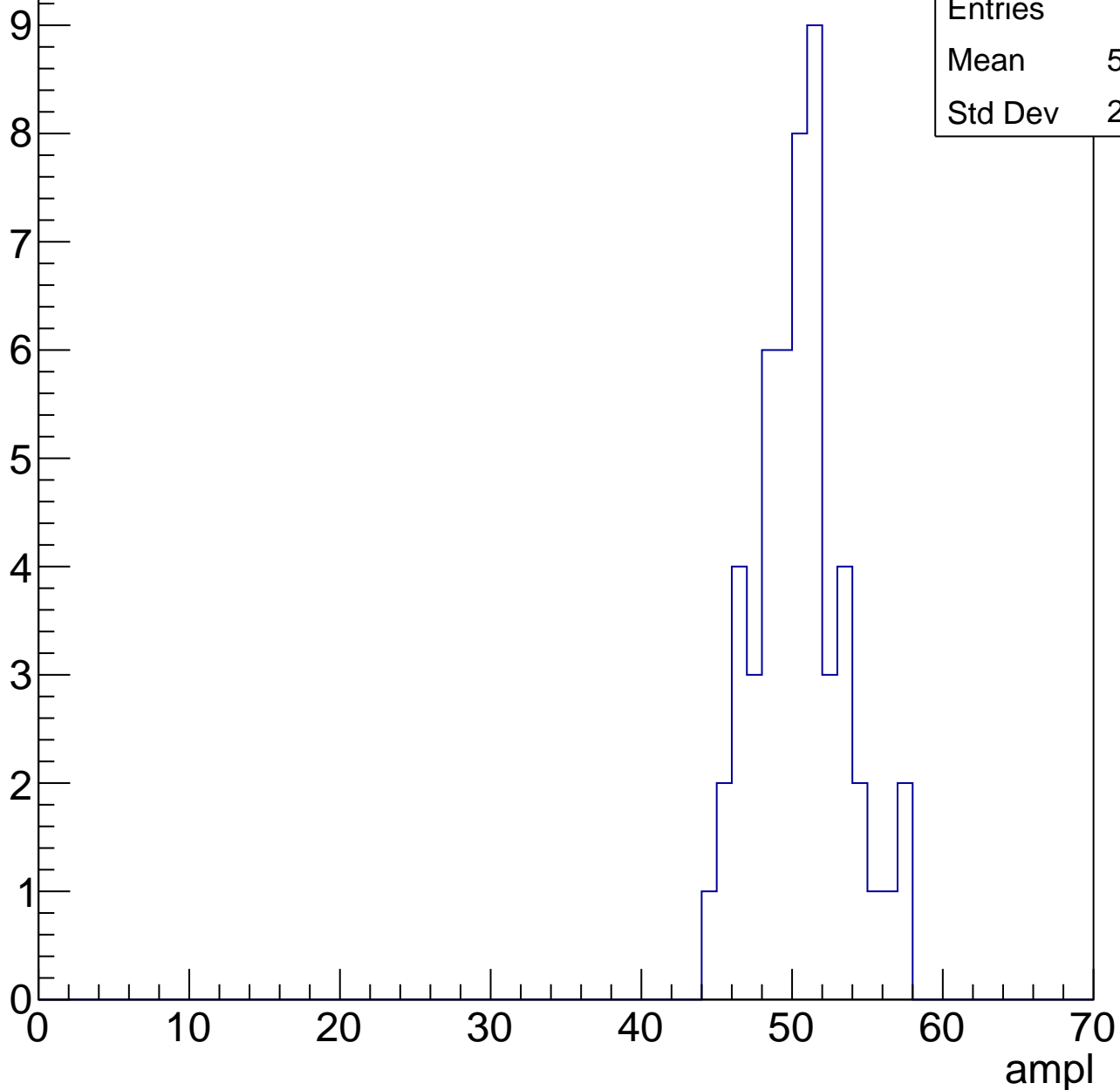
**Gaus mean : 42.8438**  
**Gaus Width: 3.6961**



# B1L102S, U25-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



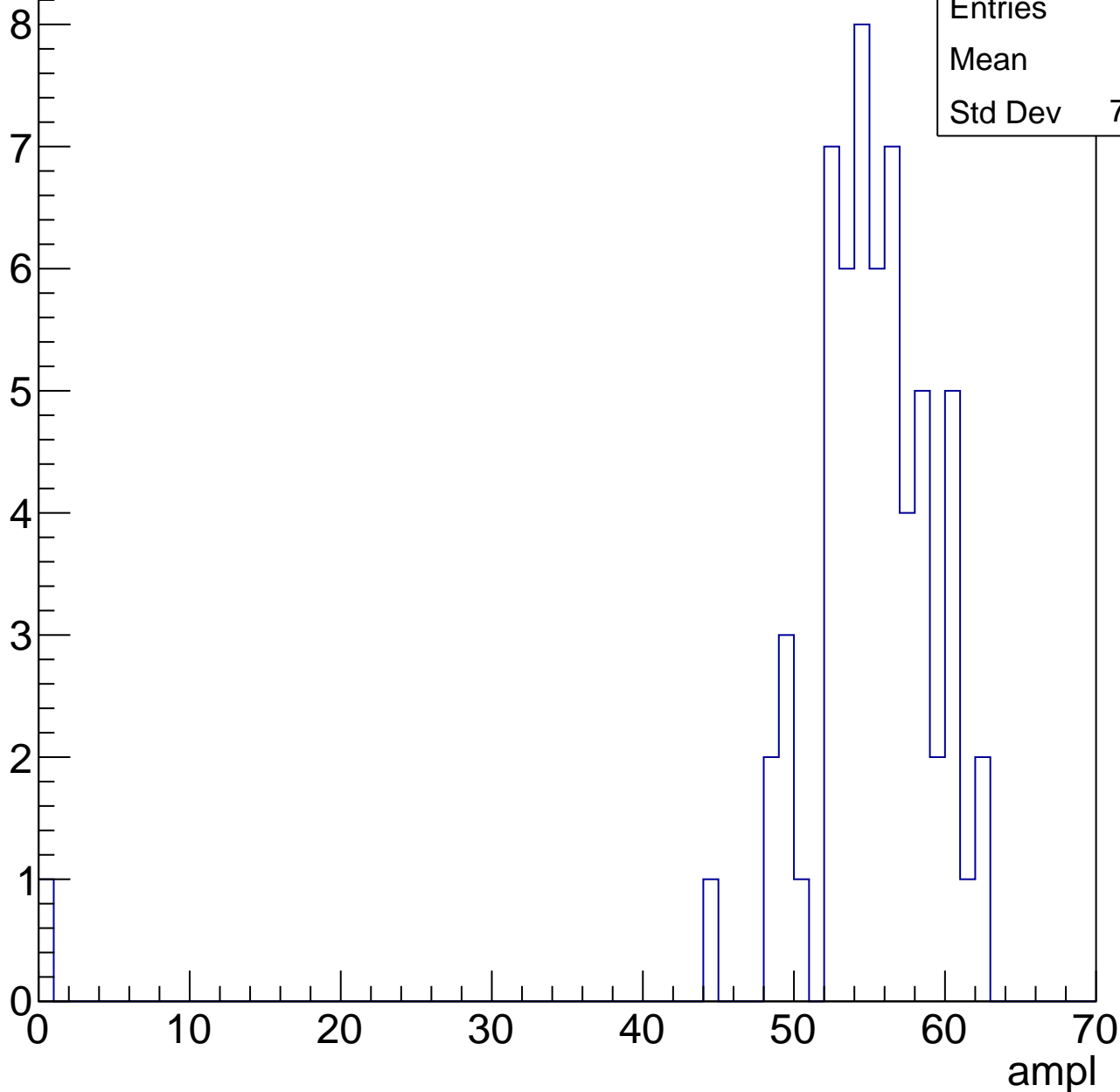
Entries	52
Mean	50.02
Std Dev	2.958

# B1L102S, U25-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54
Std Dev	7.862



# B1L102S, U25-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	59.81
Std Dev	2.36

ampl

0 10 20 30 40 50 60 70

3

4

5

6

7

8

0

1

2

3

4

5

6

7

8

# B1L102S, U25-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch15, adc0

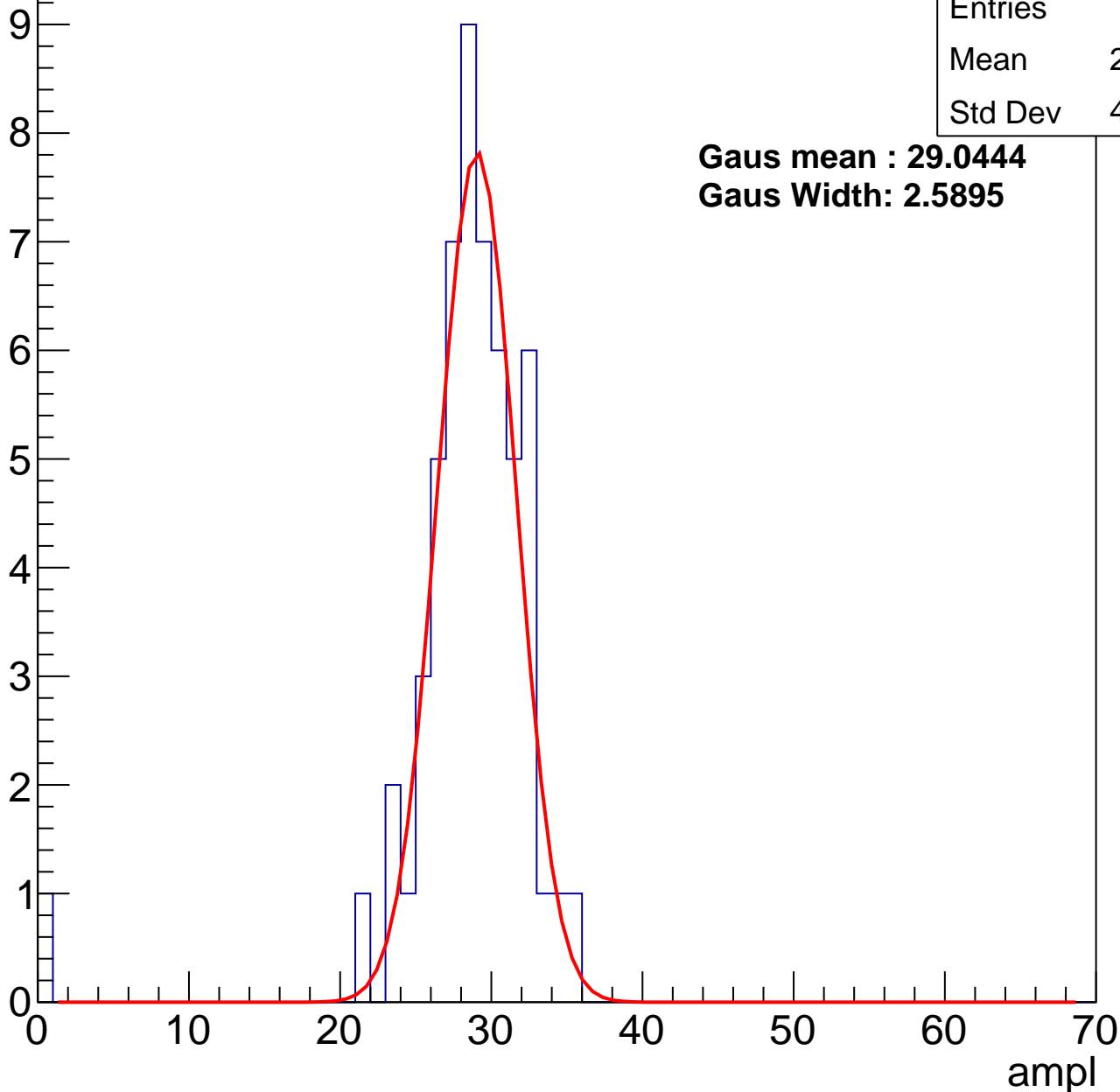
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.02
Std Dev	4.696

**Gaus mean : 29.0444**

**Gaus Width: 2.5895**



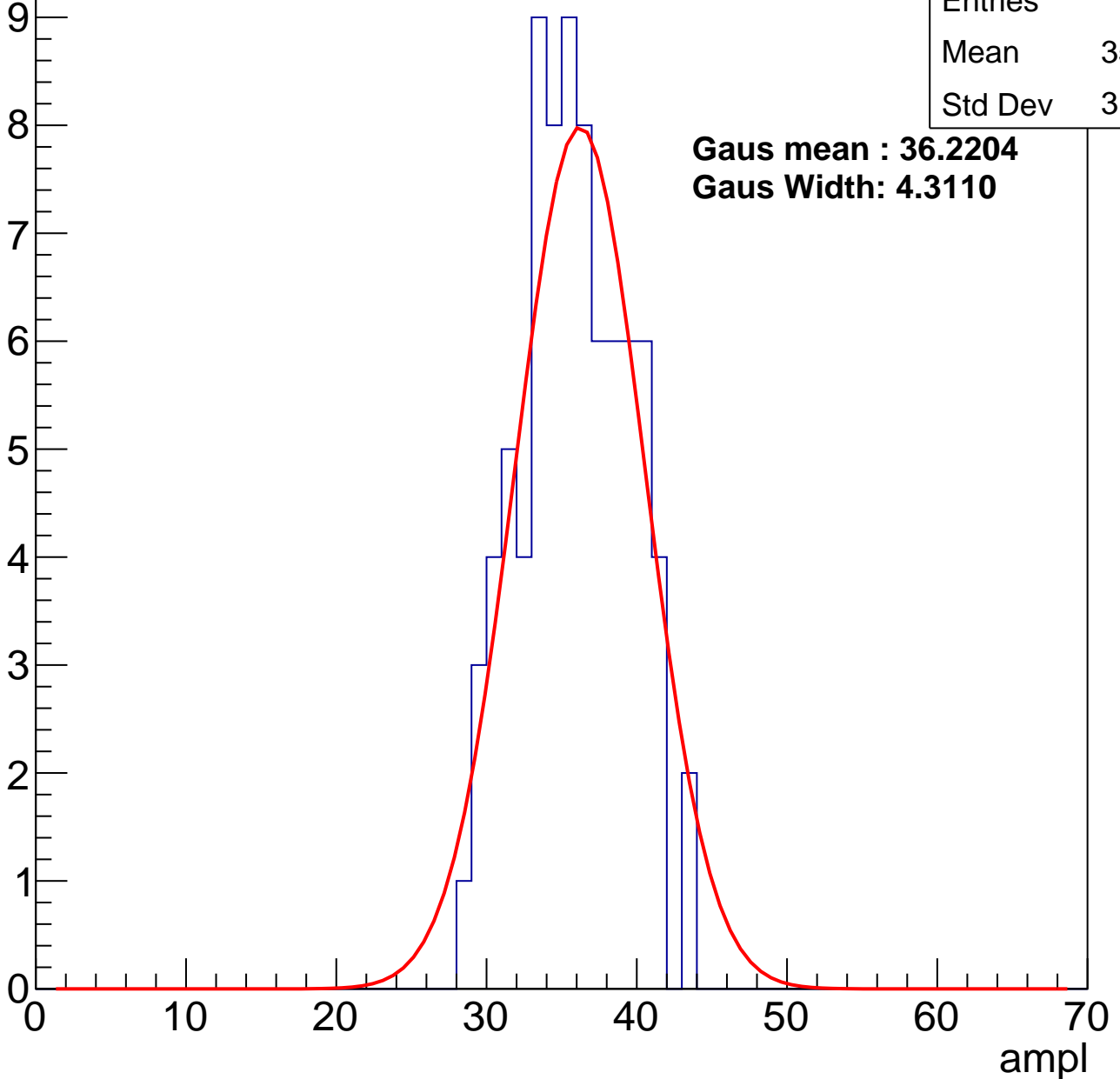
# B1L102S, U25-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.36
Std Dev	3.532

**Gaus mean : 36.2204**  
**Gaus Width: 4.3110**



# B1L102S, U25-ch15, adc2

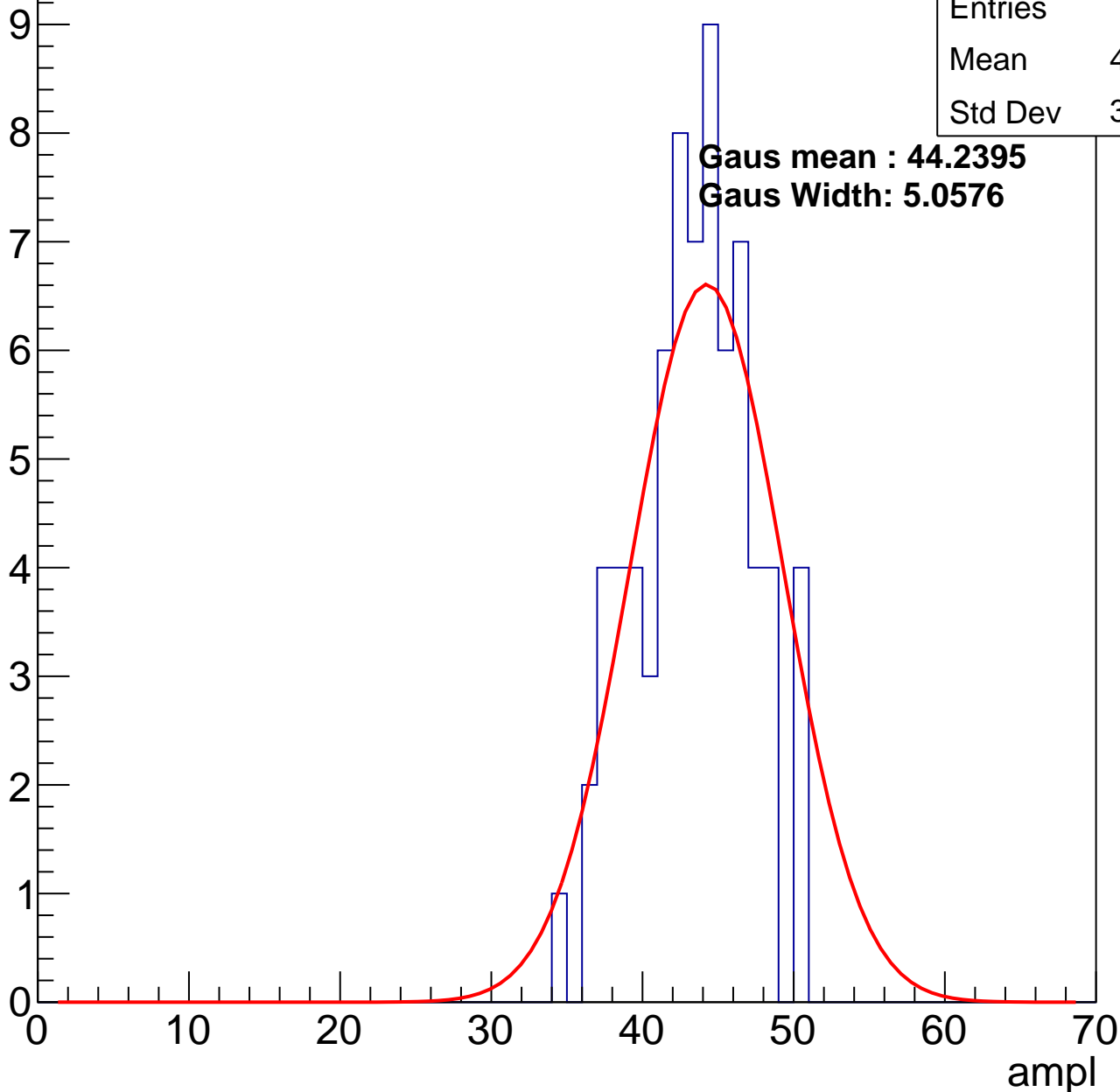
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.92
Std Dev	3.704

**Gaus mean : 44.2395**

**Gaus Width: 5.0576**

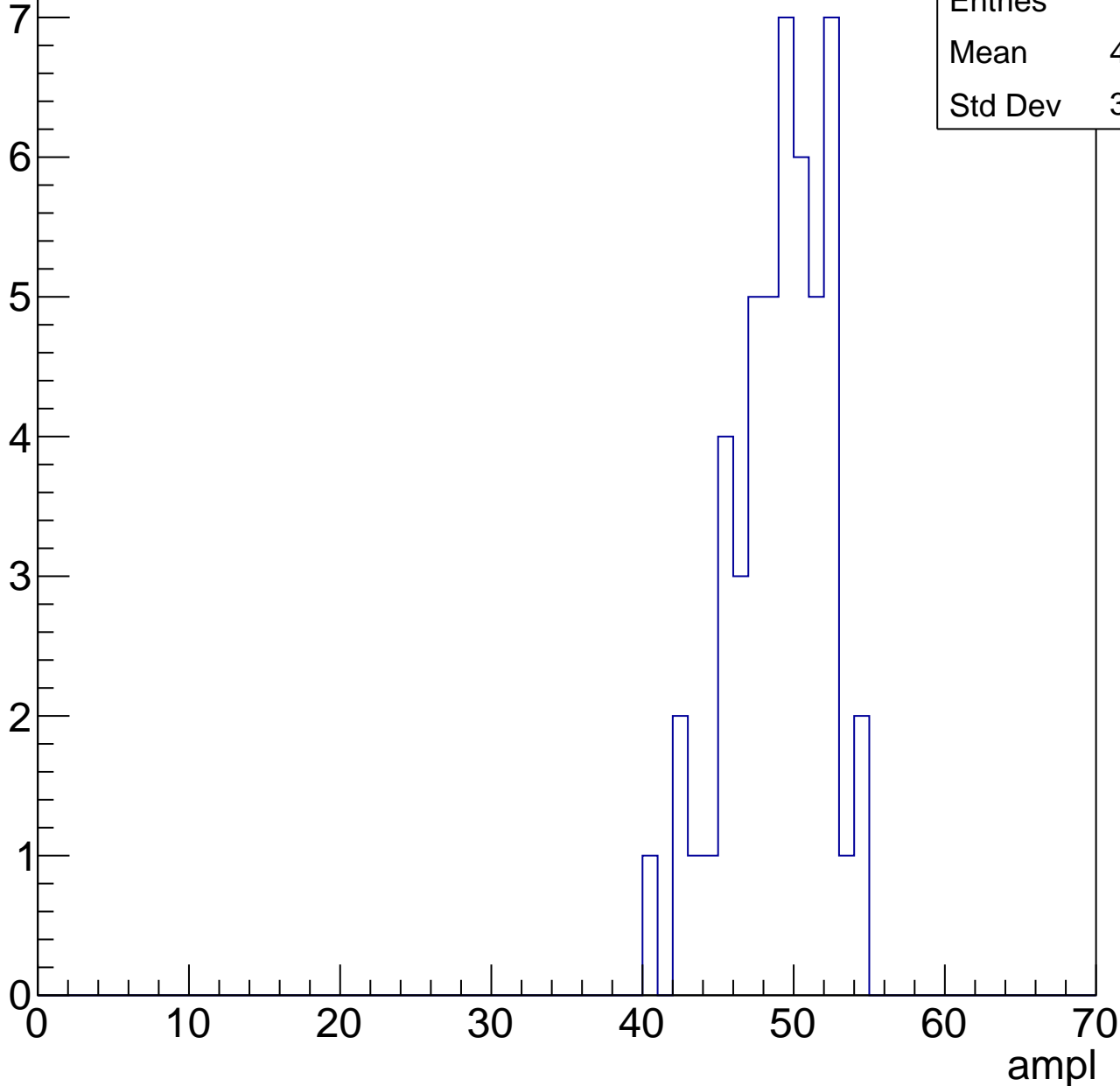


# B1L102S, U25-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.54
Std Dev	3.164



# B1L102S, U25-ch15, adc4

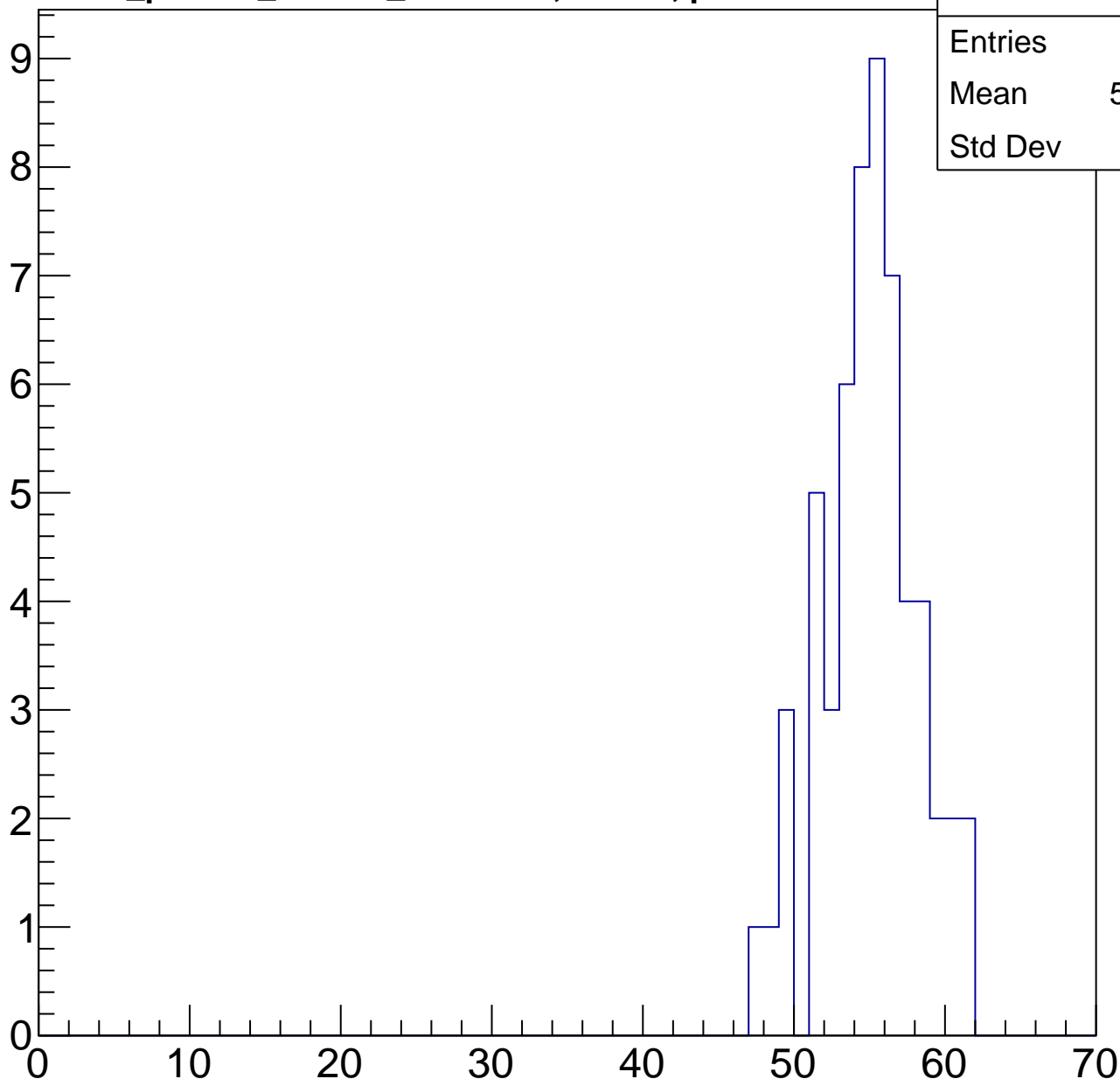
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	54.56
Std Dev	3.14

ampl

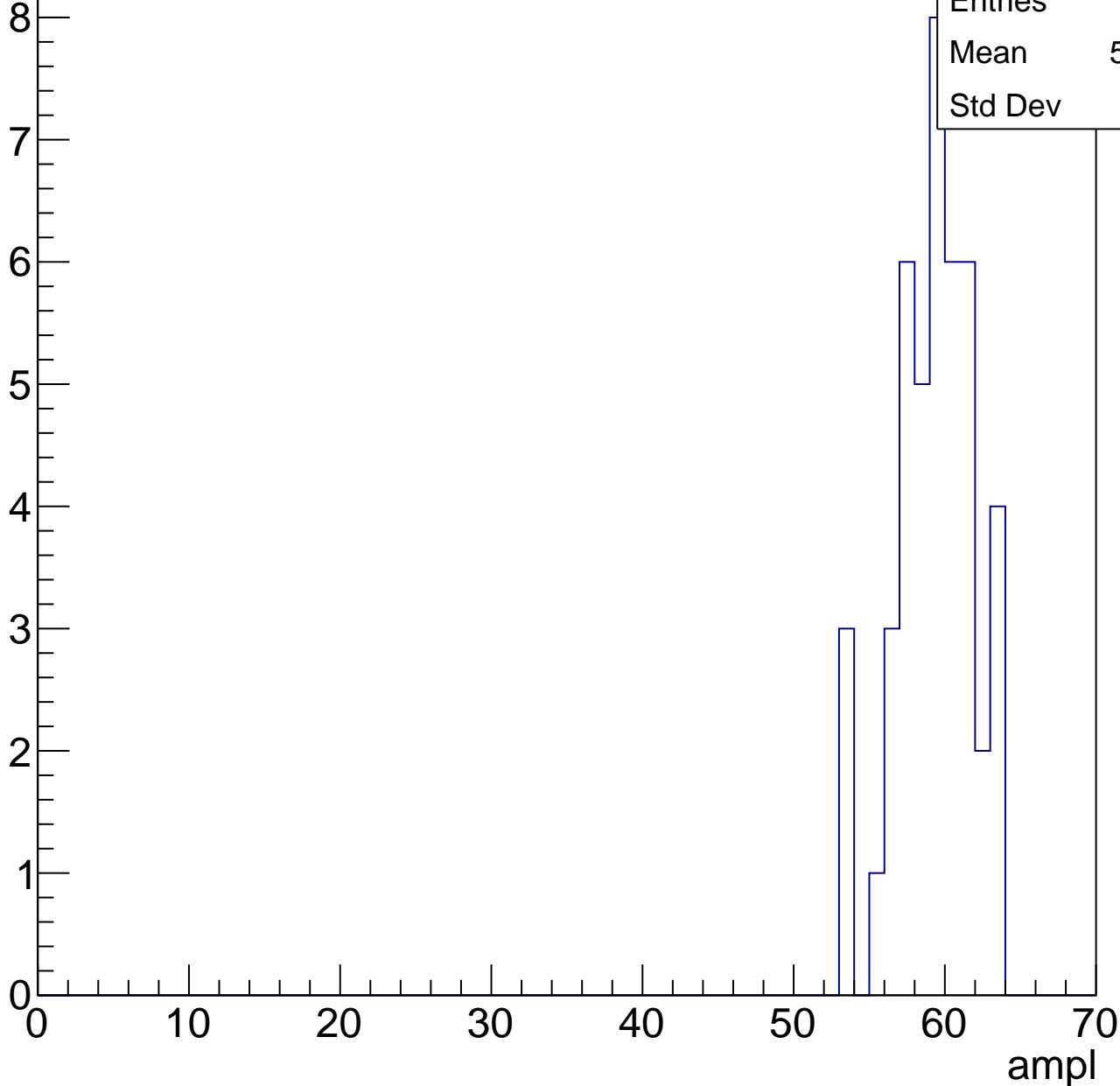


# B1L102S, U25-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.82
Std Dev	2.57

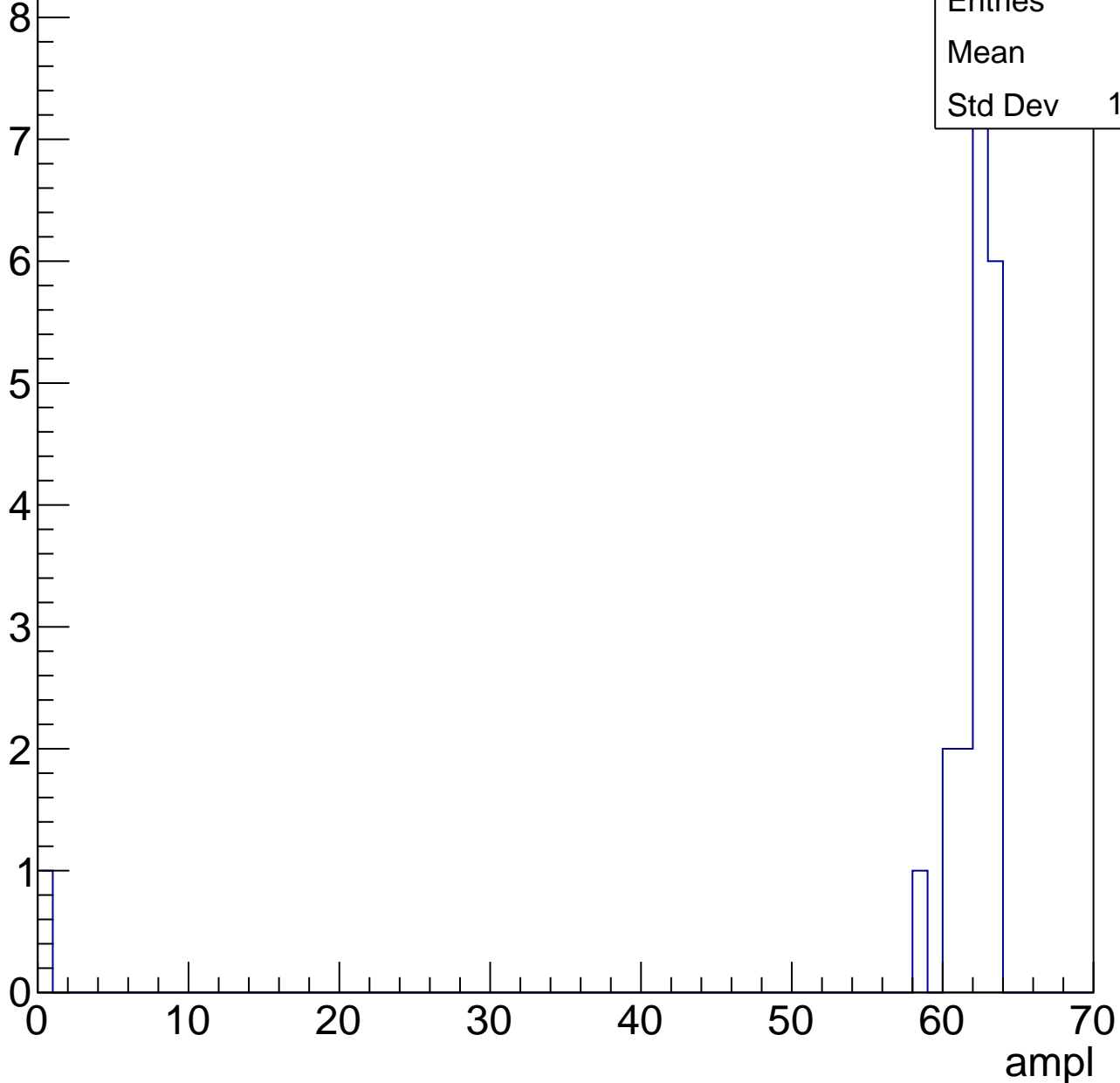


# B1L102S, U25-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	20
Mean	58.7
Std Dev	13.52





# B1L102S, U25-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch16, adc0

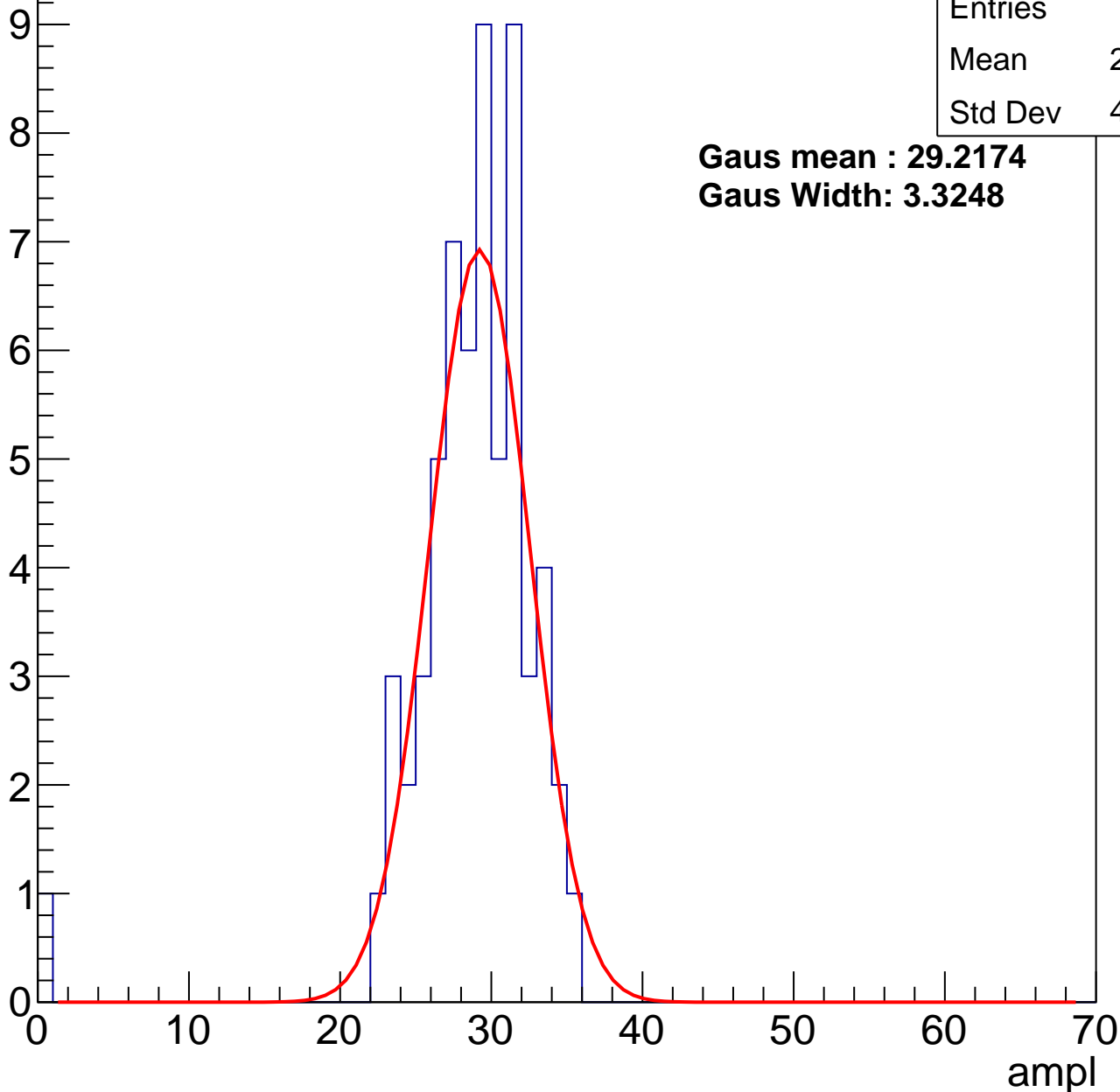
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.23
Std Dev	4.716

**Gaus mean : 29.2174**

**Gaus Width: 3.3248**



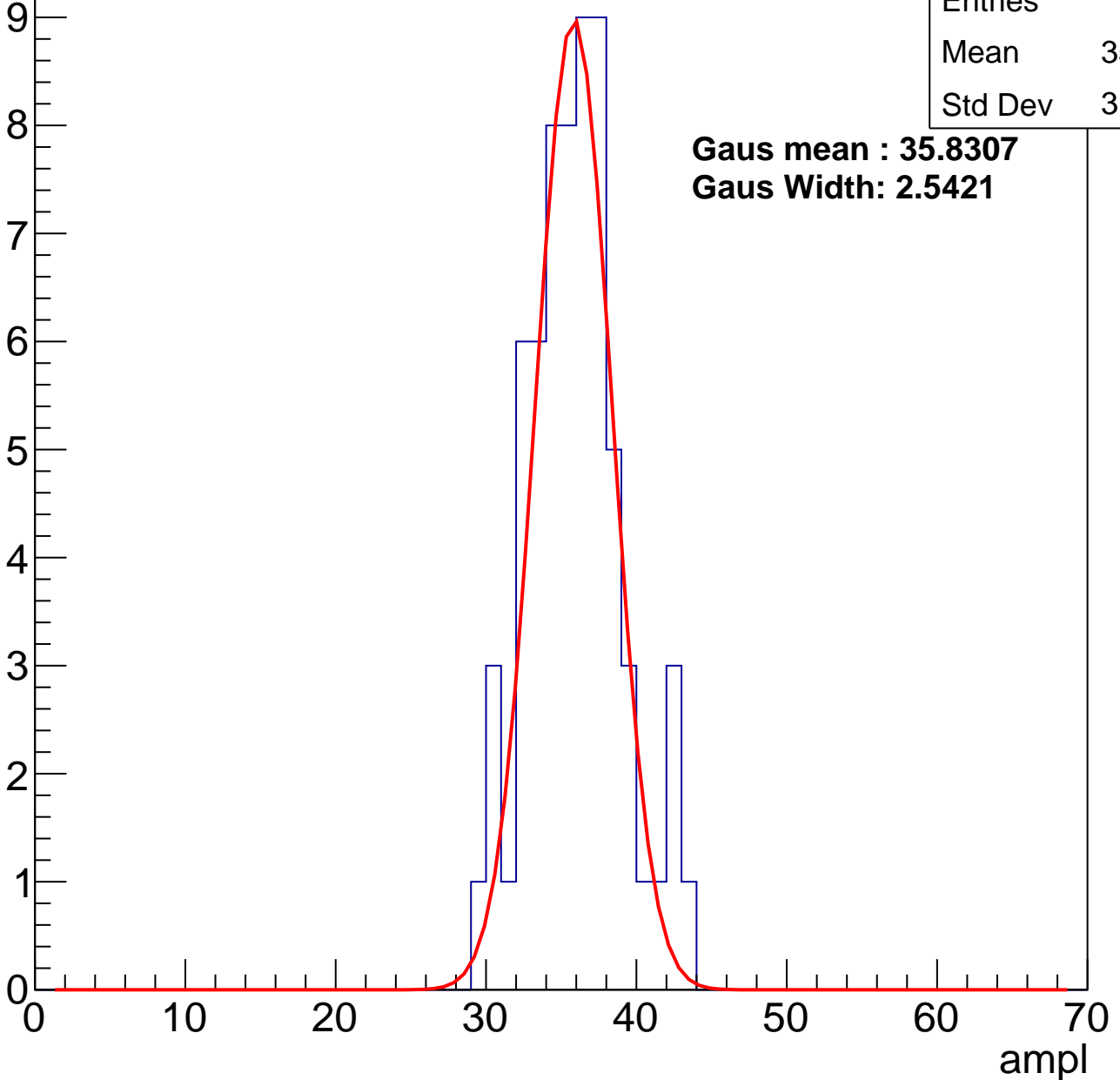
# B1L102S, U25-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.48
Std Dev	3.059

**Gaus mean : 35.8307**  
**Gaus Width: 2.5421**



# B1L102S, U25-ch16, adc2

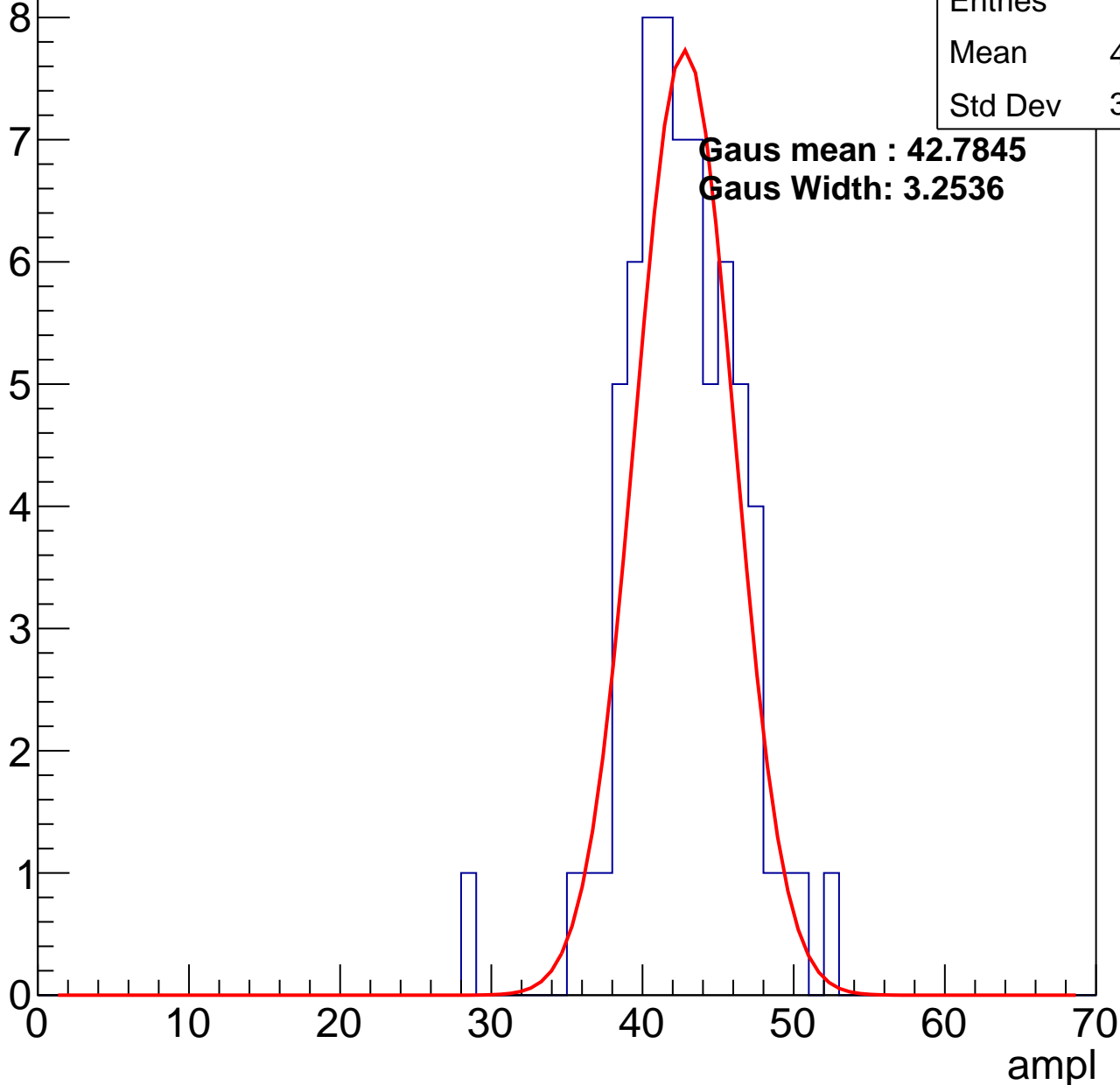
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.17
Std Dev	3.784

**Gaus mean : 42.7845**

**Gaus Width: 3.2536**

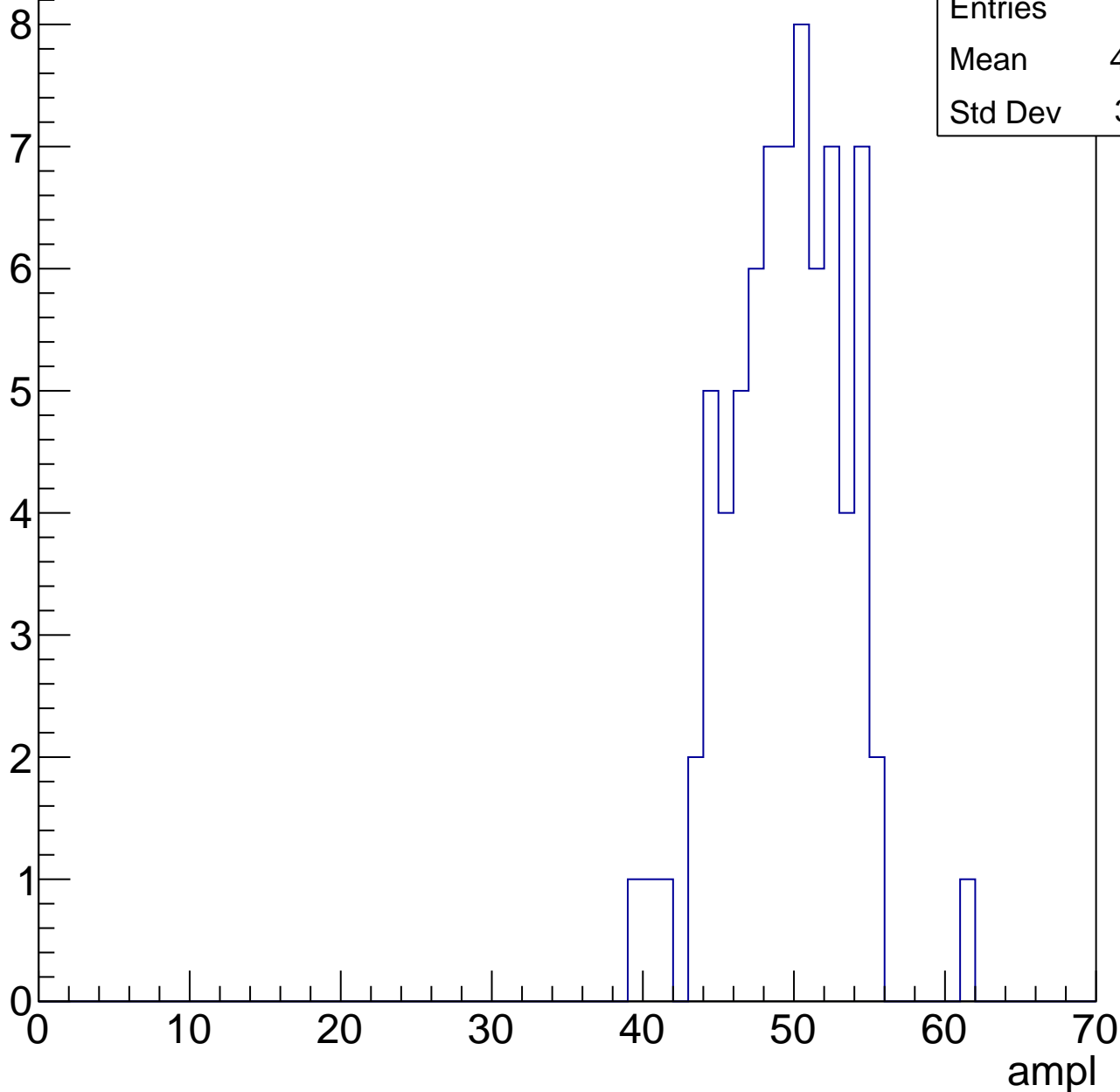


# B1L102S, U25-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	49.03
Std Dev	3.911

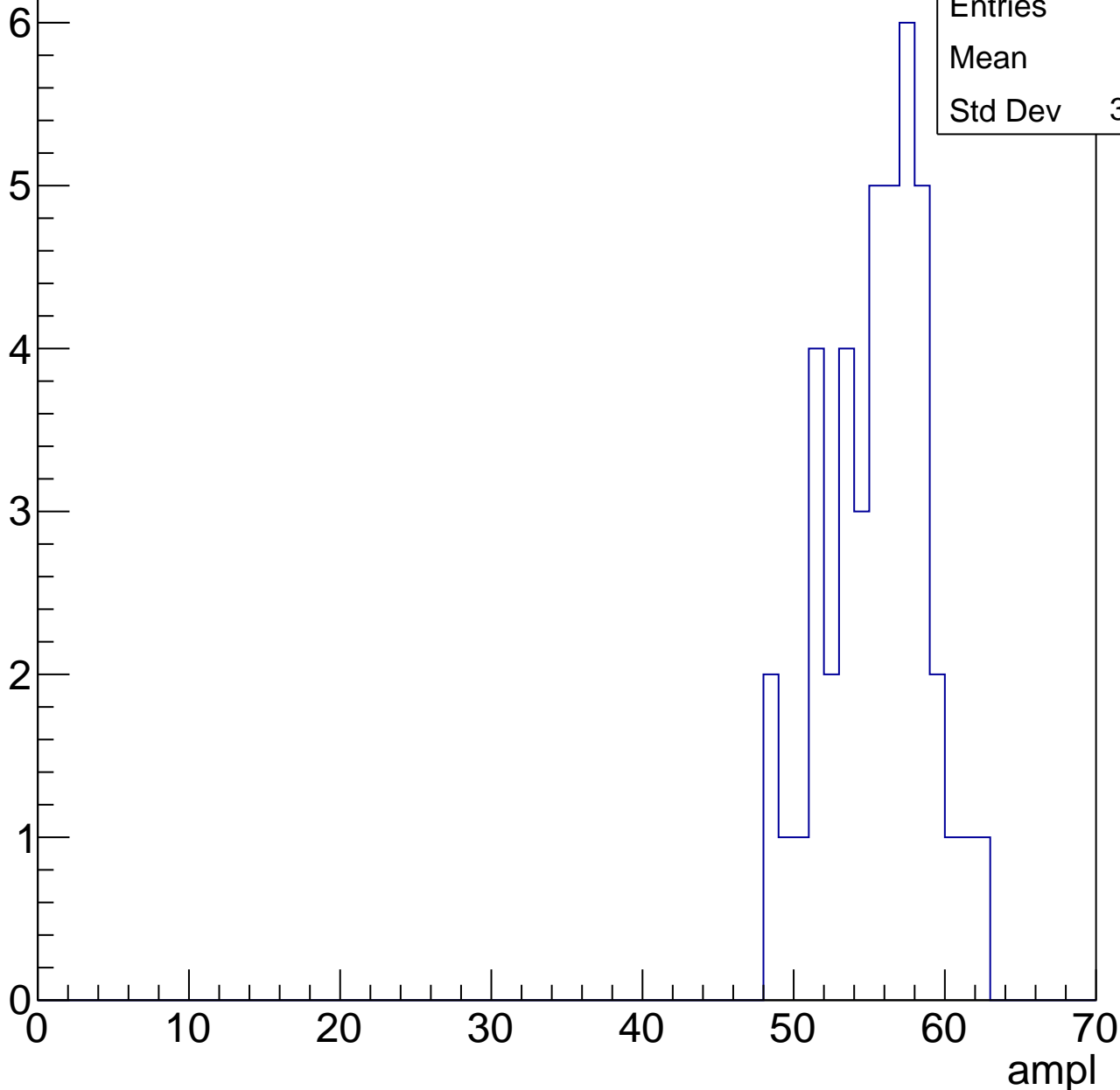


# B1L102S, U25-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55
Std Dev	3.327

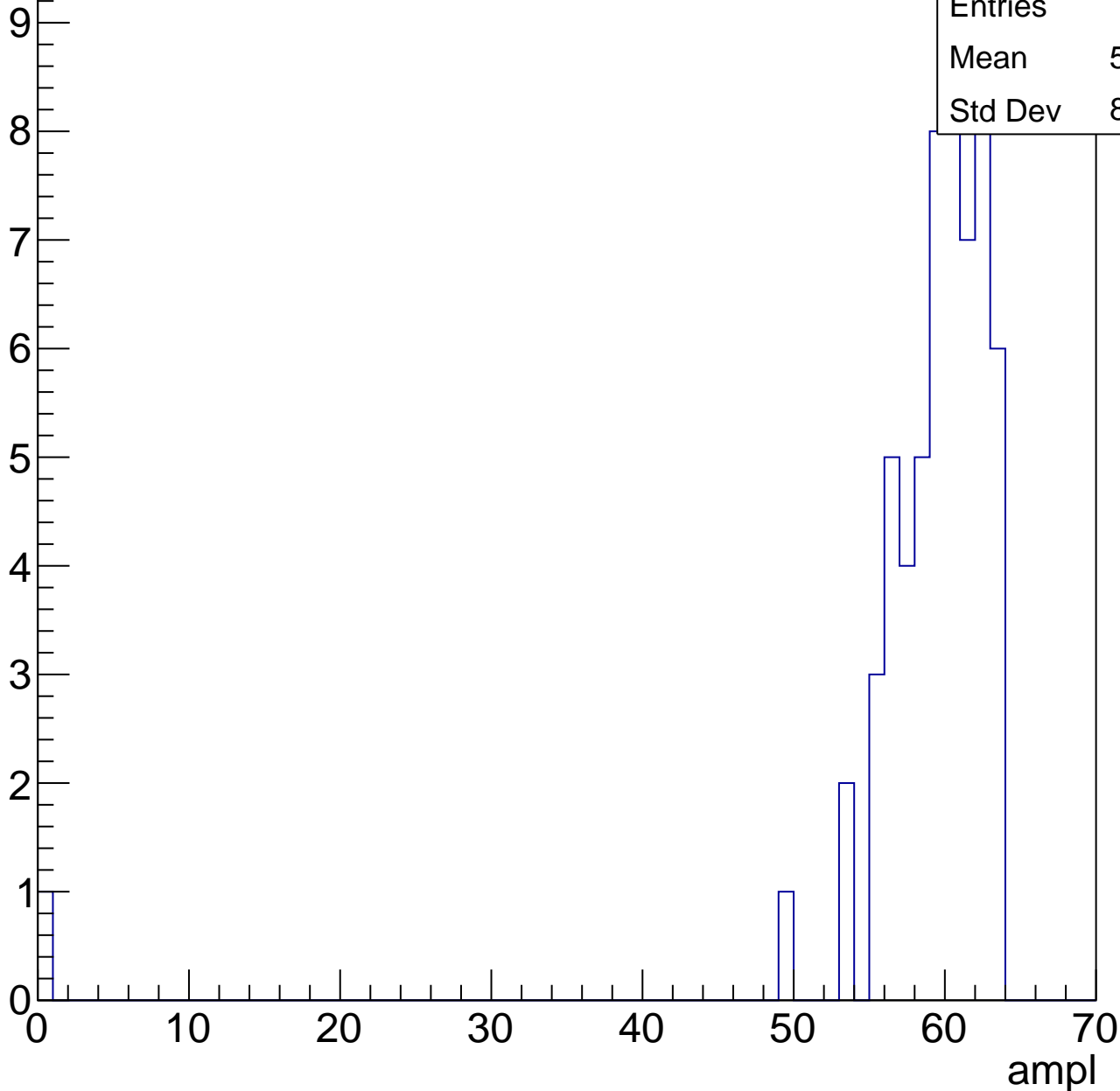


# B1L102S, U25-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

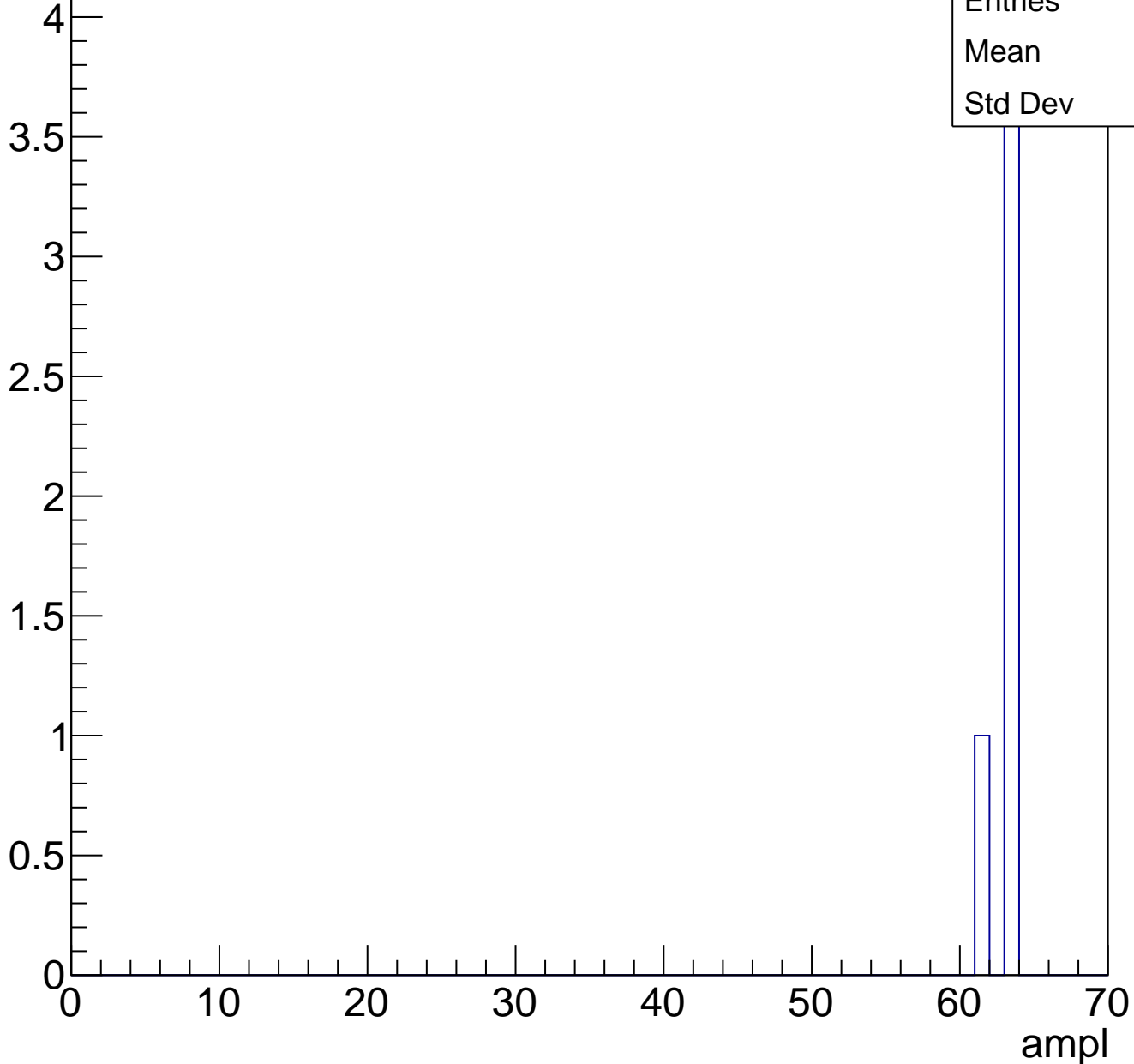
Entries	60
Mean	58.22
Std Dev	8.108



# B1L102S, U25-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

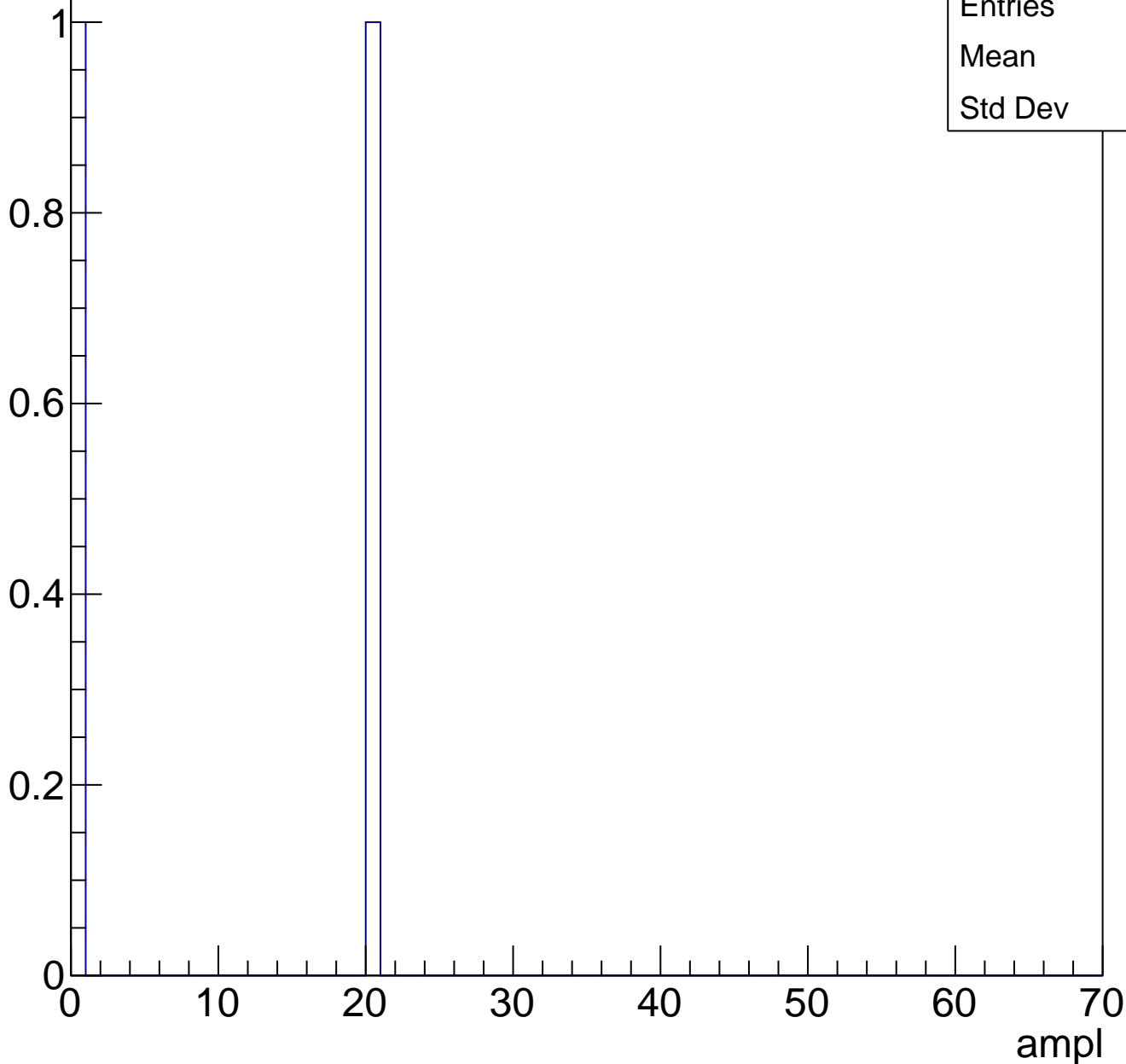




# B1L102S, U25-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch17, adc0

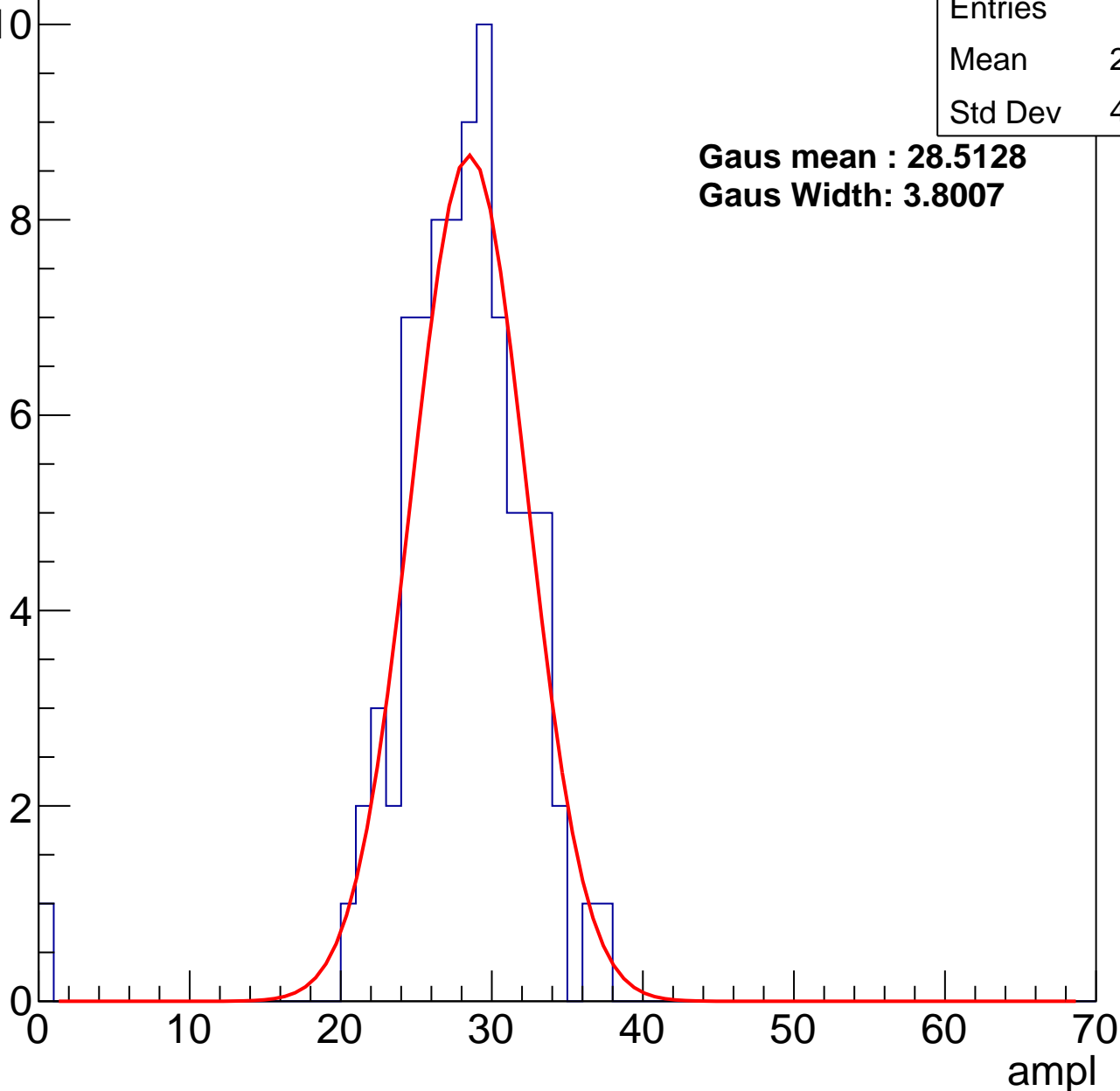
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.55
Std Dev	4.638

**Gaus mean : 28.5128**

**Gaus Width: 3.8007**



# B1L102S, U25-ch17, adc1

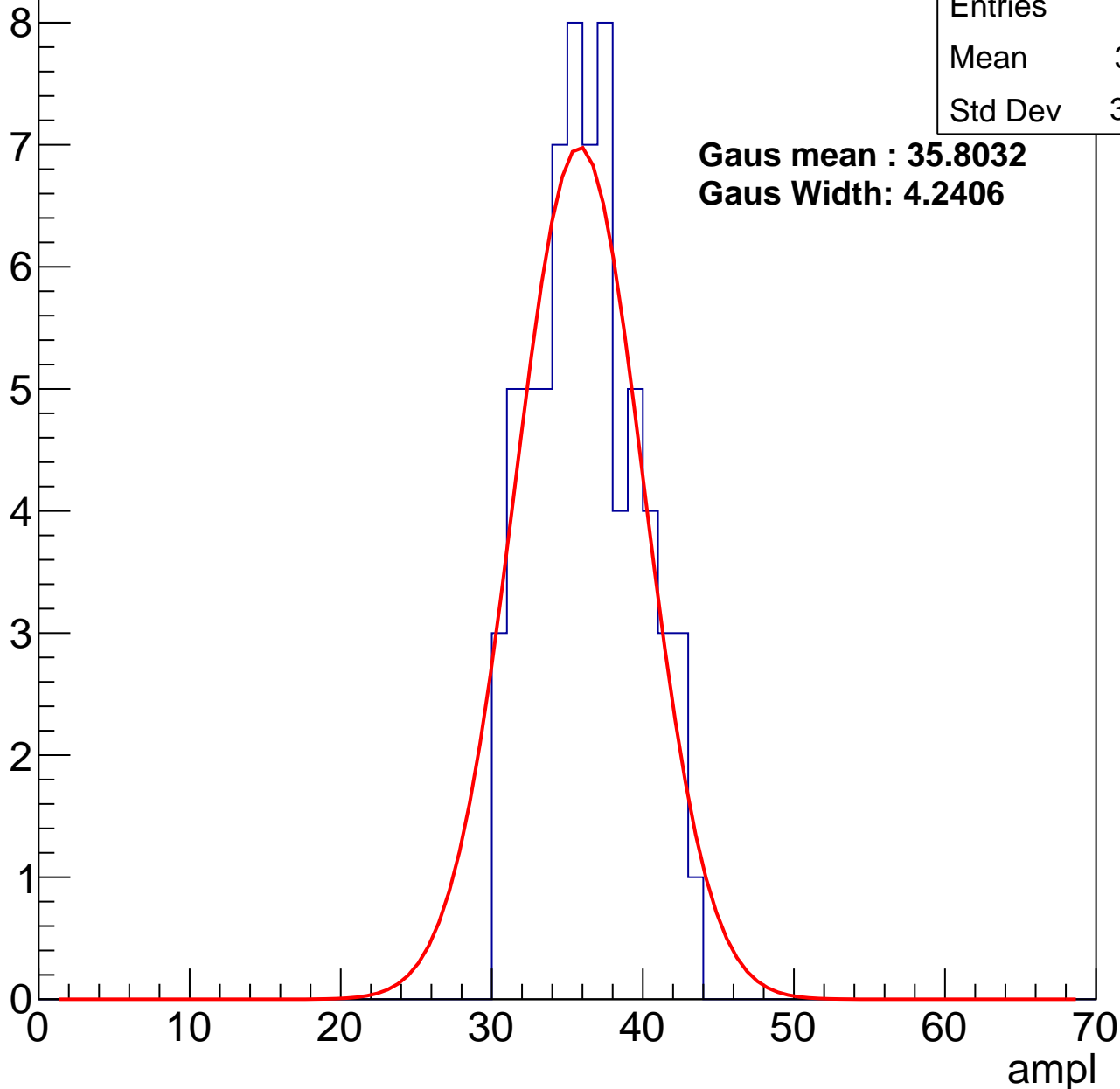
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.81
Std Dev	3.335

**Gaus mean : 35.8032**

**Gaus Width: 4.2406**



# B1L102S, U25-ch17, adc2

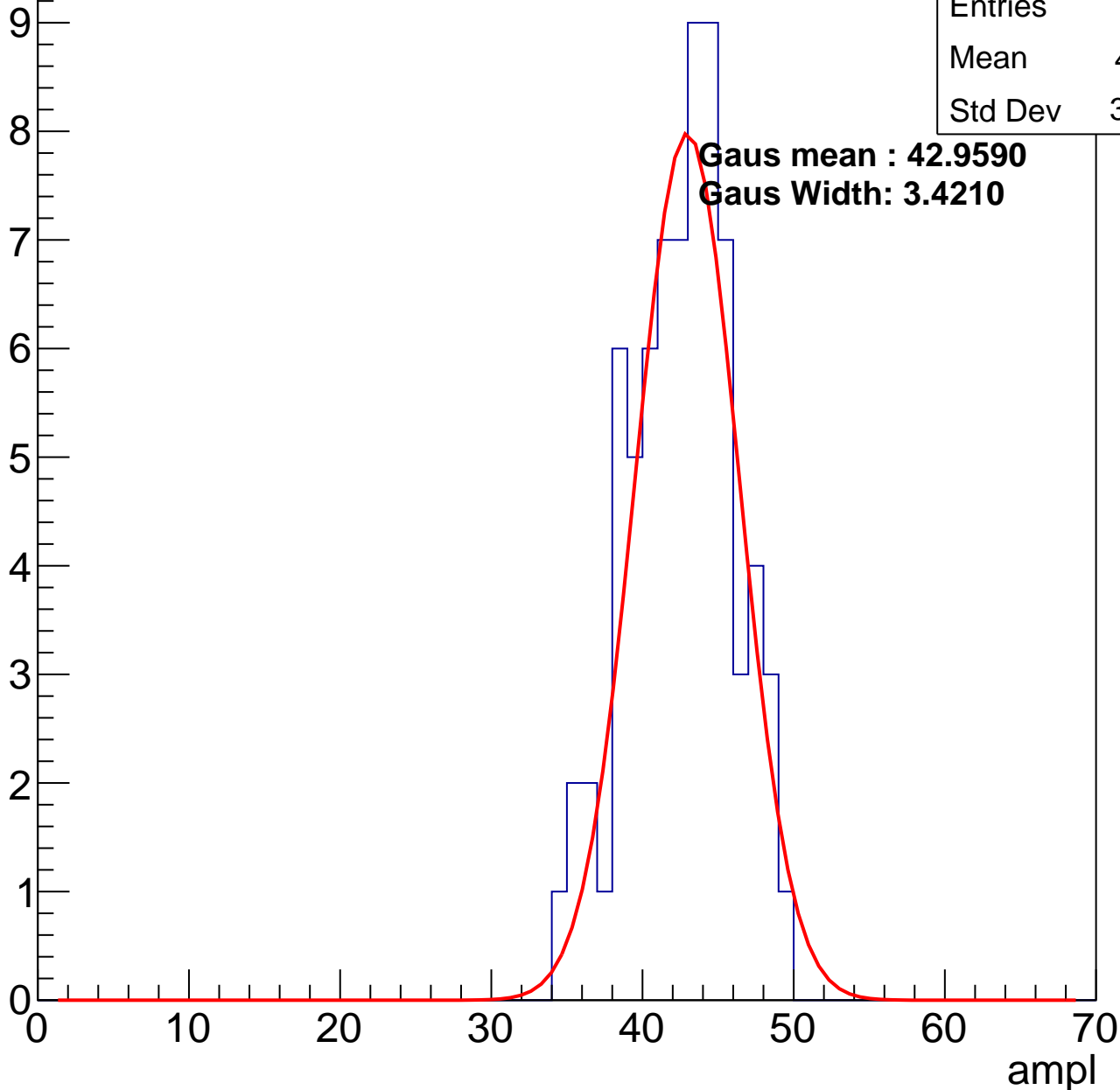
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.11
Std Dev	3.398

**Gaus mean : 42.9590**

**Gaus Width: 3.4210**



# B1L102S, U25-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

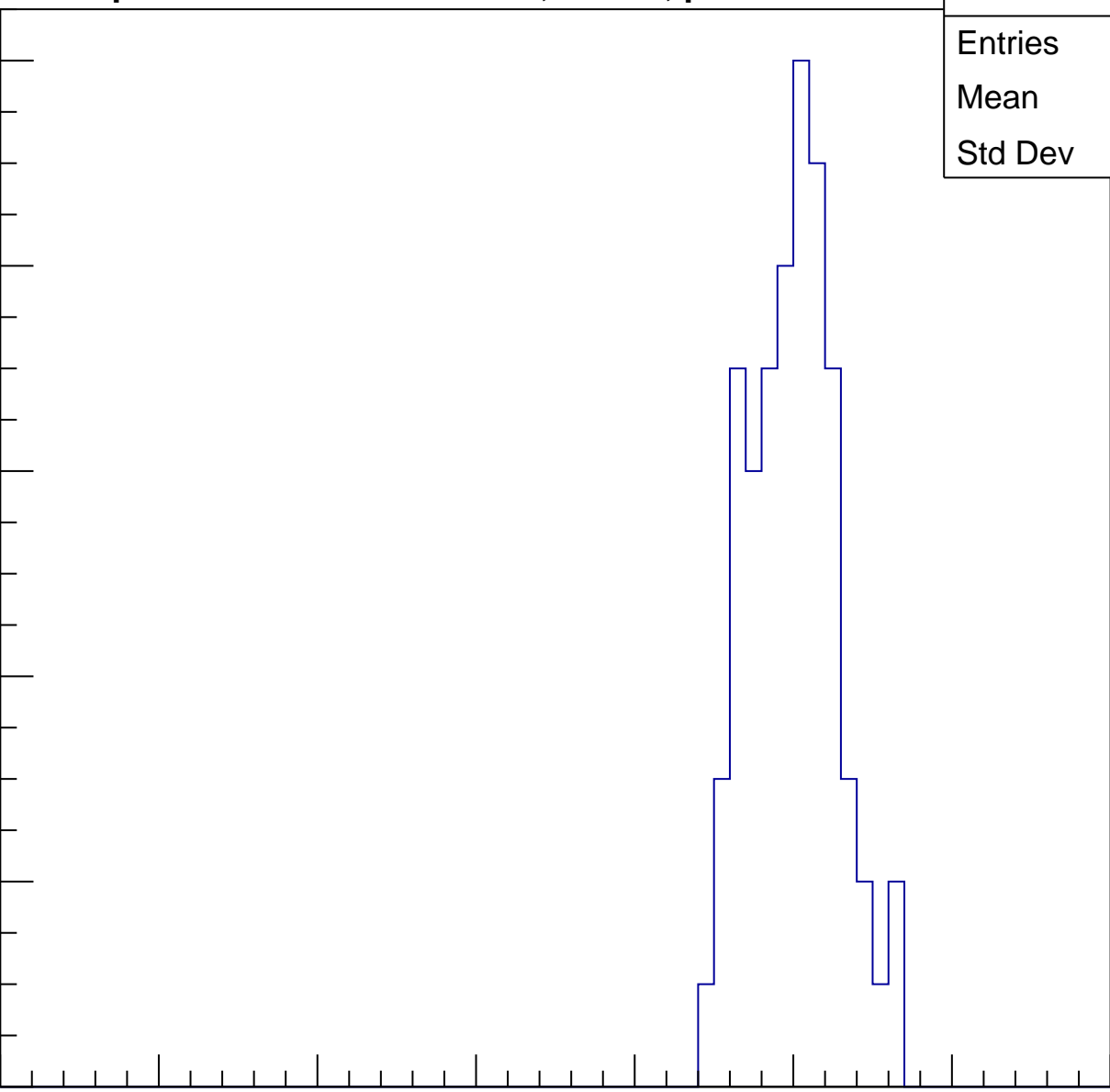
Entries	66
Mean	49.52
Std Dev	2.726

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

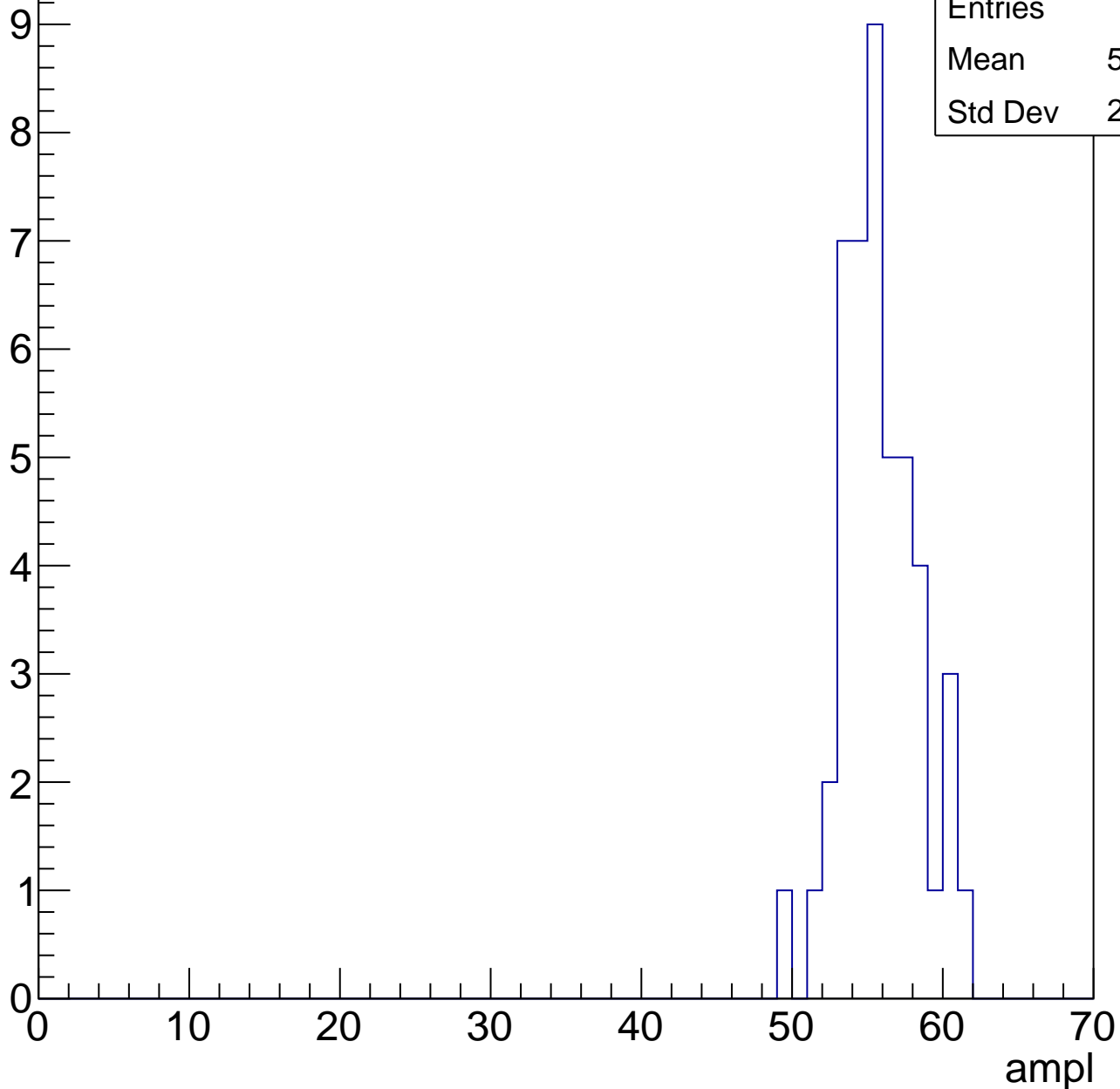
ampl



# B1L102S, U25-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

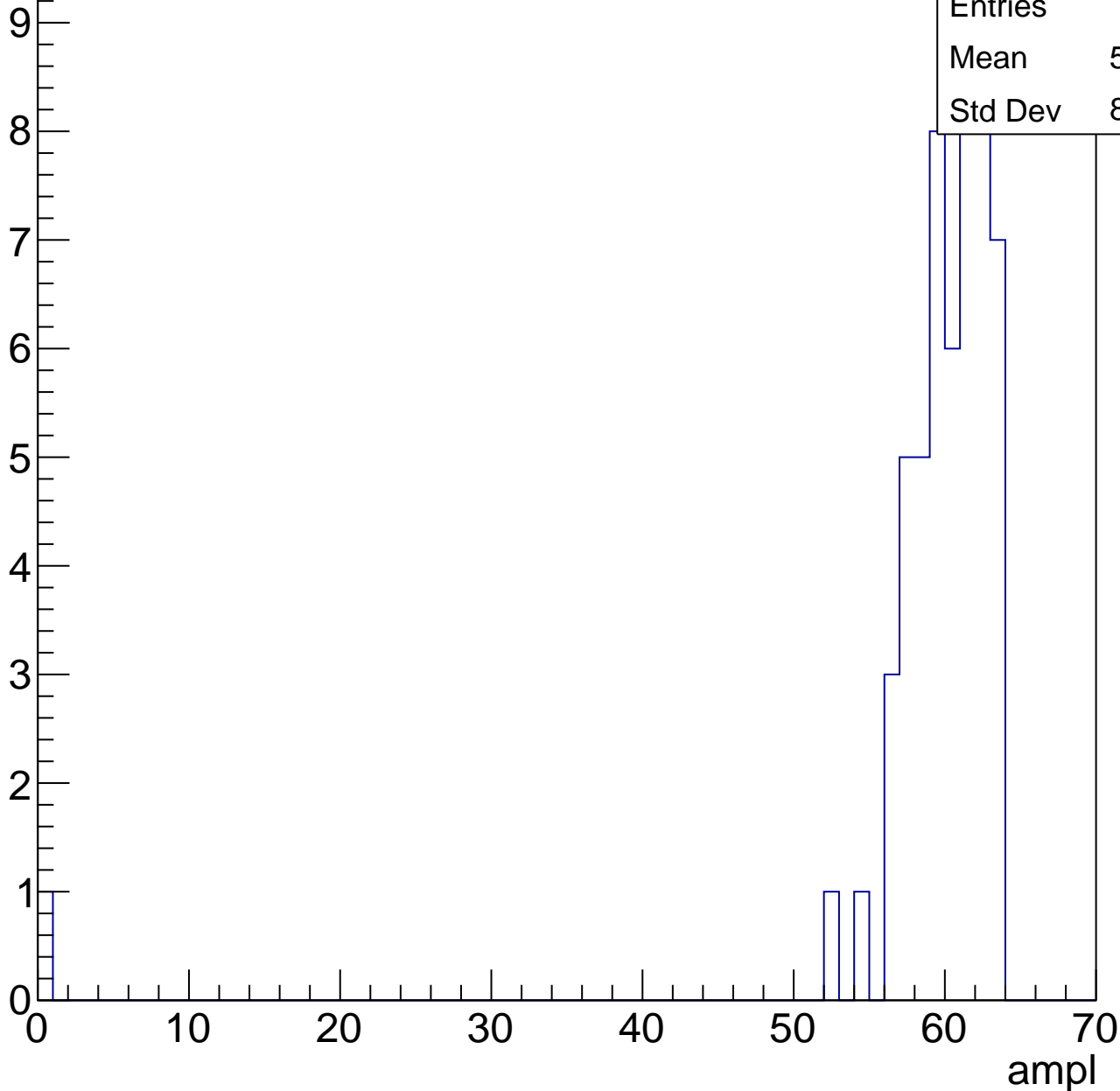


# B1L102S, U25-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

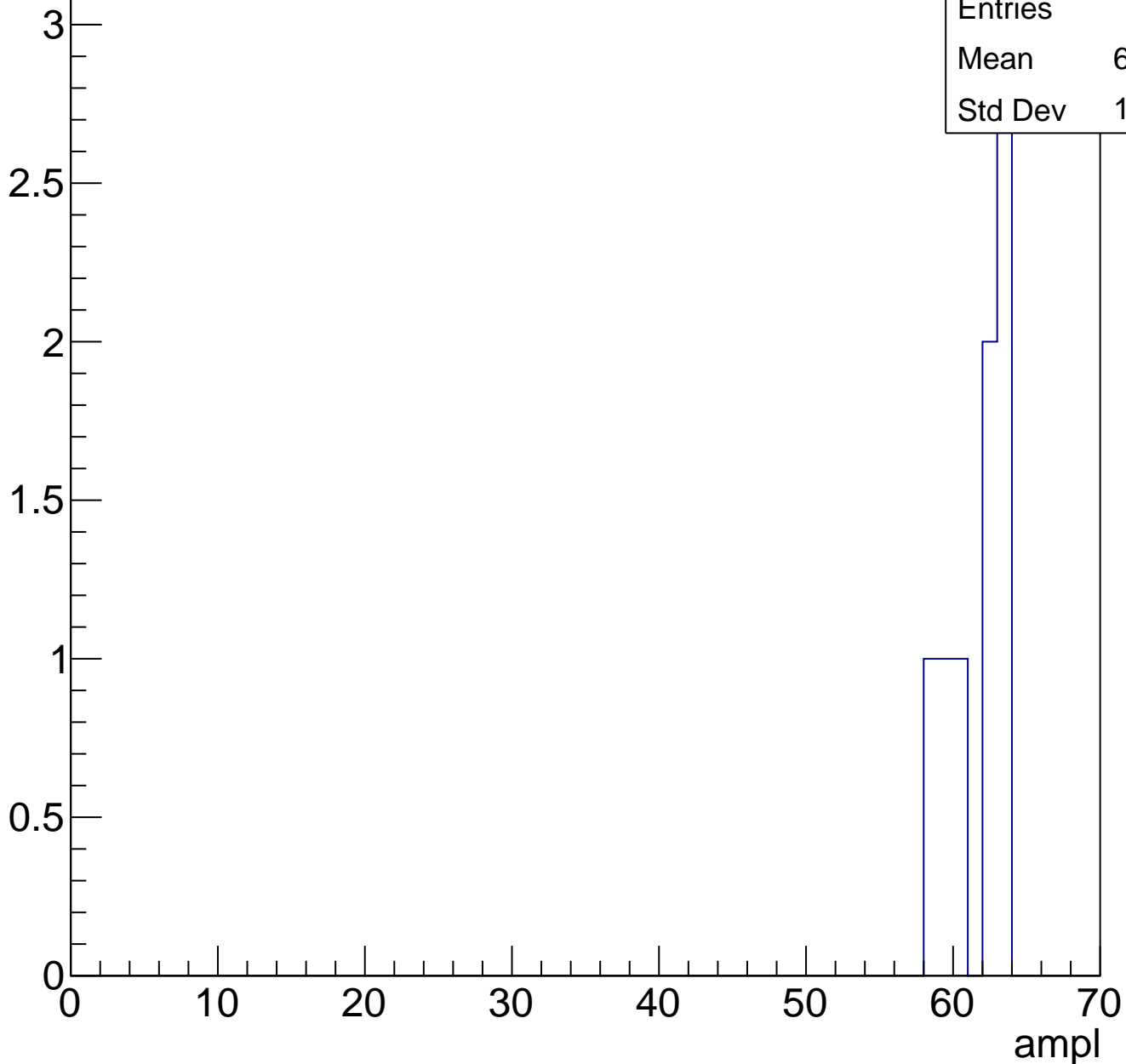
Entries	54
Mean	58.65
Std Dev	8.418



# B1L102S, U25-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

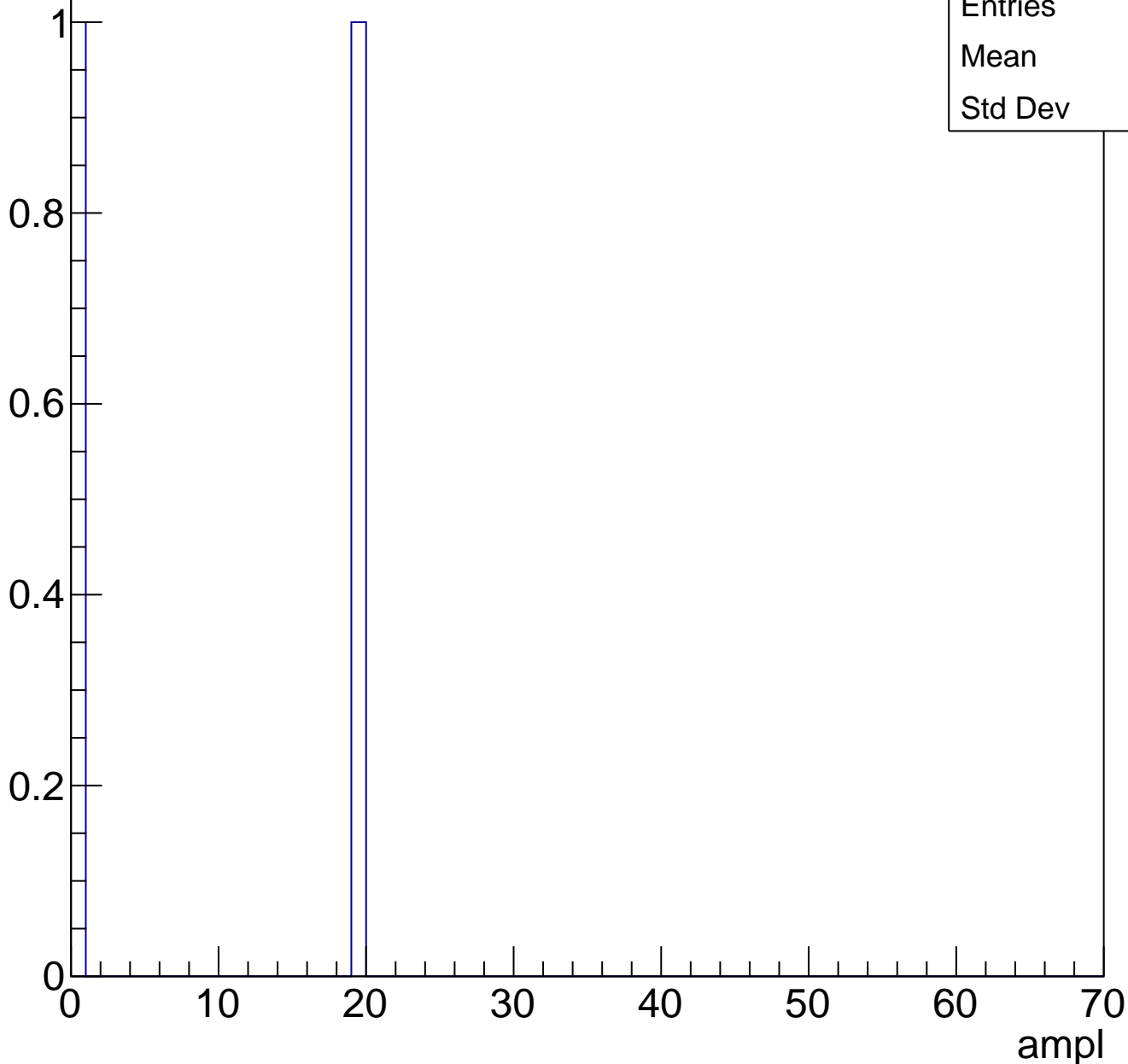




# B1L102S, U25-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L102S, U25-ch18, adc0

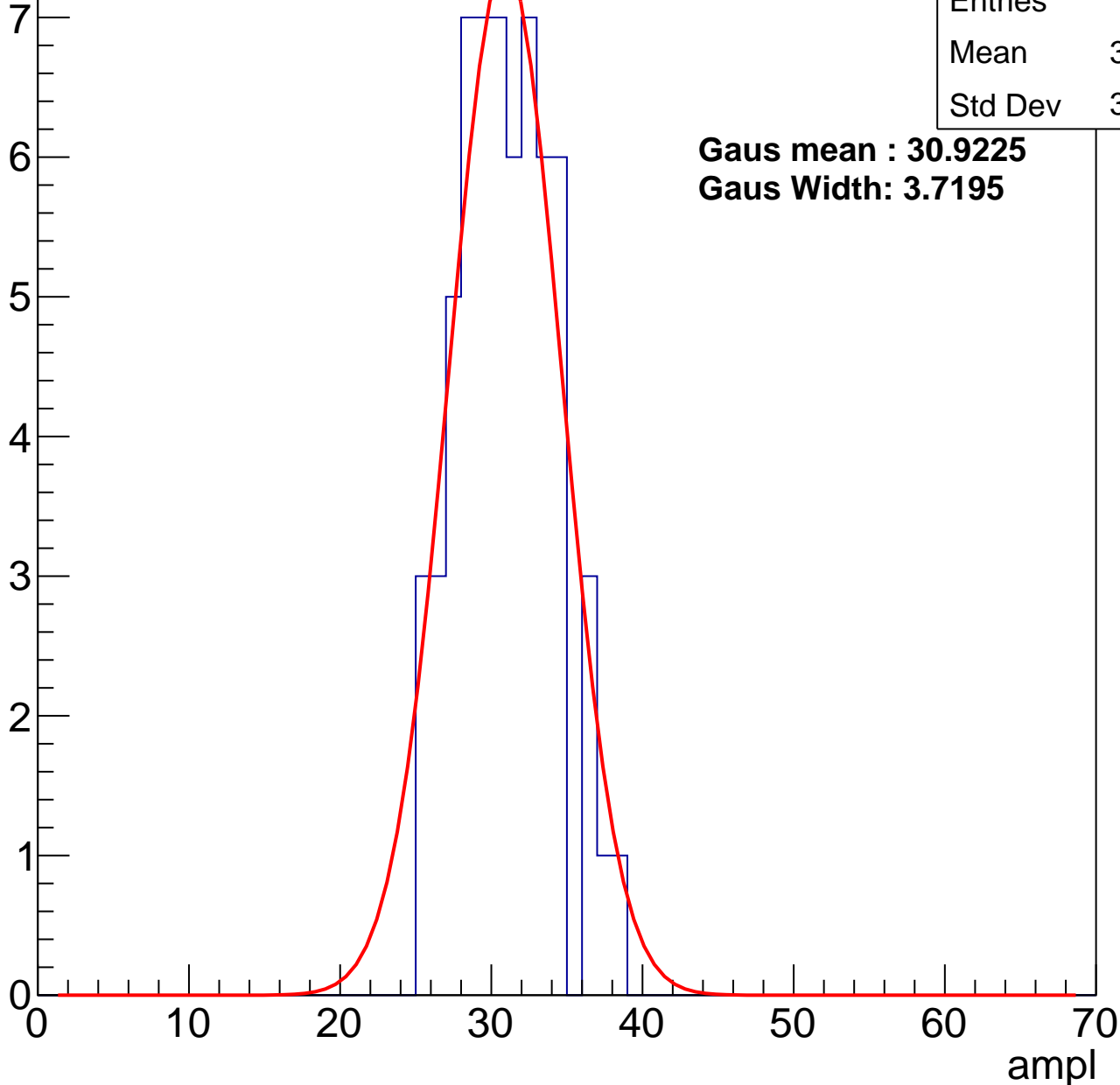
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	30.52
Std Dev	3.089

**Gaus mean : 30.9225**

**Gaus Width: 3.7195**



# B1L102S, U25-ch18, adc1

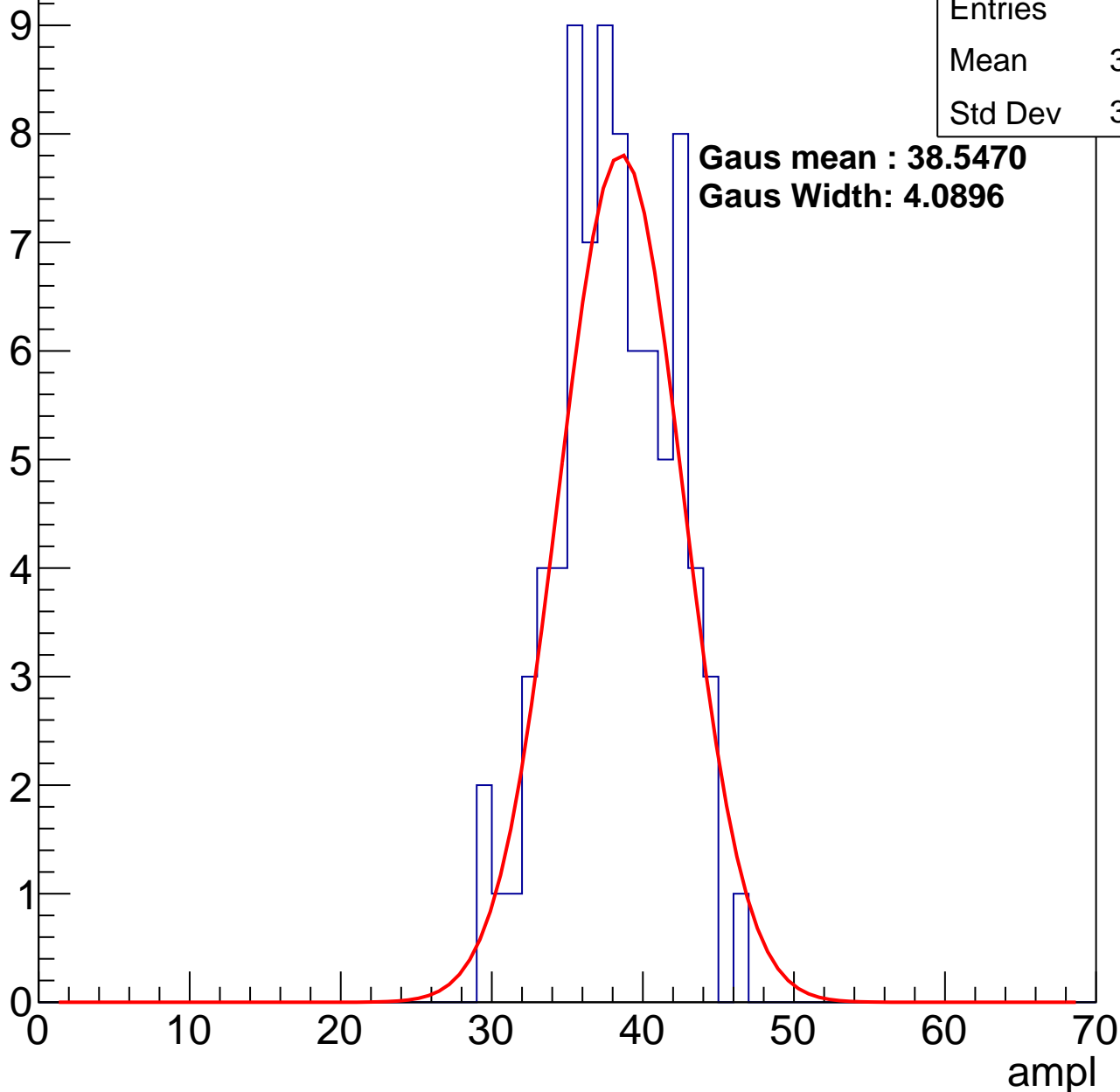
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	37.68
Std Dev	3.738

**Gaus mean : 38.5470**

**Gaus Width: 4.0896**



# B1L102S, U25-ch18, adc2

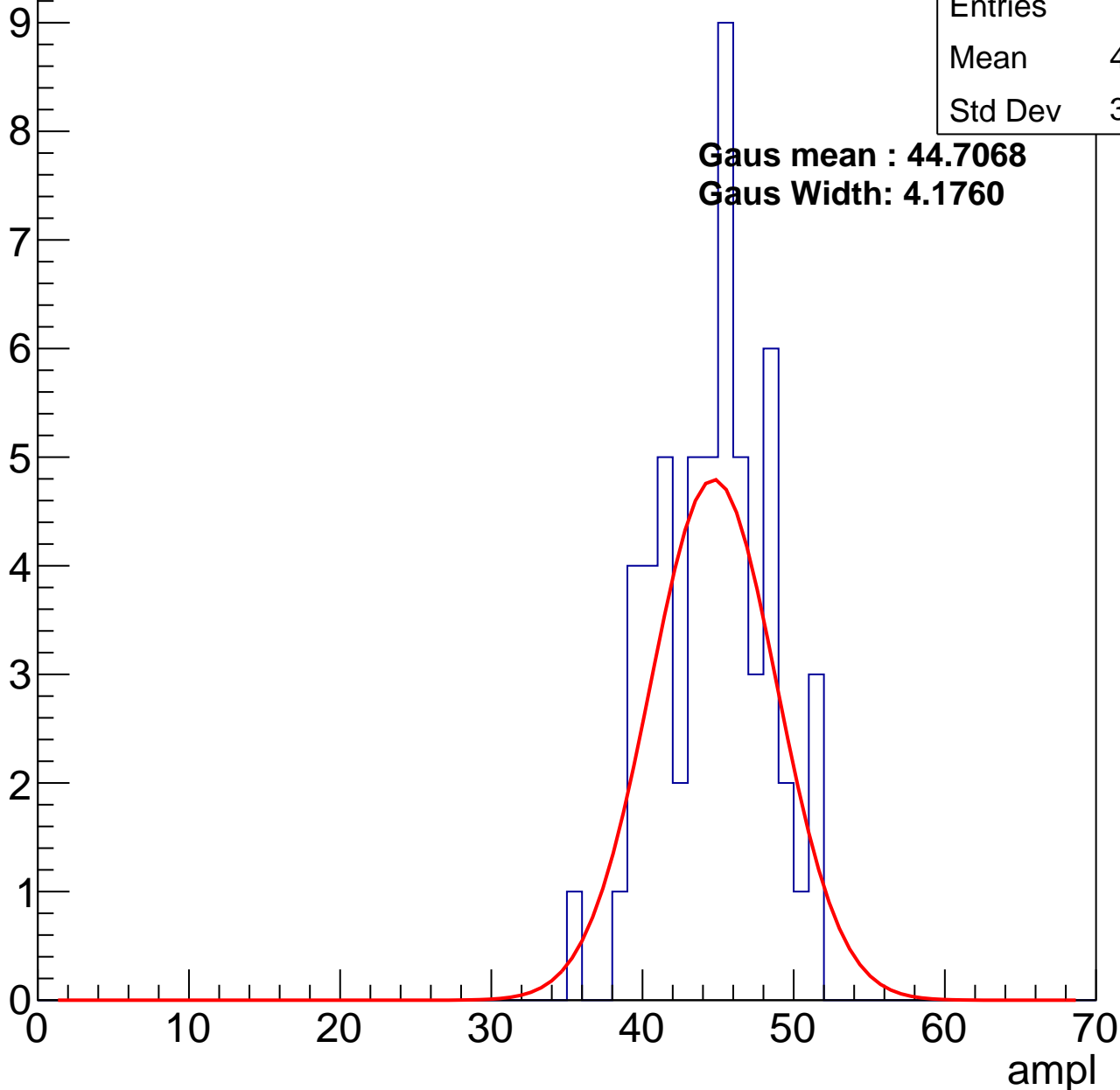
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	44.25
Std Dev	3.587

**Gaus mean : 44.7068**

**Gaus Width: 4.1760**



# B1L102S, U25-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	50.62
Std Dev	3.321

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B1L102S, U25-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 58

Mean 57.64

Std Dev 2.814

ampl

0

10

20

30

40

50

60

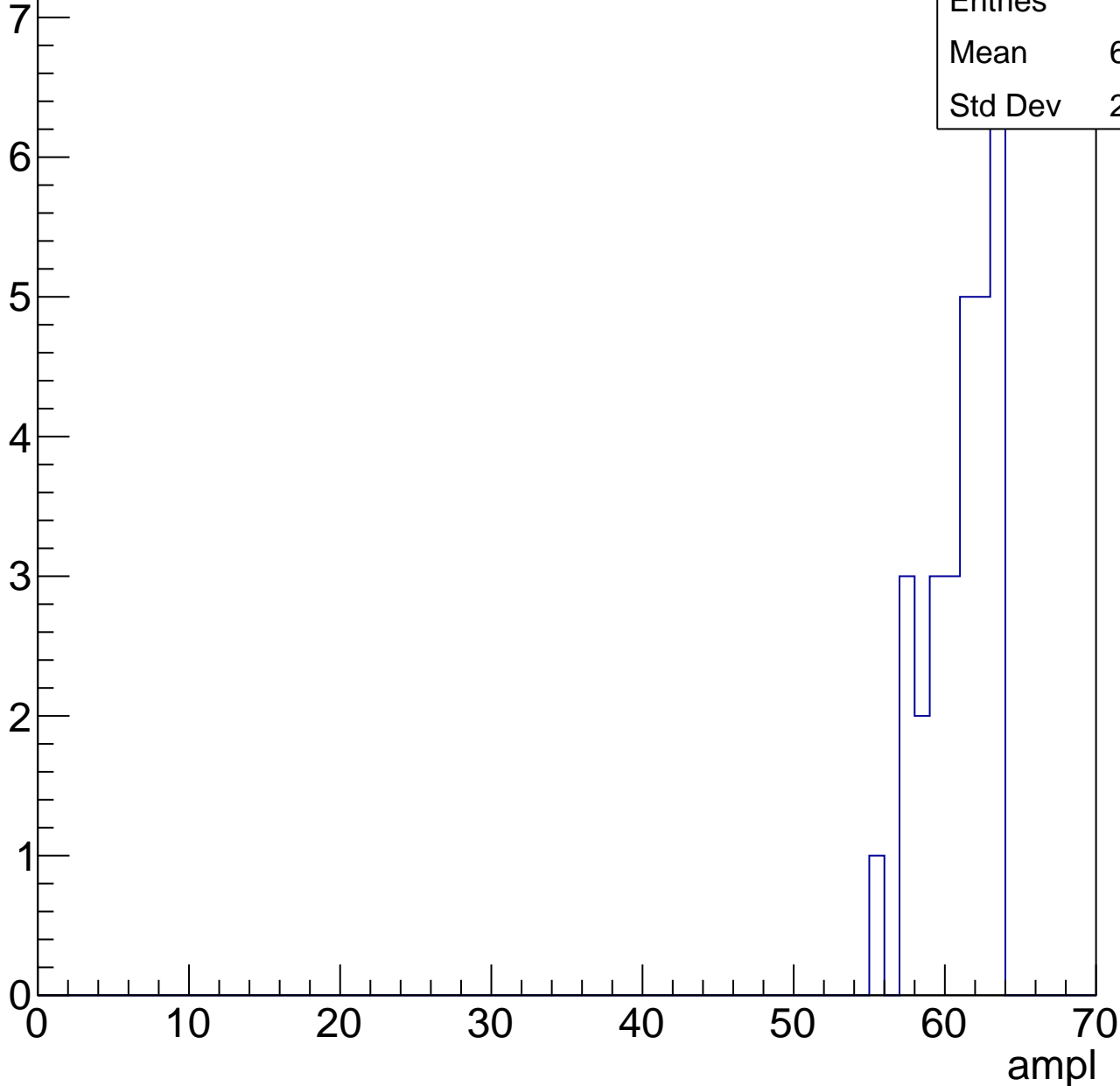
70

# B1L102S, U25-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	29
Mean	60.52
Std Dev	2.222



# B1L102S, U25-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	4
Mean	46.75
Std Dev	26.99



# B1L102S, U25-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L102S, U25-ch19, adc0

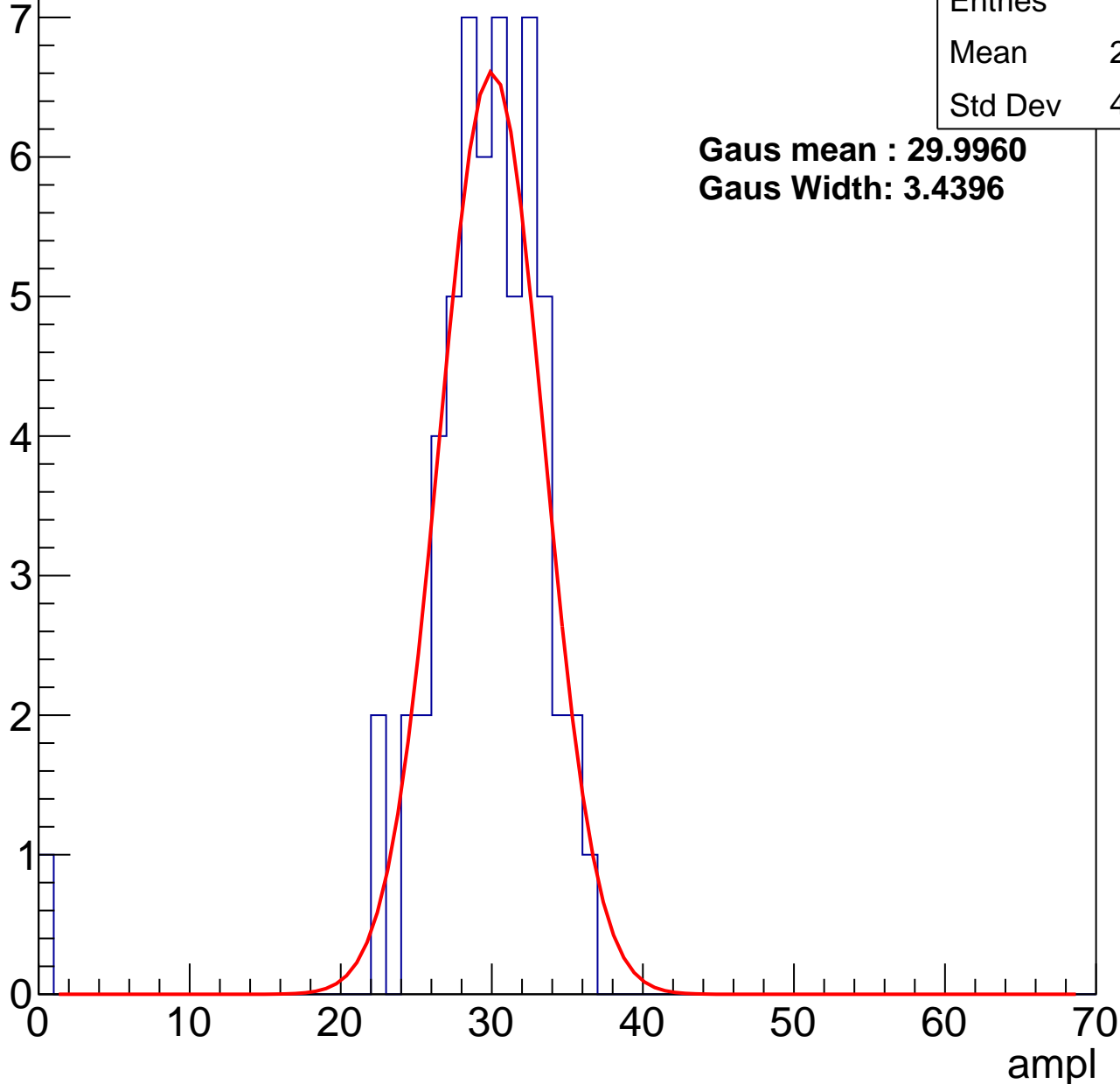
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.95
Std Dev	4.956

**Gaus mean : 29.9960**

**Gaus Width: 3.4396**



# B1L102S, U25-ch19, adc1

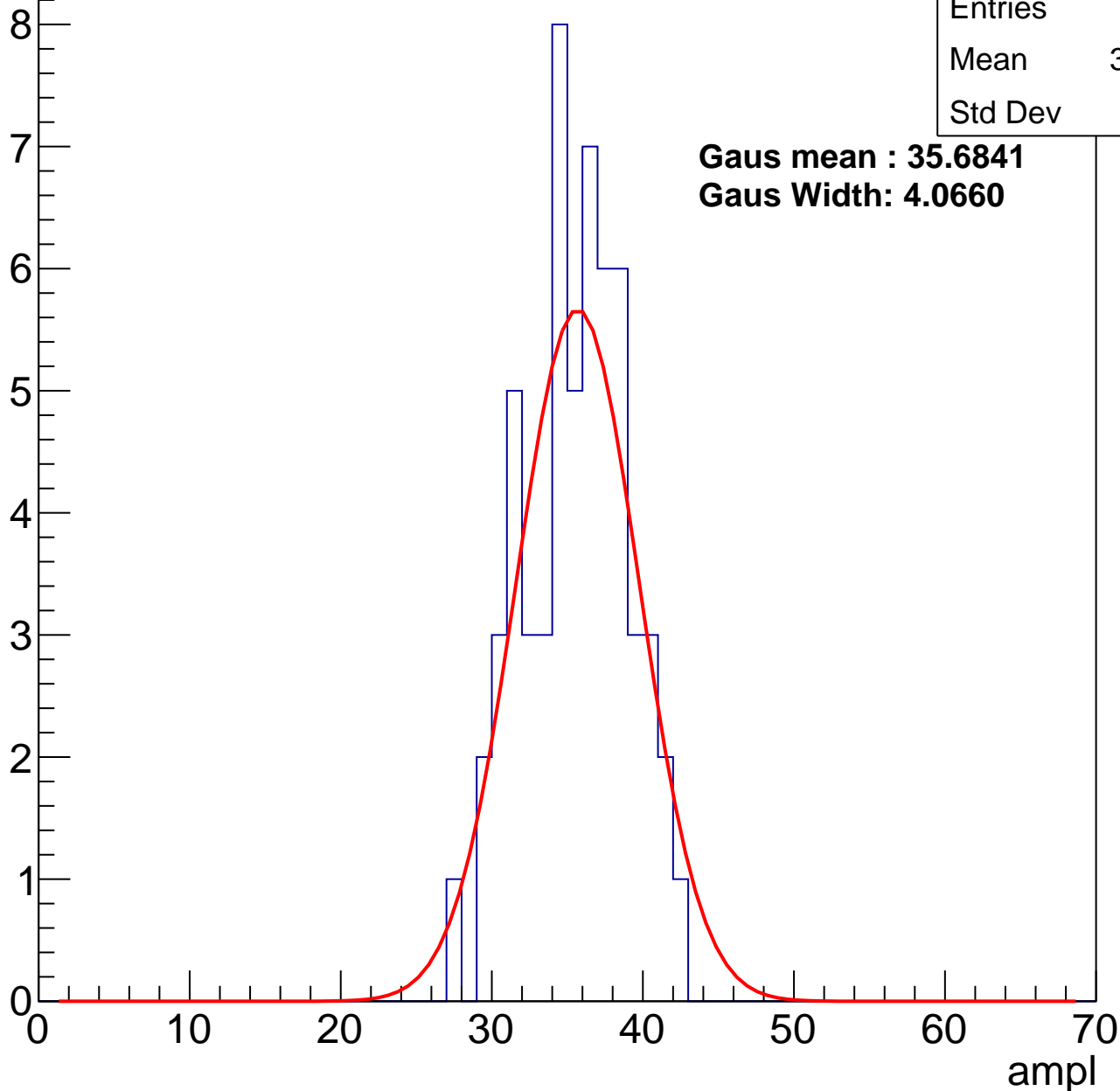
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.09
Std Dev	3.39

**Gaus mean : 35.6841**

**Gaus Width: 4.0660**



# B1L102S, U25-ch19, adc2

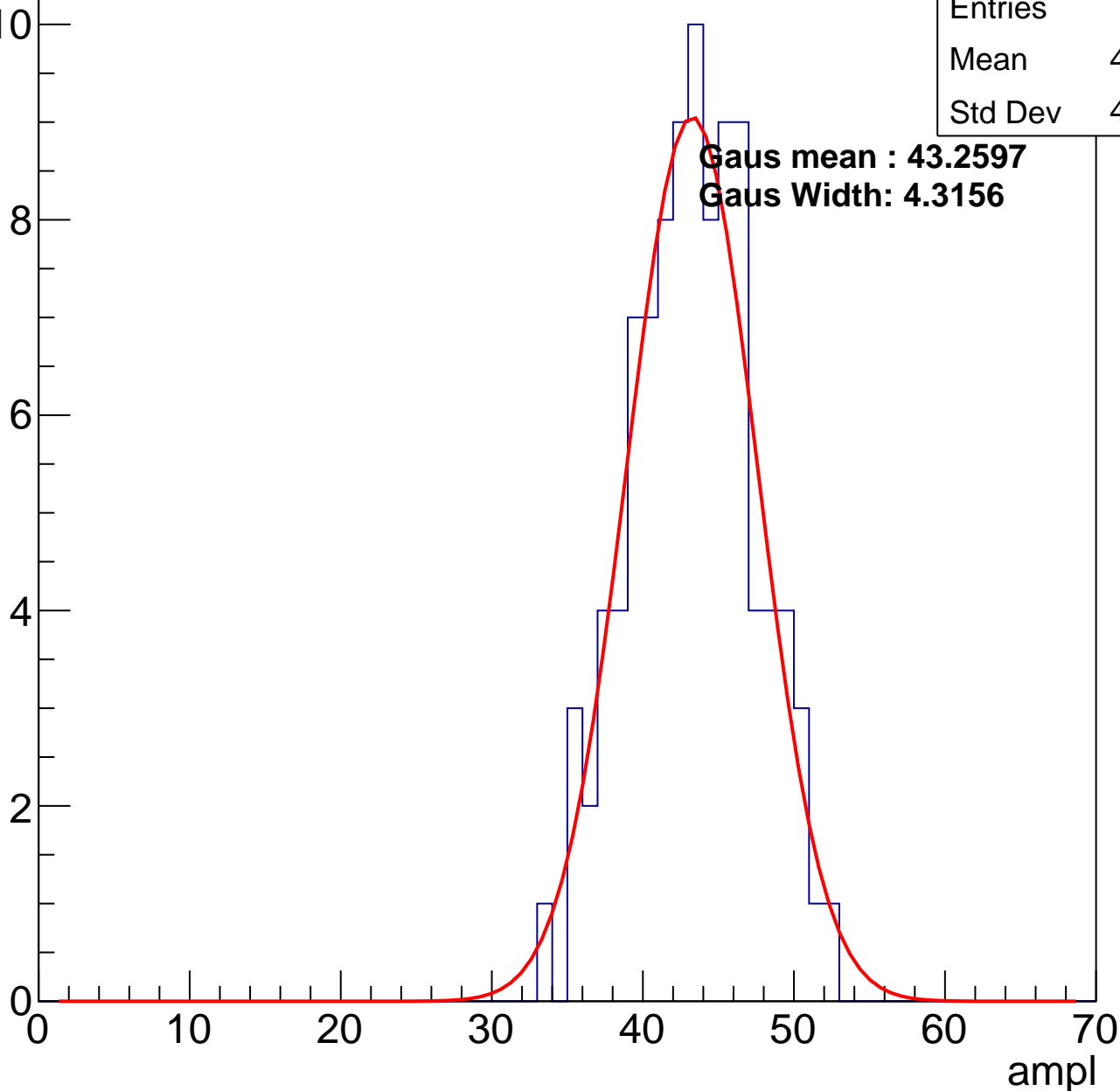
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	98
Mean	42.85
Std Dev	4.006

**Gaus mean : 43.2597**

**Gaus Width: 4.3156**

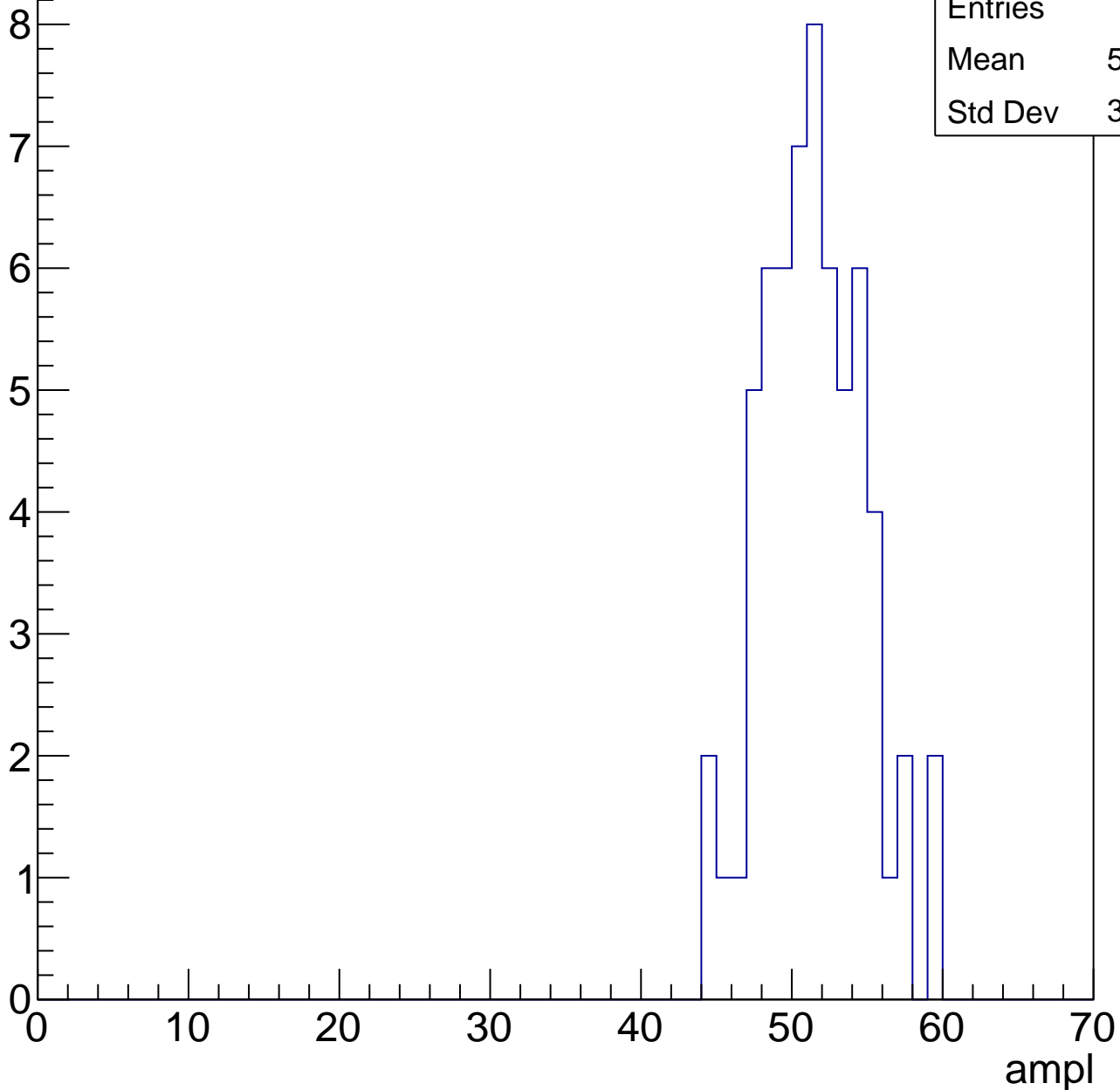


# B1L102S, U25-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

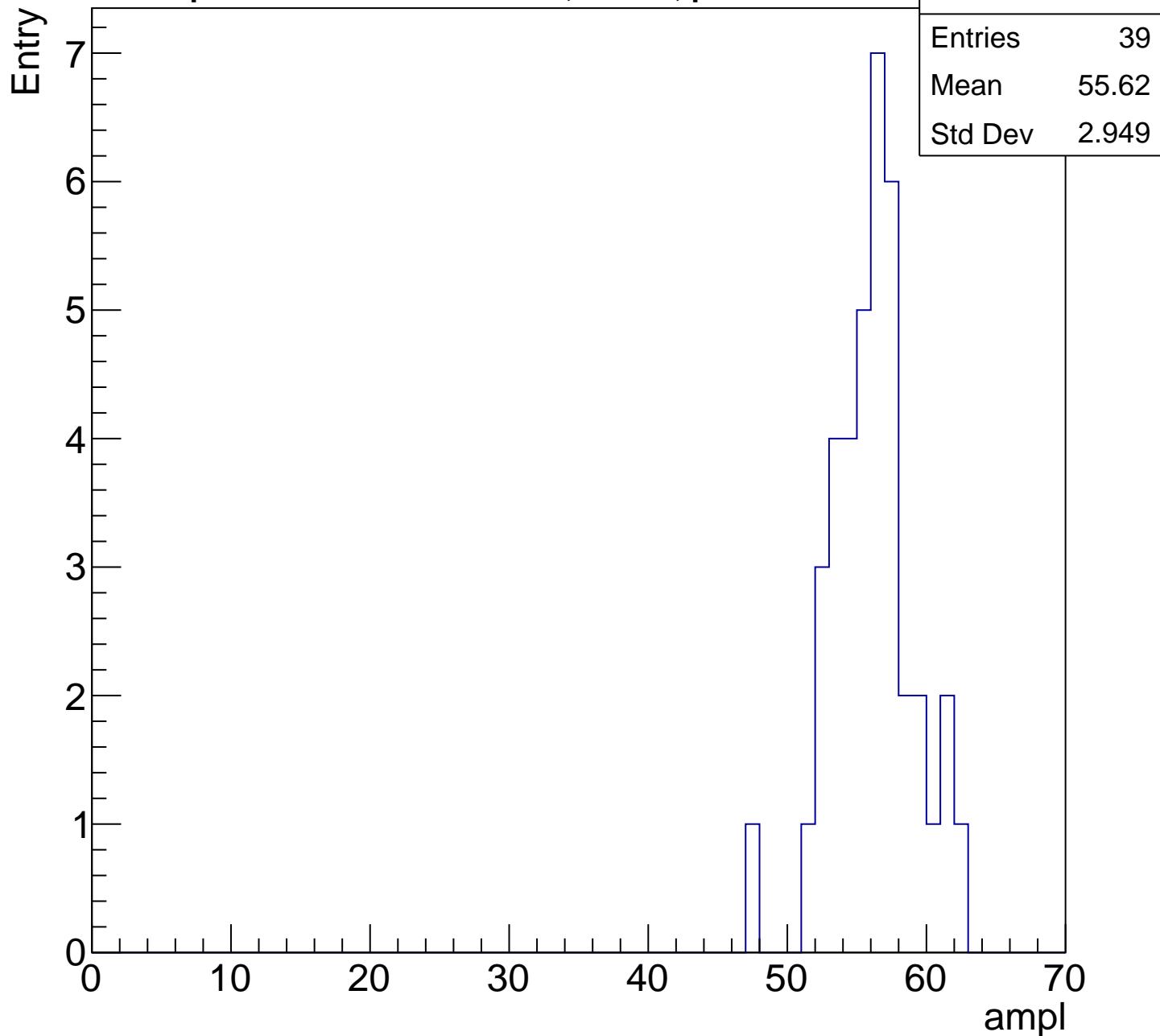
Entry

Entries	62
Mean	51.02
Std Dev	3.343



# B1L102S, U25-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

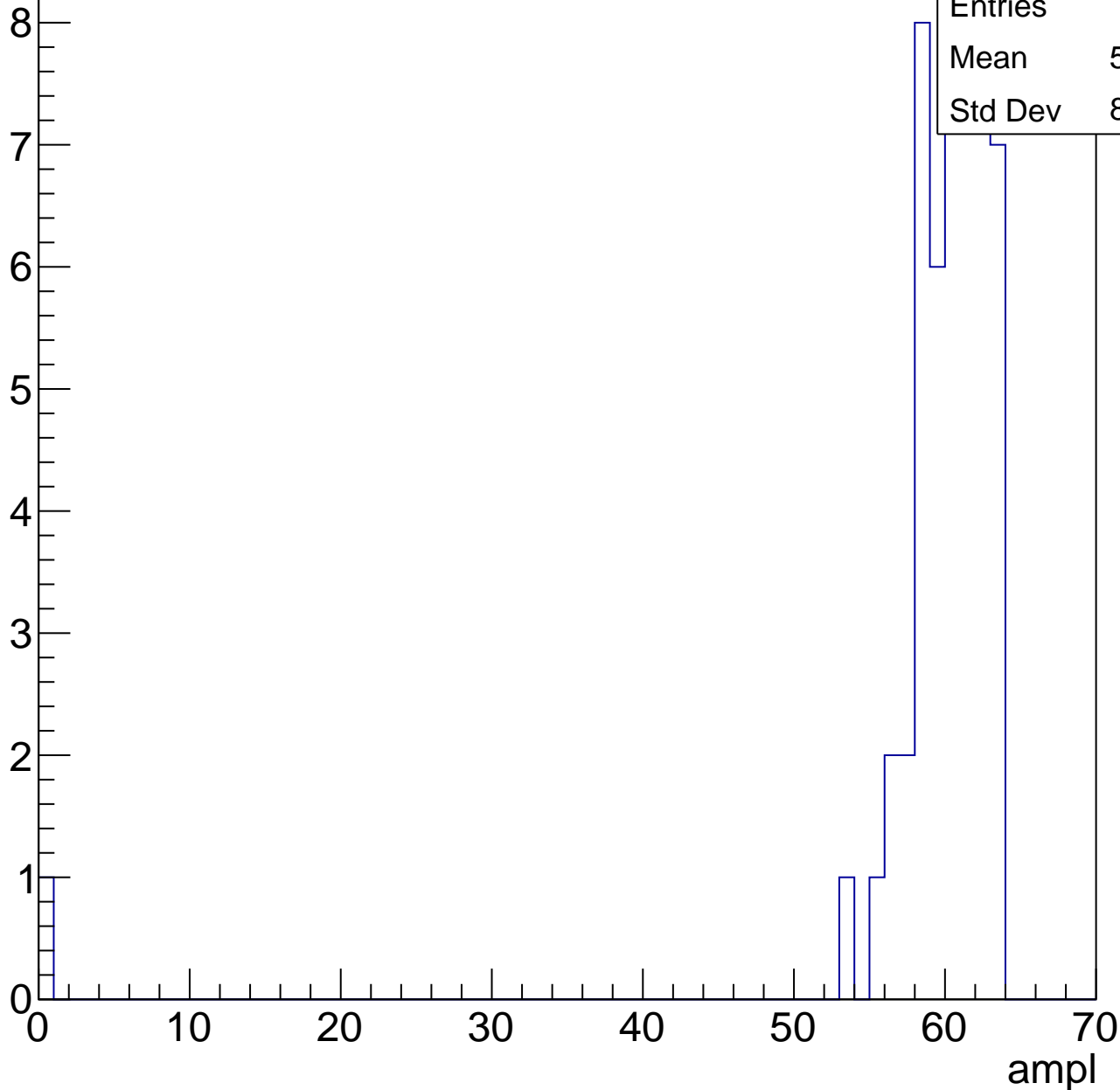


# B1L102S, U25-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.79
Std Dev	8.536



# B1L102S, U25-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch20, adc0

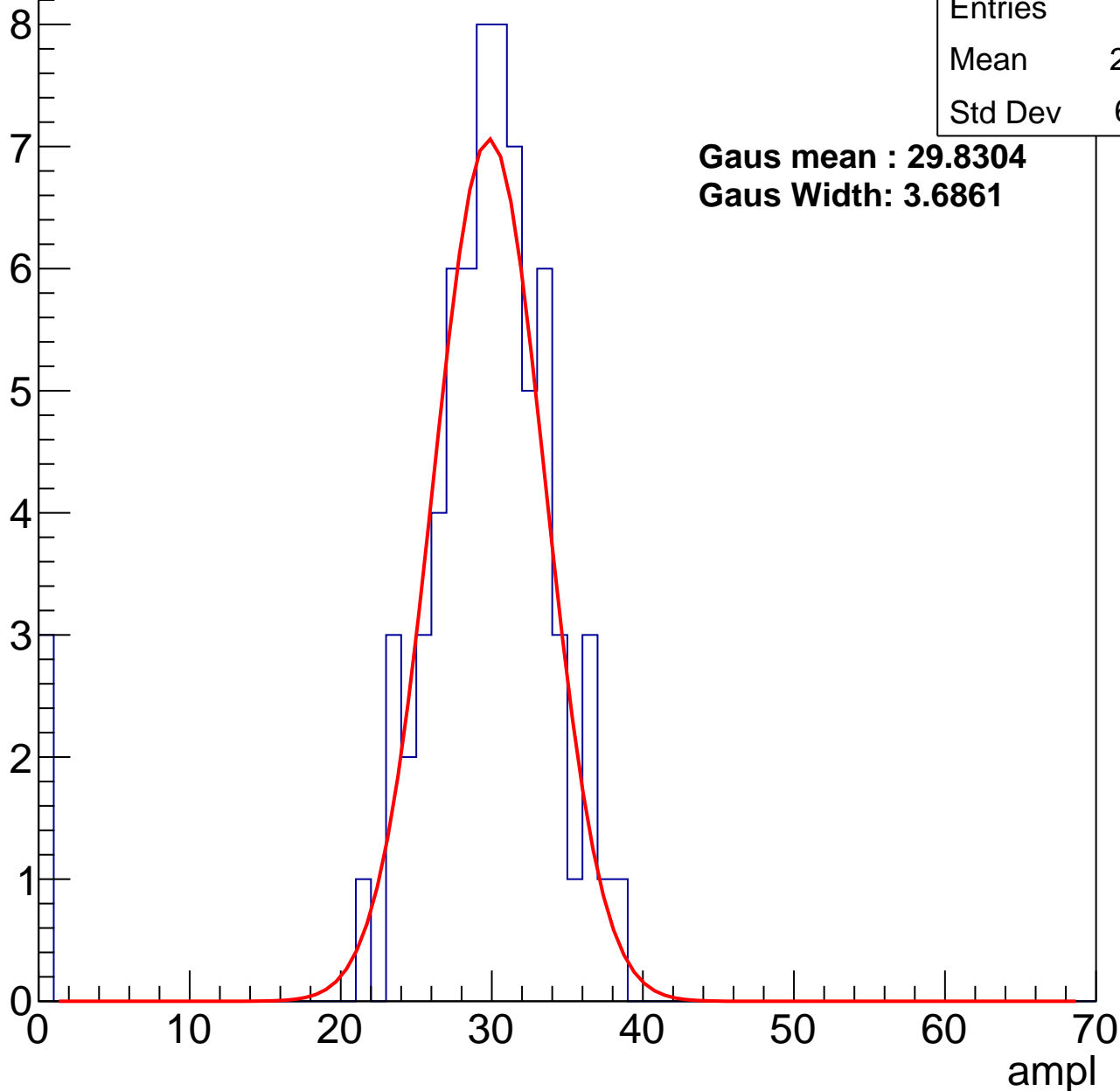
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.37
Std Dev	6.931

**Gaus mean : 29.8304**

**Gaus Width: 3.6861**



# B1L102S, U25-ch20, adc1

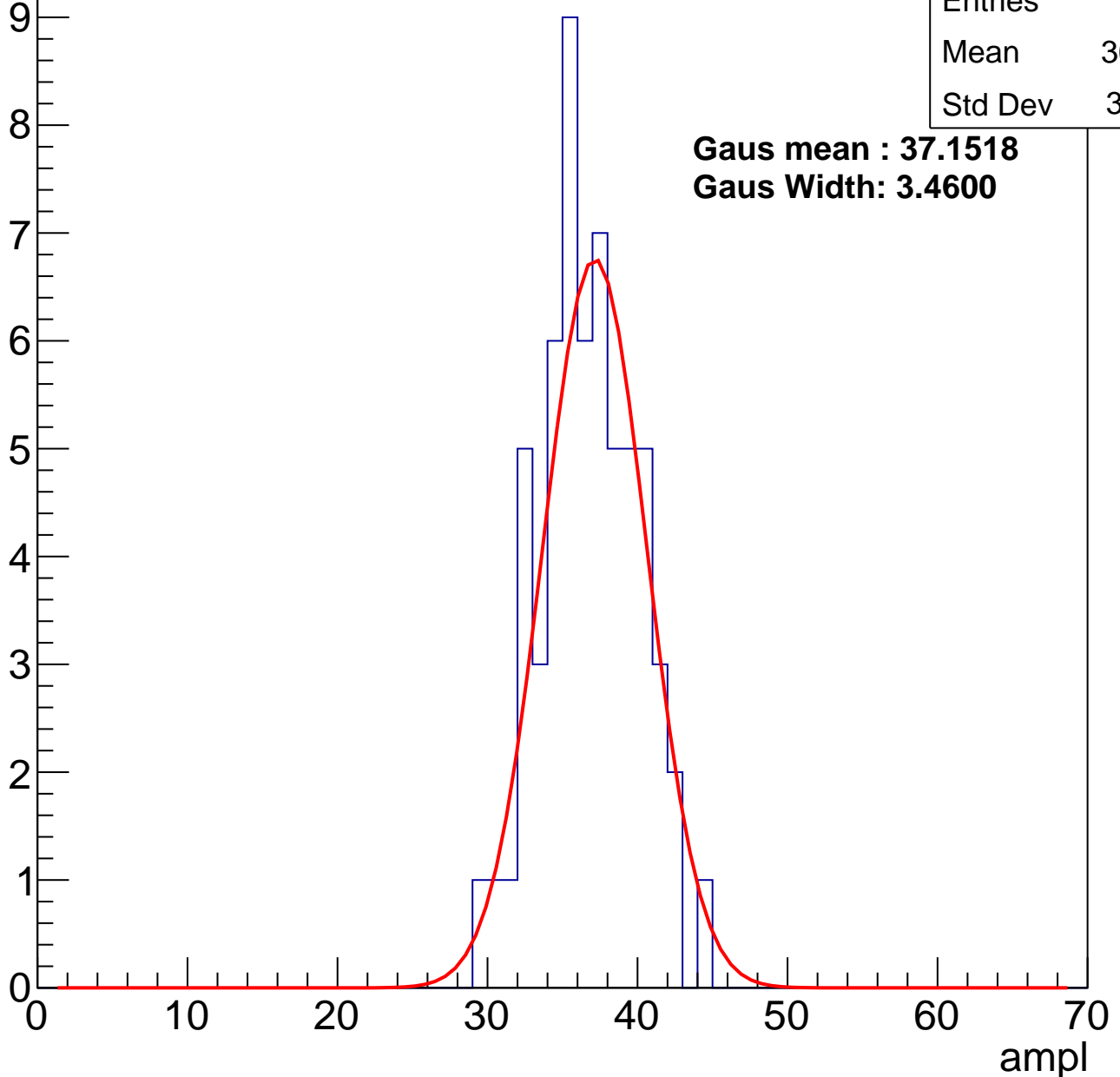
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.32
Std Dev	3.181

**Gaus mean : 37.1518**

**Gaus Width: 3.4600**



# B1L102S, U25-ch20, adc2

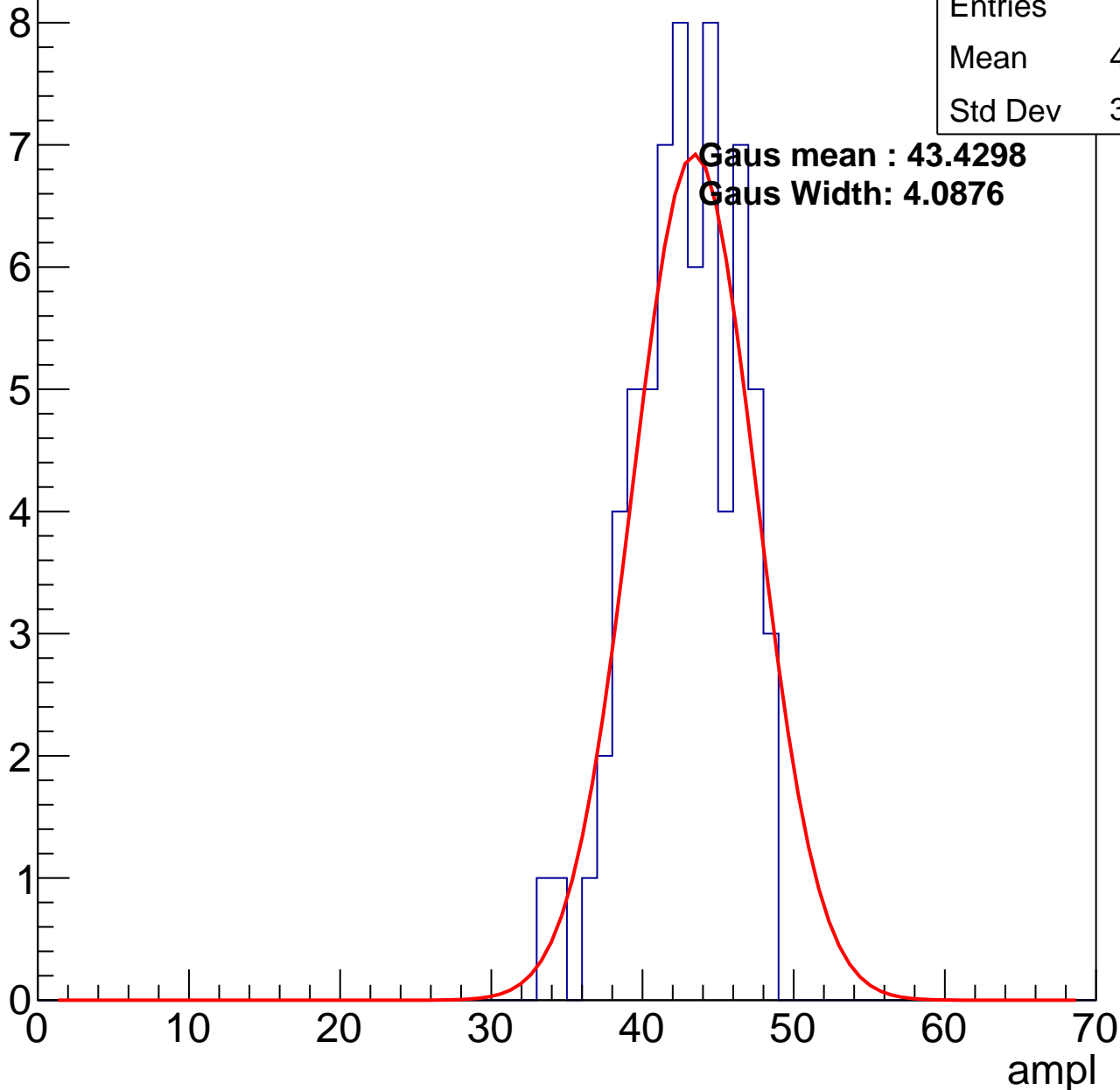
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.36
Std Dev	3.406

**Gaus mean : 43.4298**

**Gaus Width: 4.0876**

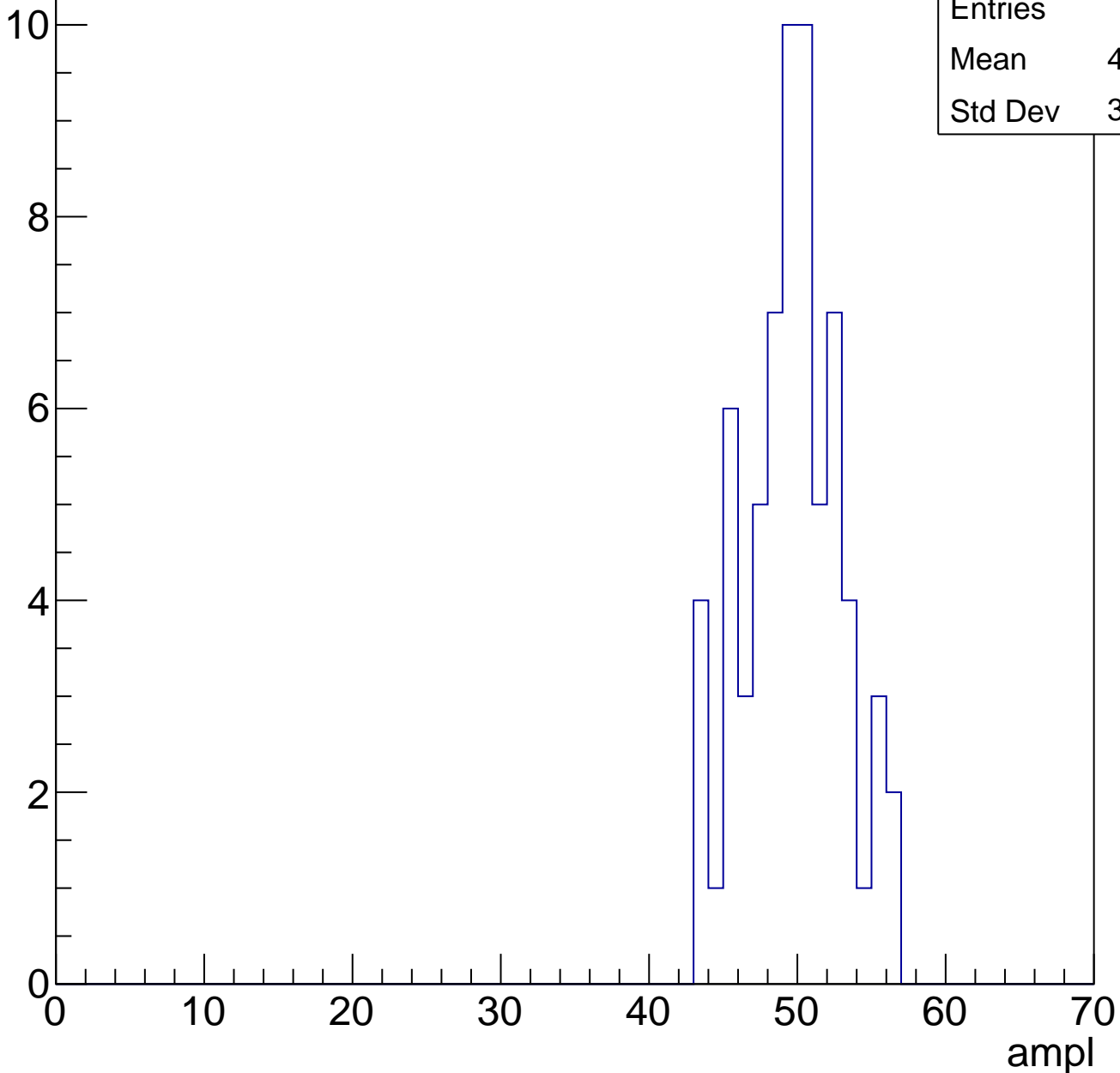


# B1L102S, U25-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

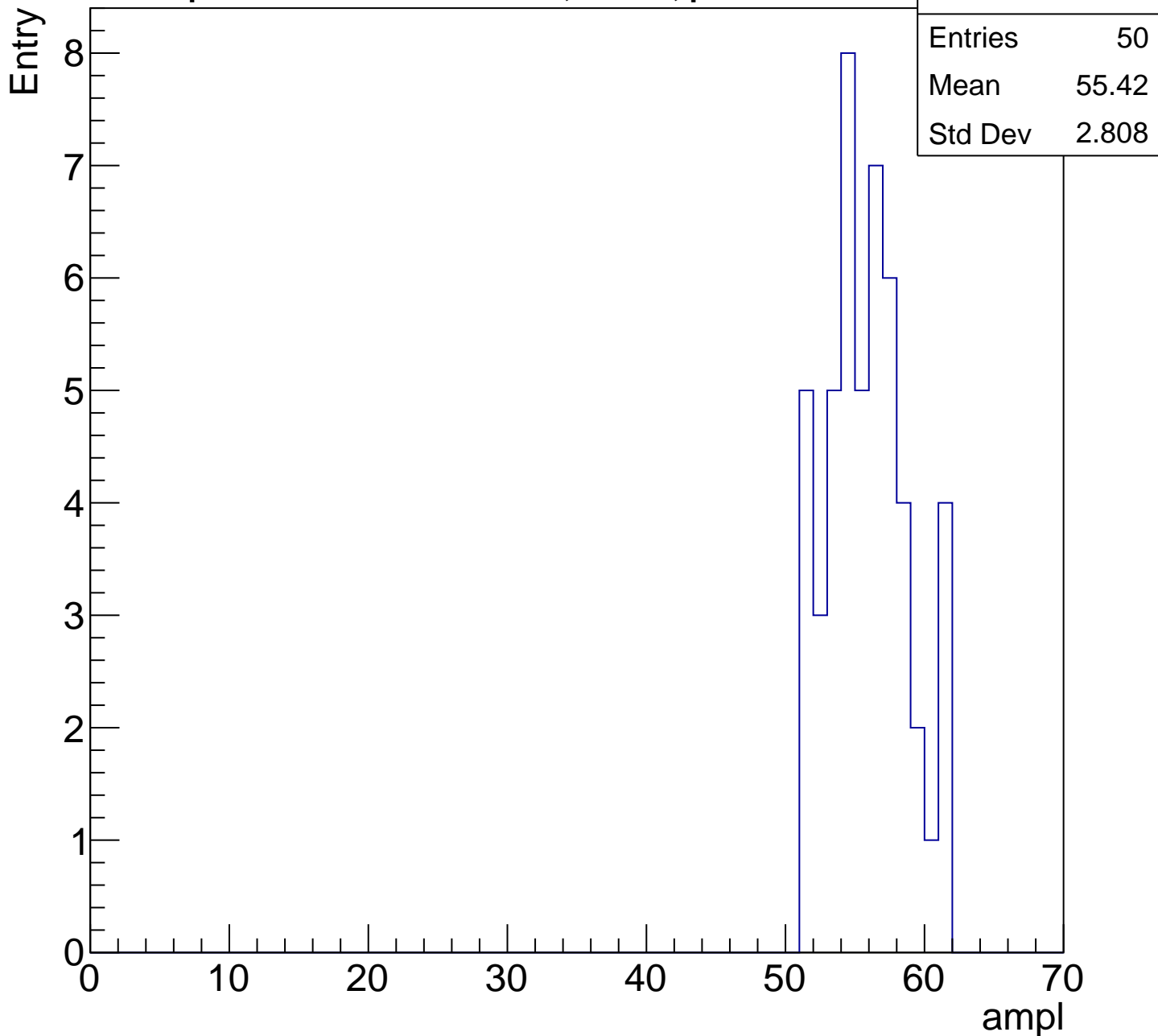
Entry

Entries	68
Mean	49.22
Std Dev	3.217



# B1L102S, U25-ch20, adc4

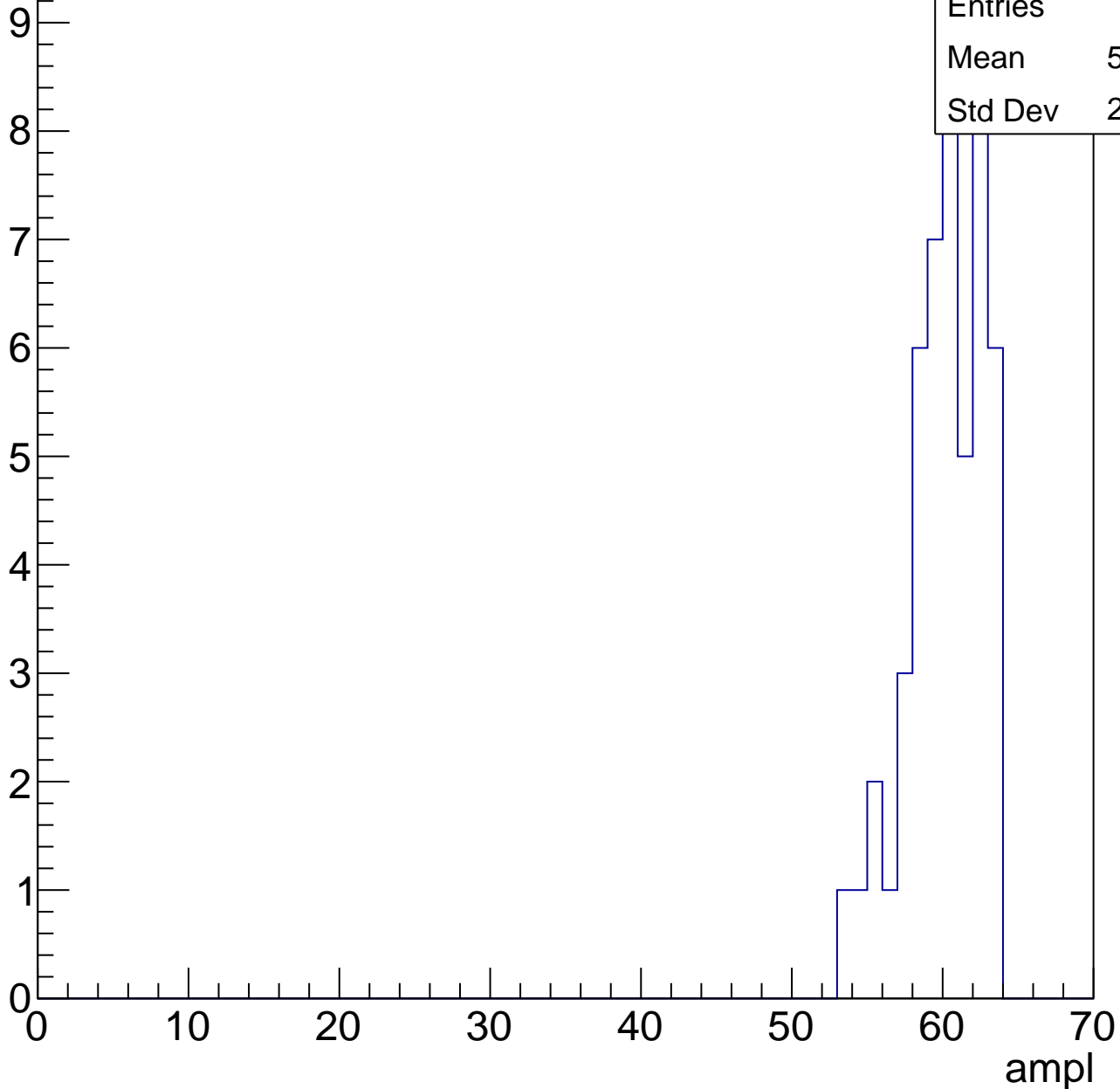
calib\_packv5\_042523\_0143.root, FC#11, port A2



# B1L102S, U25-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

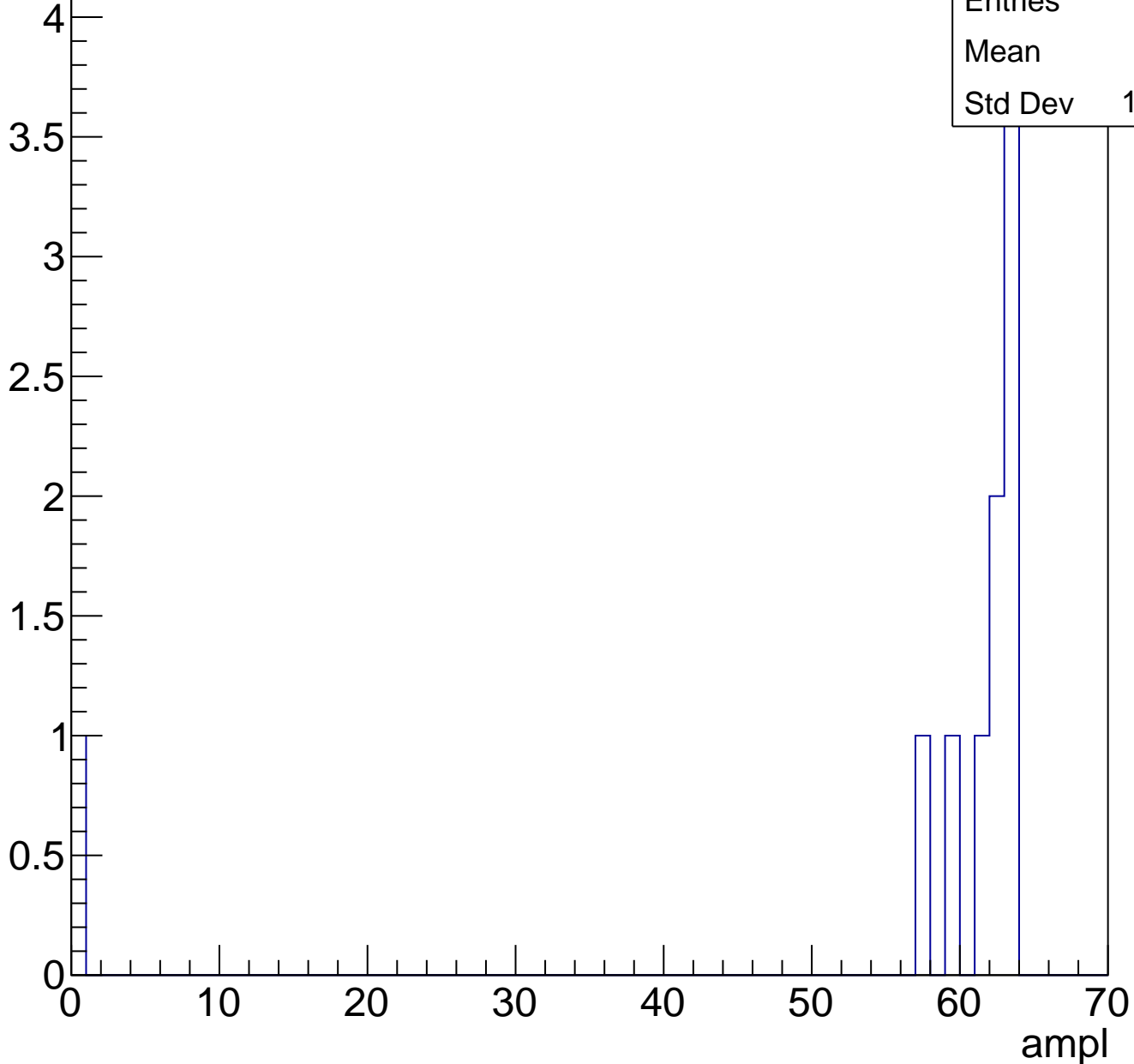
Entry



# B1L102S, U25-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch21, adc0

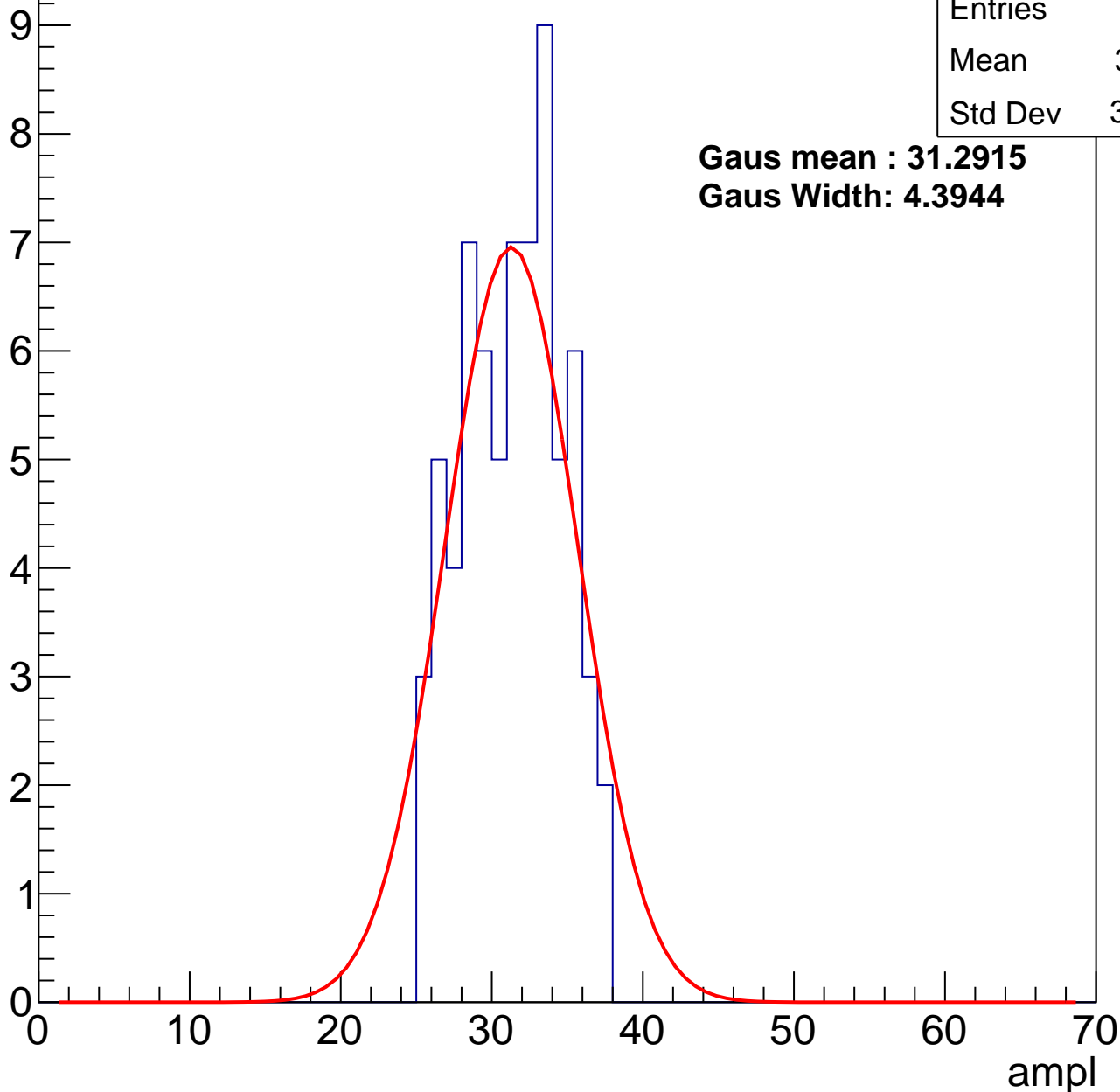
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	30.91
Std Dev	3.229

**Gaus mean : 31.2915**

**Gaus Width: 4.3944**



# B1L102S, U25-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	38.25
Std Dev	3.572

**Gaus mean : 38.9553**

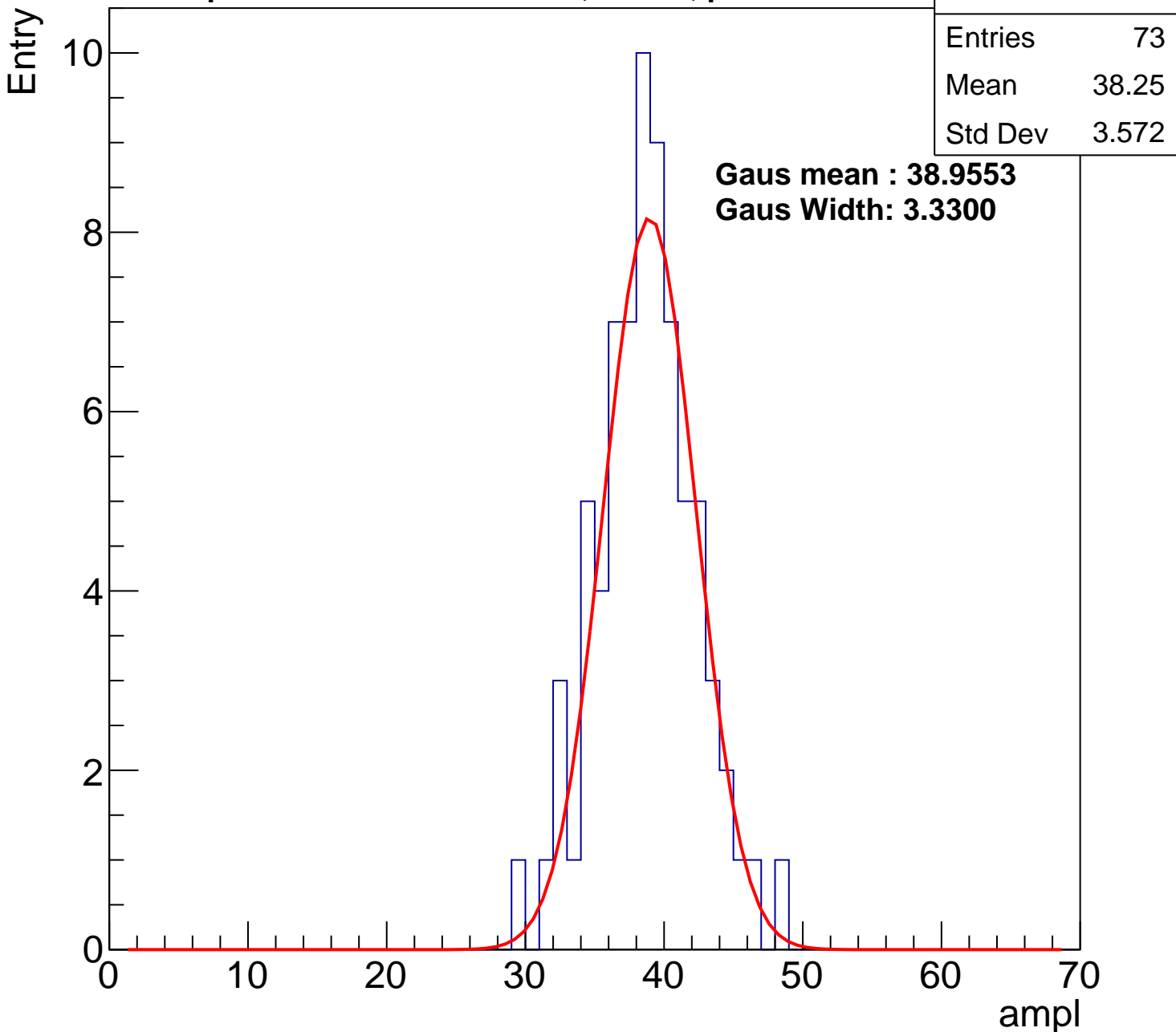
**Gaus Width: 3.3300**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U25-ch21, adc2

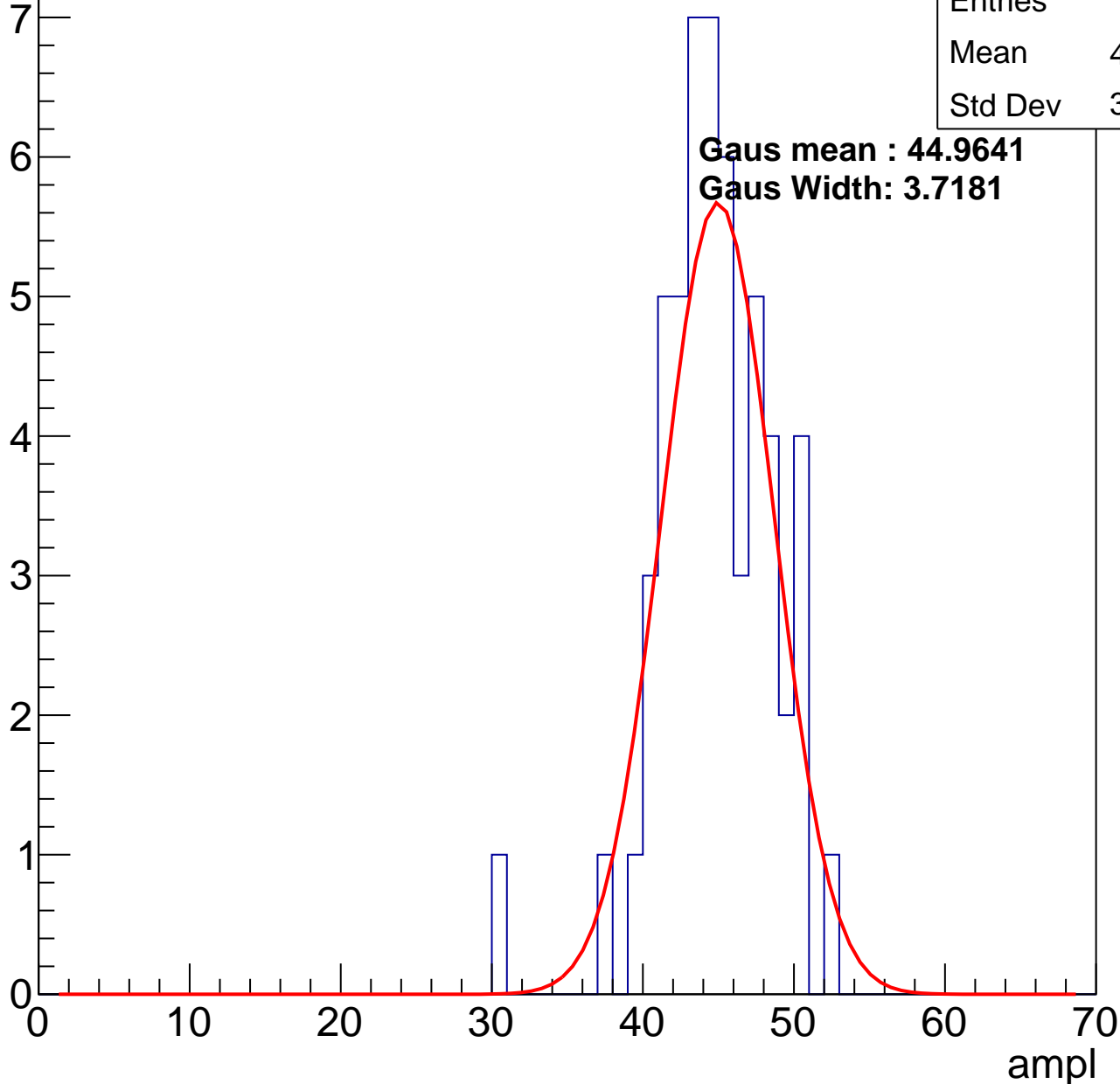
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	44.27
Std Dev	3.744

**Gaus mean : 44.9641**

**Gaus Width: 3.7181**

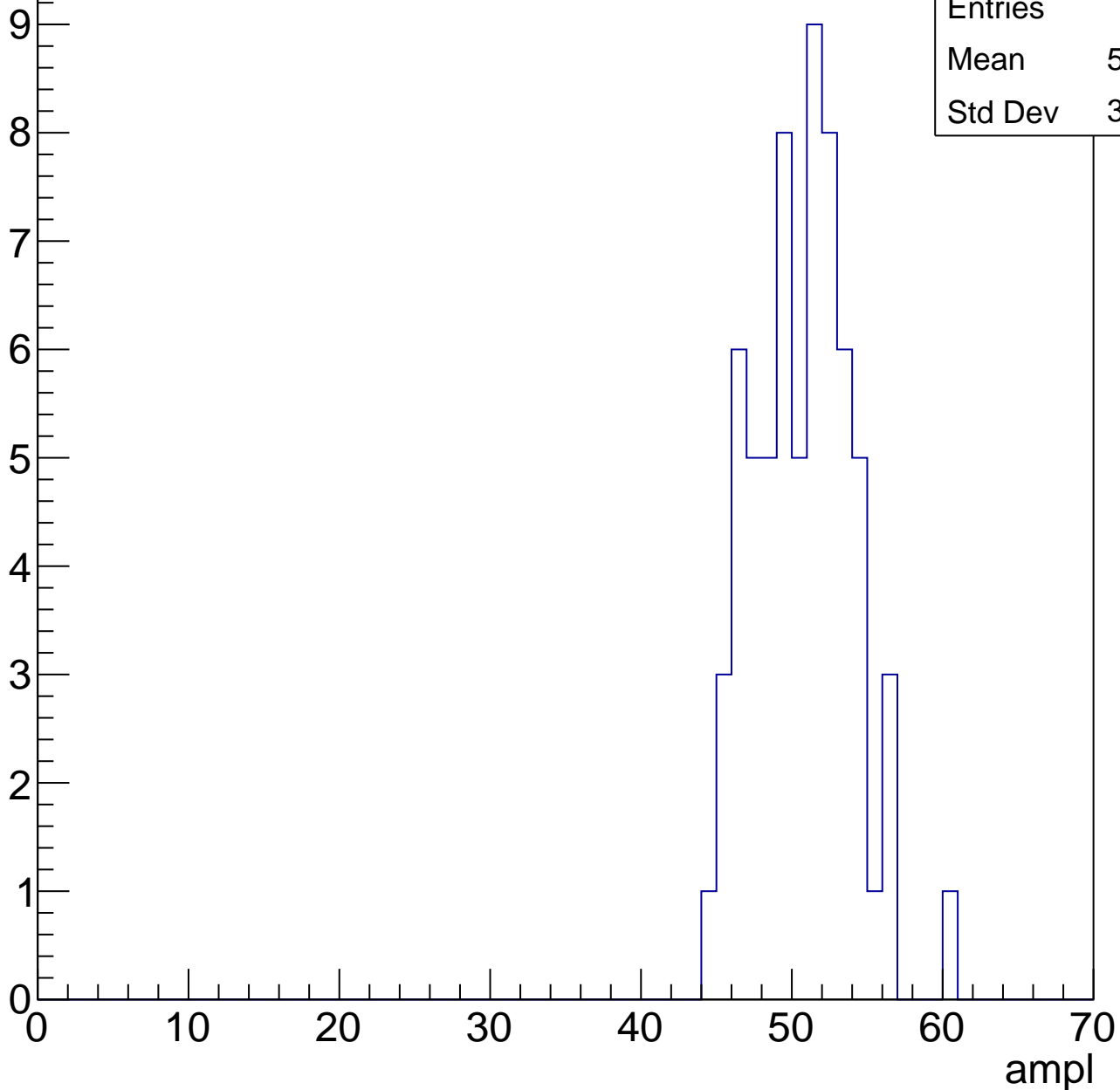


# B1L102S, U25-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.27
Std Dev	3.217

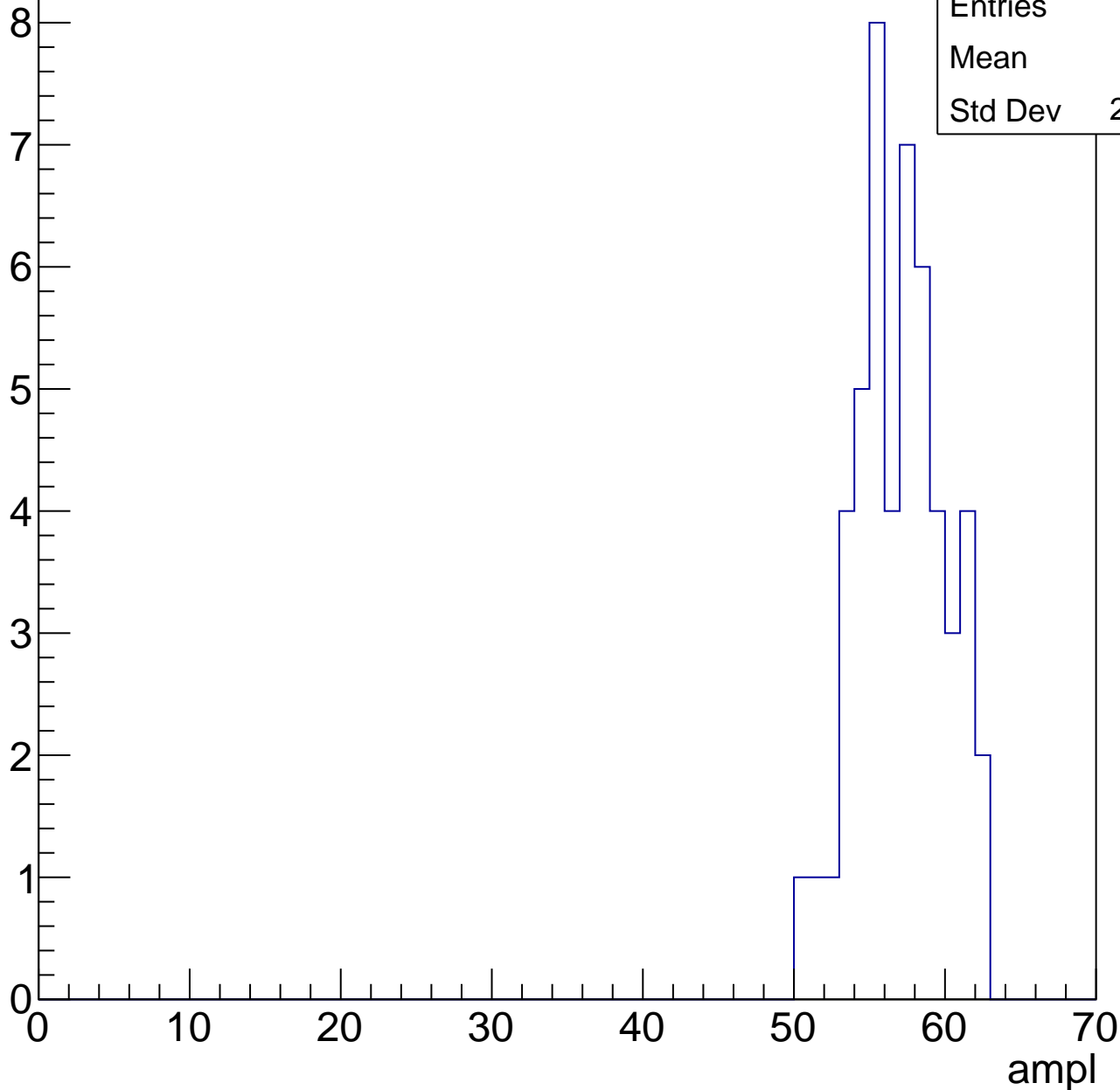


# B1L102S, U25-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.6
Std Dev	2.864

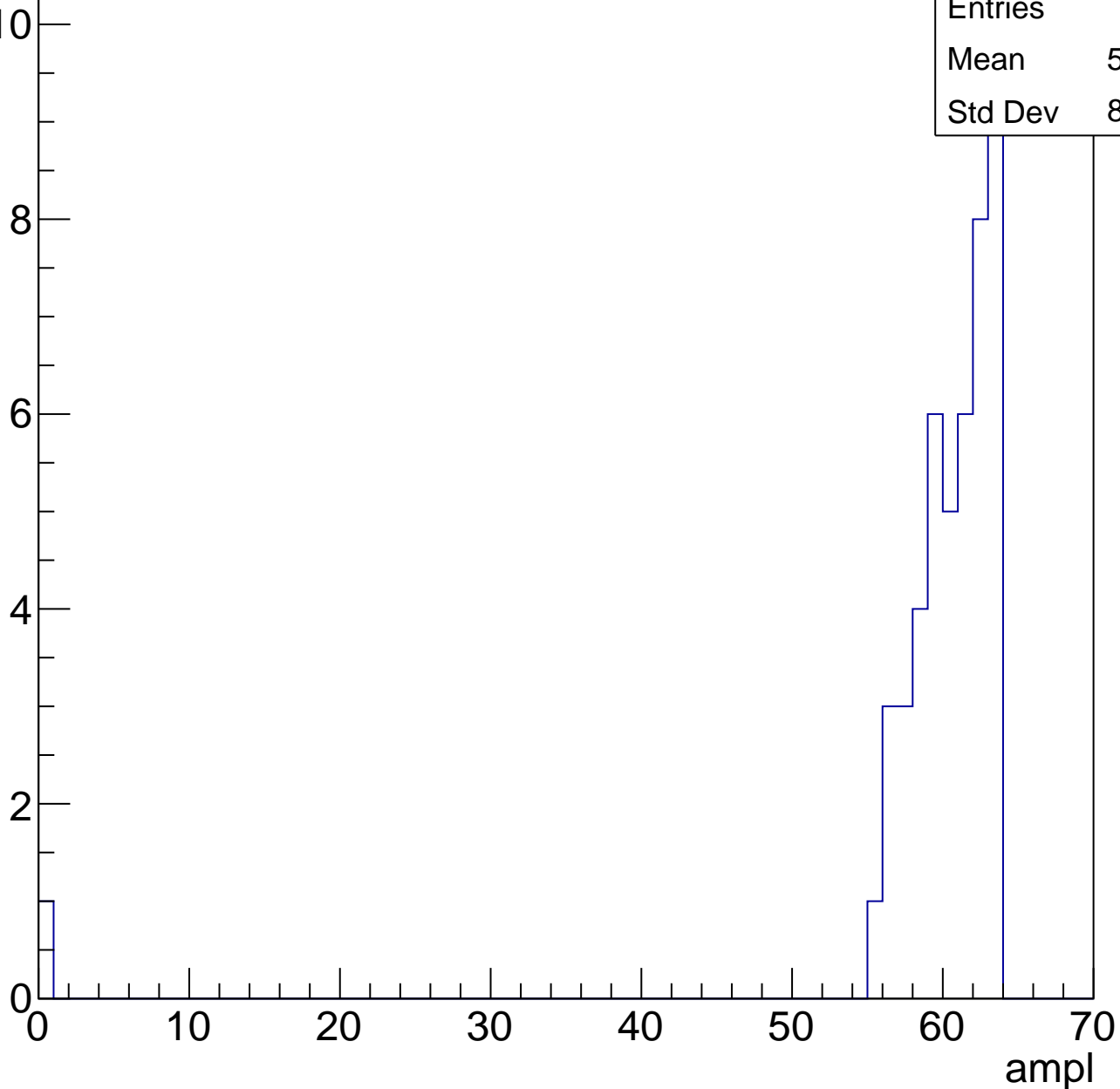


# B1L102S, U25-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.98
Std Dev	8.993



# B1L102S, U25-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch22, adc0

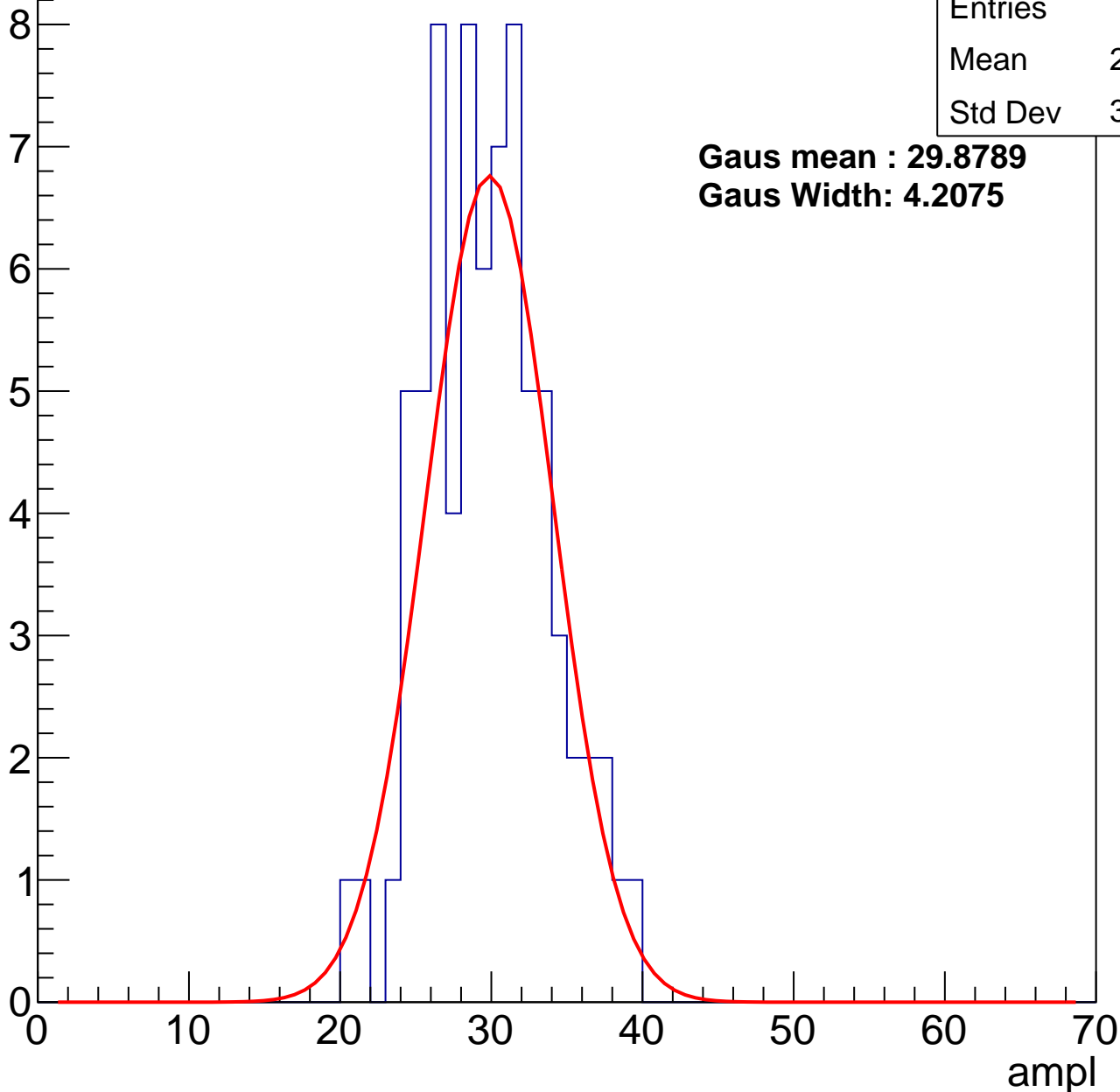
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.35
Std Dev	3.985

**Gaus mean : 29.8789**

**Gaus Width: 4.2075**



# B1L102S, U25-ch22, adc1

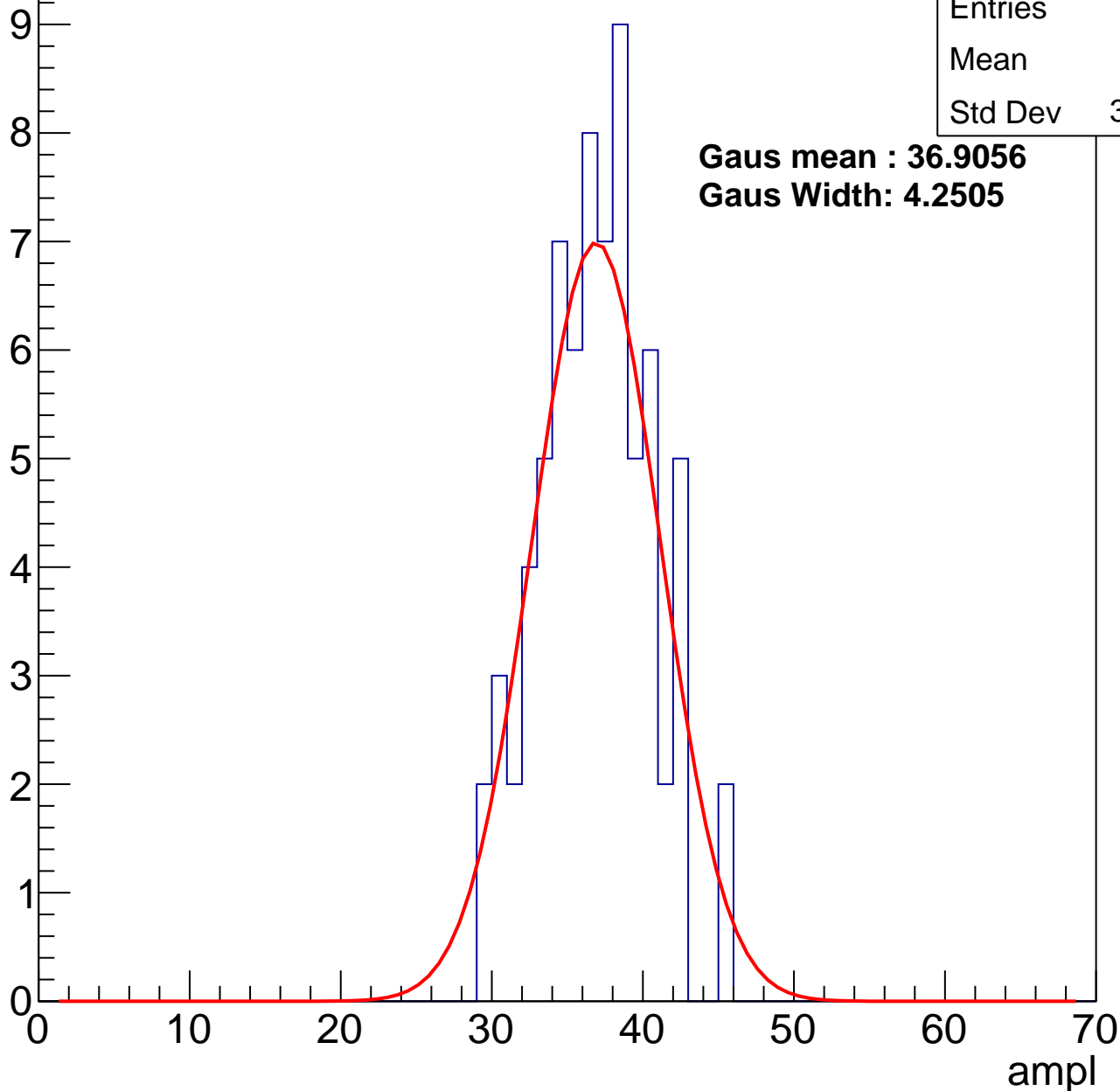
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.4
Std Dev	3.633

**Gaus mean : 36.9056**

**Gaus Width: 4.2505**



# B1L102S, U25-ch22, adc2

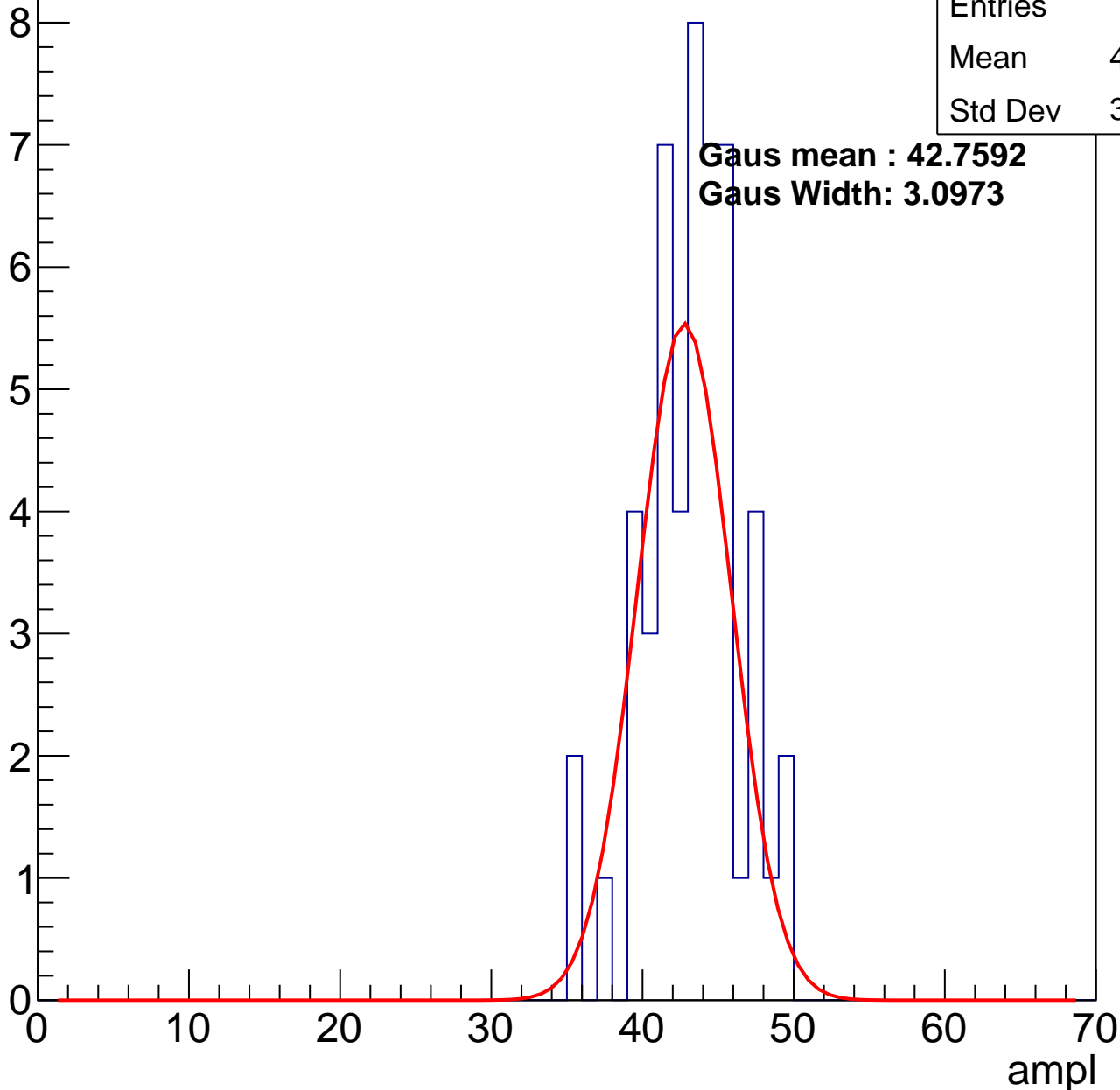
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.84
Std Dev	3.102

**Gaus mean : 42.7592**

**Gaus Width: 3.0973**

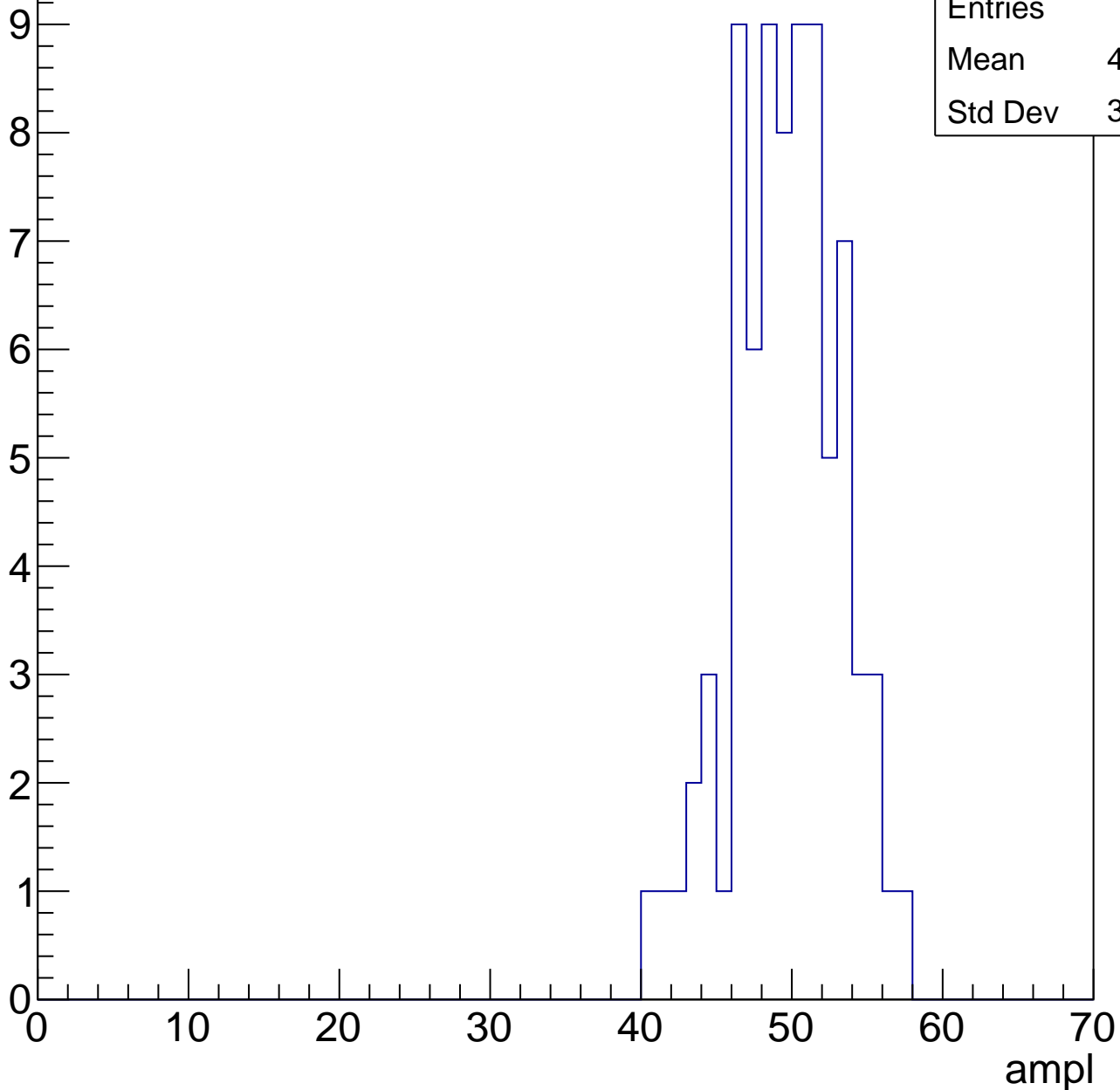


# B1L102S, U25-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

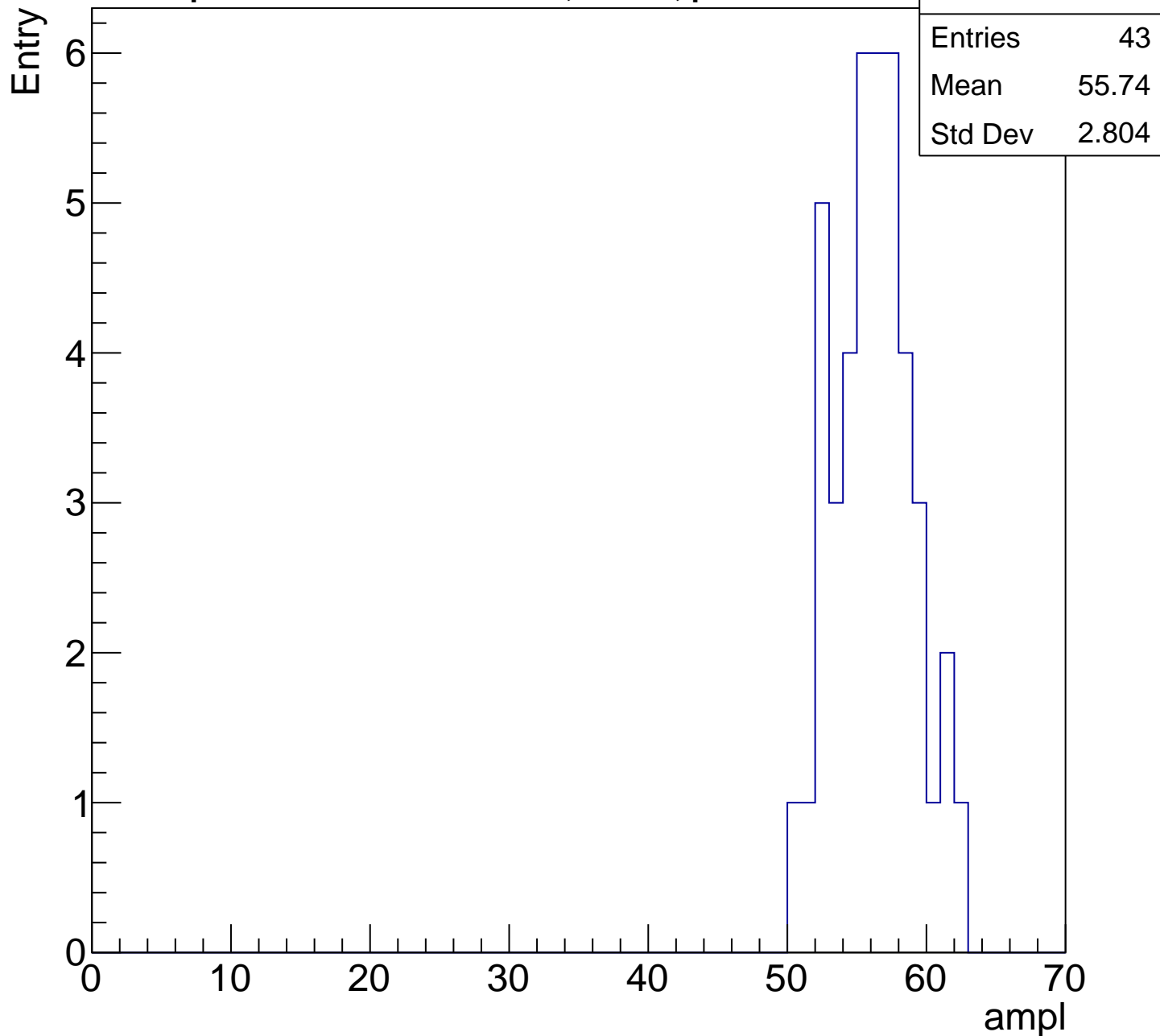
Entry

Entries	79
Mean	49.19
Std Dev	3.497



# B1L102S, U25-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

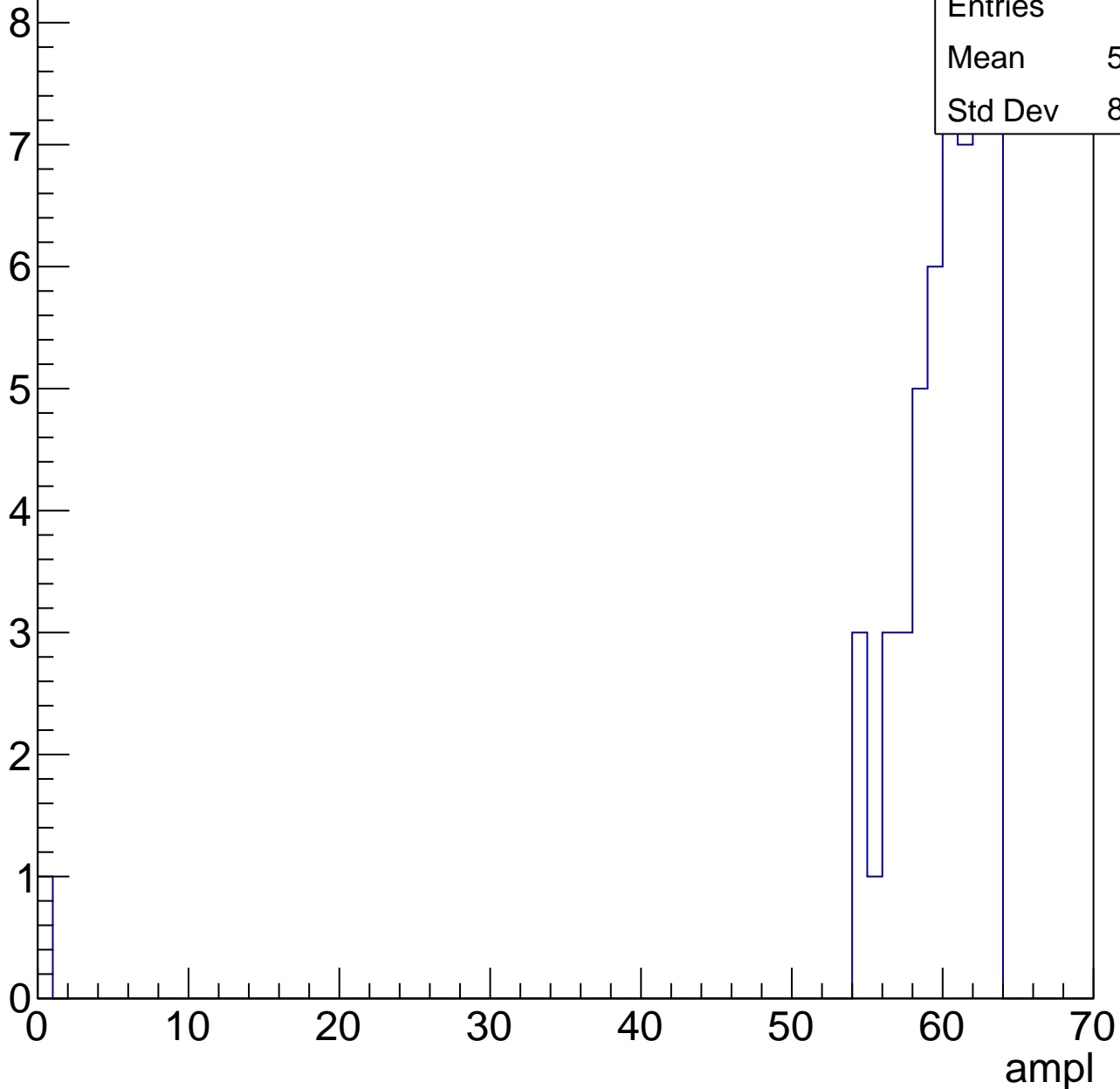


# B1L102S, U25-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

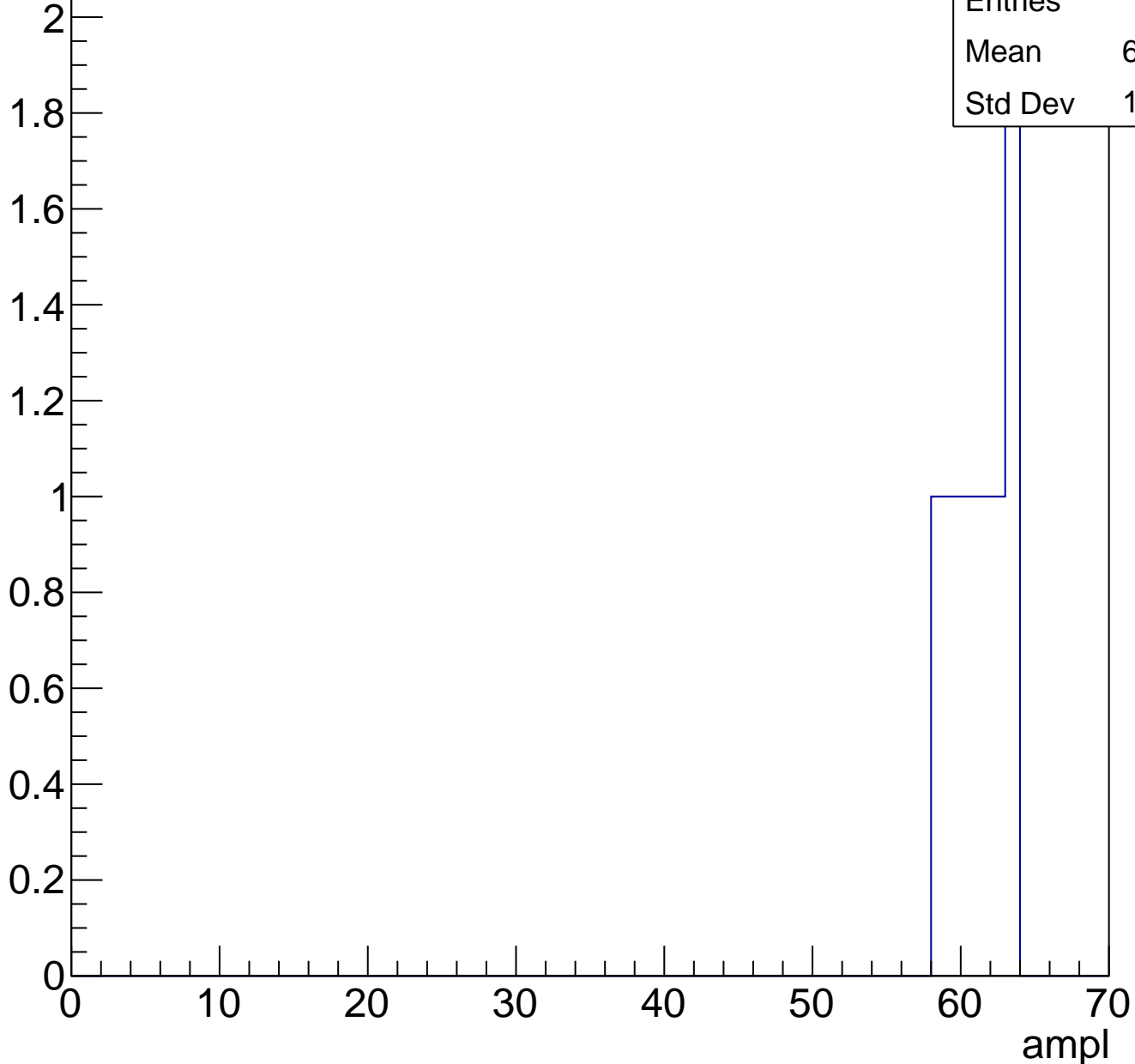
Entries	53
Mean	58.62
Std Dev	8.517



# B1L102S, U25-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch23, adc0

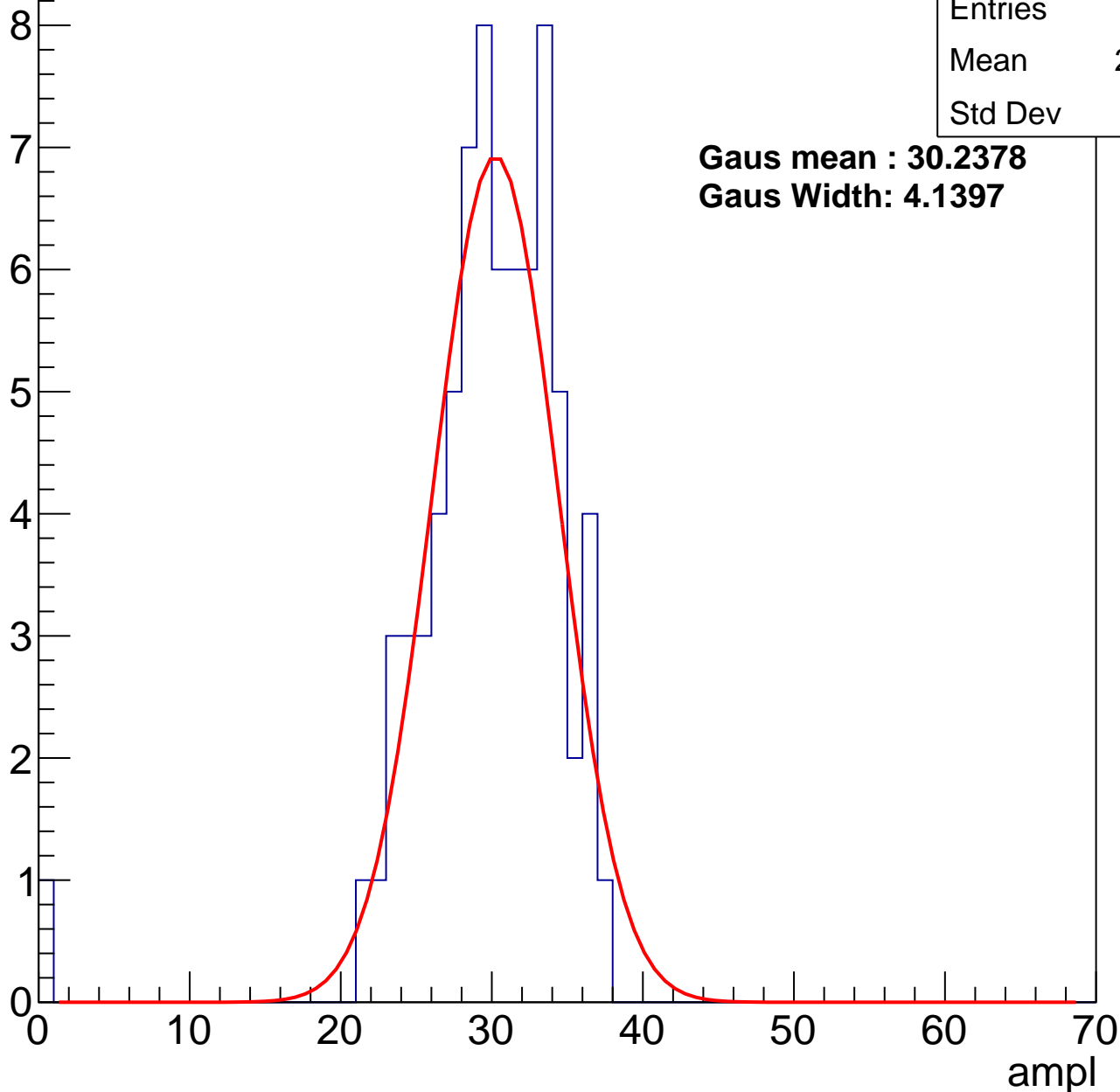
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	29.31
Std Dev	5.08

**Gaus mean : 30.2378**

**Gaus Width: 4.1397**



# B1L102S, U25-ch23, adc1

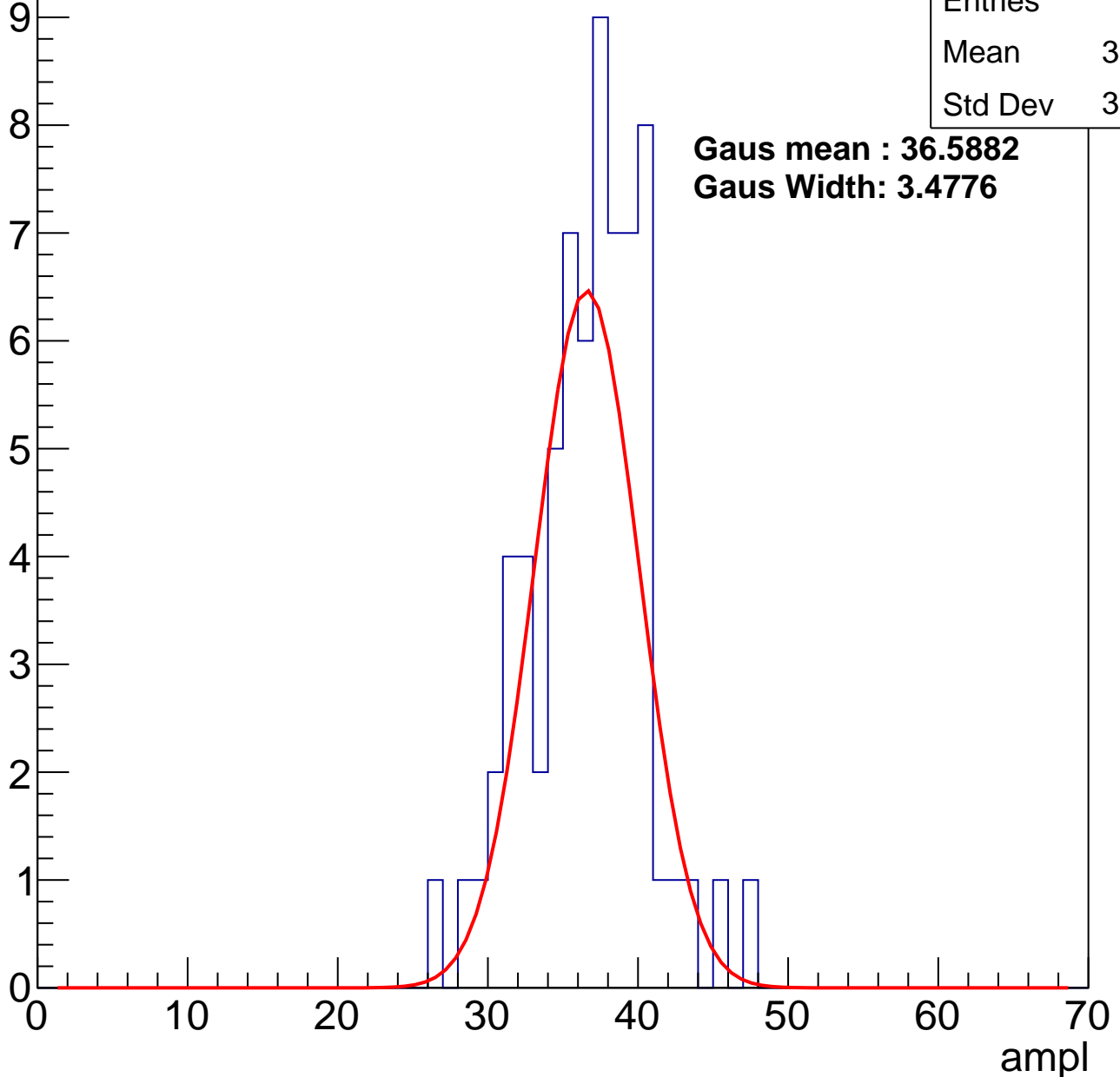
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.26
Std Dev	3.862

**Gaus mean : 36.5882**

**Gaus Width: 3.4776**



# B1L102S, U25-ch23, adc2

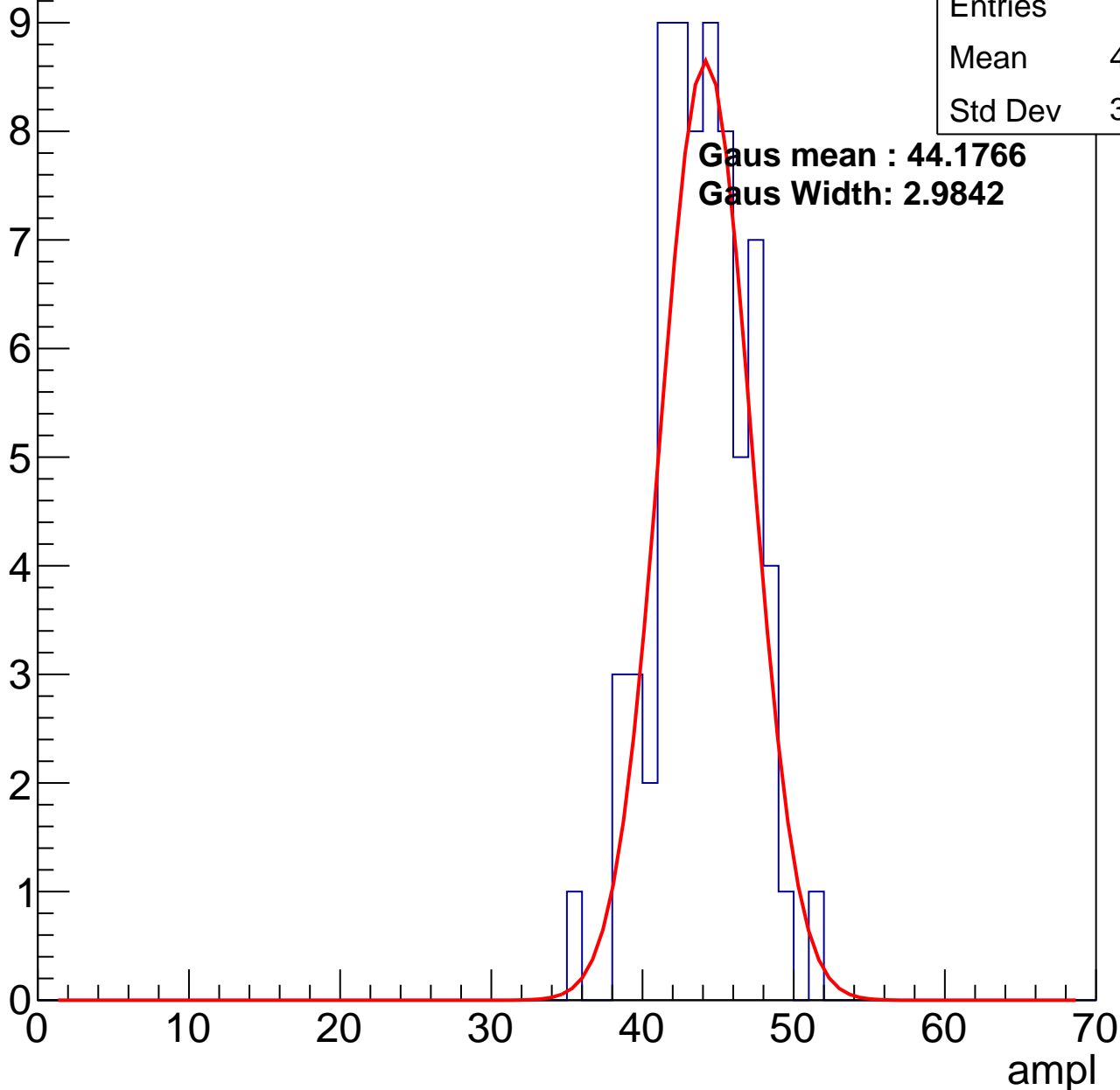
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.49
Std Dev	3.004

**Gaus mean : 44.1766**

**Gaus Width: 2.9842**

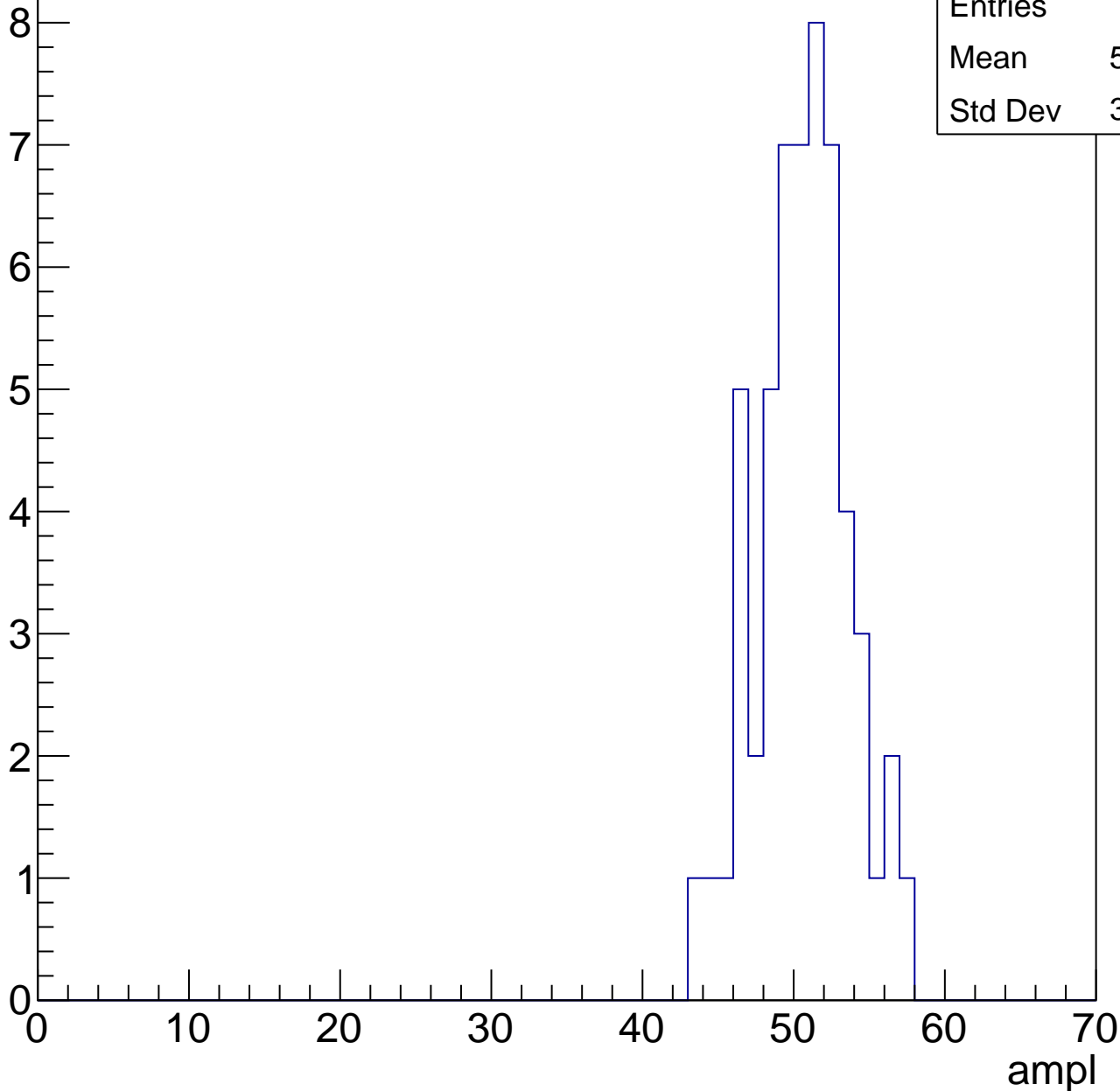


# B1L102S, U25-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.16
Std Dev	3.014

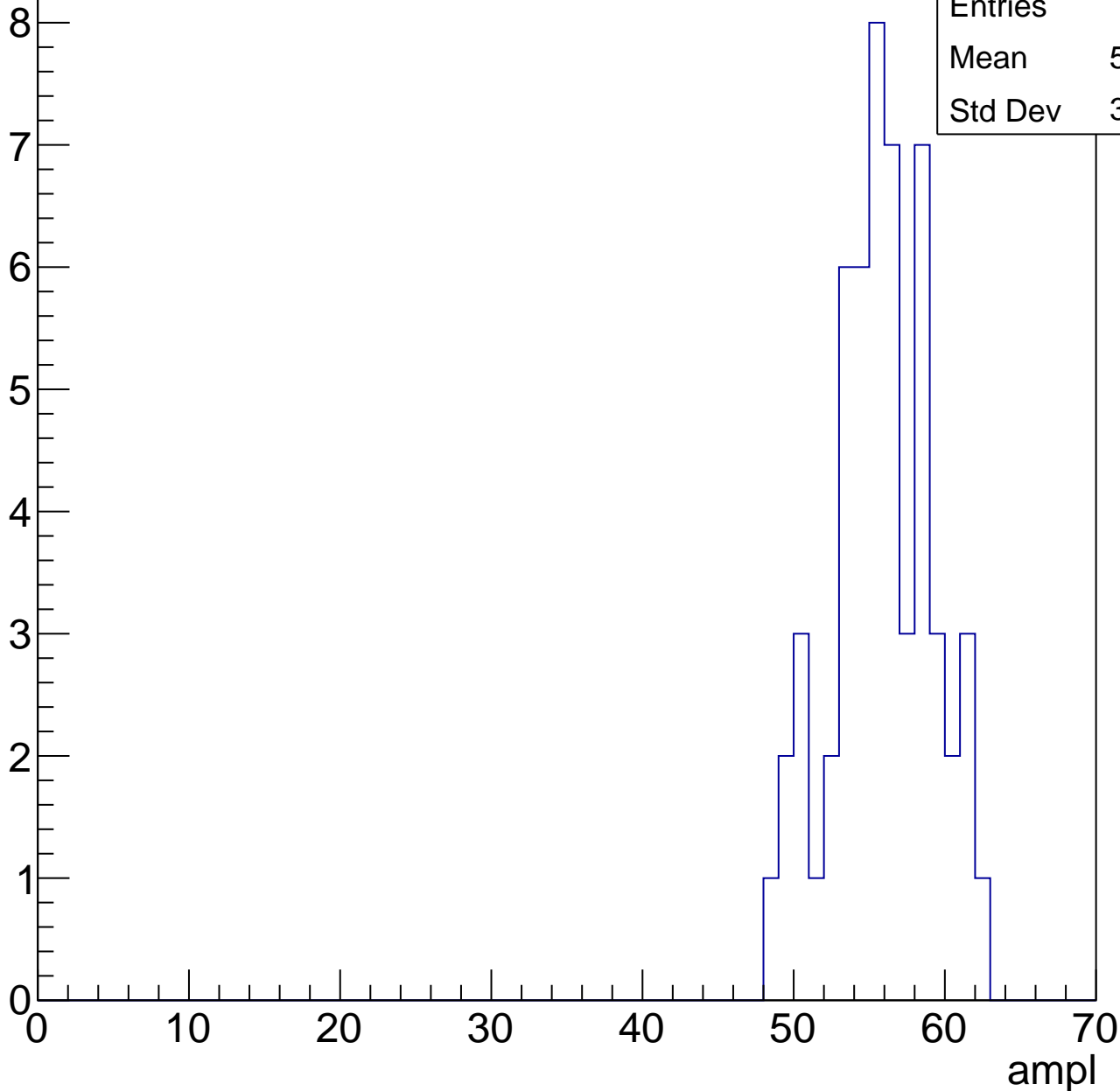


# B1L102S, U25-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

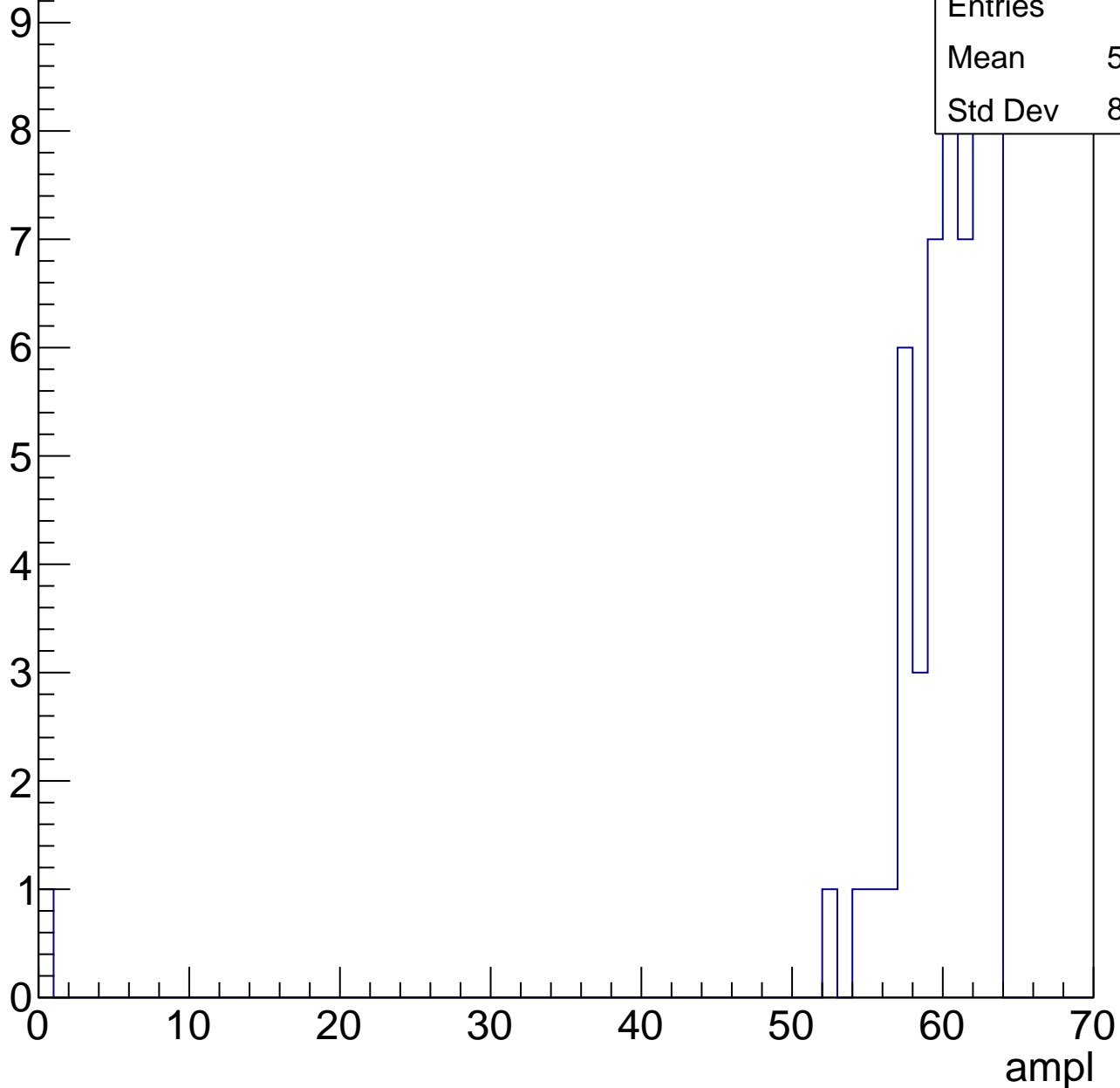
Entries	55
Mean	55.35
Std Dev	3.276



# B1L102S, U25-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U25-ch24, adc0

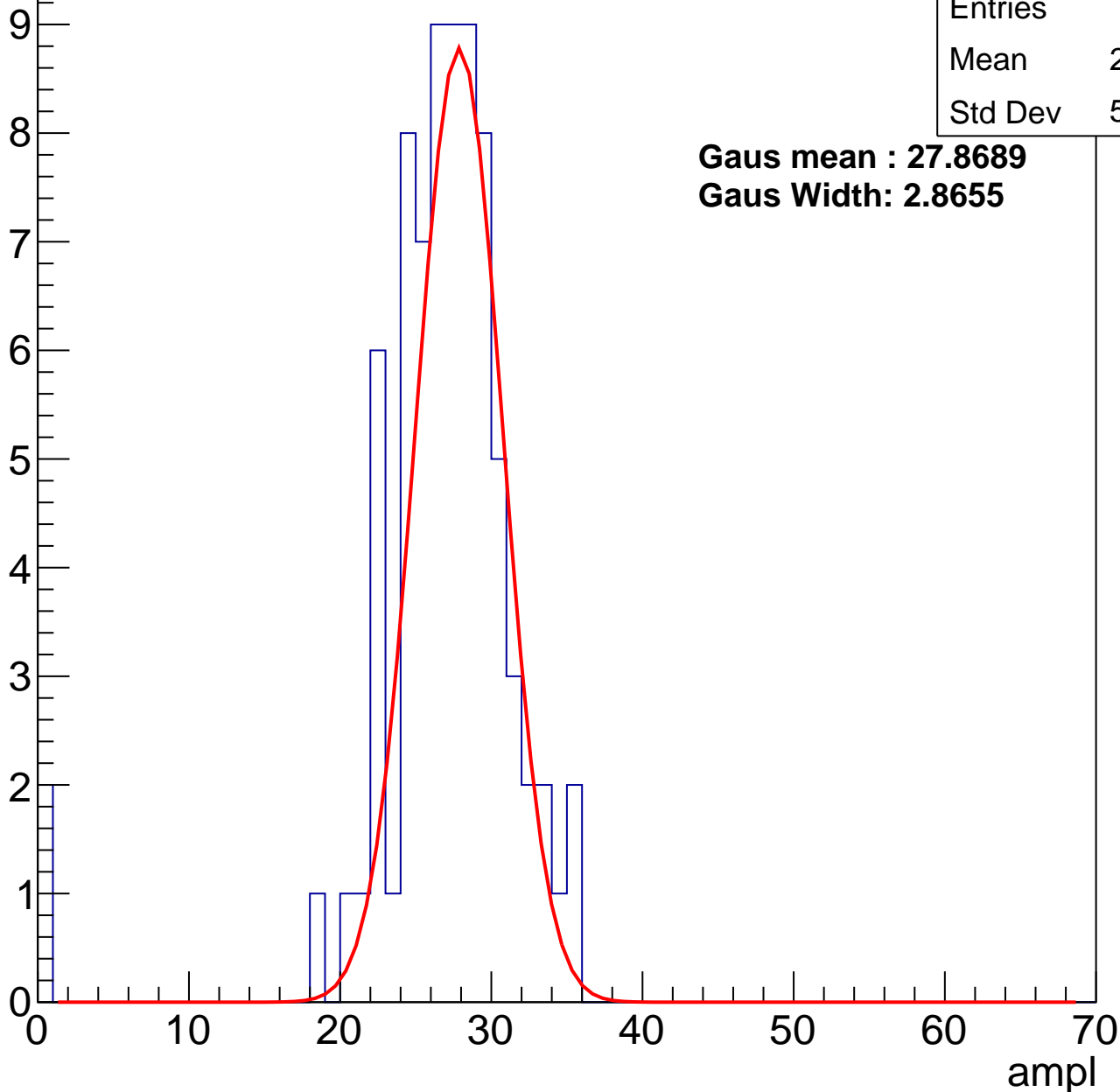
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	26.22
Std Dev	5.448

**Gaus mean : 27.8689**

**Gaus Width: 2.8655**



# B1L102S, U25-ch24, adc1

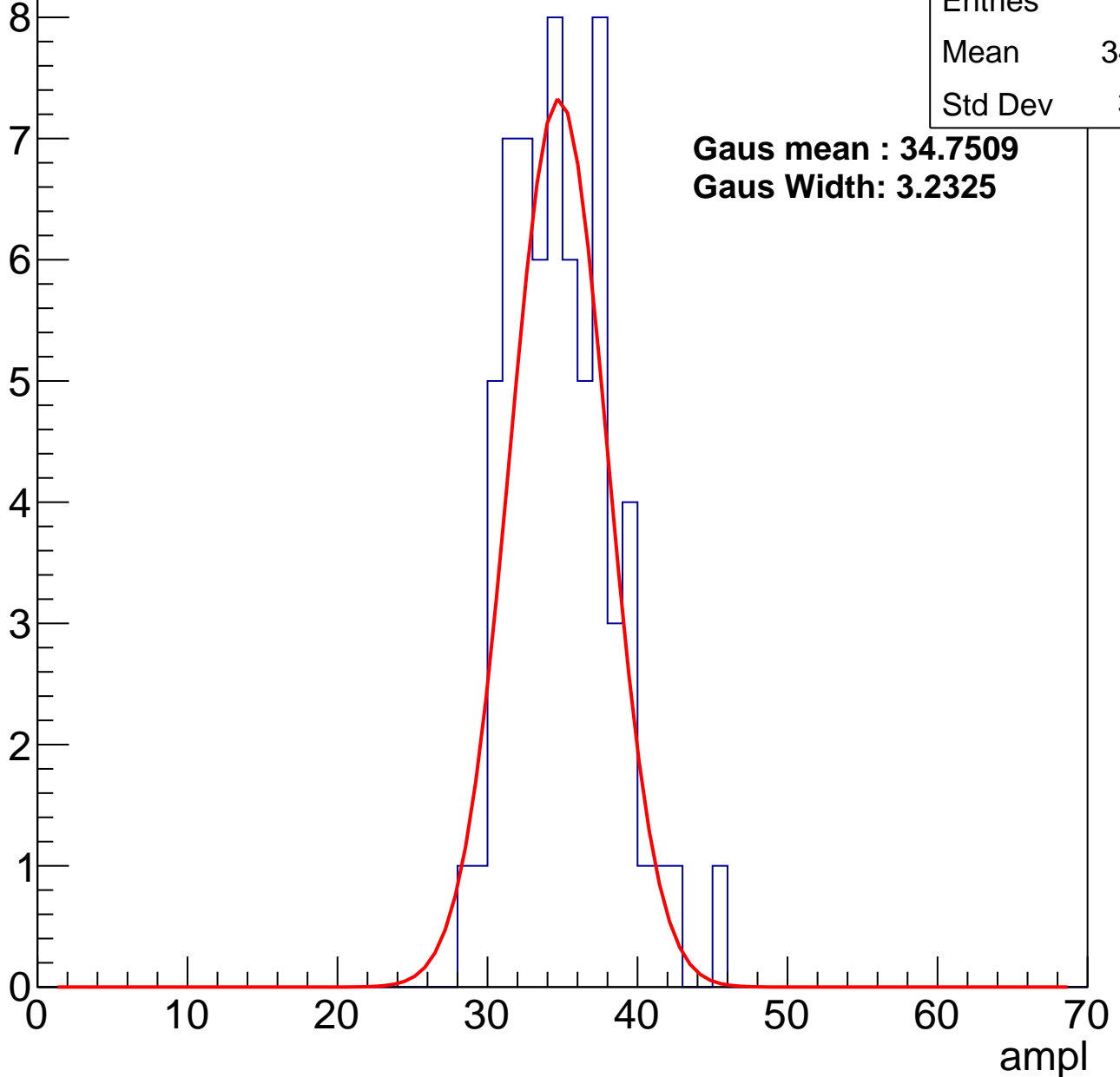
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.49
Std Dev	3.37

**Gaus mean : 34.7509**

**Gaus Width: 3.2325**



# B1L102S, U25-ch24, adc2

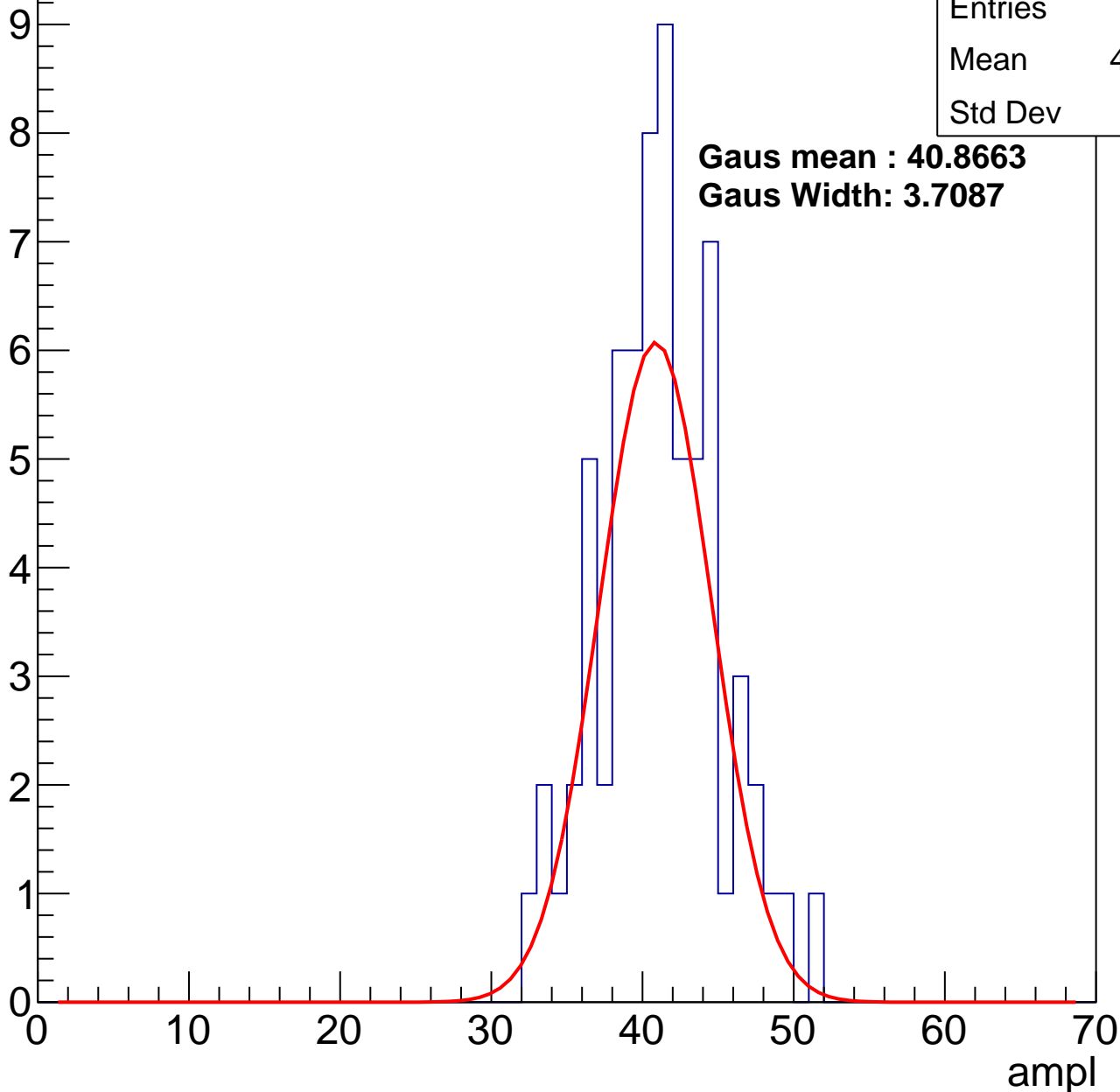
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.66
Std Dev	3.89

**Gaus mean : 40.8663**

**Gaus Width: 3.7087**

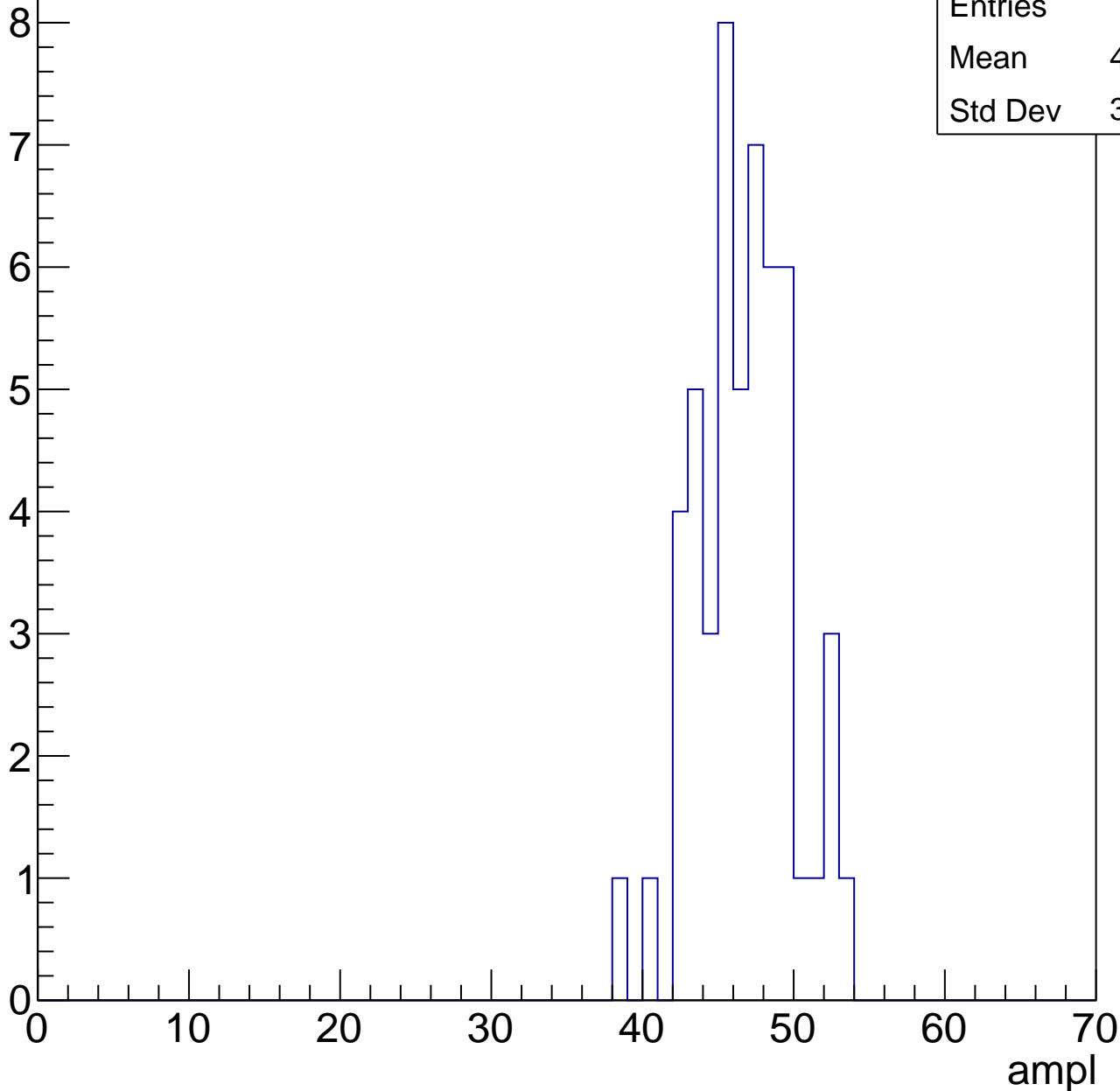


# B1L102S, U25-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	46.23
Std Dev	3.129

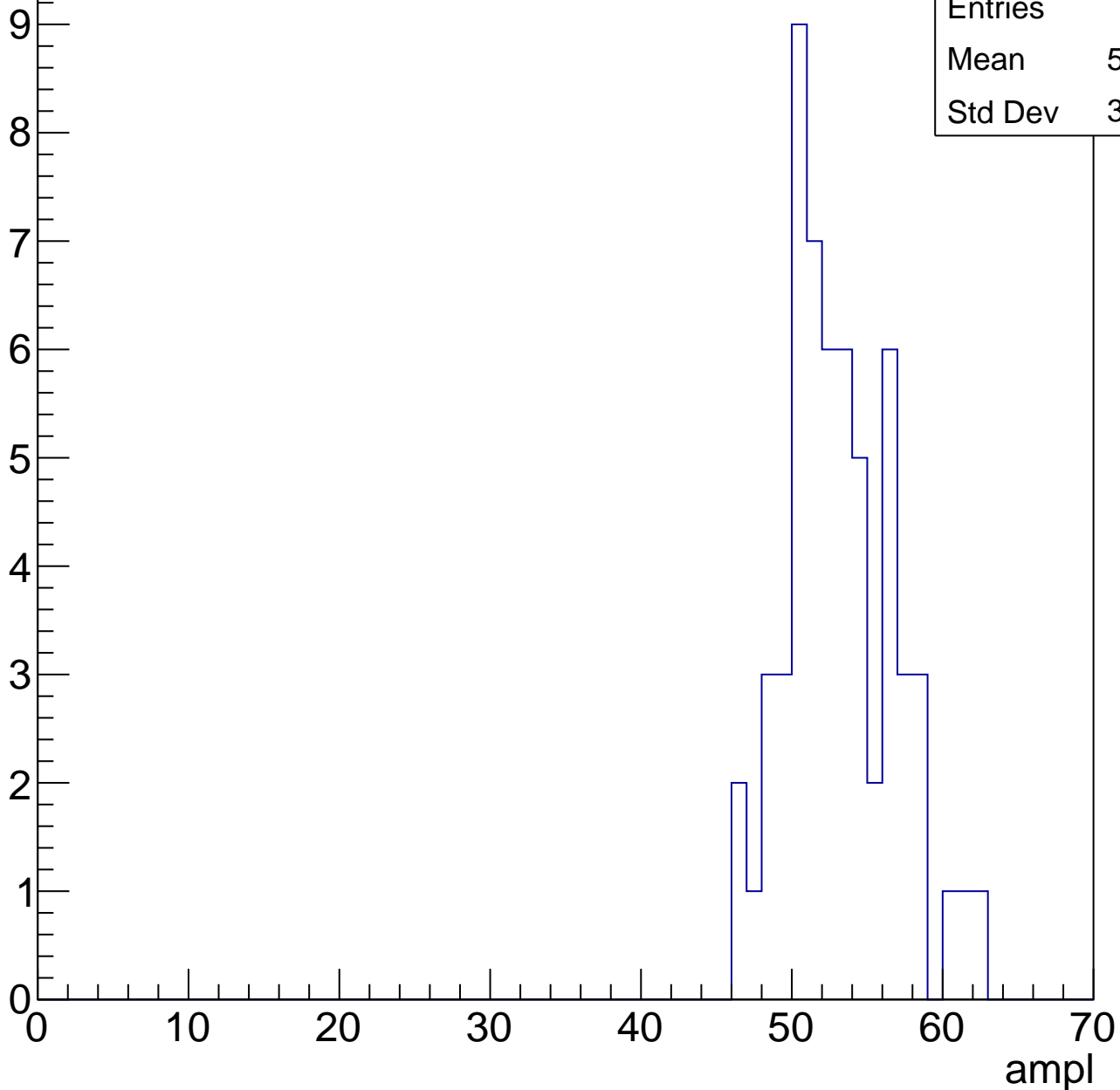


# B1L102S, U25-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	52.73
Std Dev	3.569

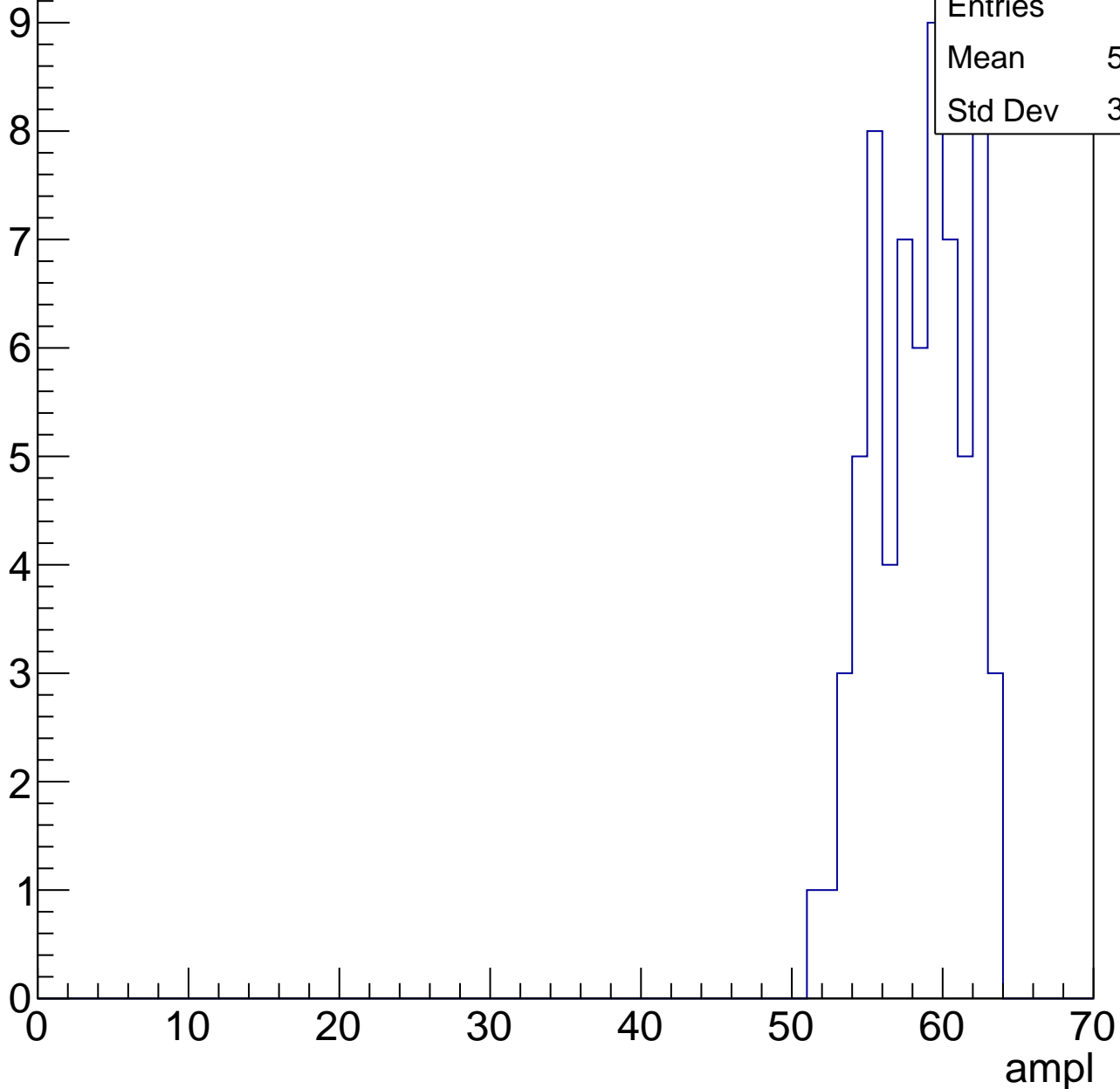


# B1L102S, U25-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	57.97
Std Dev	3.042

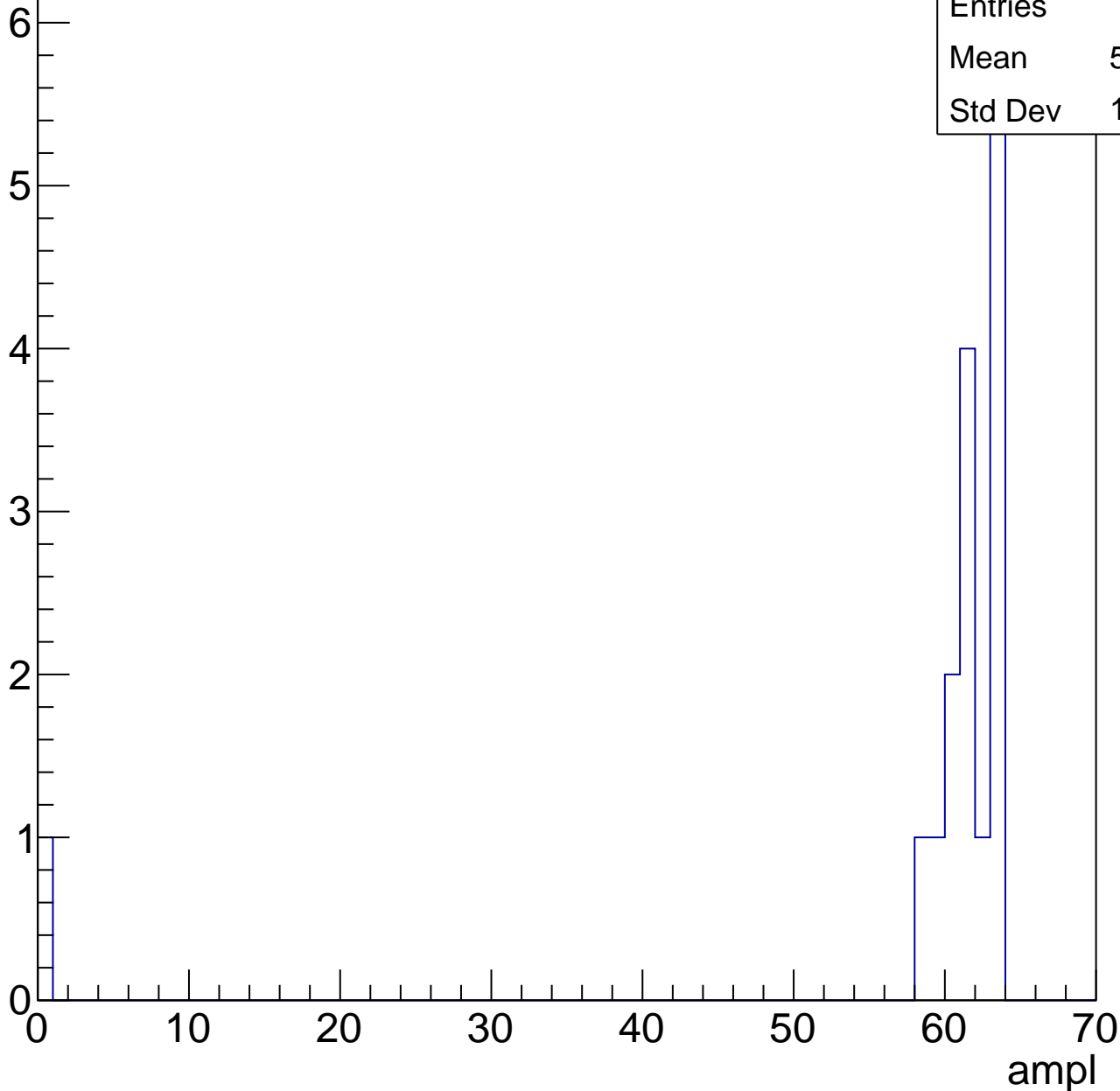


# B1L102S, U25-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.56
Std Dev	14.94





# B1L102S, U25-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch25, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	86
Mean	28.37
Std Dev	6.42

**Gaus mean : 30.0464**

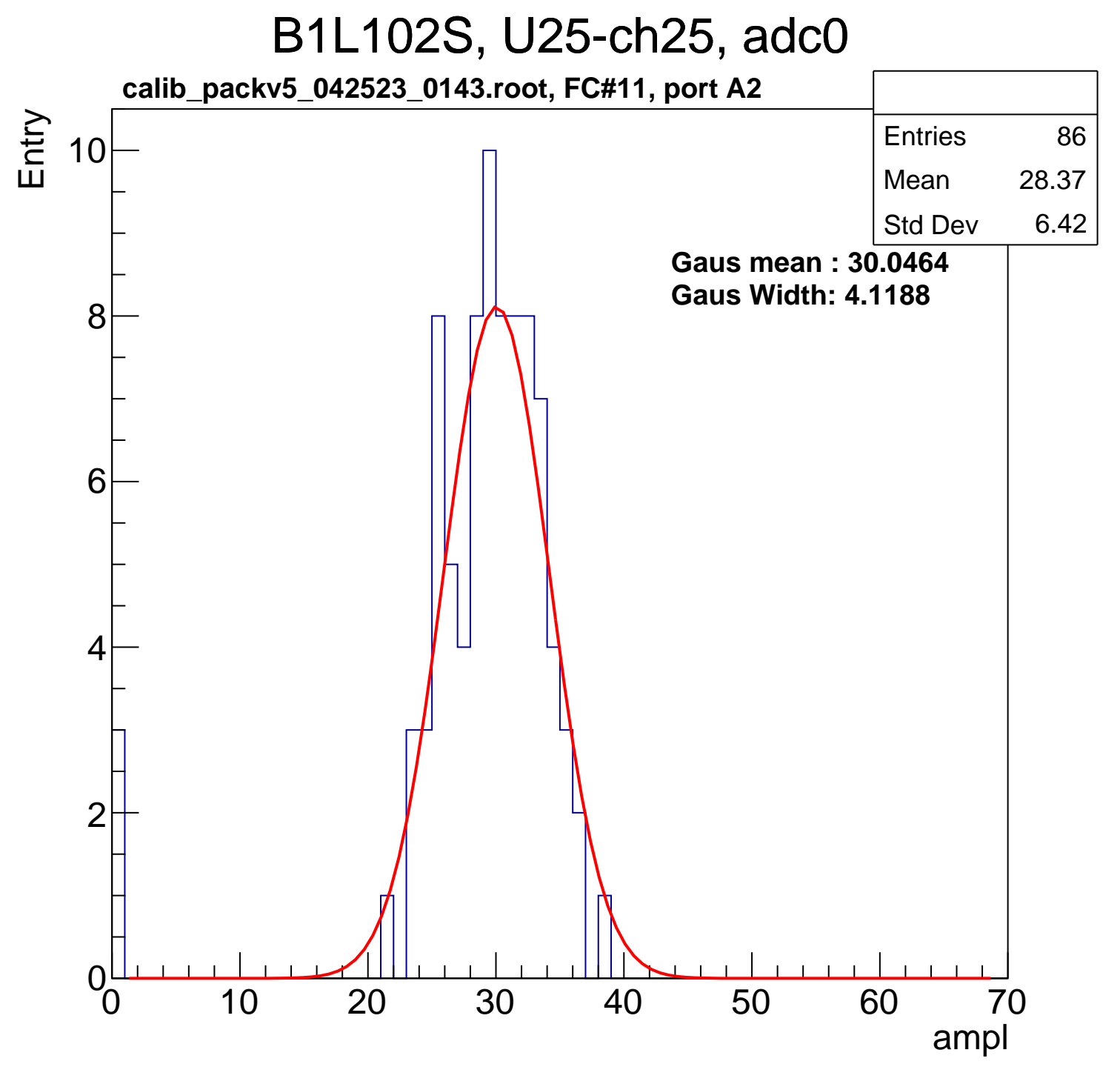
**Gaus Width: 4.1188**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch25, adc1

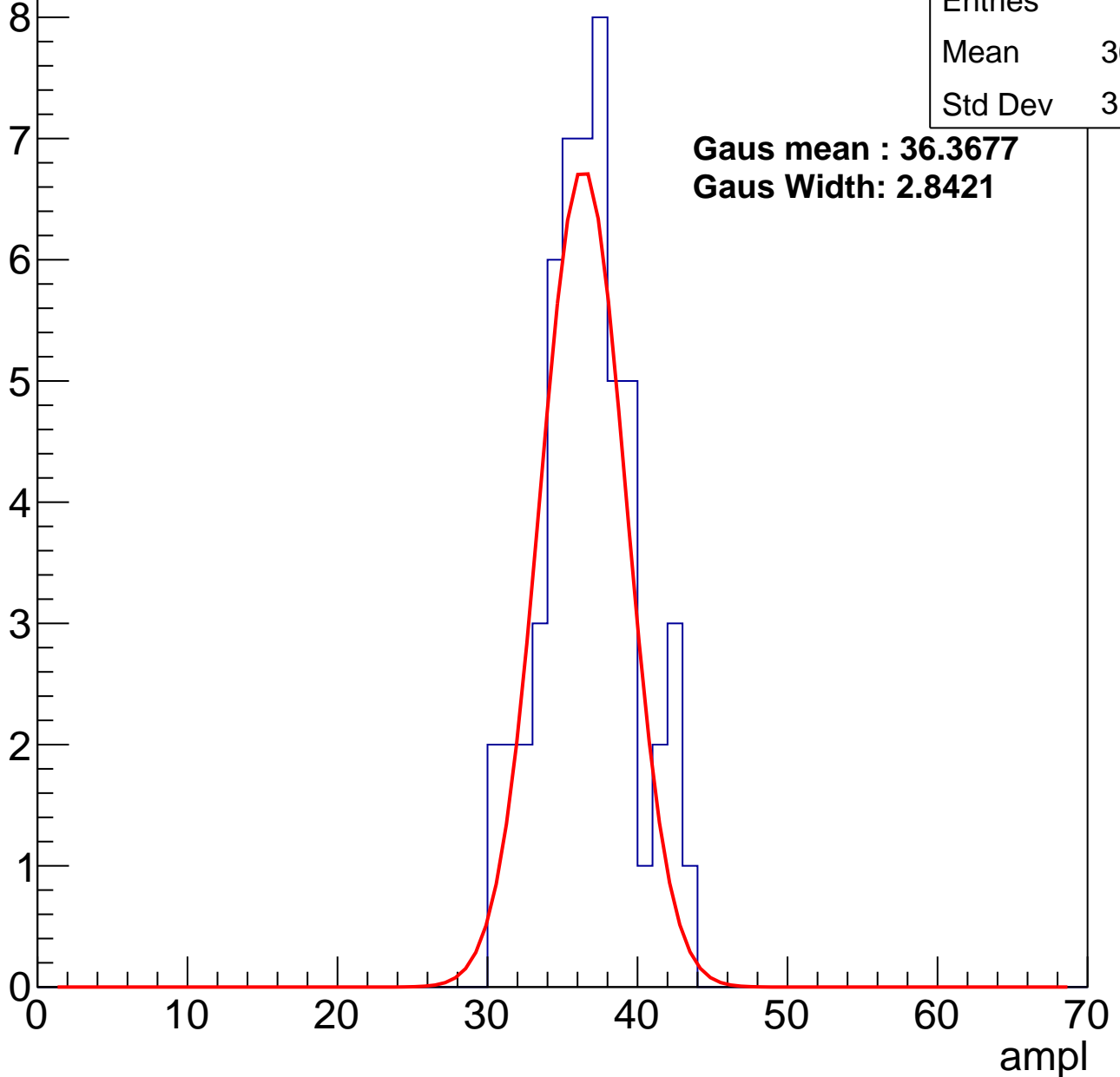
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	36.26
Std Dev	3.056

**Gaus mean : 36.3677**

**Gaus Width: 2.8421**



# B1L102S, U25-ch25, adc2

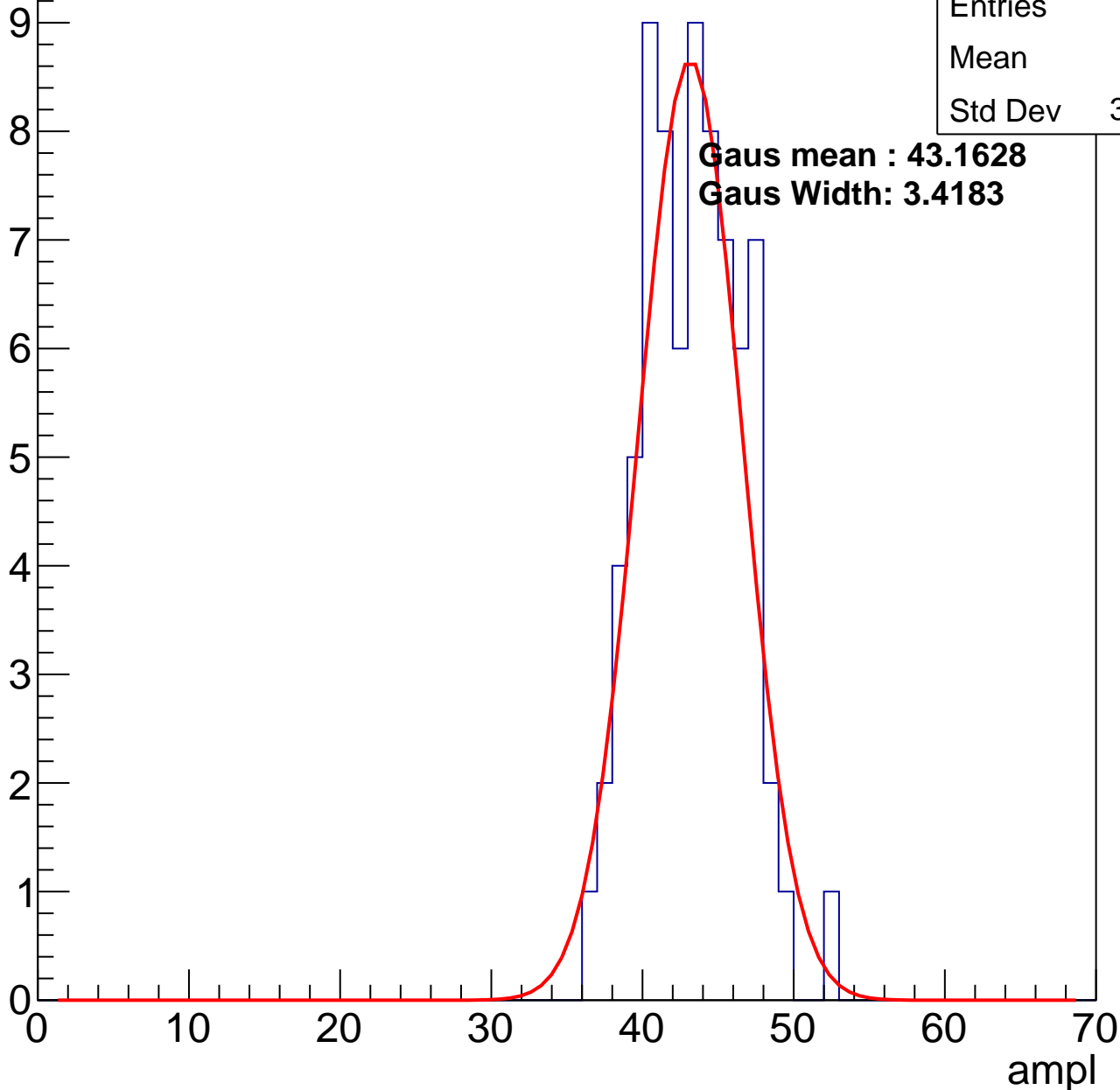
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.8
Std Dev	3.216

**Gaus mean : 43.1628**

**Gaus Width: 3.4183**

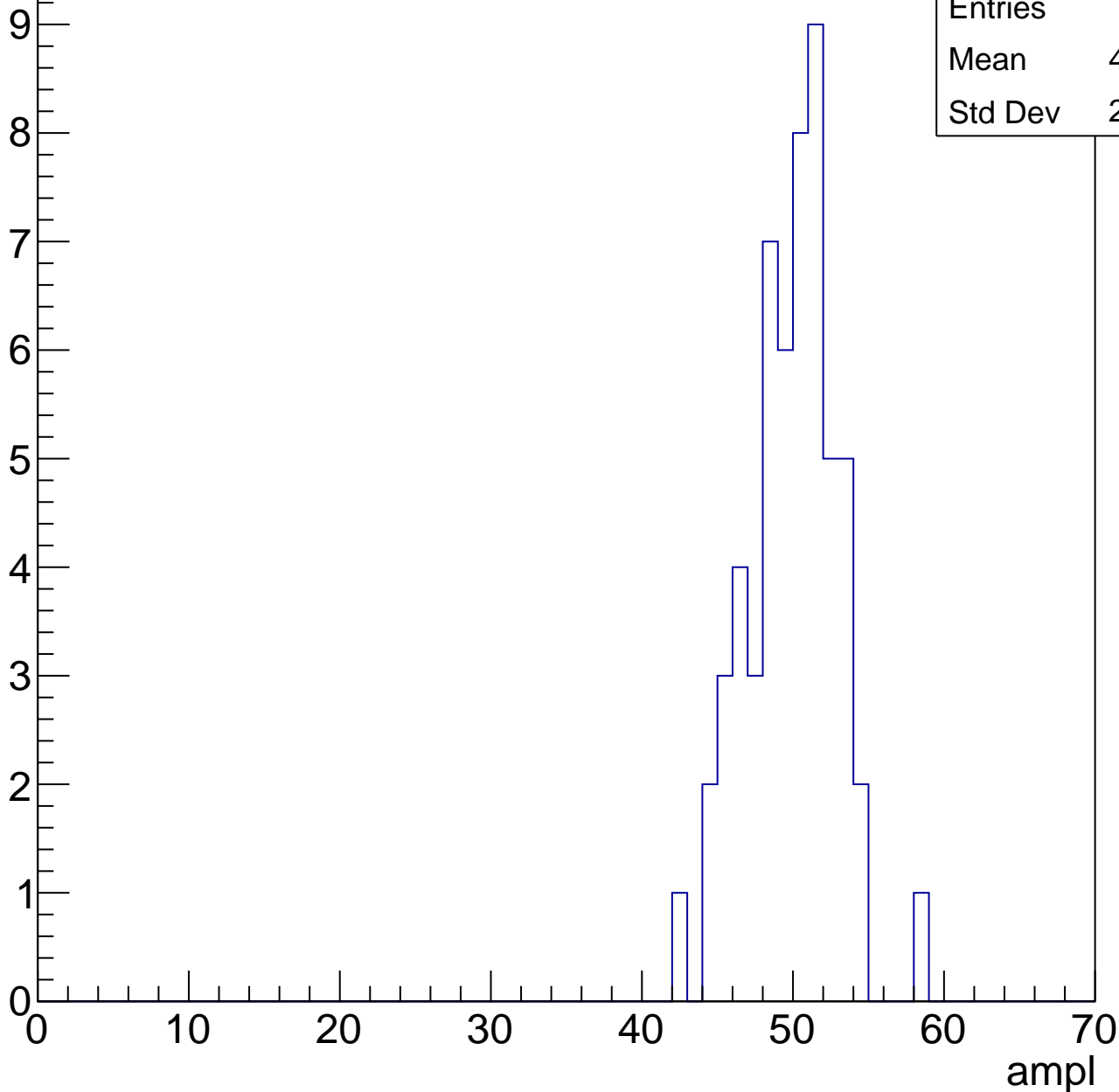


# B1L102S, U25-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.46
Std Dev	2.958

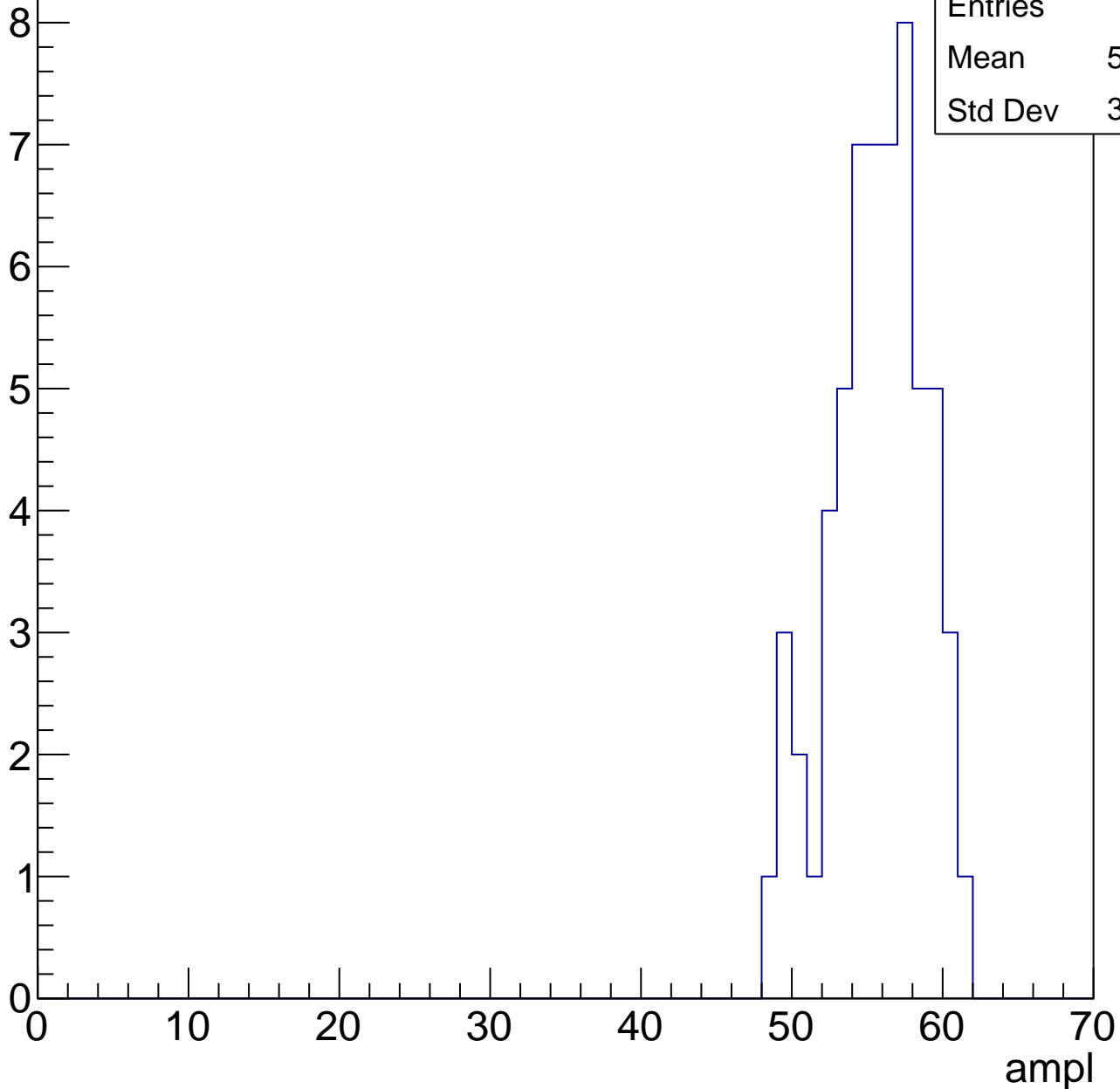


# B1L102S, U25-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.19
Std Dev	3.078

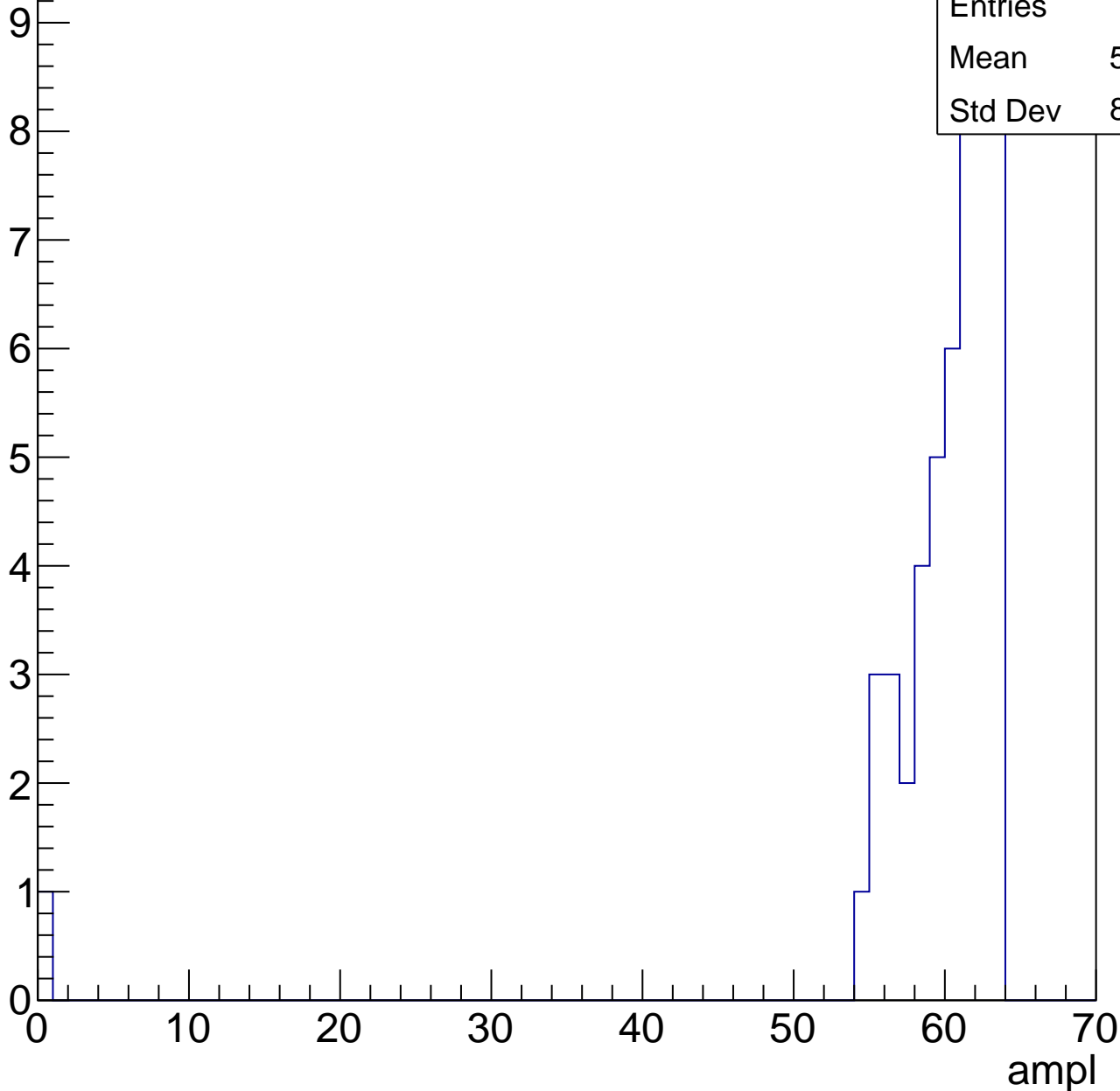


# B1L102S, U25-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.78
Std Dev	8.769



# B1L102S, U25-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch26, adc0

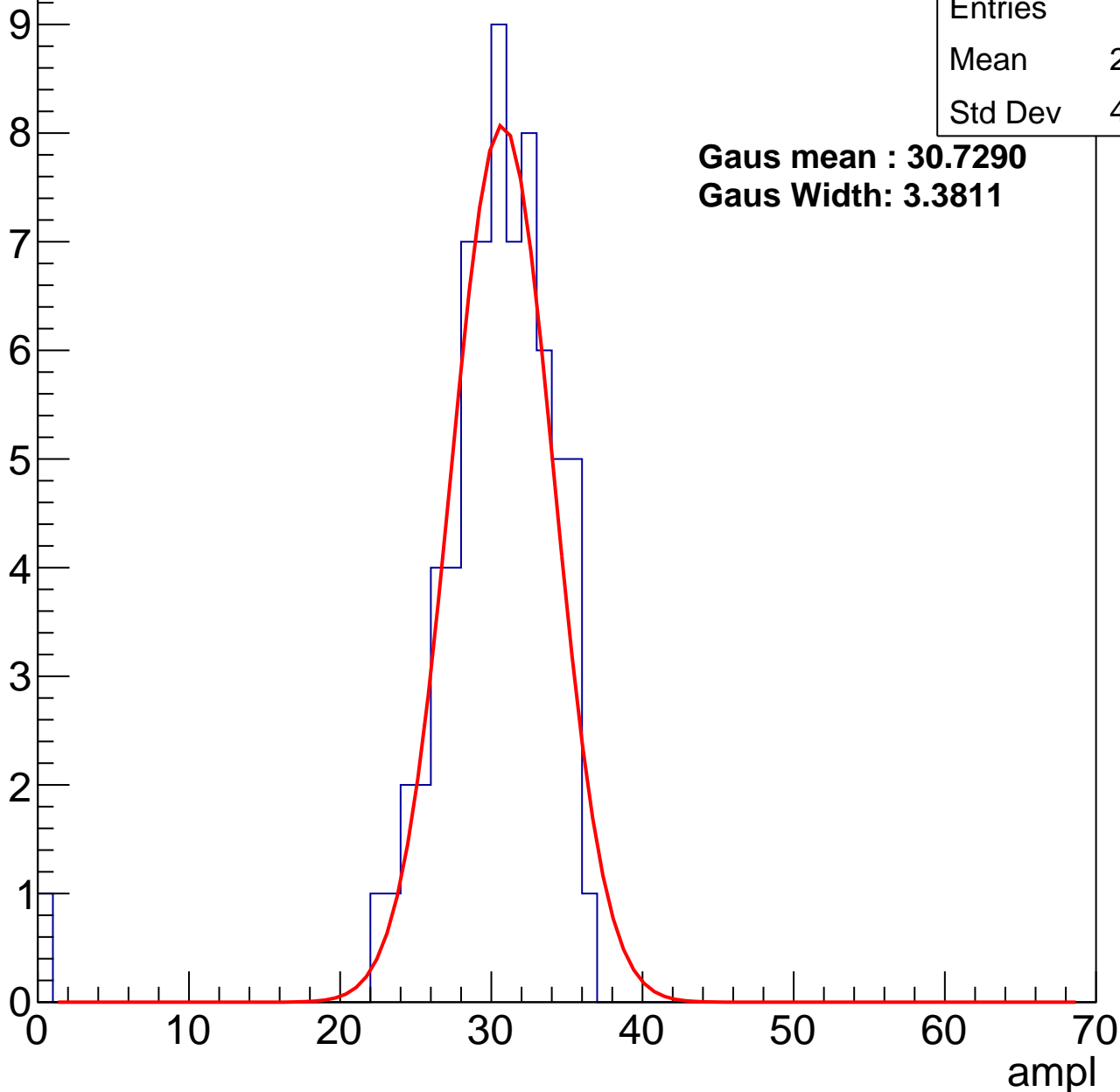
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.66
Std Dev	4.775

**Gaus mean : 30.7290**

**Gaus Width: 3.3811**



# B1L102S, U25-ch26, adc1

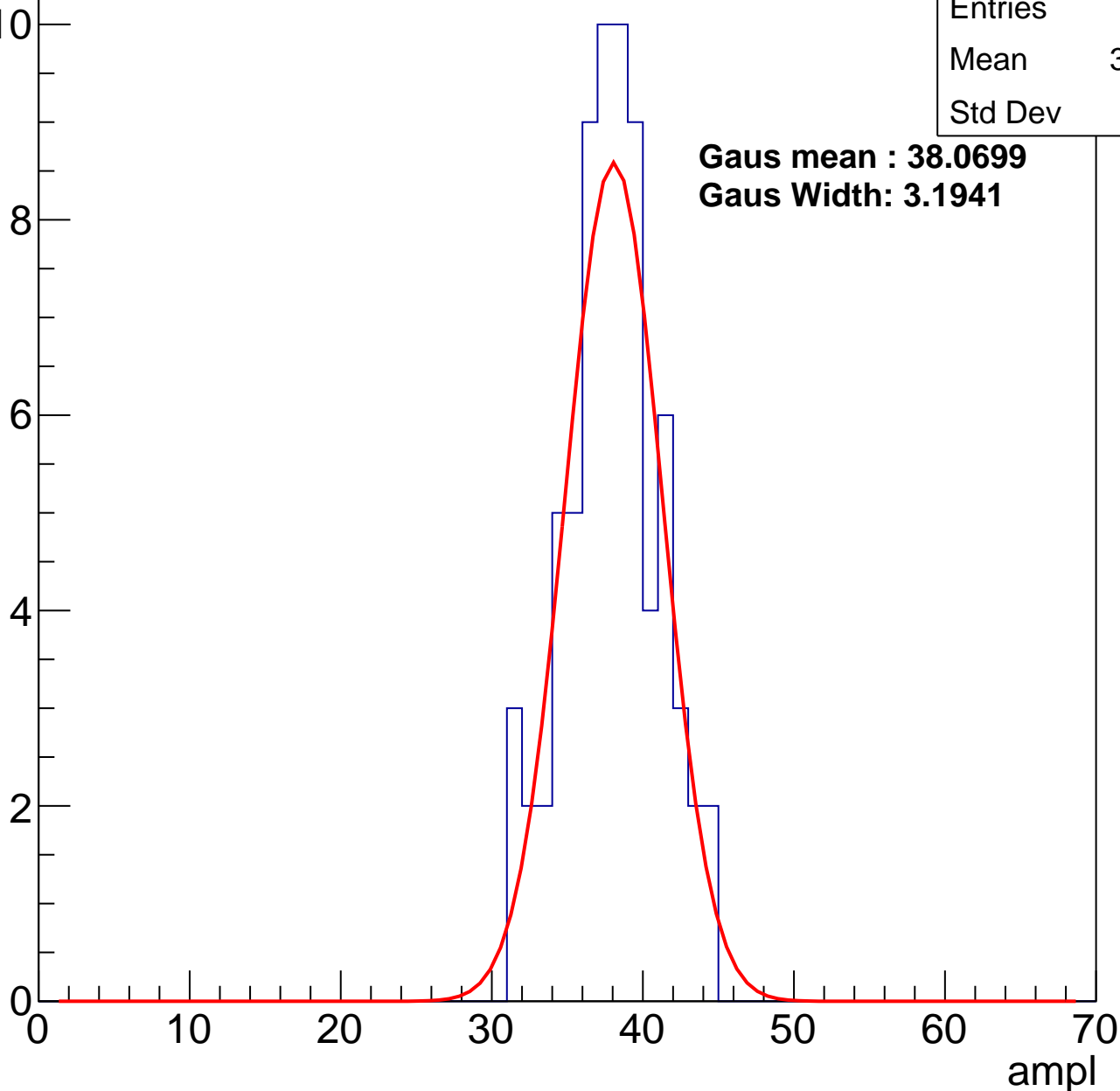
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.49
Std Dev	3.05

**Gaus mean : 38.0699**

**Gaus Width: 3.1941**



# B1L102S, U25-ch26, adc2

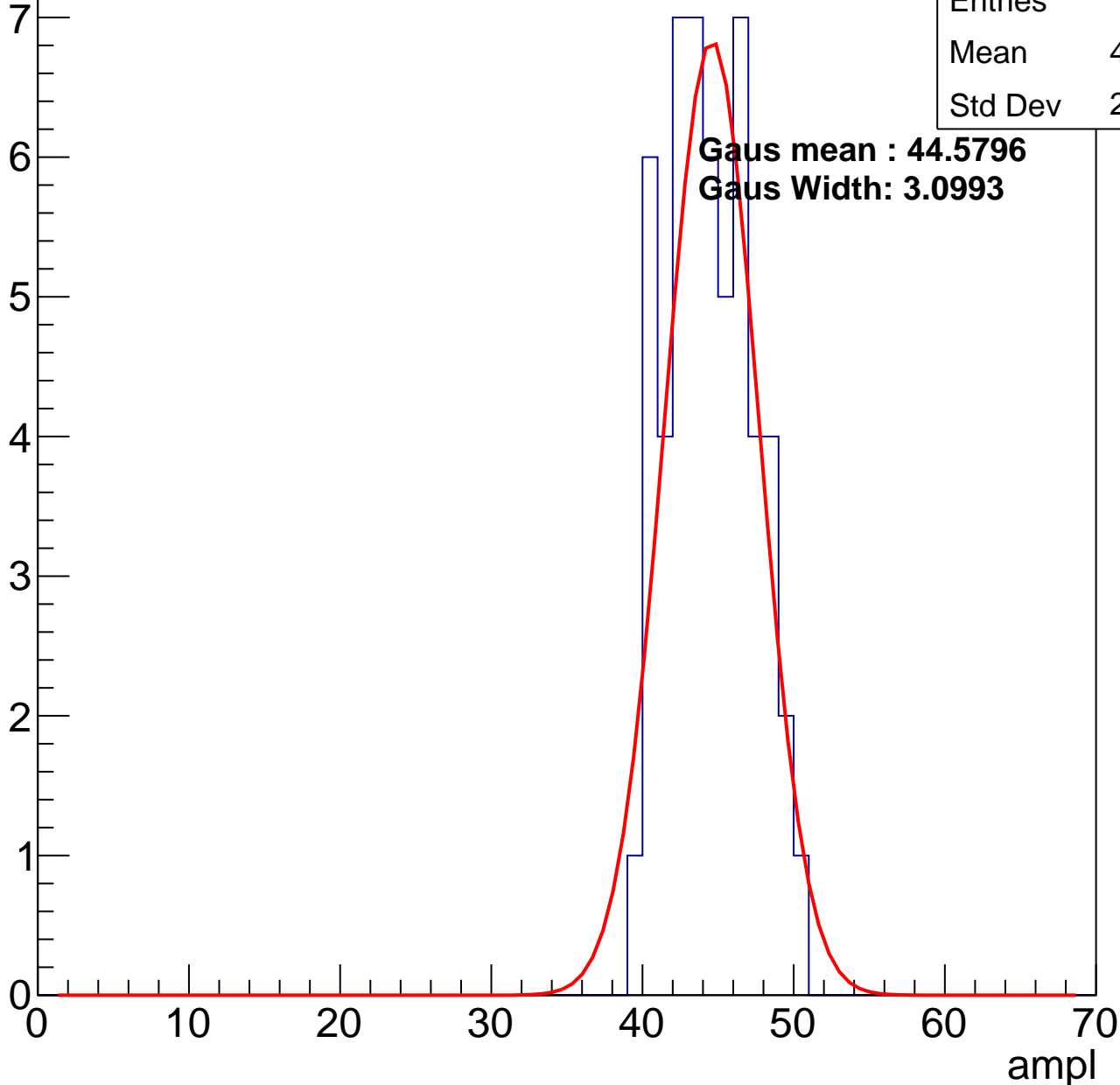
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	44.02
Std Dev	2.759

**Gaus mean : 44.5796**

**Gaus Width: 3.0993**

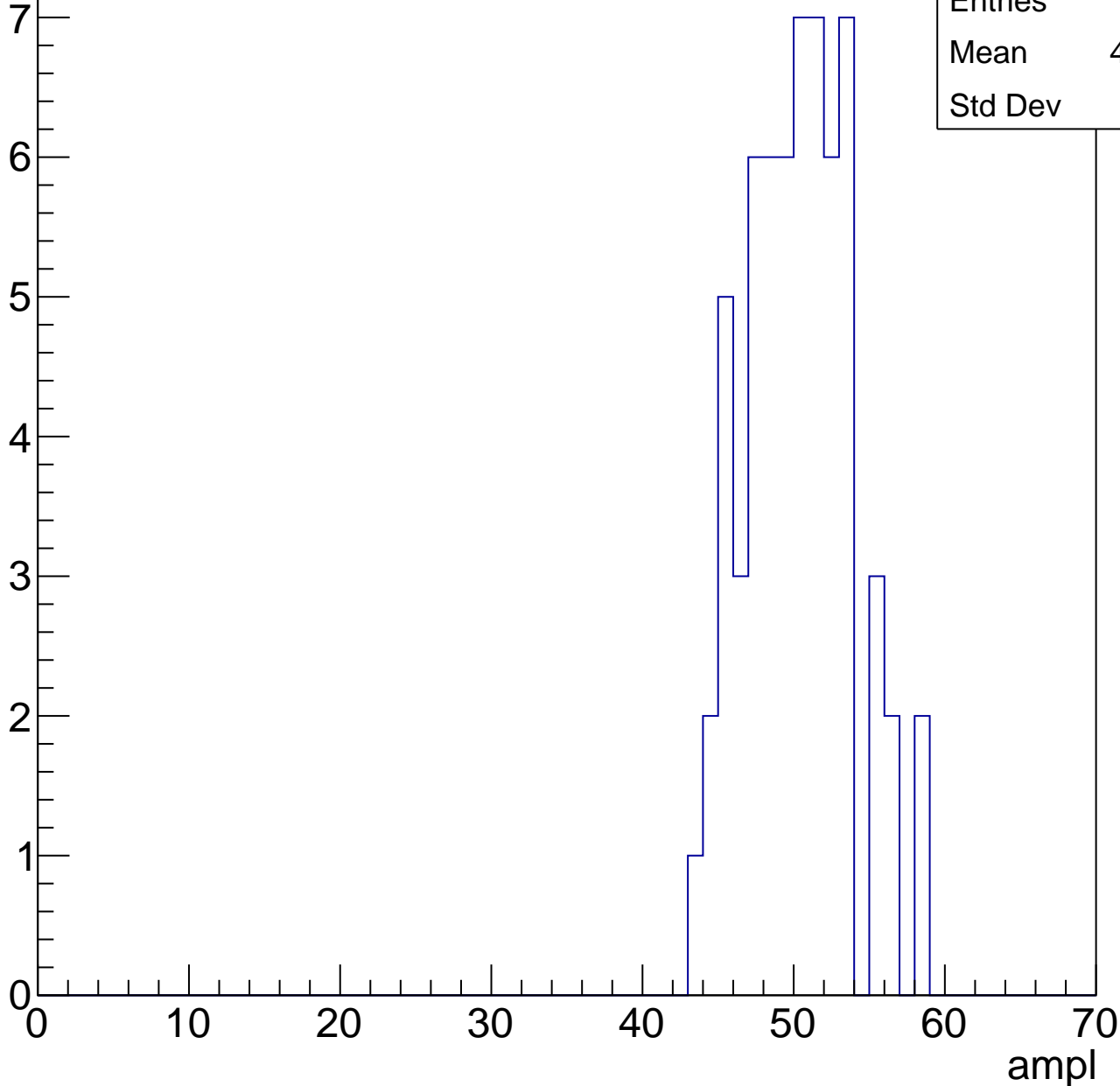


# B1L102S, U25-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.86
Std Dev	3.44



# B1L102S, U25-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	57
Mean	55.35
Std Dev	3.064

Entry

10

8

6

4

2

0

0

10

20

30

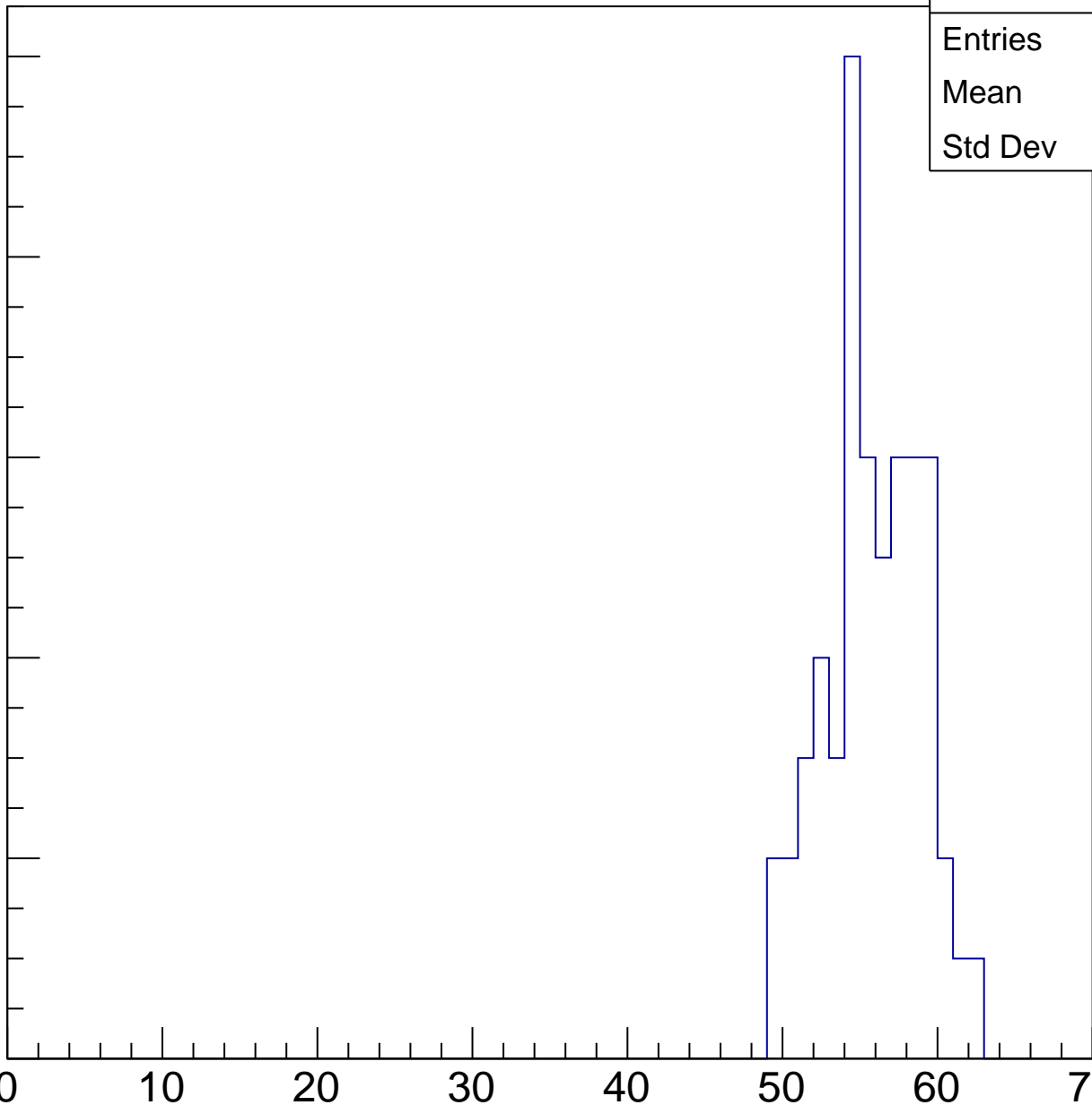
40

50

60

70

ampl

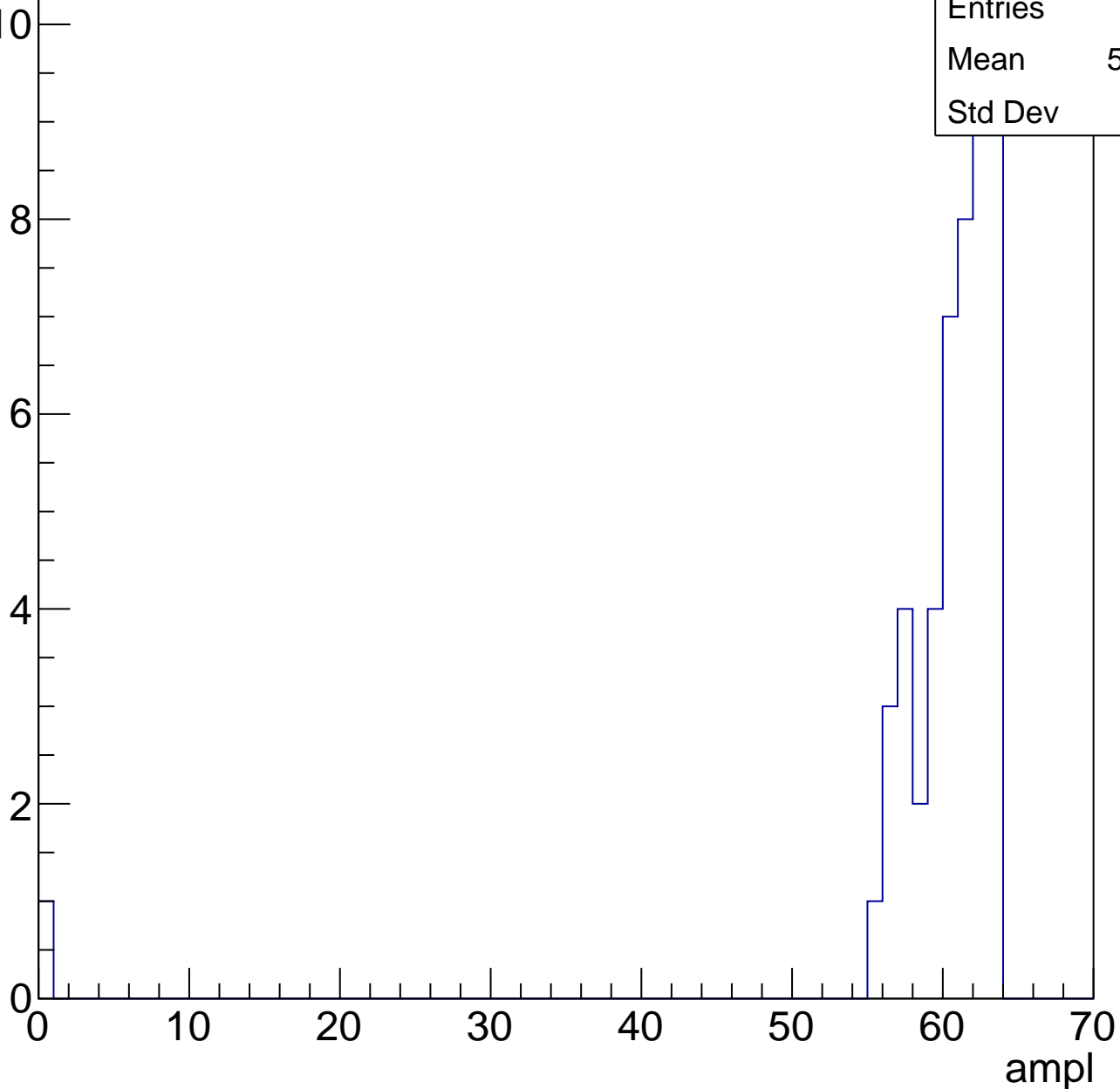


# B1L102S, U25-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.16
Std Dev	8.83



# B1L102S, U25-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch27, adc0

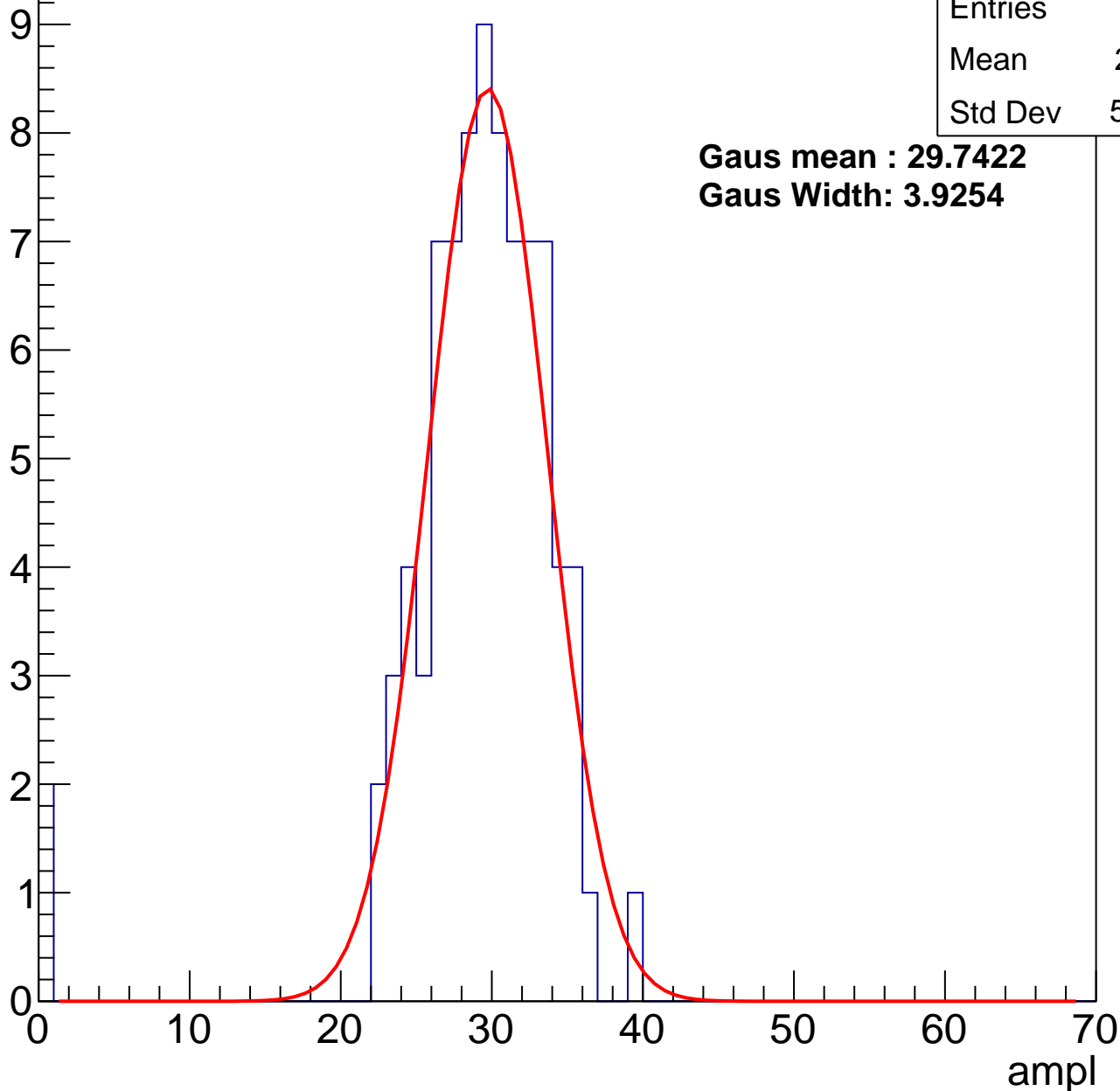
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.61
Std Dev	5.697

**Gaus mean : 29.7422**

**Gaus Width: 3.9254**



# B1L102S, U25-ch27, adc1

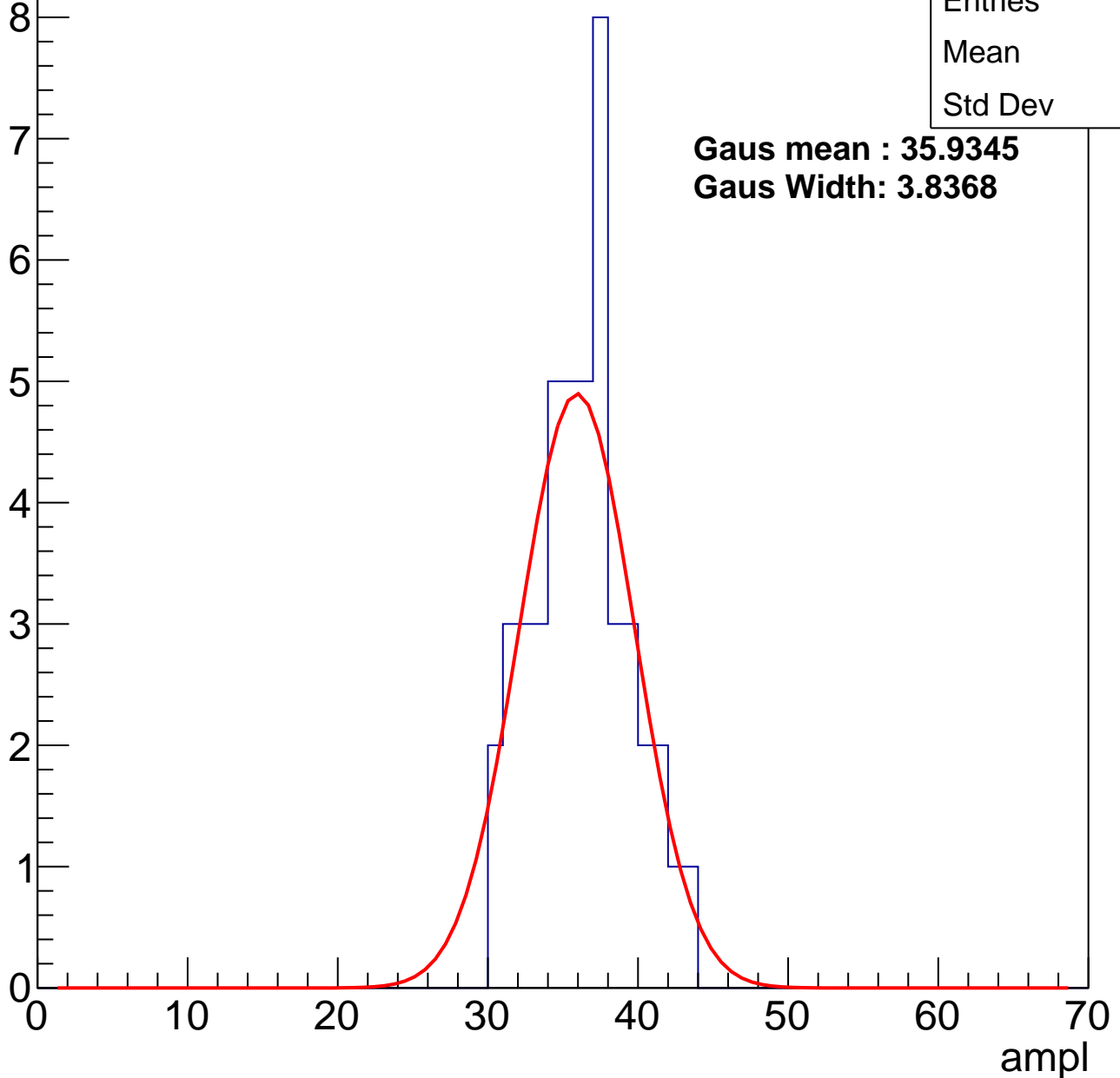
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	35.8
Std Dev	3.16

**Gaus mean : 35.9345**

**Gaus Width: 3.8368**



# B1L102S, U25-ch27, adc2

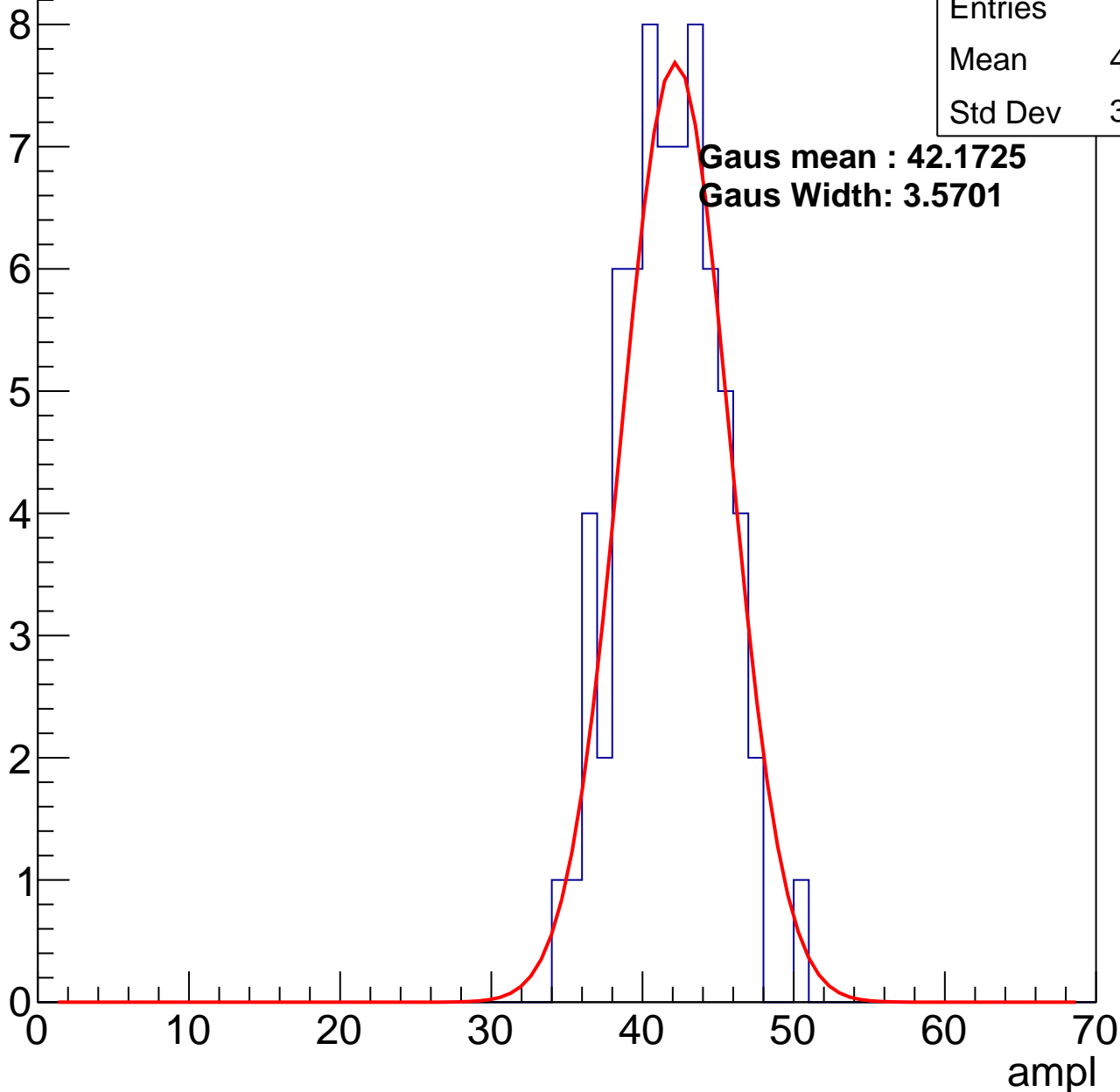
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.34
Std Dev	3.257

**Gaus mean : 42.1725**

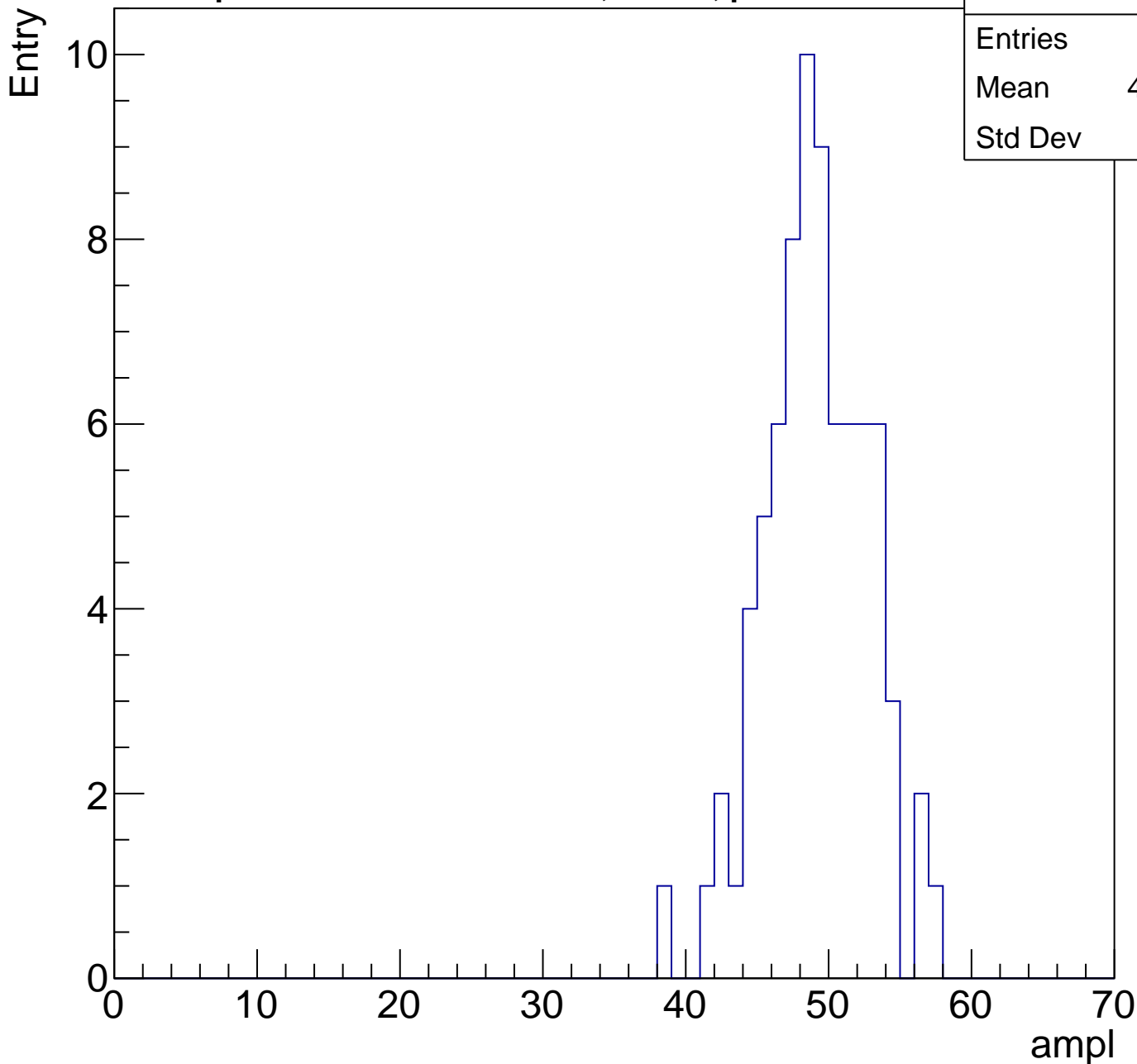
**Gaus Width: 3.5701**



# B1L102S, U25-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

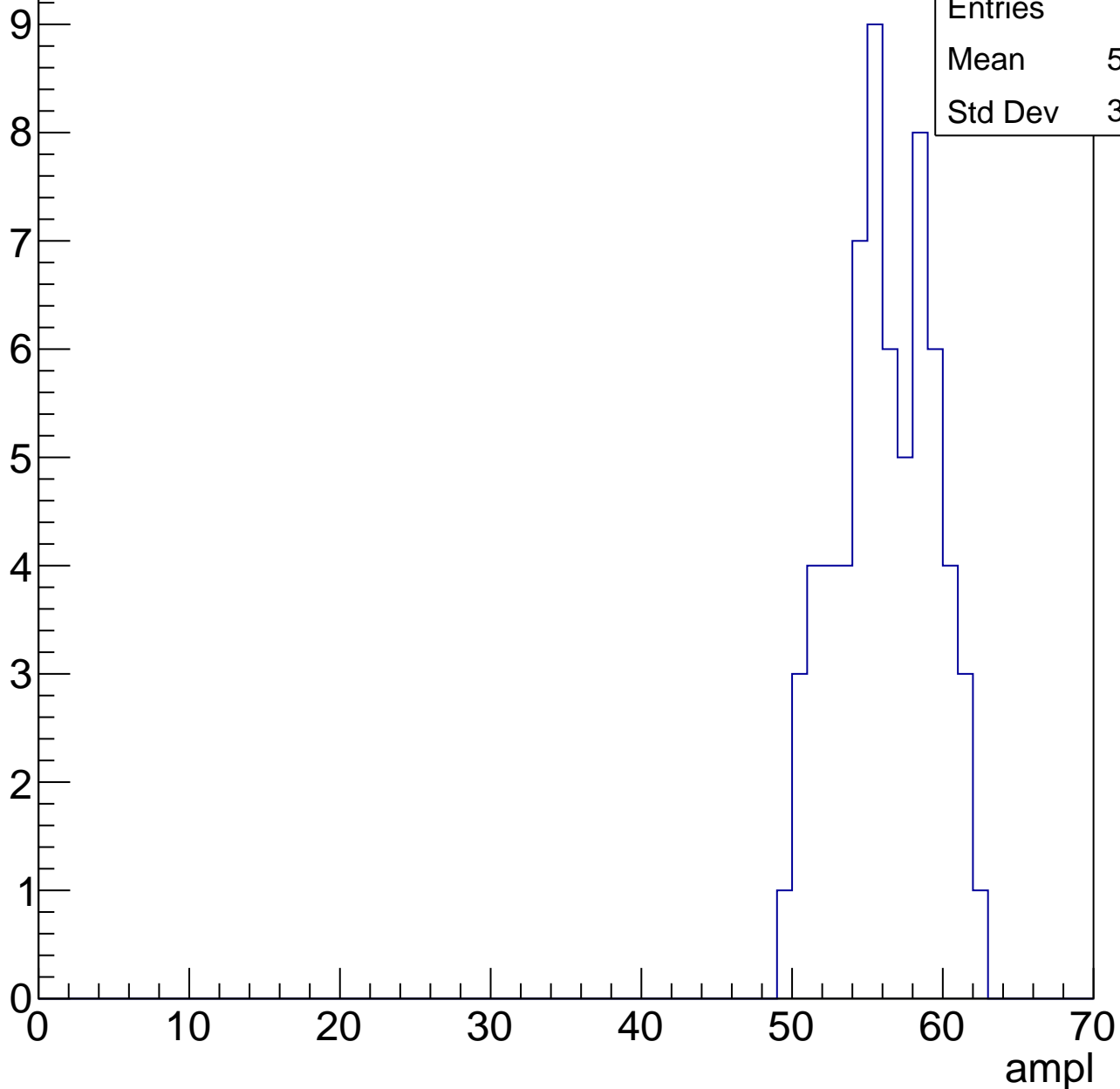
Entries	77
Mean	48.66
Std Dev	3.61



# B1L102S, U25-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

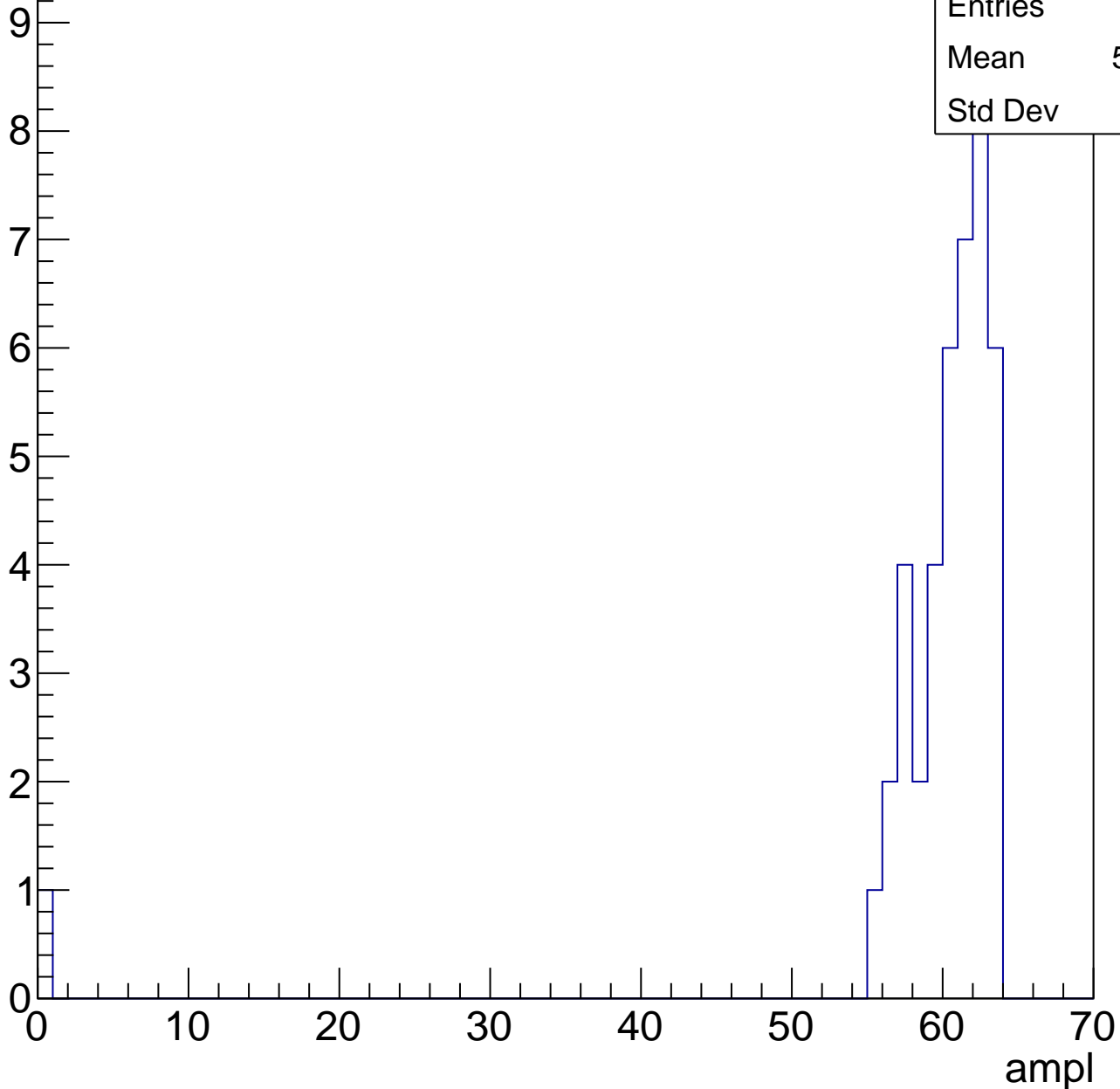


# B1L102S, U25-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.81
Std Dev	9.44



# B1L102S, U25-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

63

Std Dev

0



# B1L102S, U25-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch28, adc0

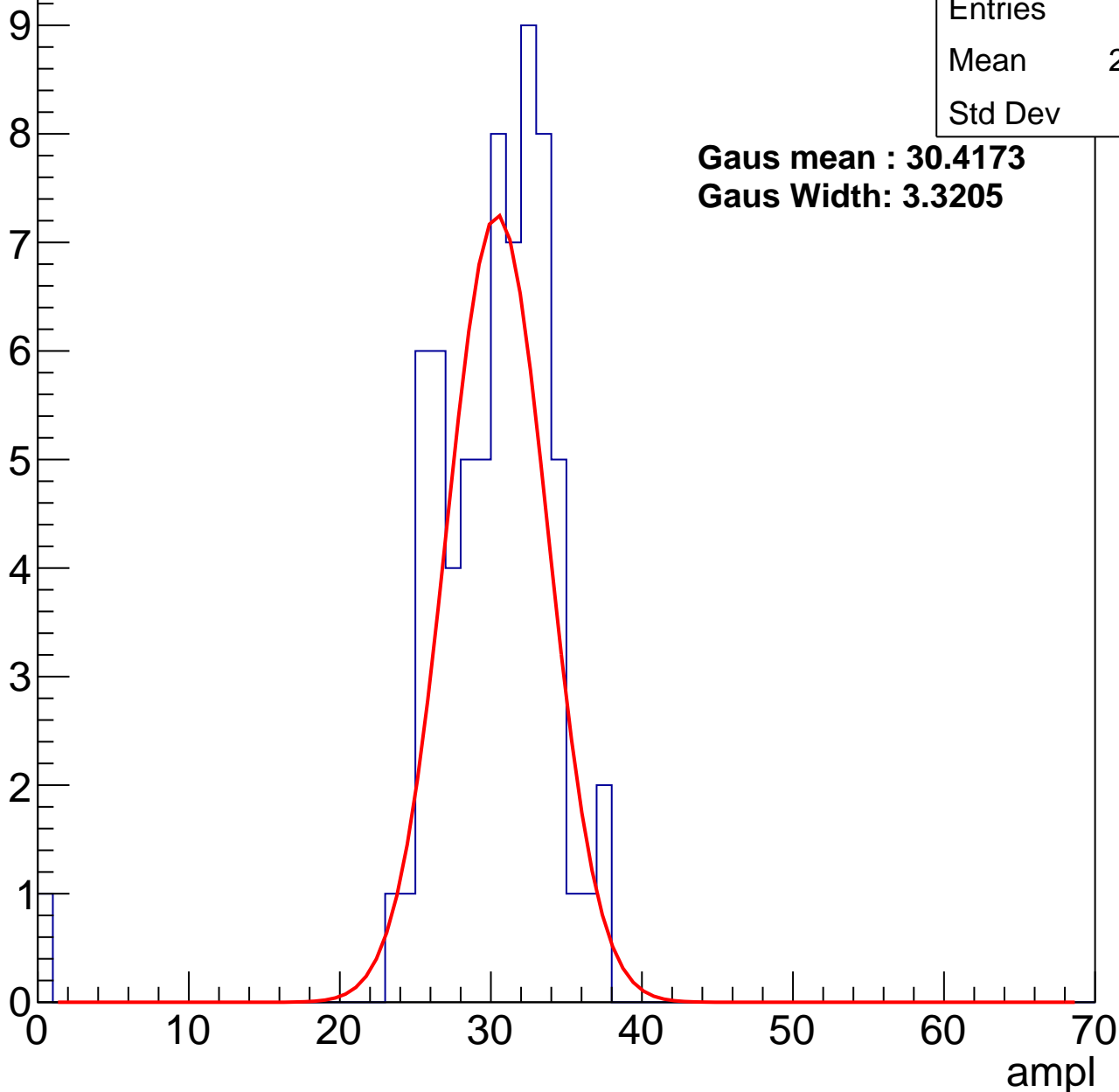
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.57
Std Dev	4.83

**Gaus mean : 30.4173**

**Gaus Width: 3.3205**



# B1L102S, U25-ch28, adc1

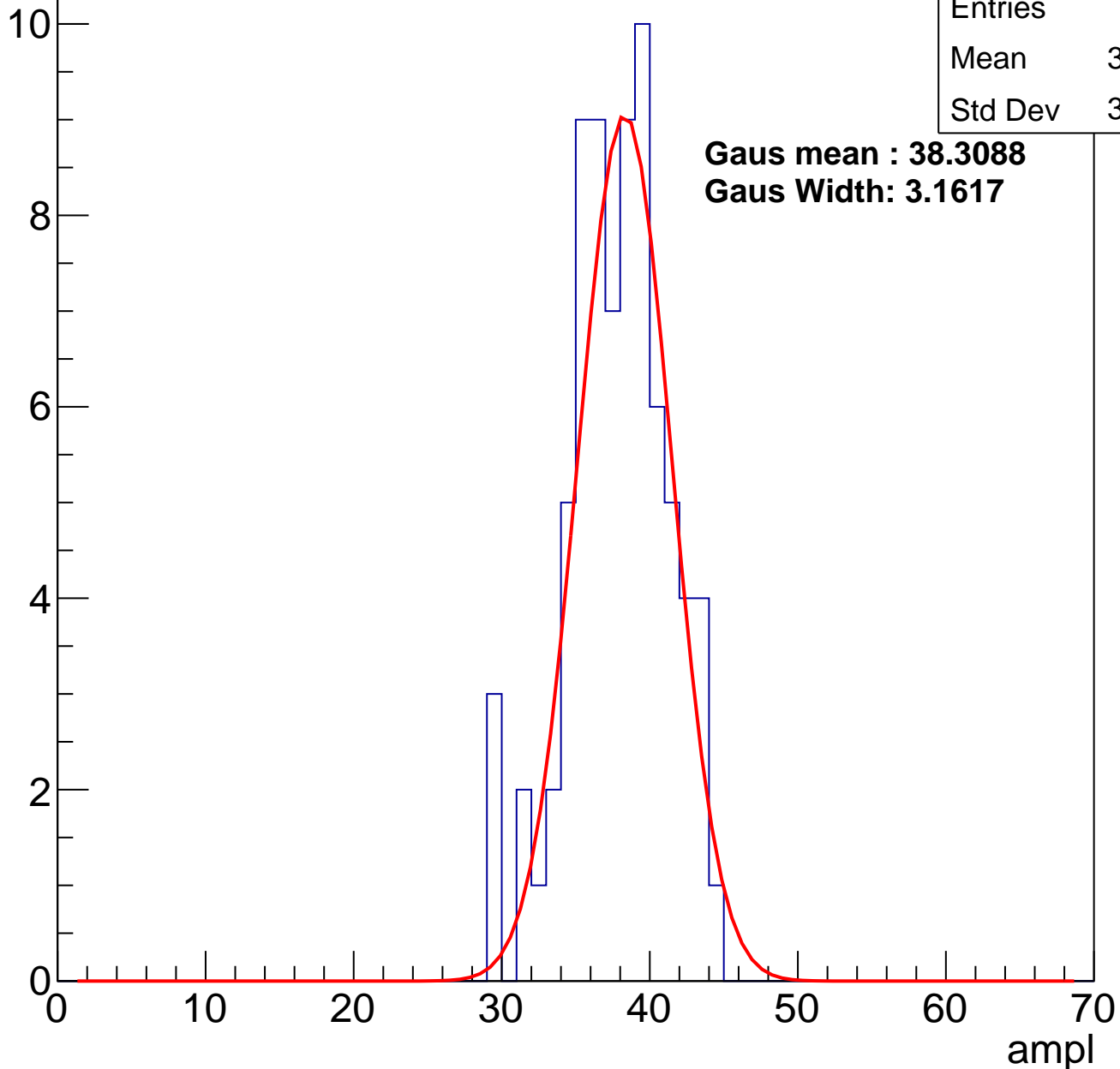
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	37.35
Std Dev	3.376

**Gaus mean : 38.3088**

**Gaus Width: 3.1617**

Entry



# B1L102S, U25-ch28, adc2

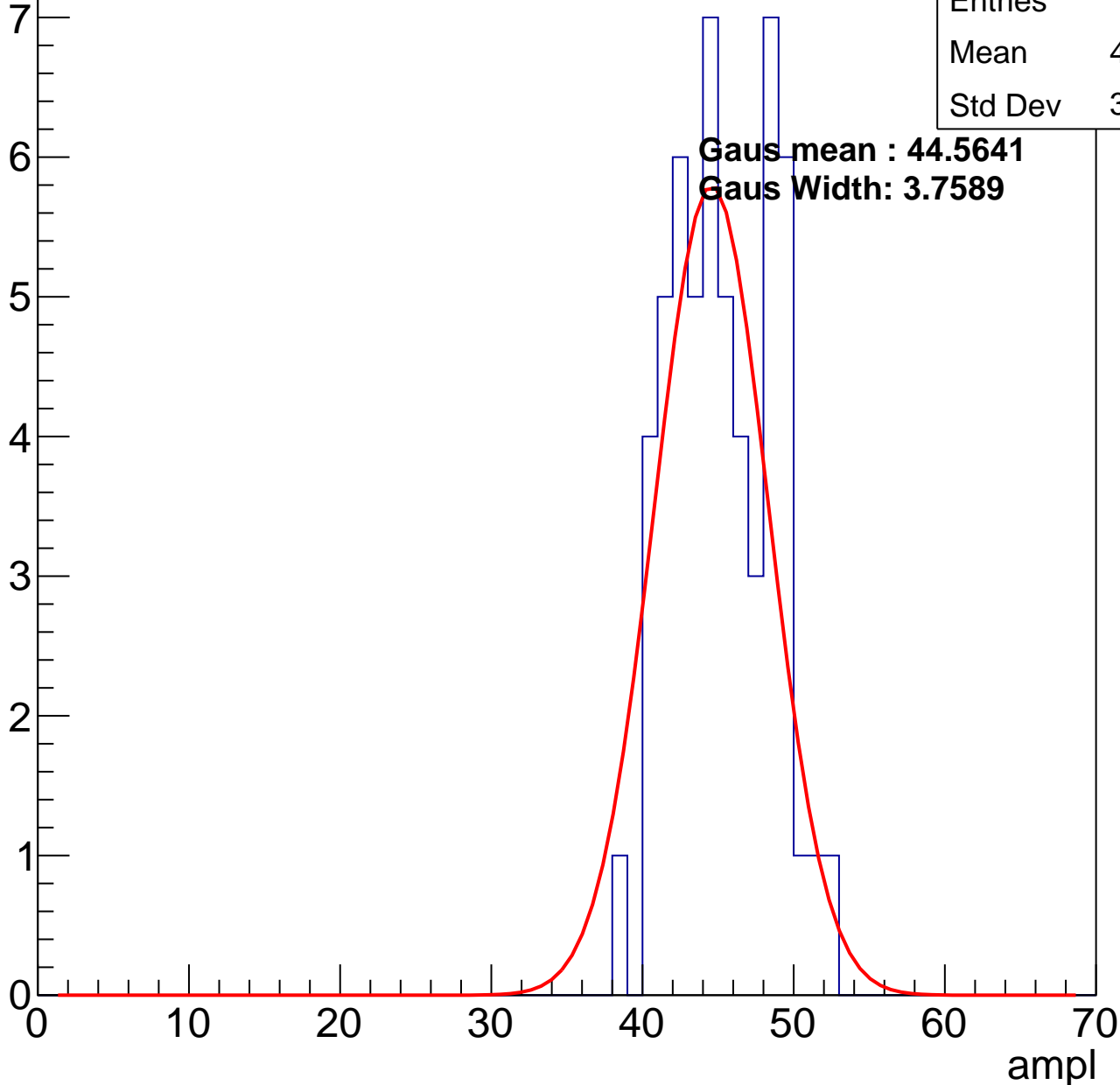
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	44.84
Std Dev	3.256

**Gaus mean : 44.5641**

**Gaus Width: 3.7589**

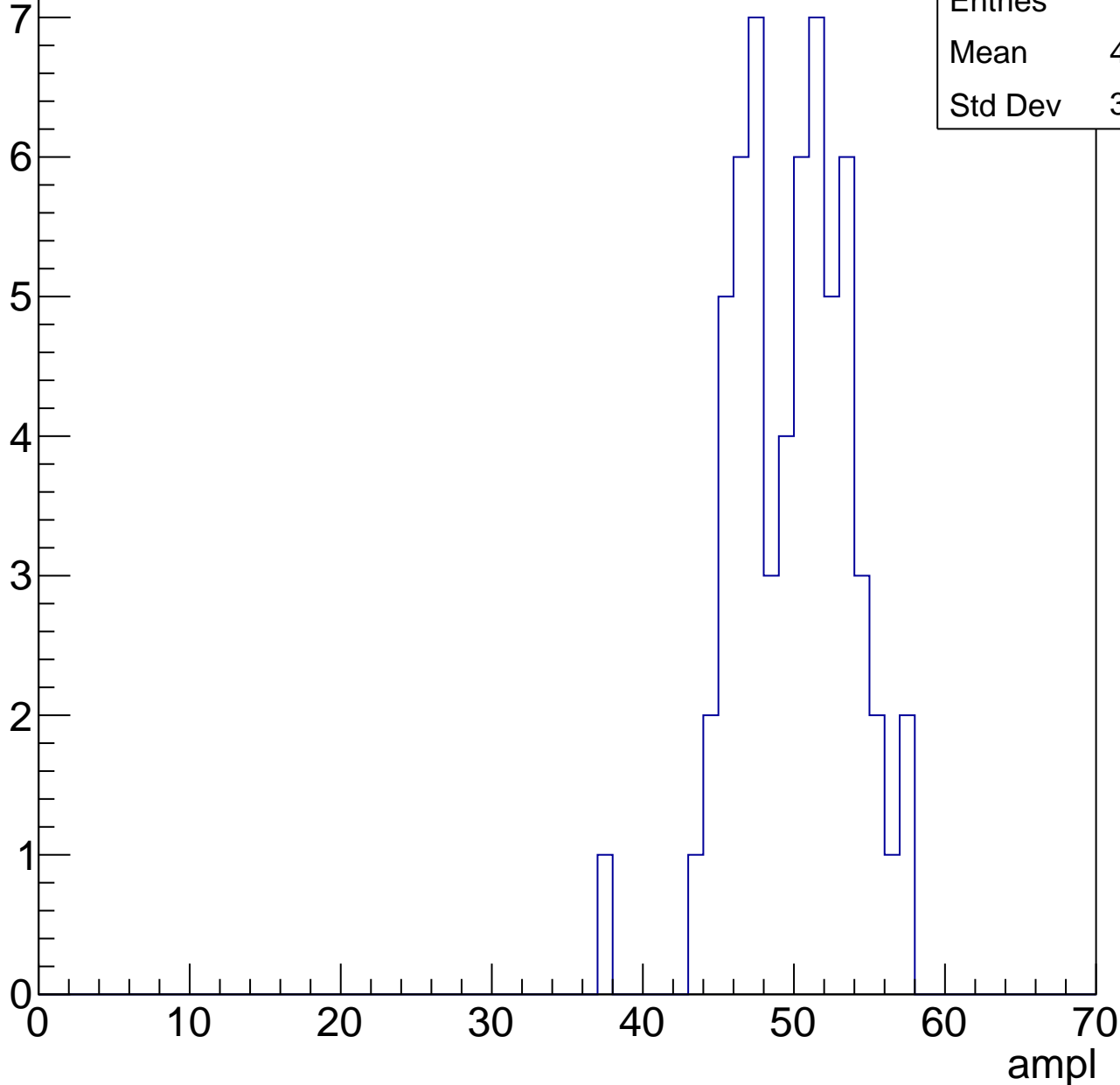


# B1L102S, U25-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.43
Std Dev	3.804

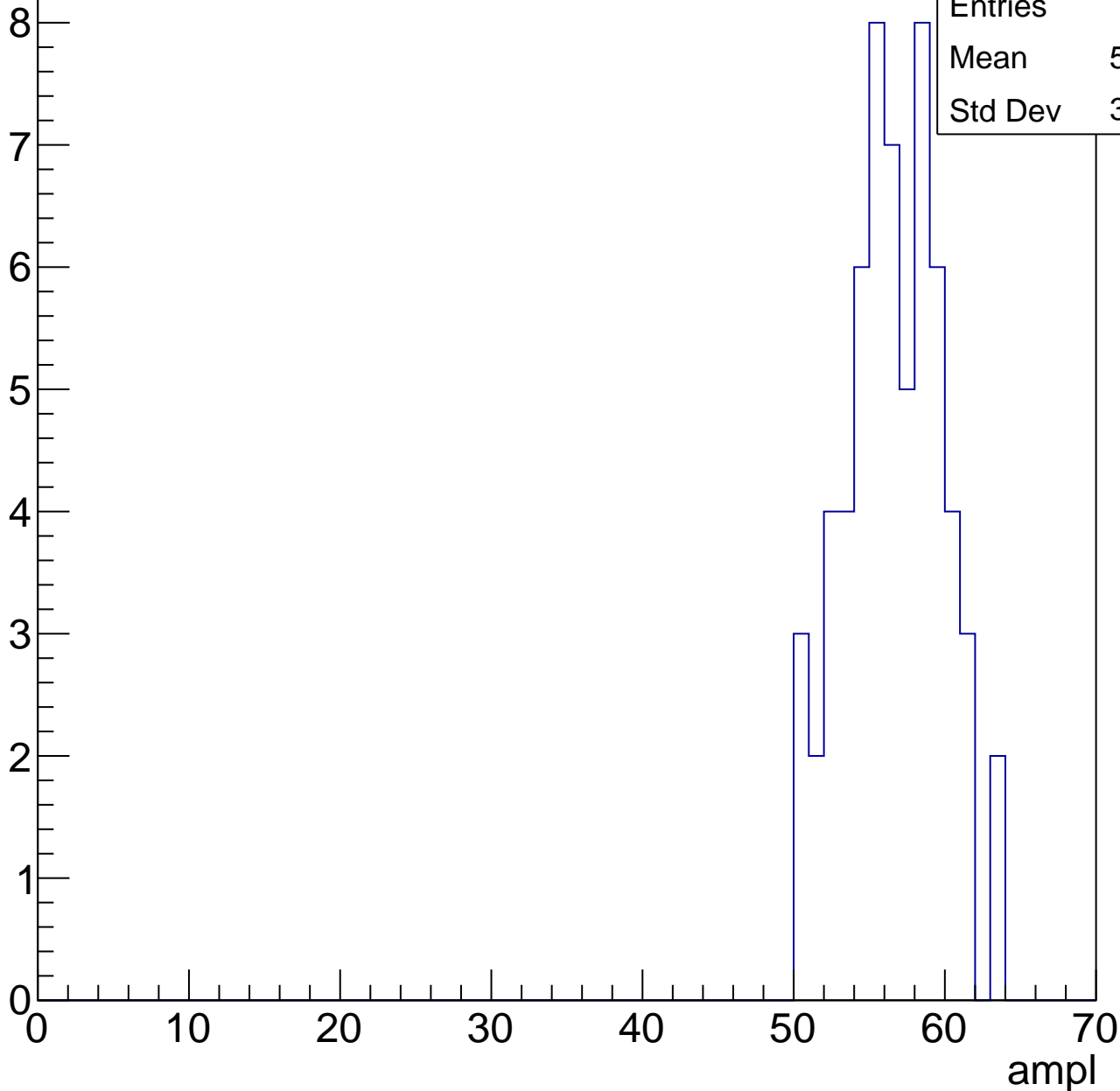


# B1L102S, U25-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.13
Std Dev	3.155

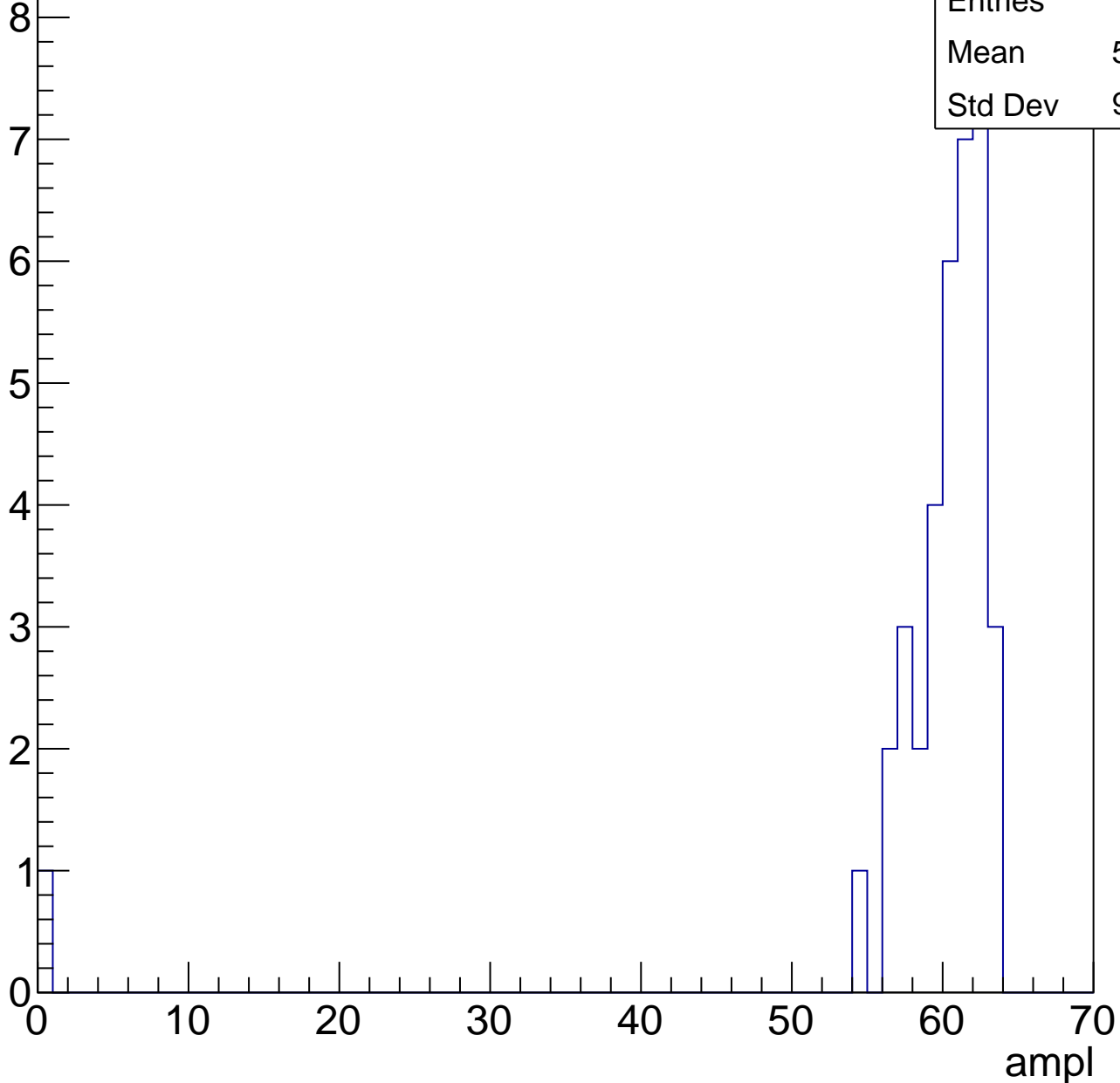


# B1L102S, U25-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.41
Std Dev	9.971

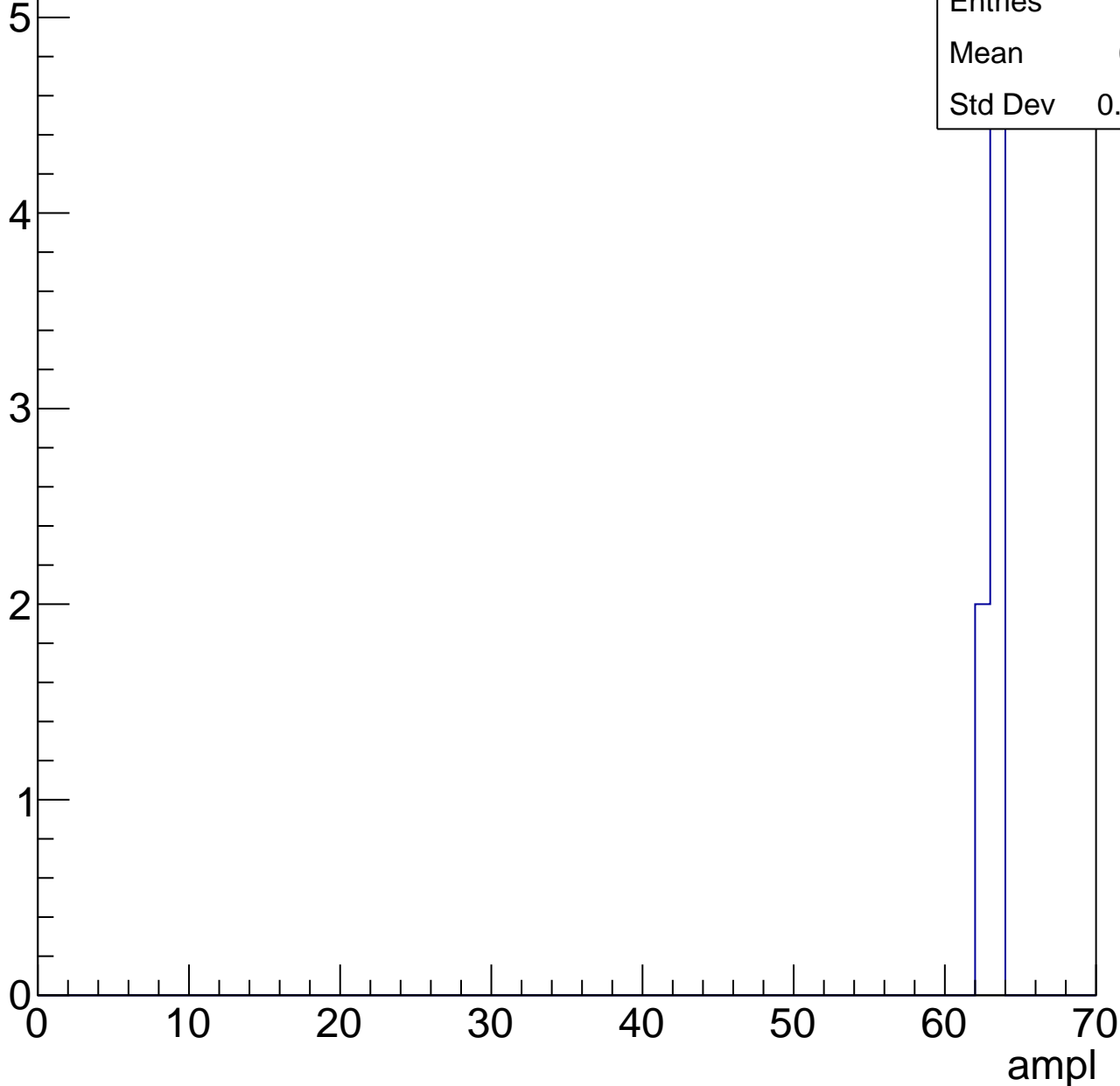


# B1L102S, U25-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518





# B1L102S, U25-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	23
Std Dev	0

# B1L102S, U25-ch29, adc0

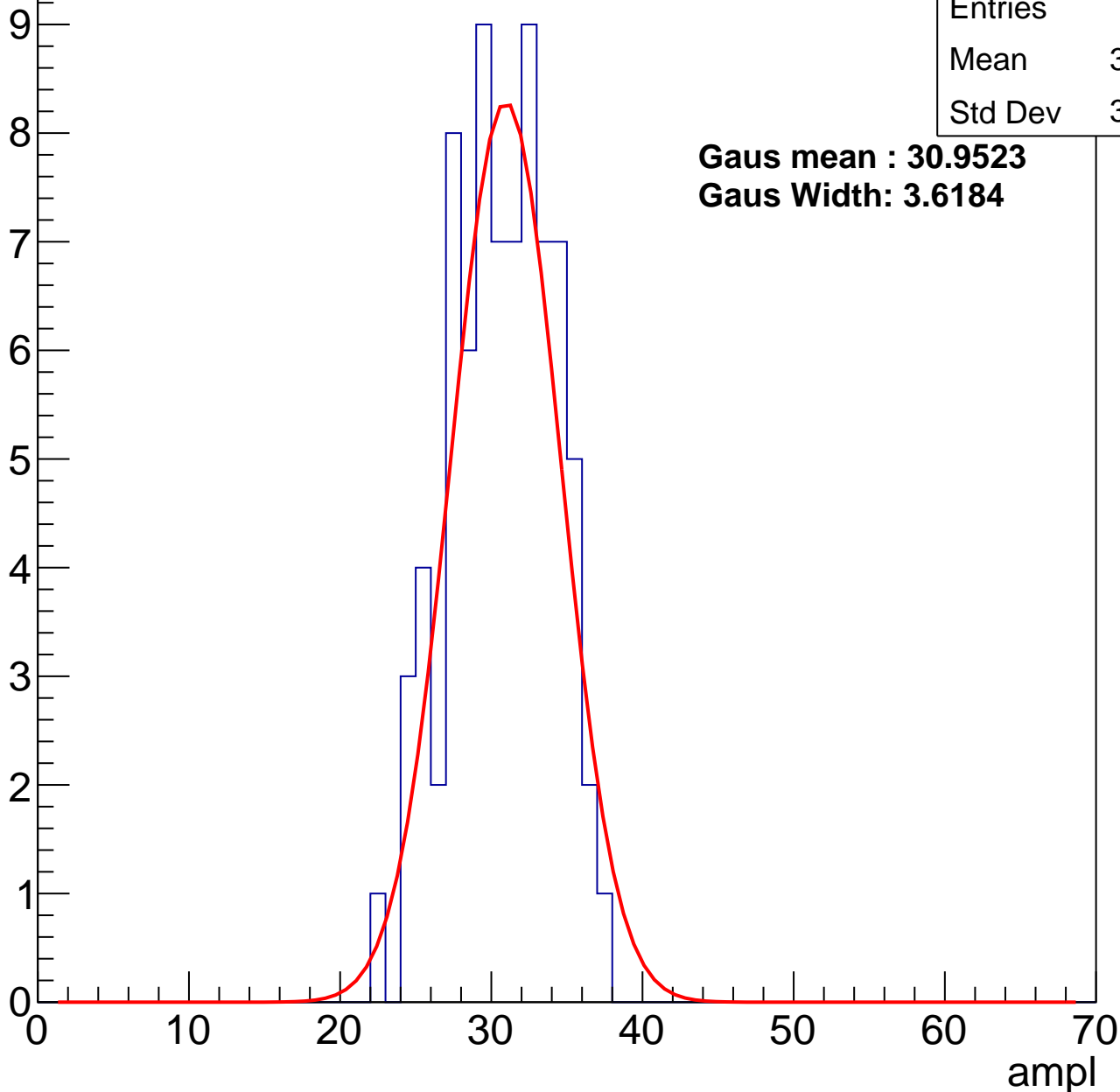
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	30.24
Std Dev	3.337

**Gaus mean : 30.9523**

**Gaus Width: 3.6184**



# B1L102S, U25-ch29, adc1

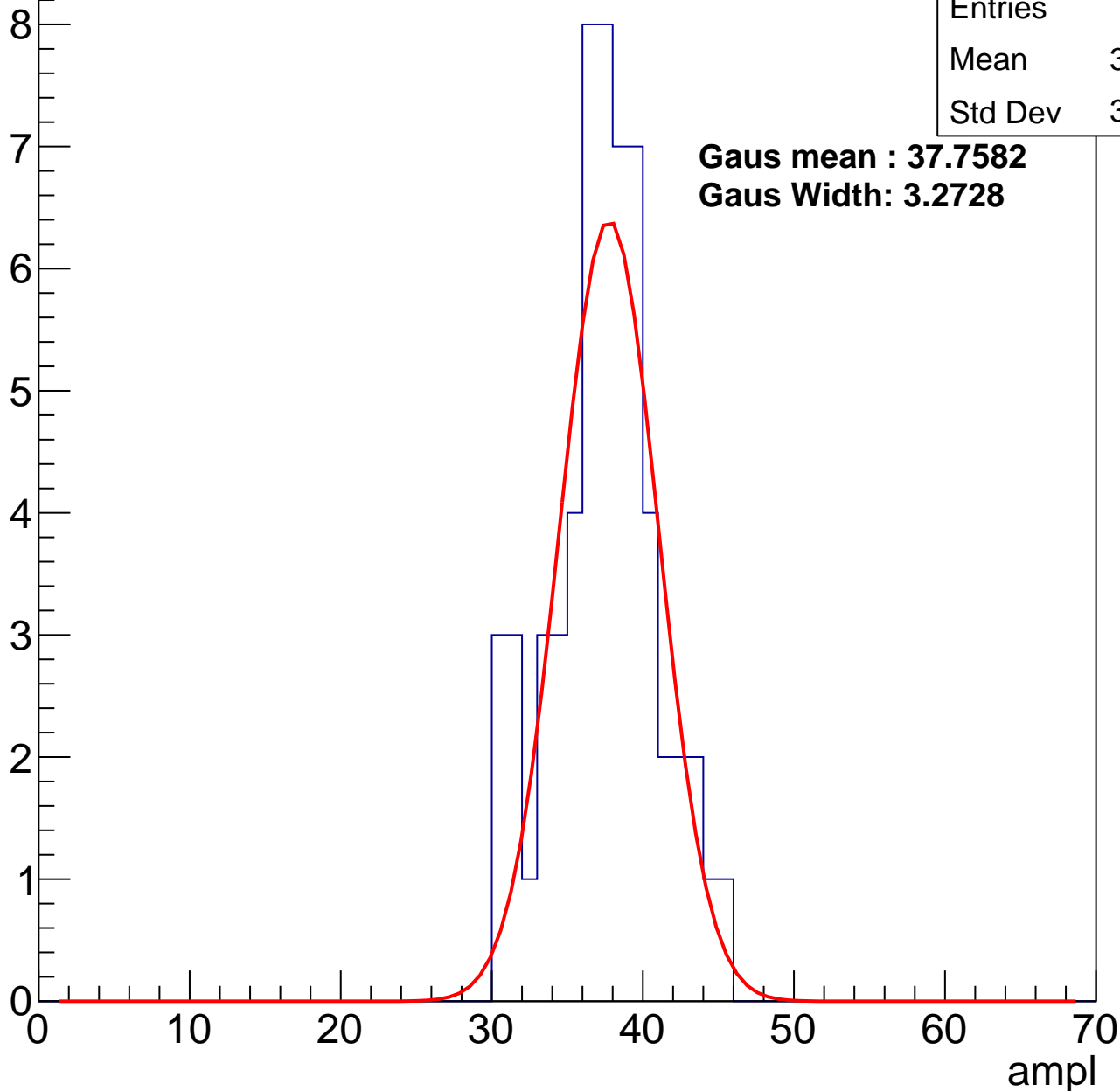
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.95
Std Dev	3.486

**Gaus mean : 37.7582**

**Gaus Width: 3.2728**



# B1L102S, U25-ch29, adc2

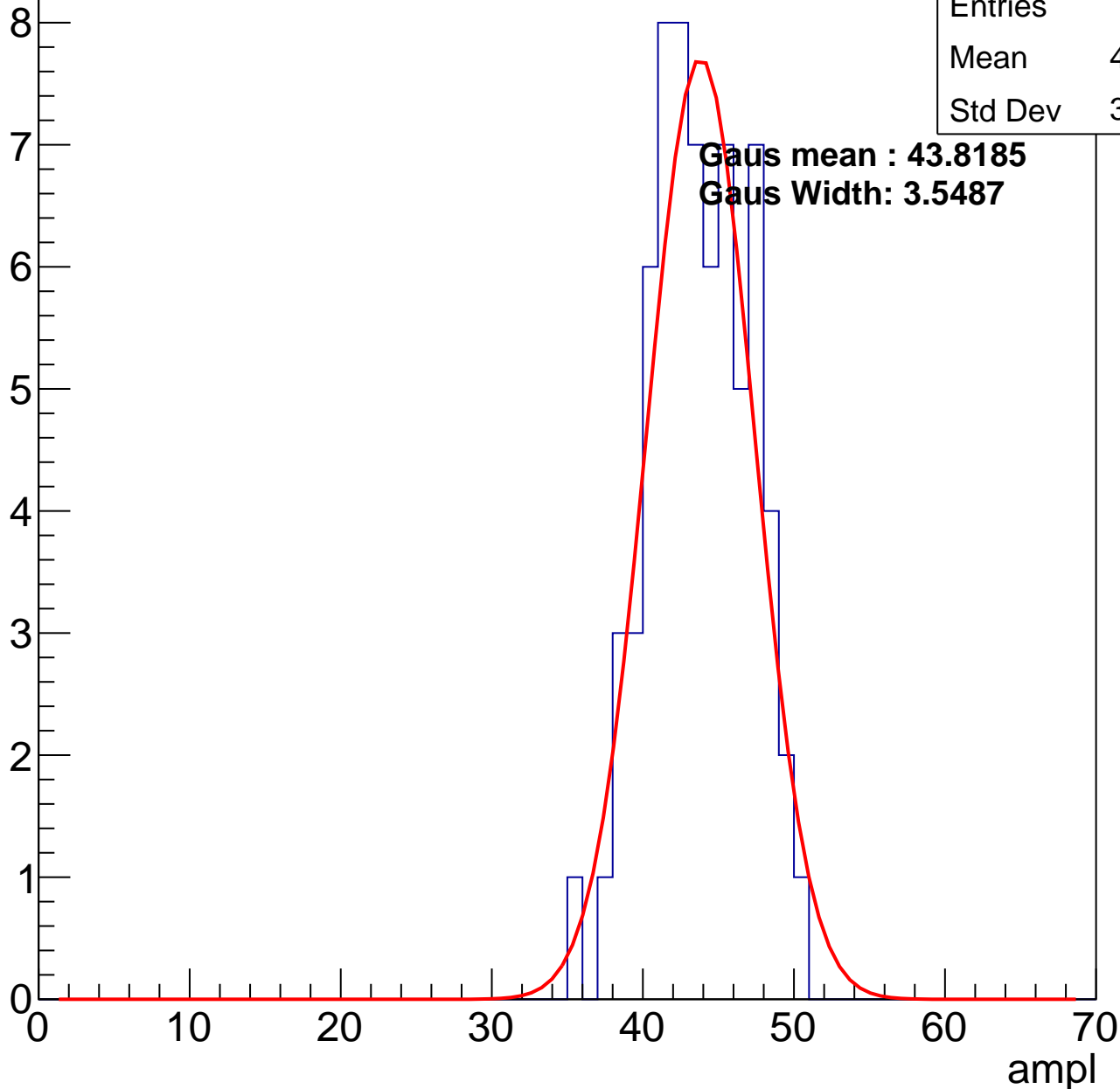
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.28
Std Dev	3.239

**Gaus mean : 43.8185**

**Gaus Width: 3.5487**

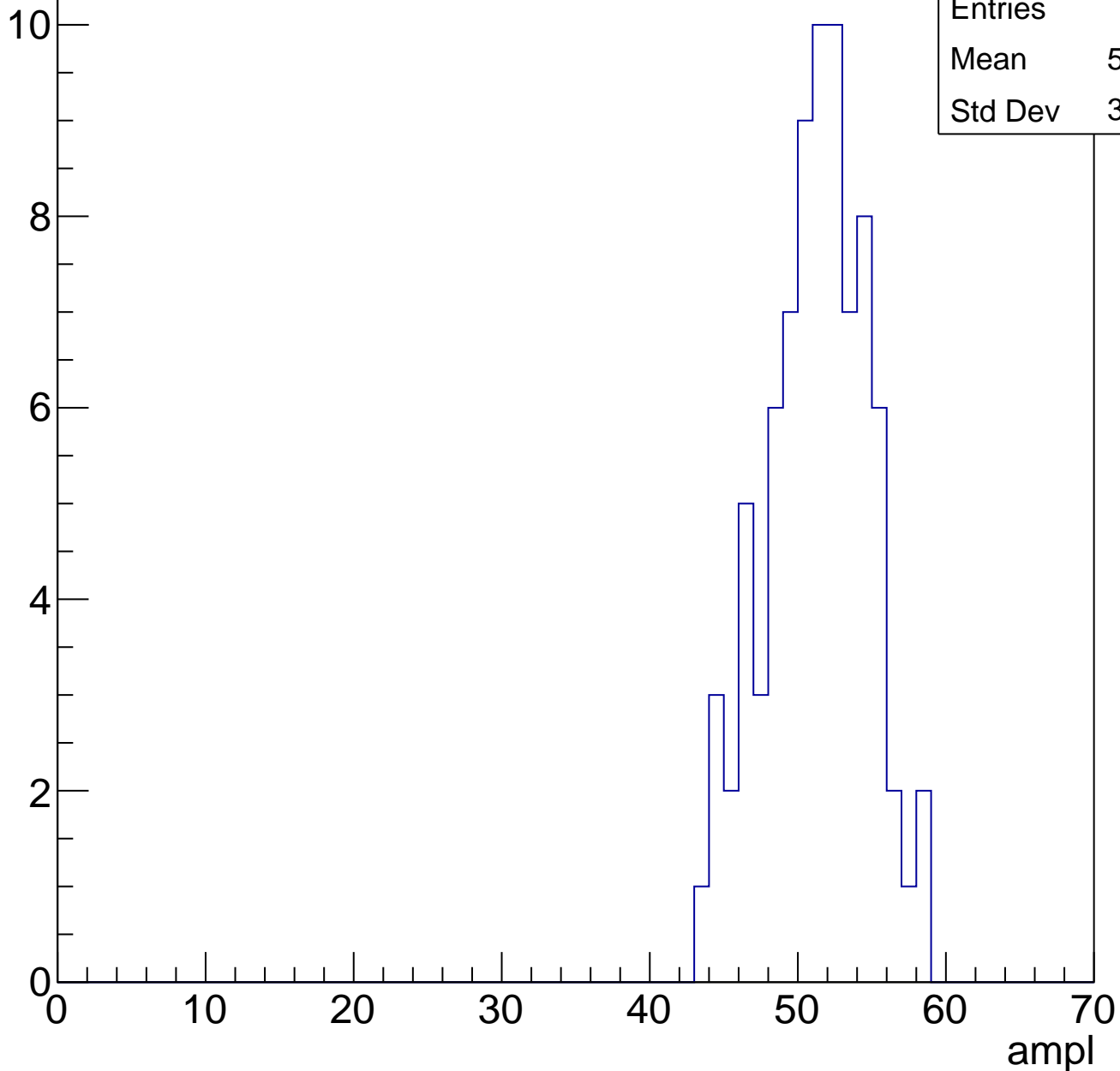


# B1L102S, U25-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	82
Mean	50.79
Std Dev	3.374

Entry

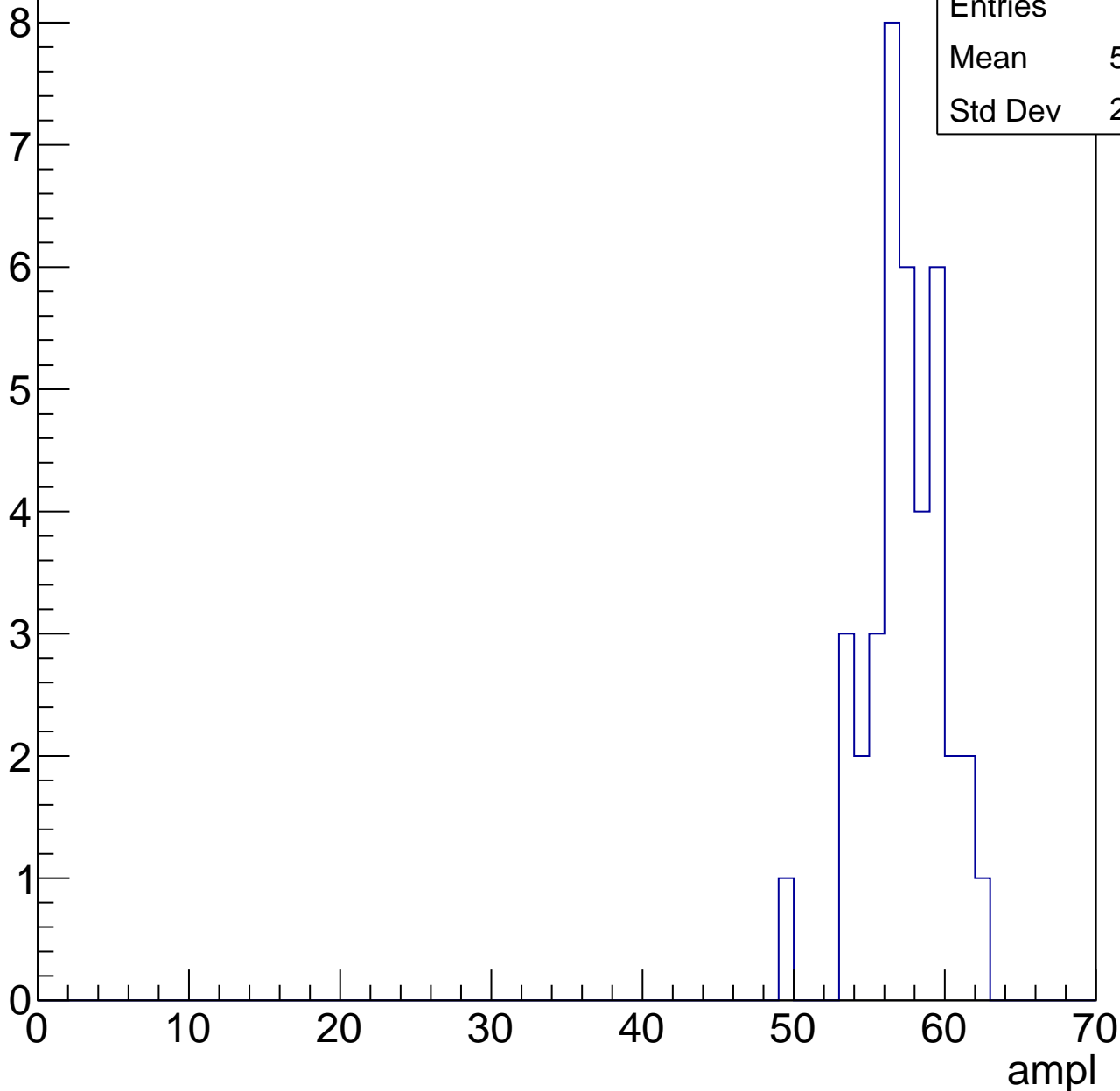


# B1L102S, U25-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	38
Mean	56.87
Std Dev	2.577



# B1L102S, U25-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

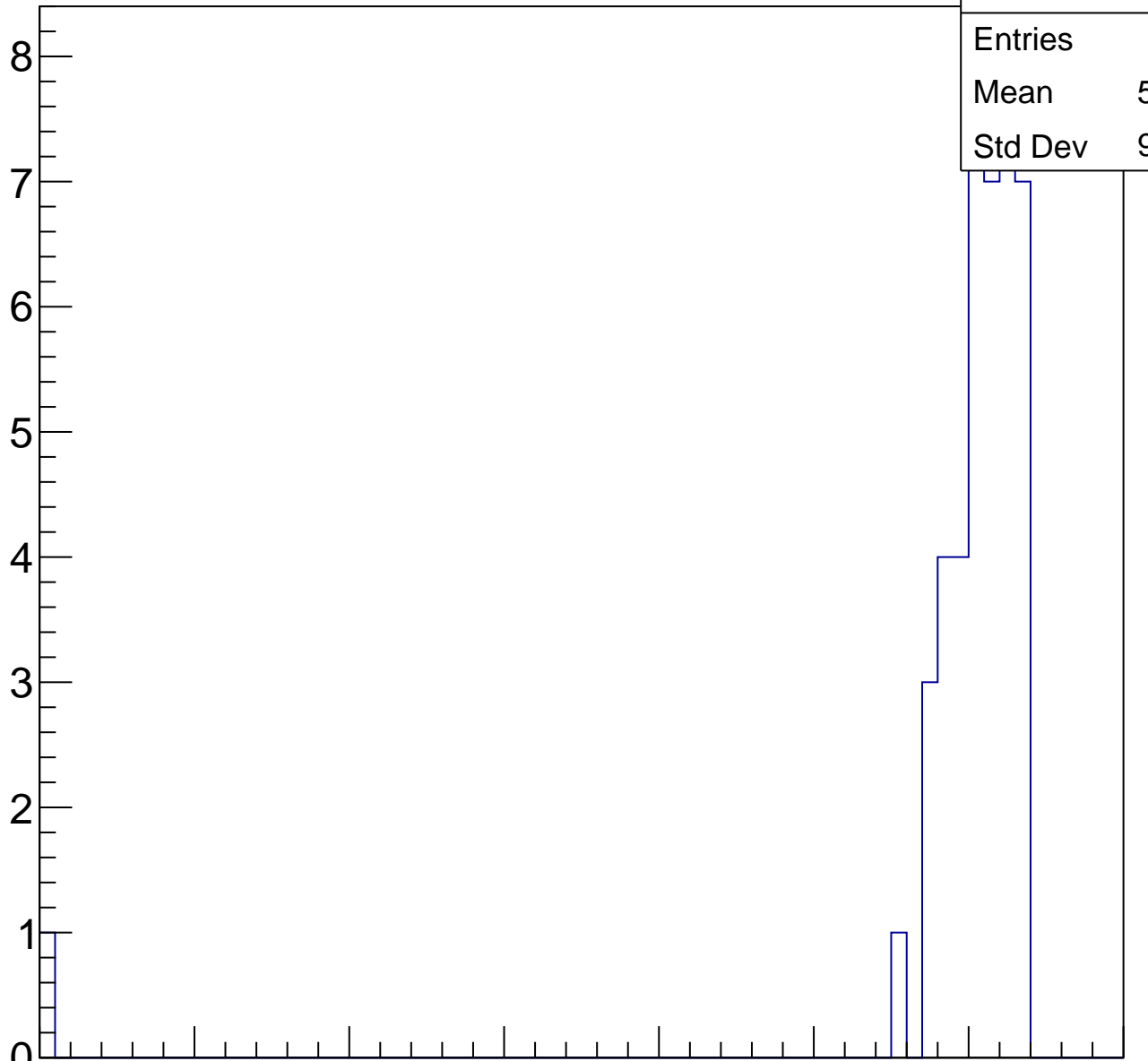
Entry

Entries	43
Mean	59.02
Std Dev	9.317

8  
7  
6  
5  
4  
3  
2  
1  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U25-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch30, adc0

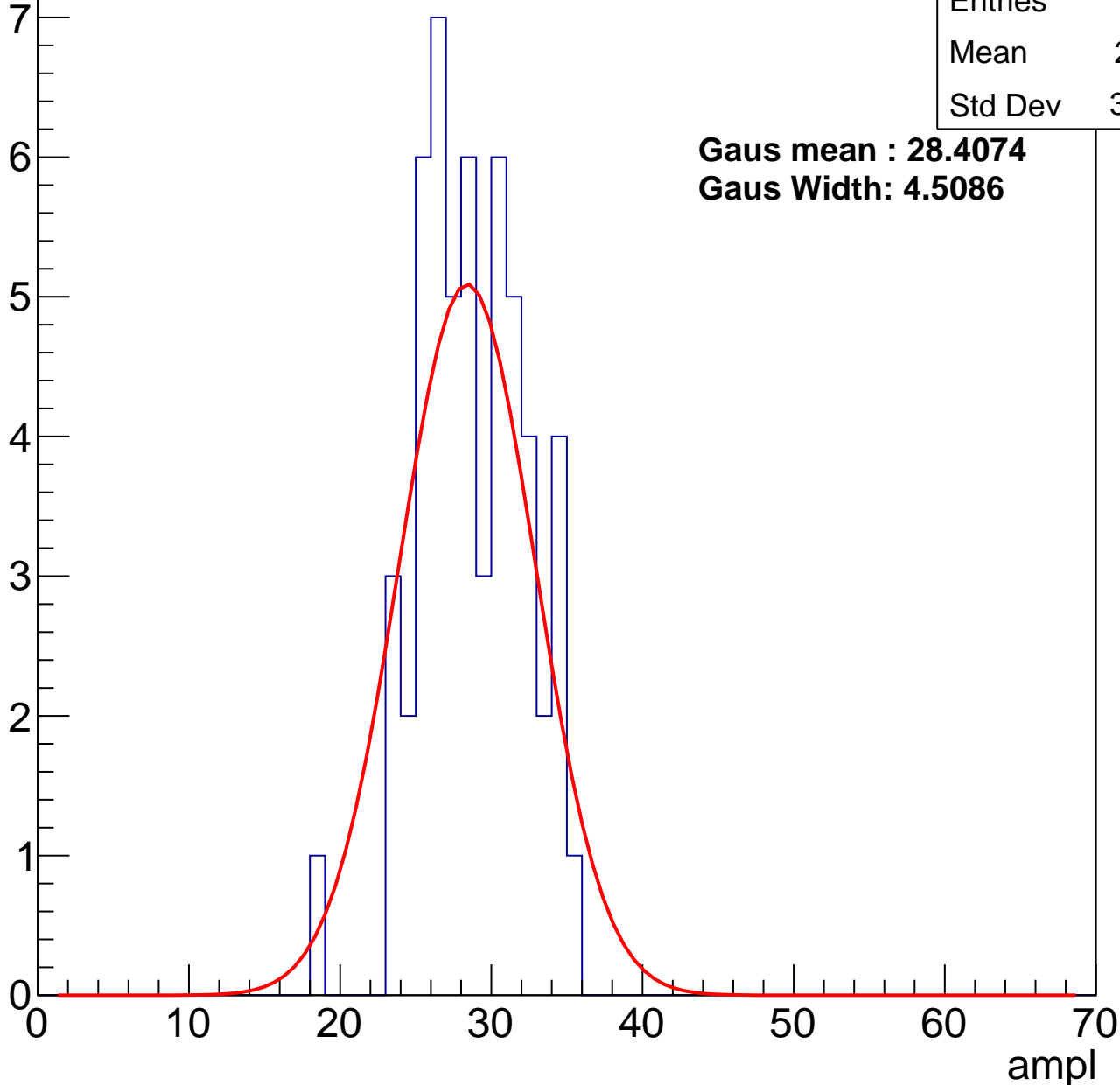
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.31
Std Dev	3.484

**Gaus mean : 28.4074**

**Gaus Width: 4.5086**

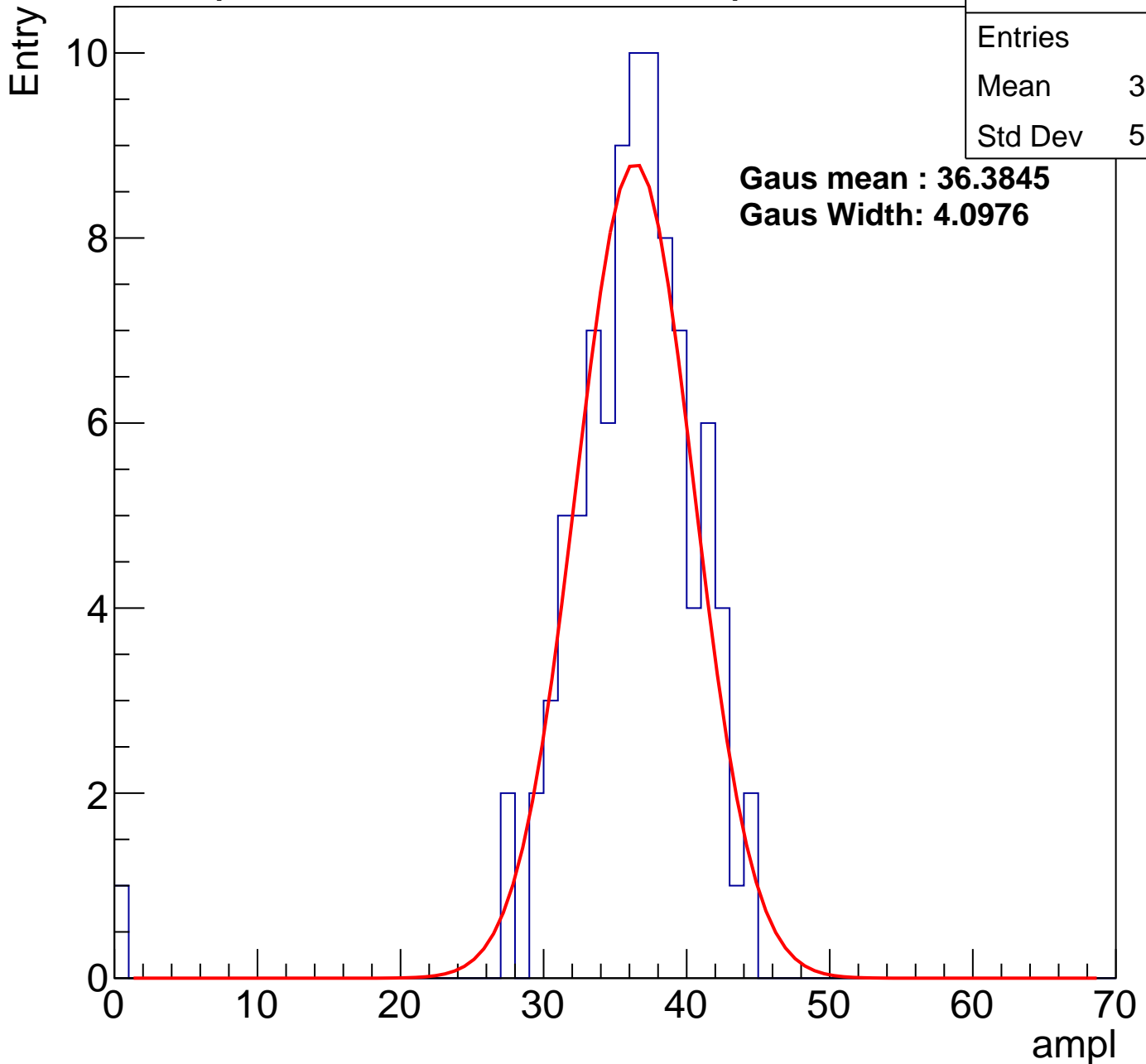


# B1L102S, U25-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	92
Mean	35.64
Std Dev	5.309

**Gaus mean : 36.3845**  
**Gaus Width: 4.0976**



# B1L102S, U25-ch30, adc2

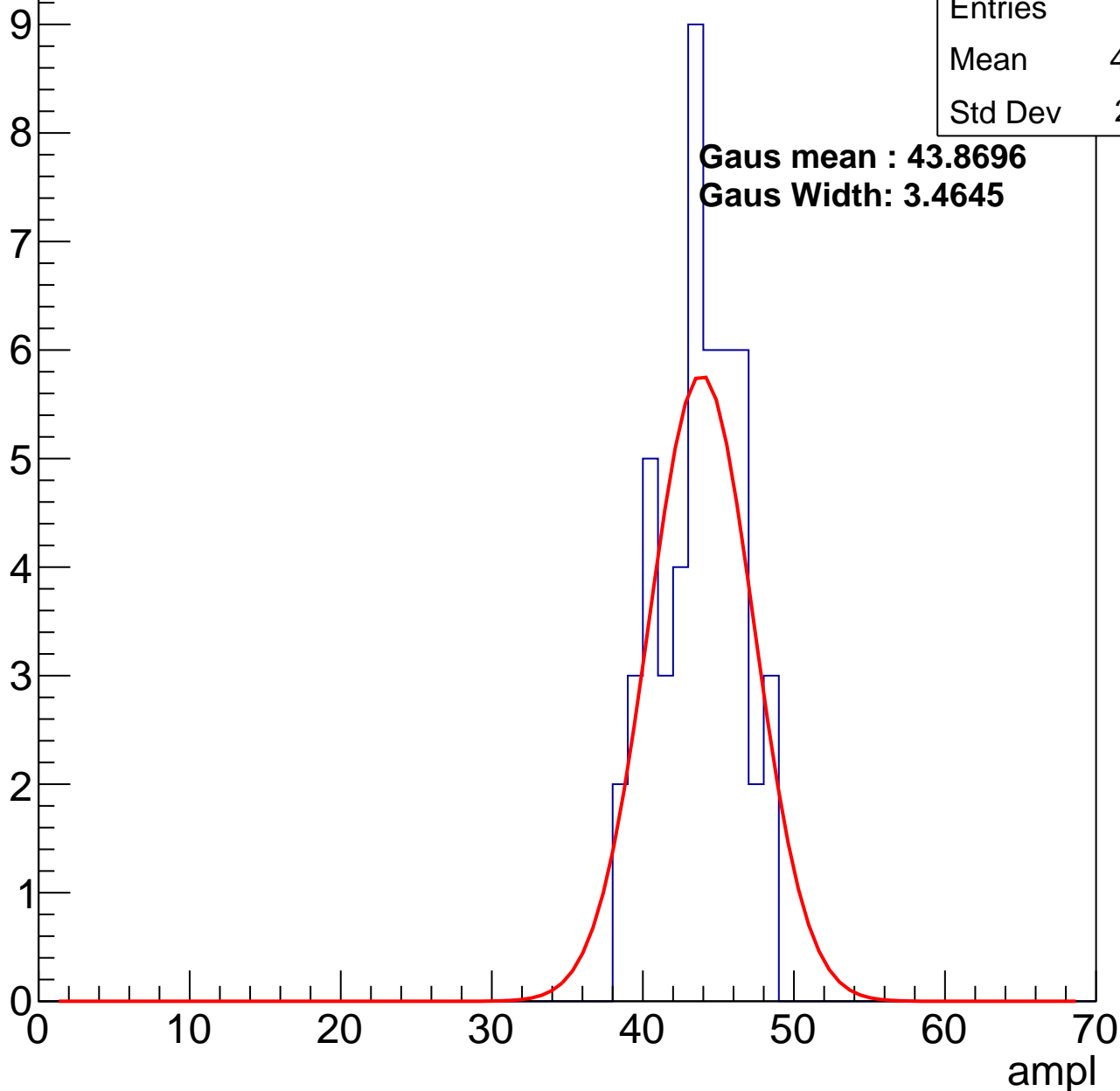
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.24
Std Dev	2.661

**Gaus mean : 43.8696**

**Gaus Width: 3.4645**

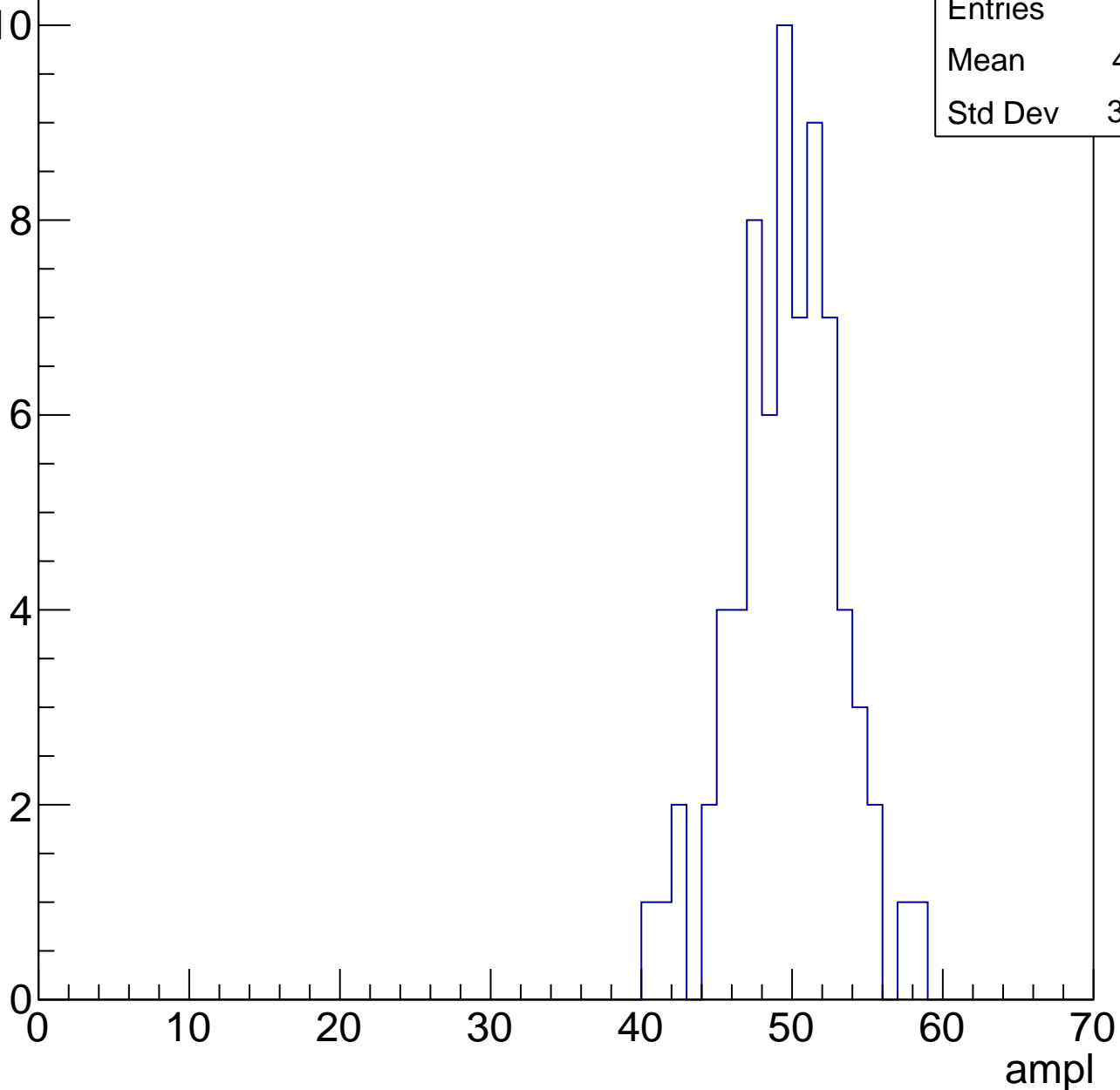


# B1L102S, U25-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	49.21
Std Dev	3.516

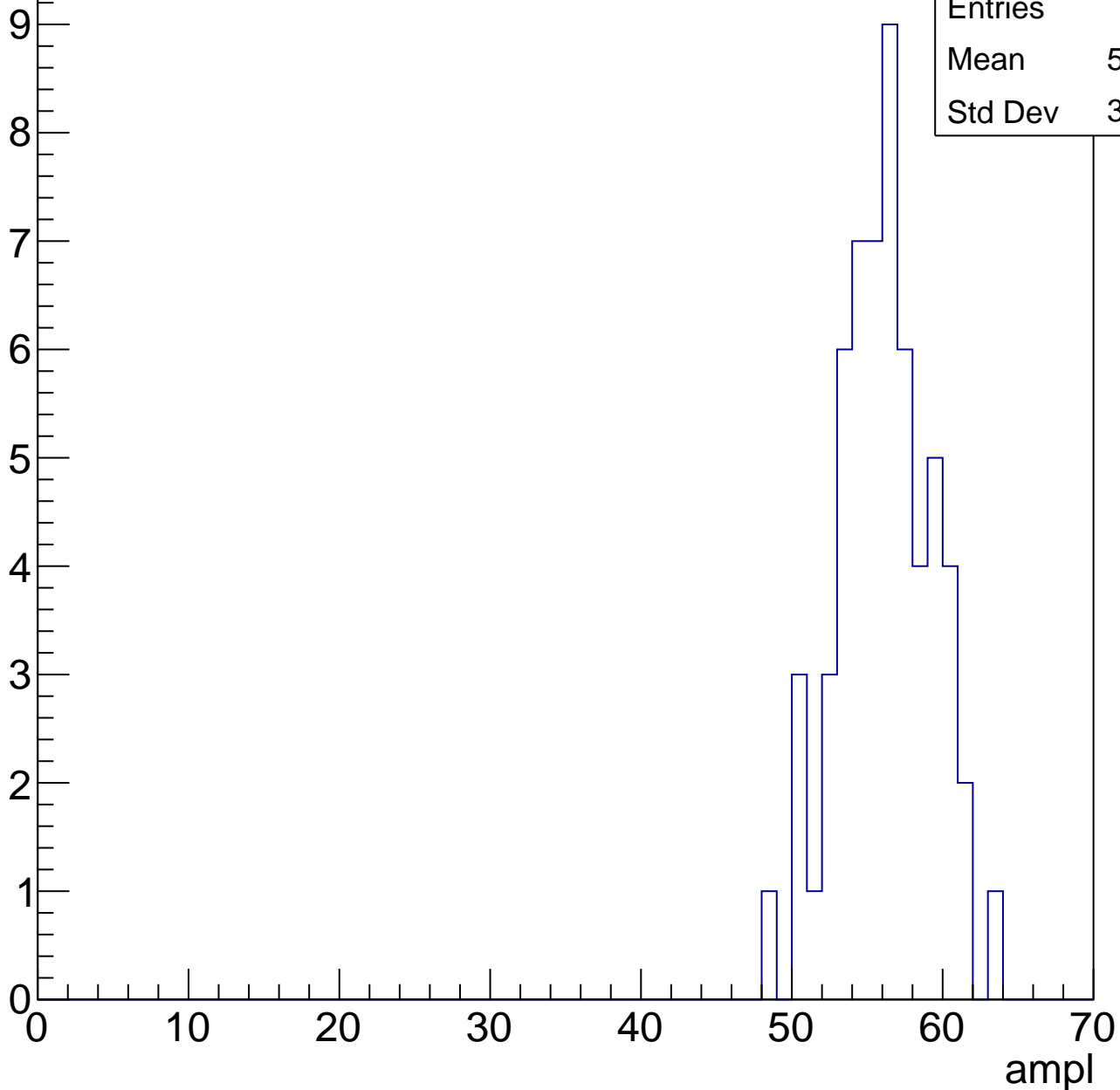


# B1L102S, U25-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.66
Std Dev	3.073

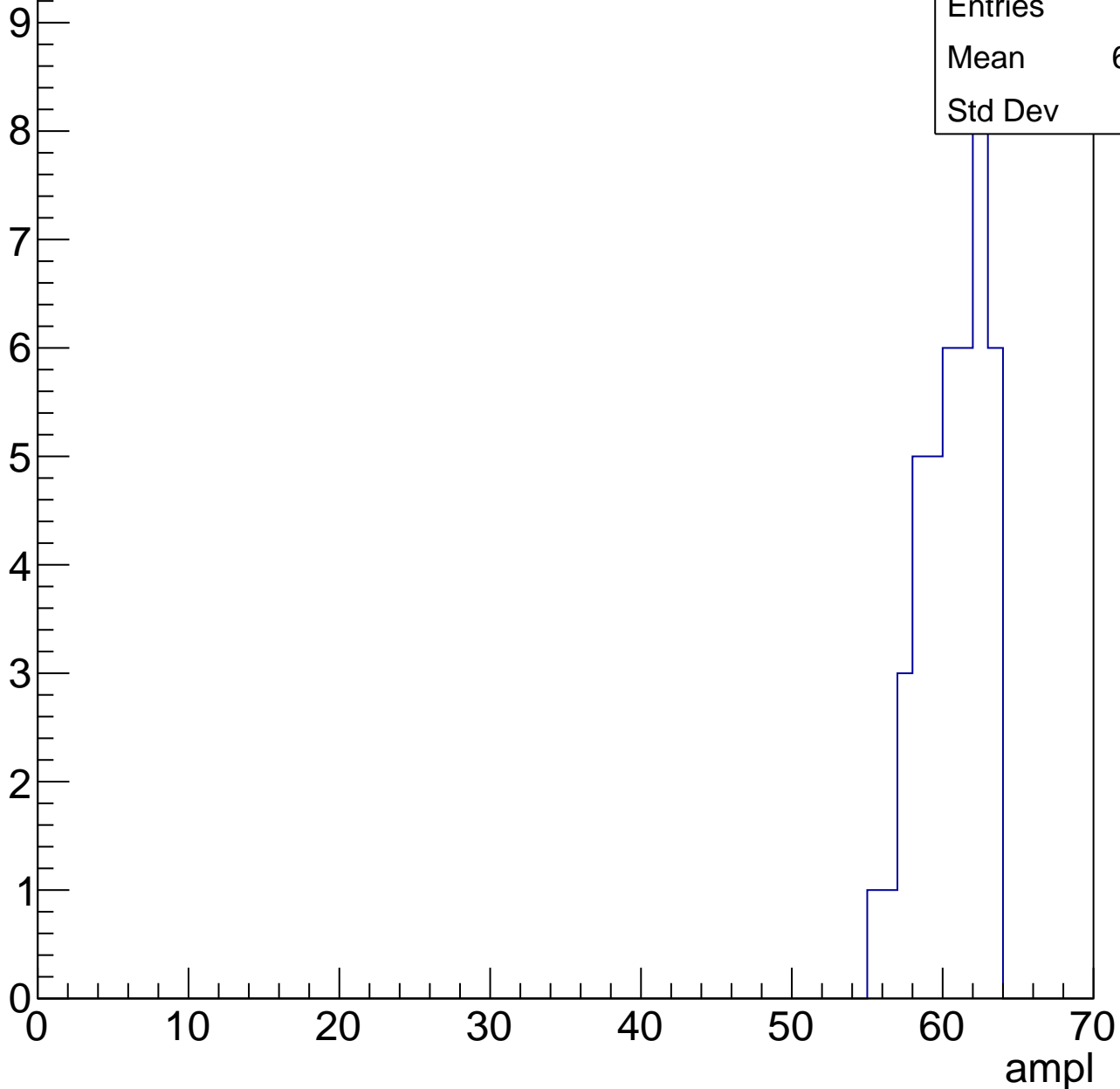


# B1L102S, U25-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

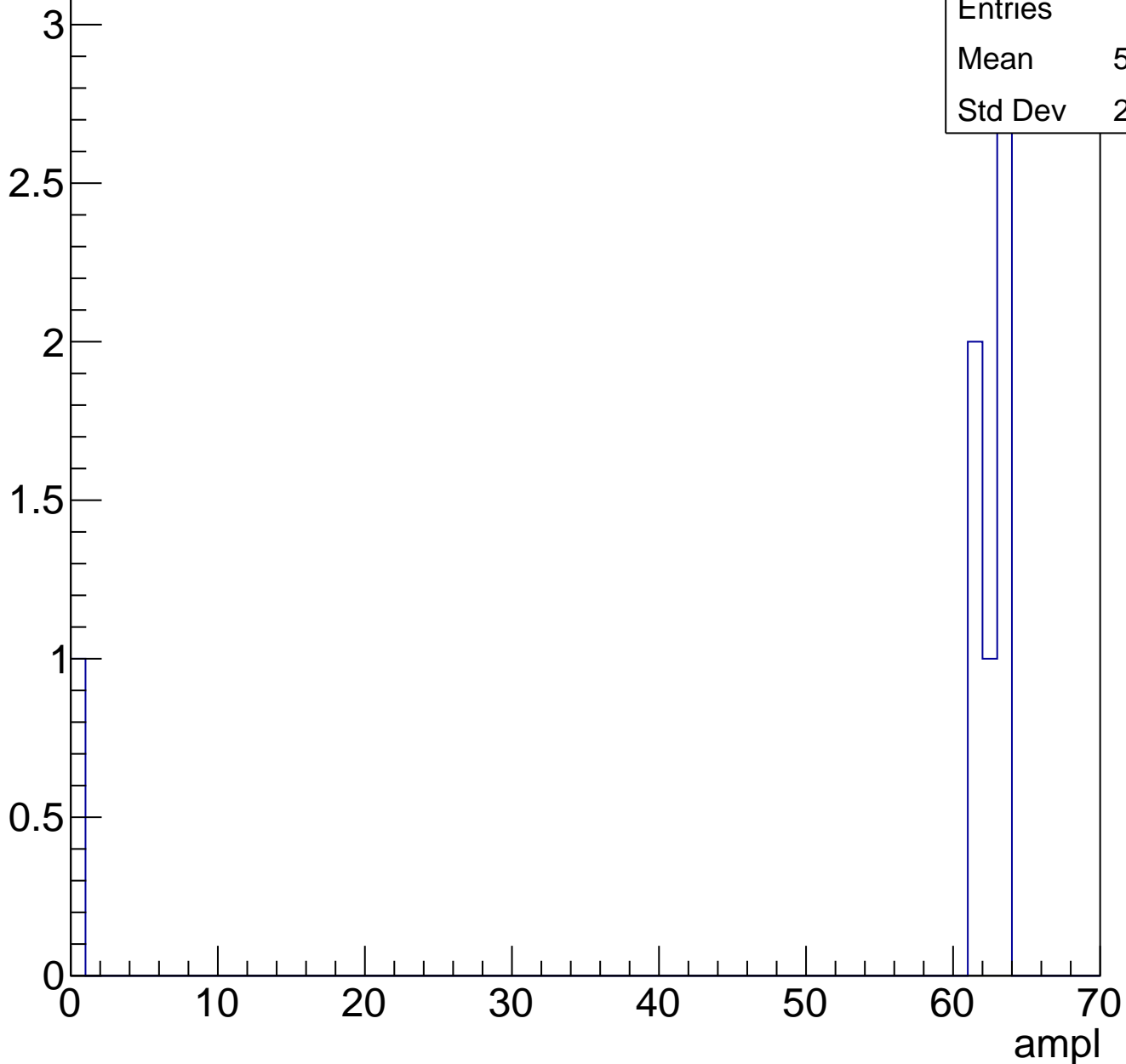
Entries	42
Mean	60.21
Std Dev	2.11



# B1L102S, U25-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch31, adc0

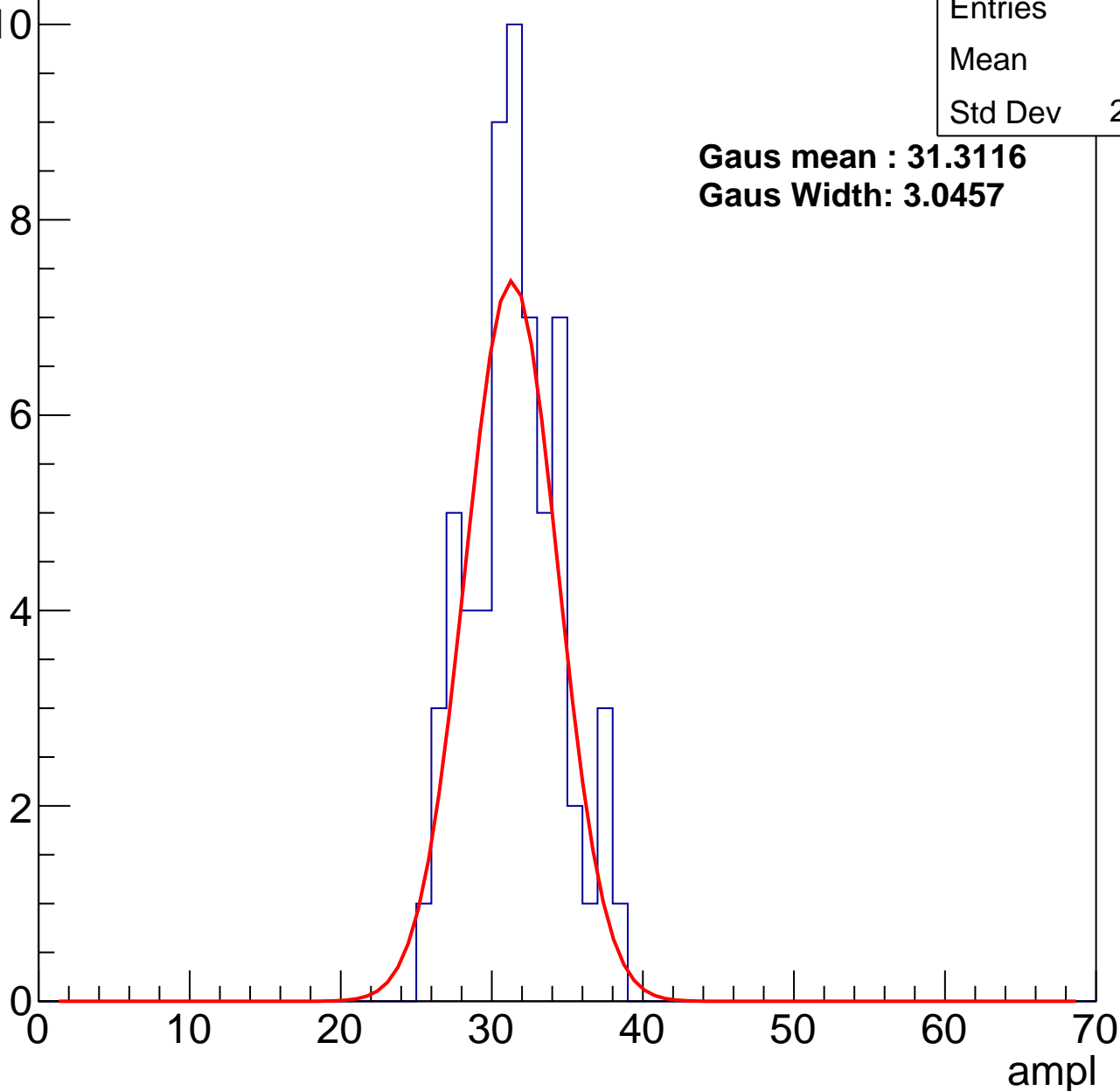
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	31.1
Std Dev	2.993

**Gaus mean : 31.3116**

**Gaus Width: 3.0457**



# B1L102S, U25-ch31, adc1

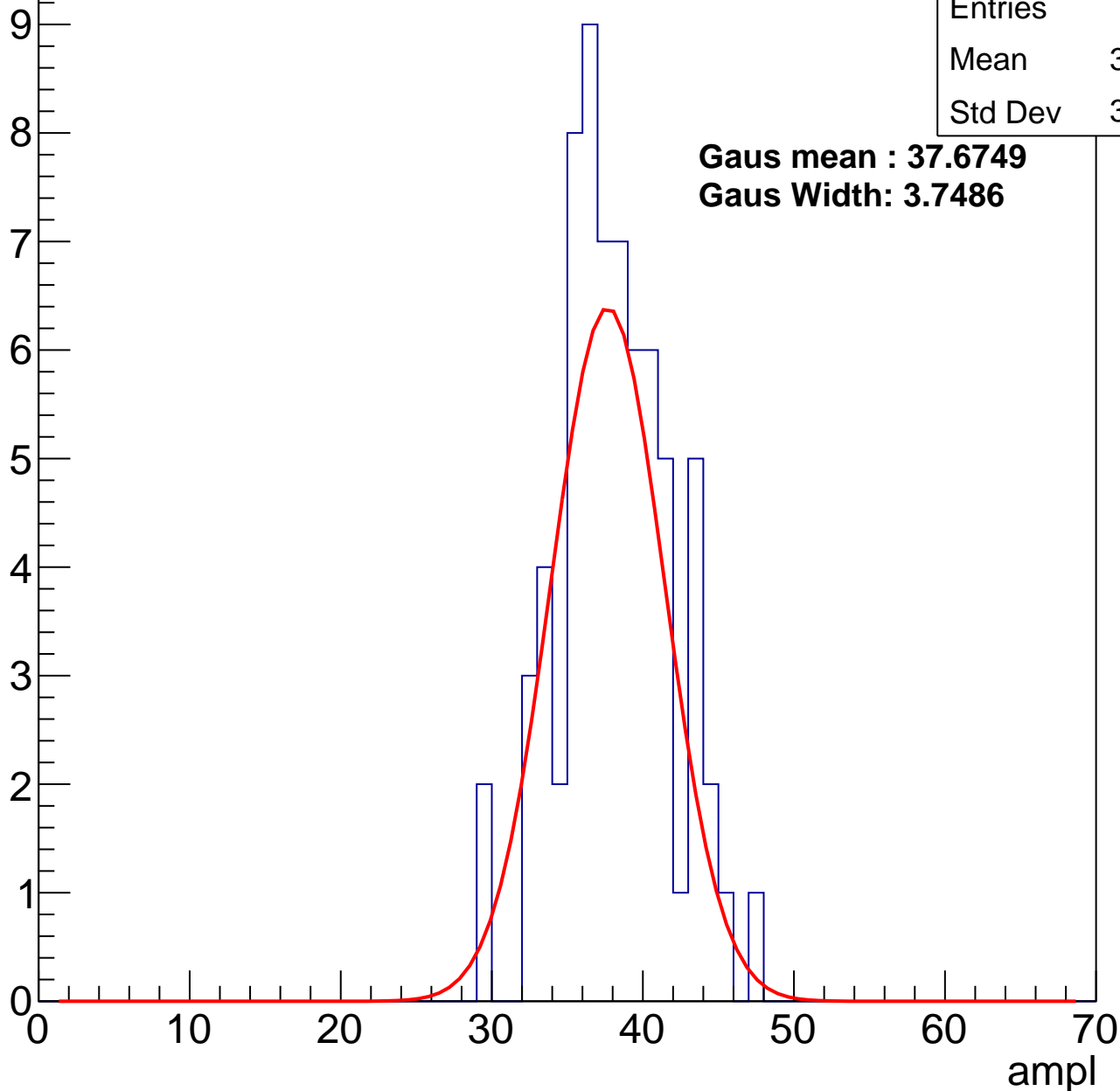
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	37.67
Std Dev	3.666

**Gaus mean : 37.6749**

**Gaus Width: 3.7486**



# B1L102S, U25-ch31, adc2

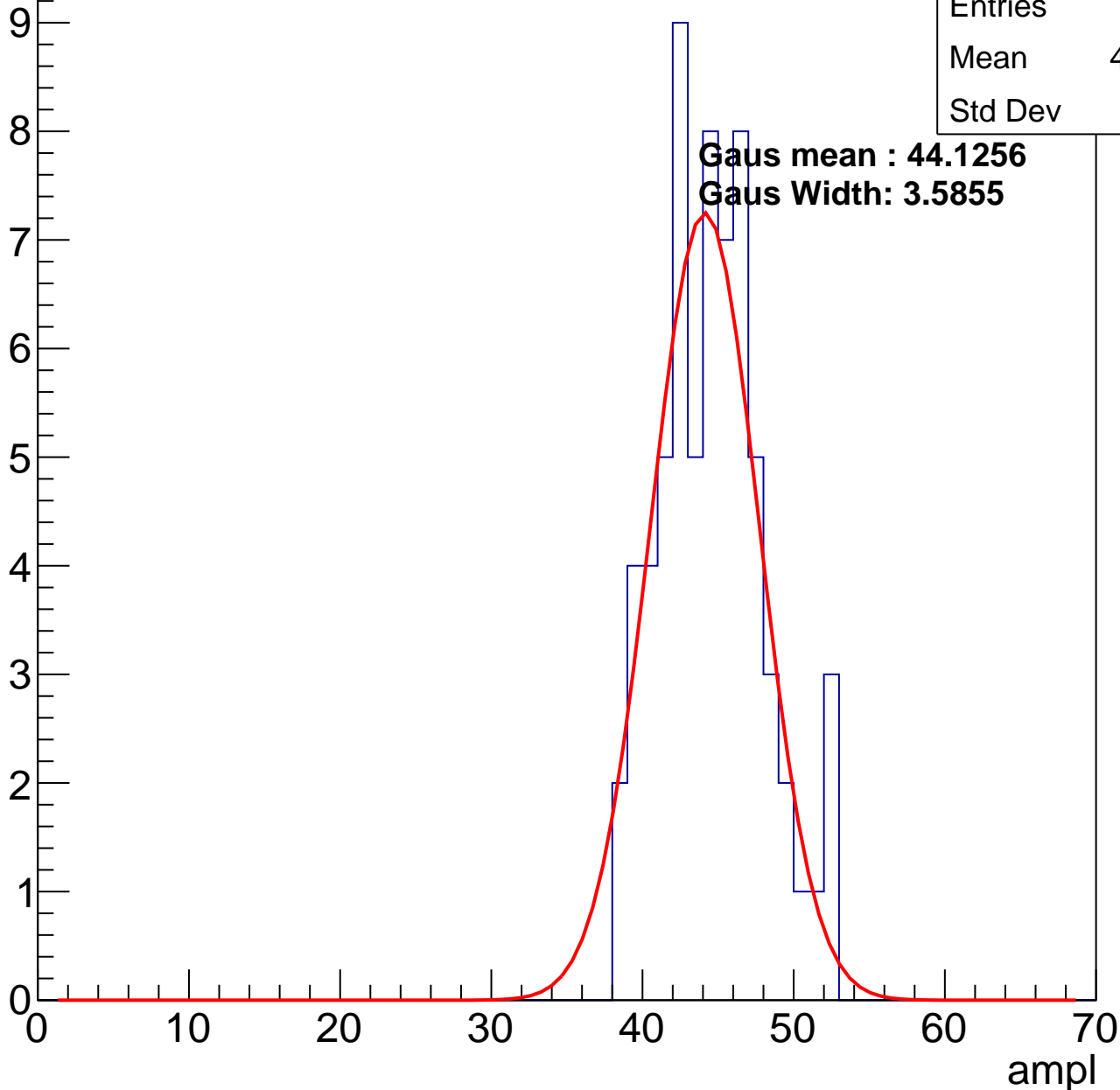
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.16
Std Dev	3.41

**Gaus mean : 44.1256**

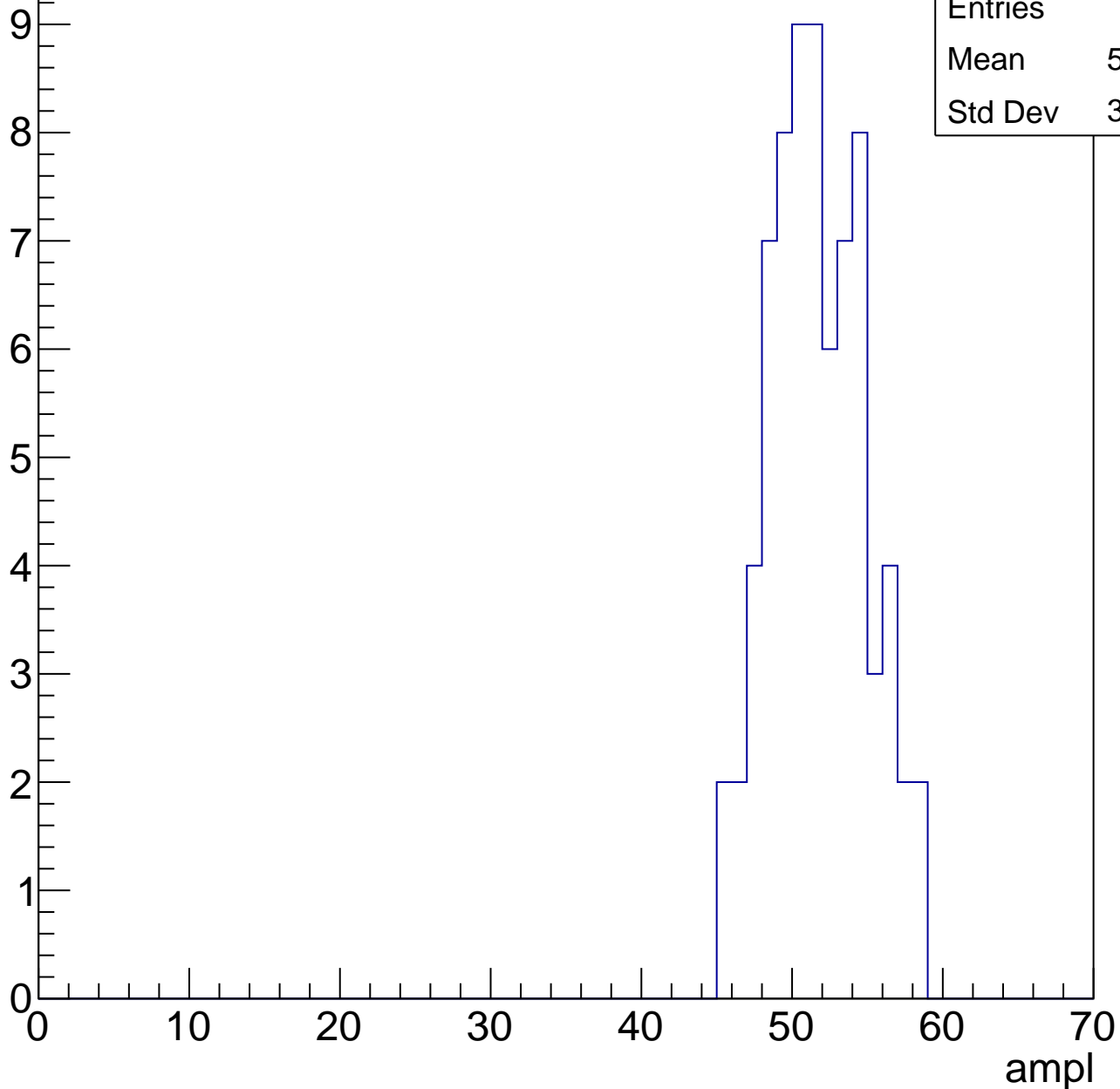
**Gaus Width: 3.5855**



# B1L102S, U25-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

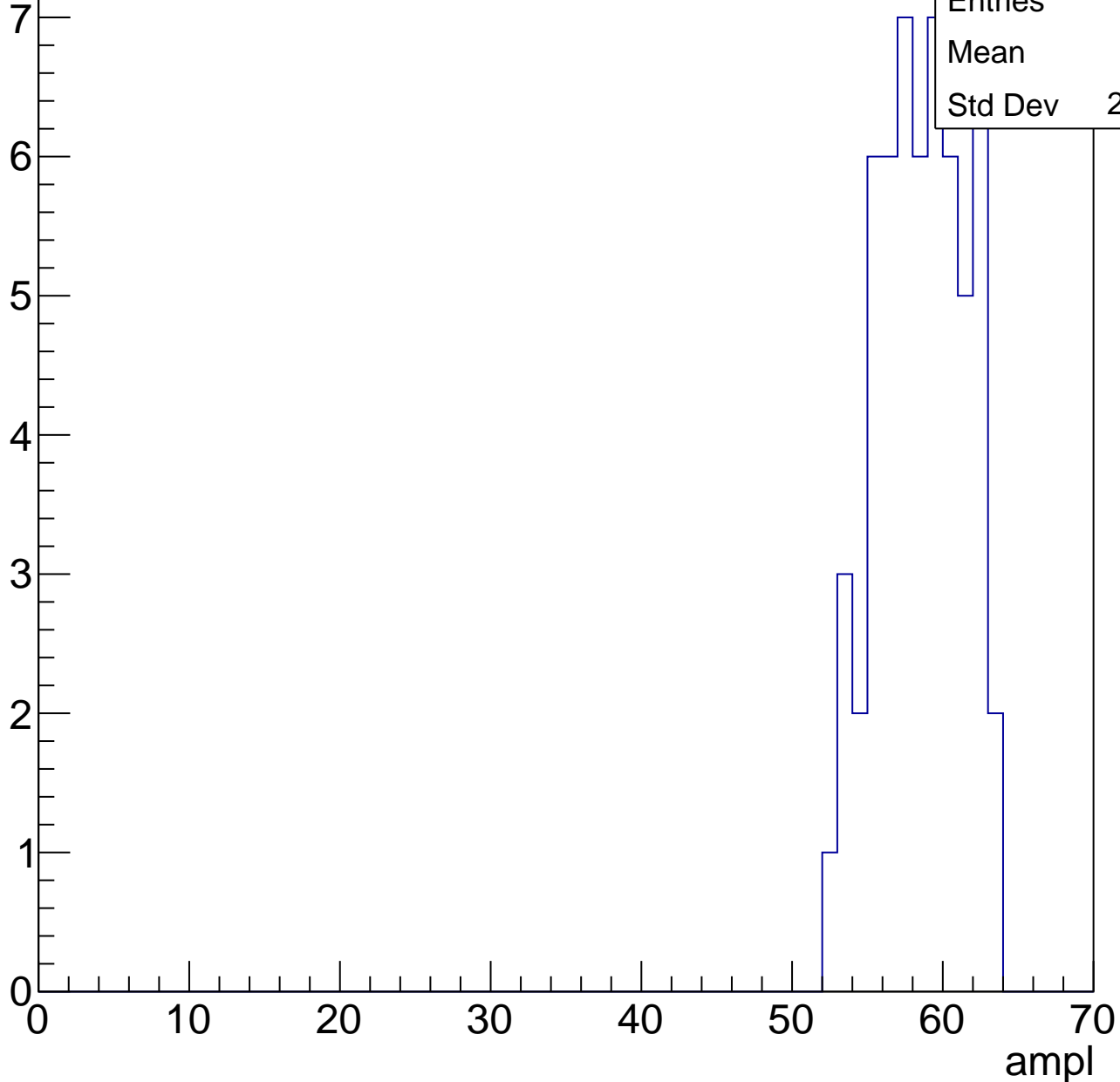
Entry



# B1L102S, U25-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



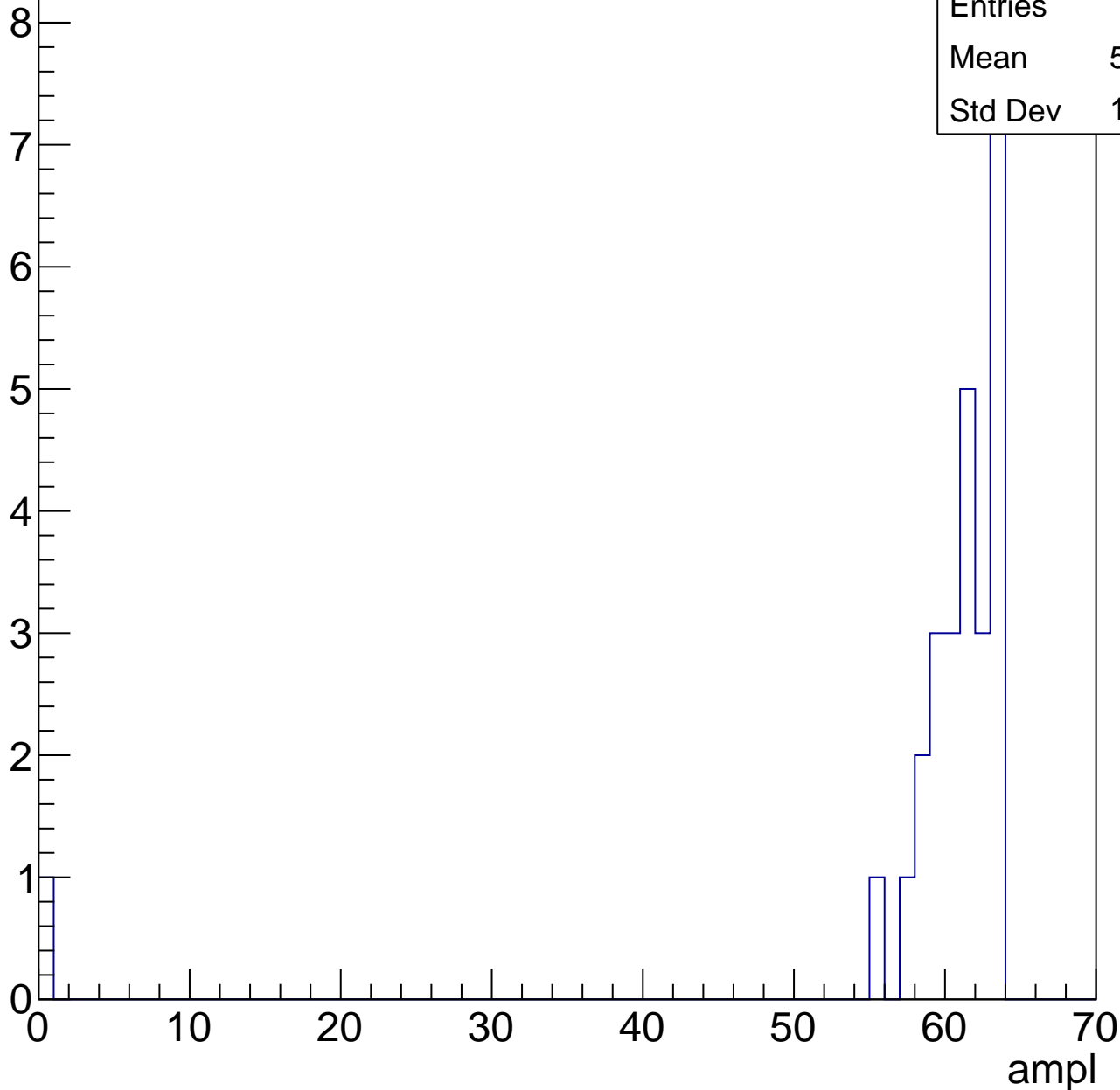
Entries	58
Mean	58.1
Std Dev	2.833

# B1L102S, U25-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	27
Mean	58.52
Std Dev	11.67



# B1L102S, U25-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

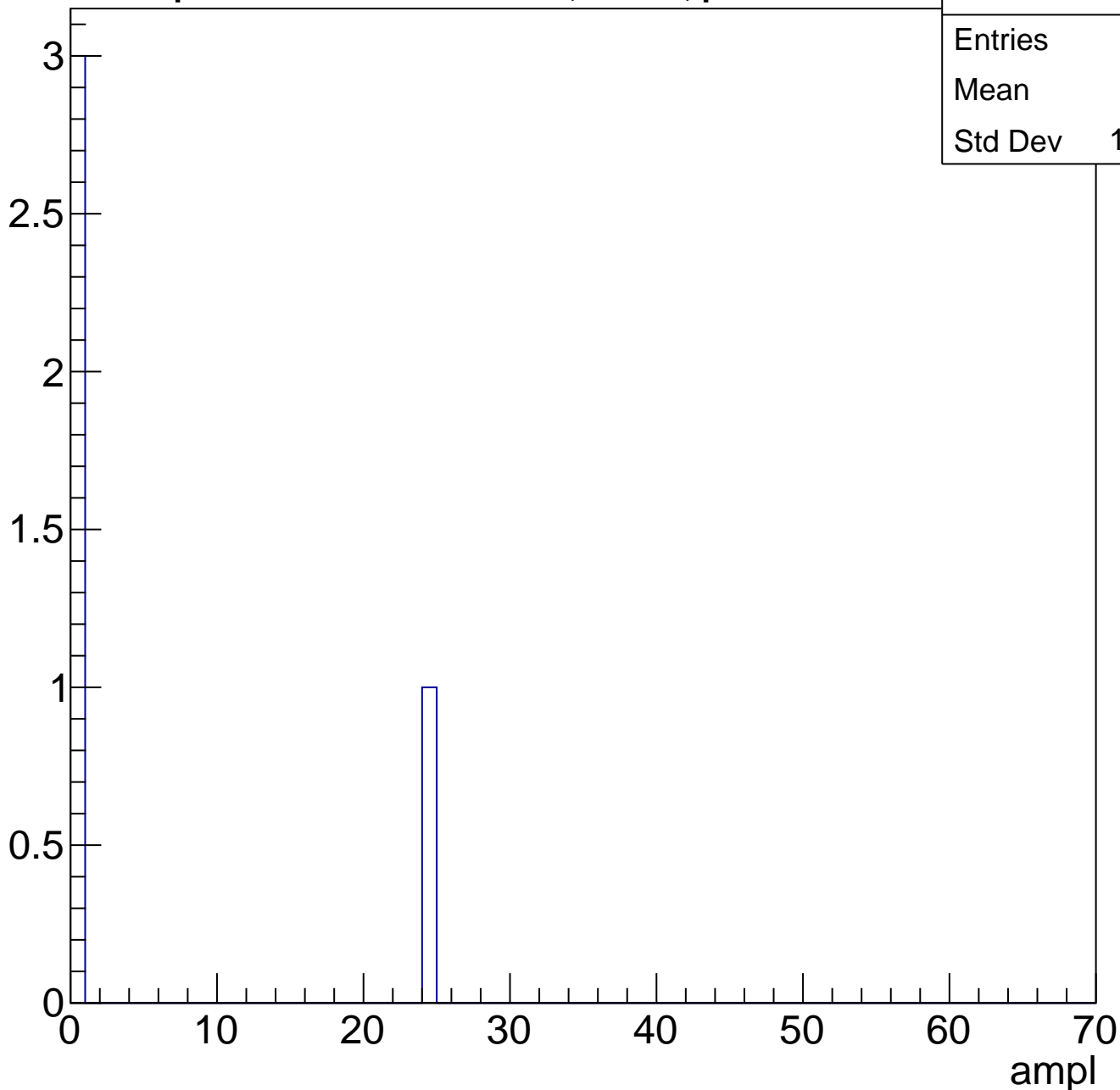
0



# B1L102S, U25-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch32, adc0

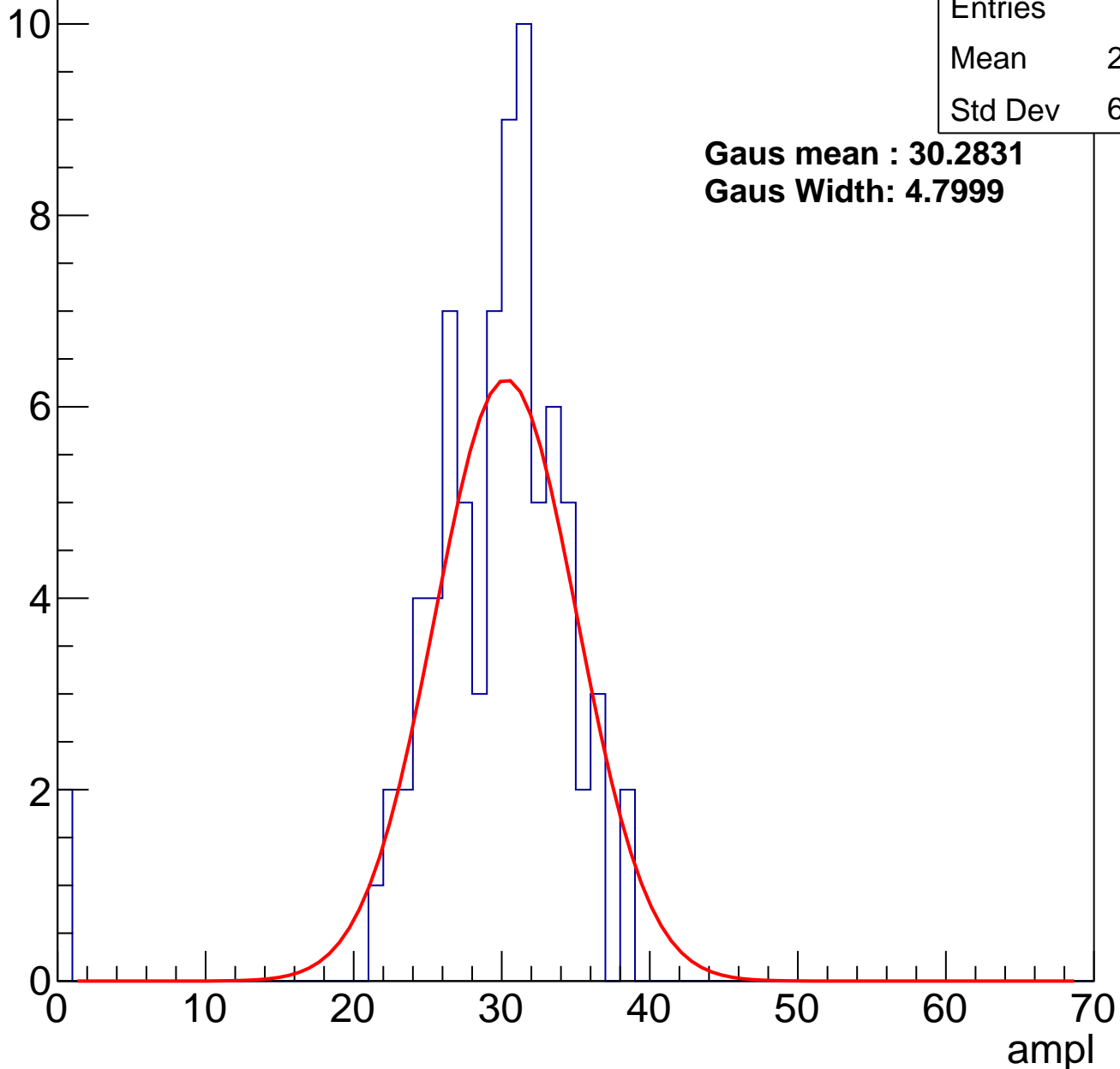
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	79
Mean	28.77
Std Dev	6.015

**Gaus mean : 30.2831**

**Gaus Width: 4.7999**

Entry



# B1L102S, U25-ch32, adc1

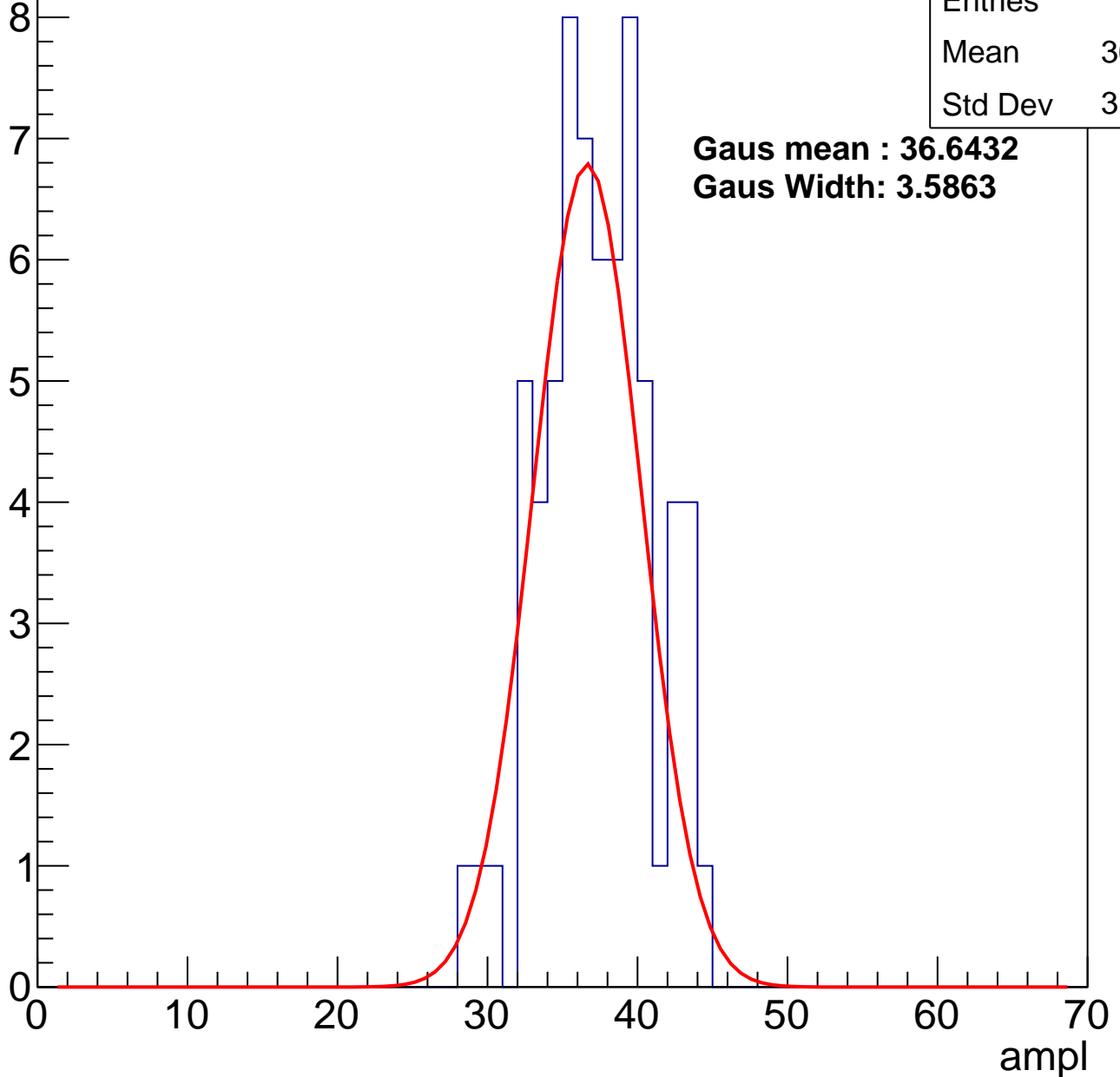
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.84
Std Dev	3.577

**Gaus mean : 36.6432**

**Gaus Width: 3.5863**



# B1L102S, U25-ch32, adc2

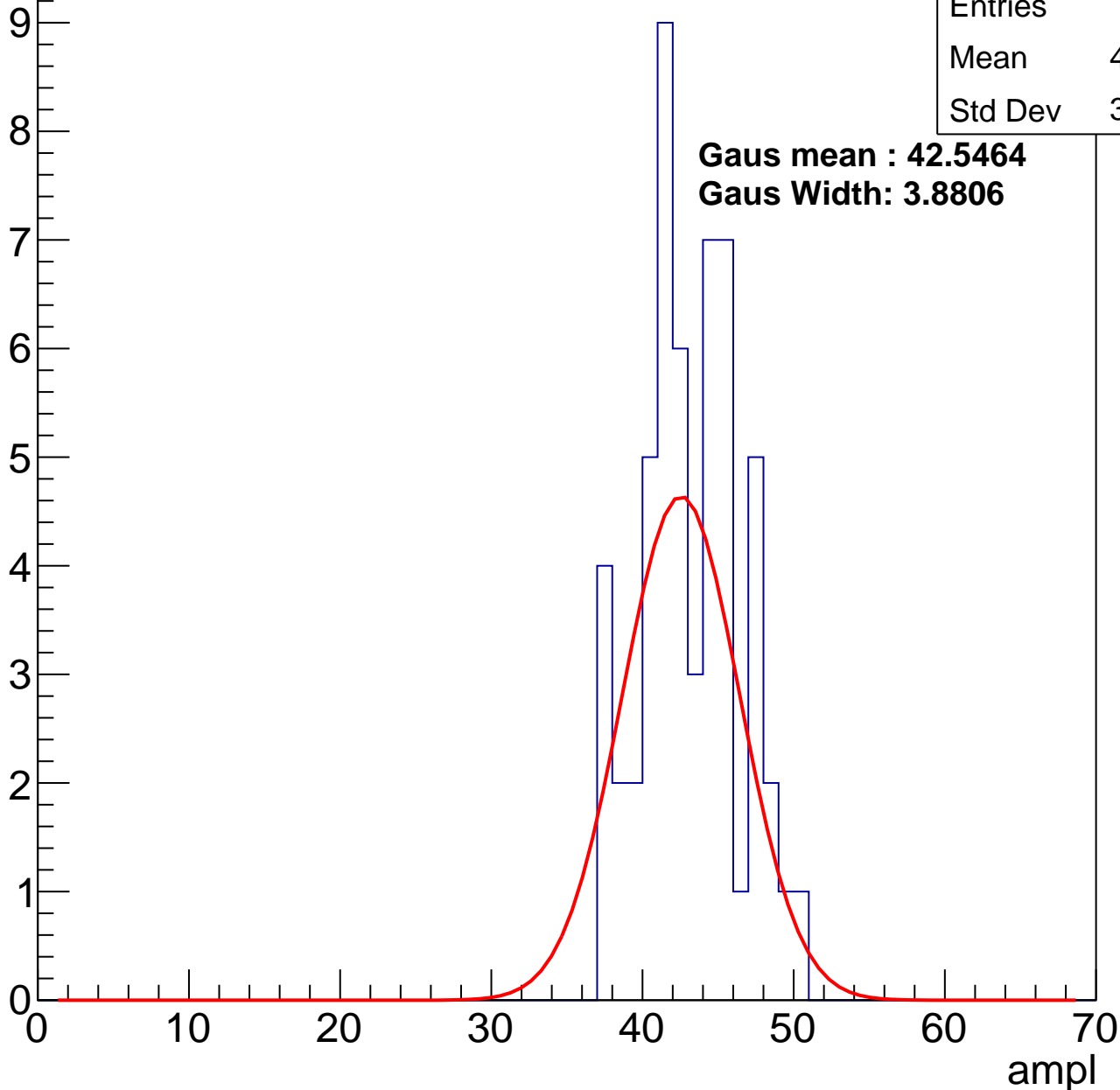
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.75
Std Dev	3.215

**Gaus mean : 42.5464**

**Gaus Width: 3.8806**

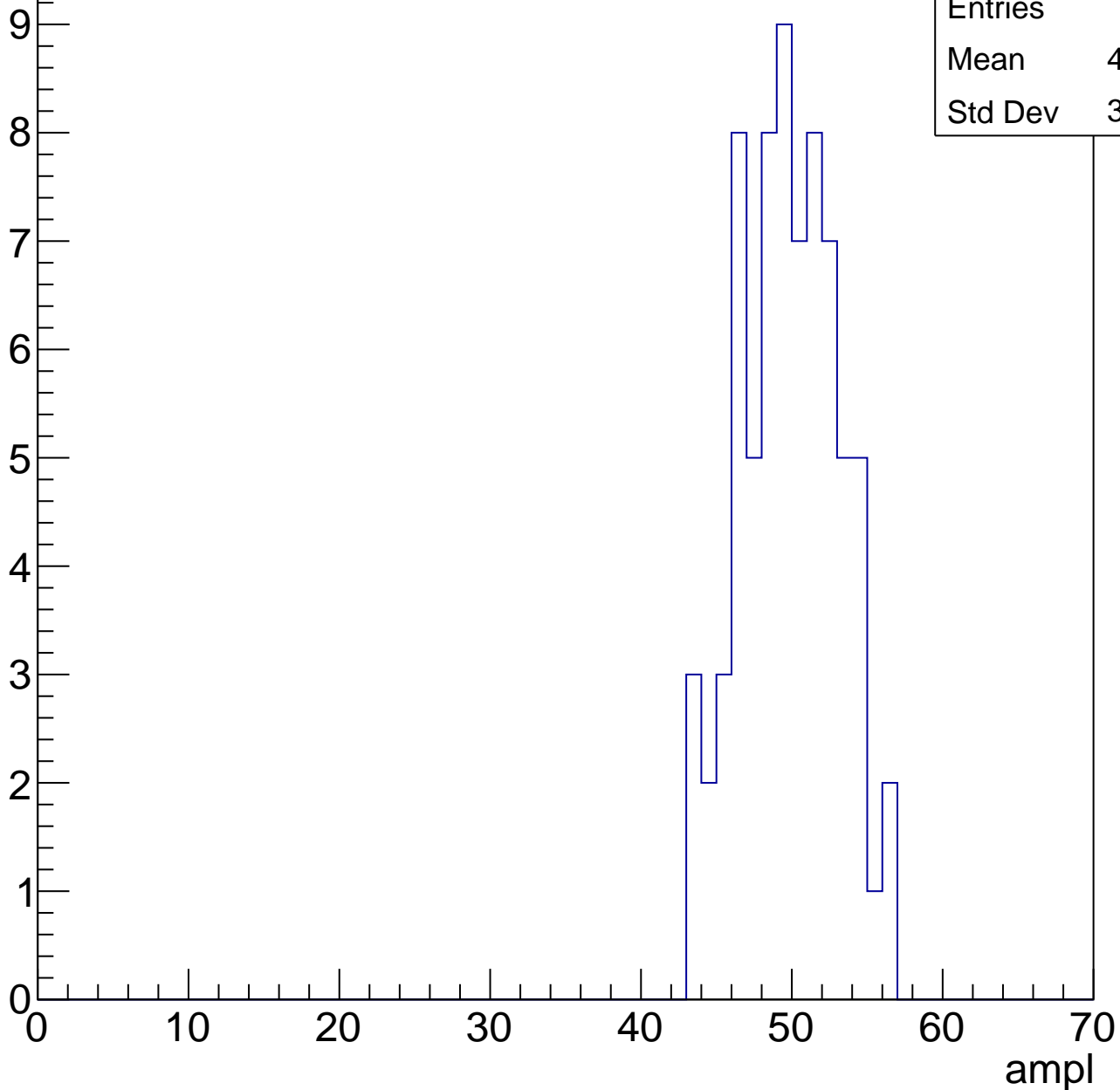


# B1L102S, U25-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.37
Std Dev	3.177

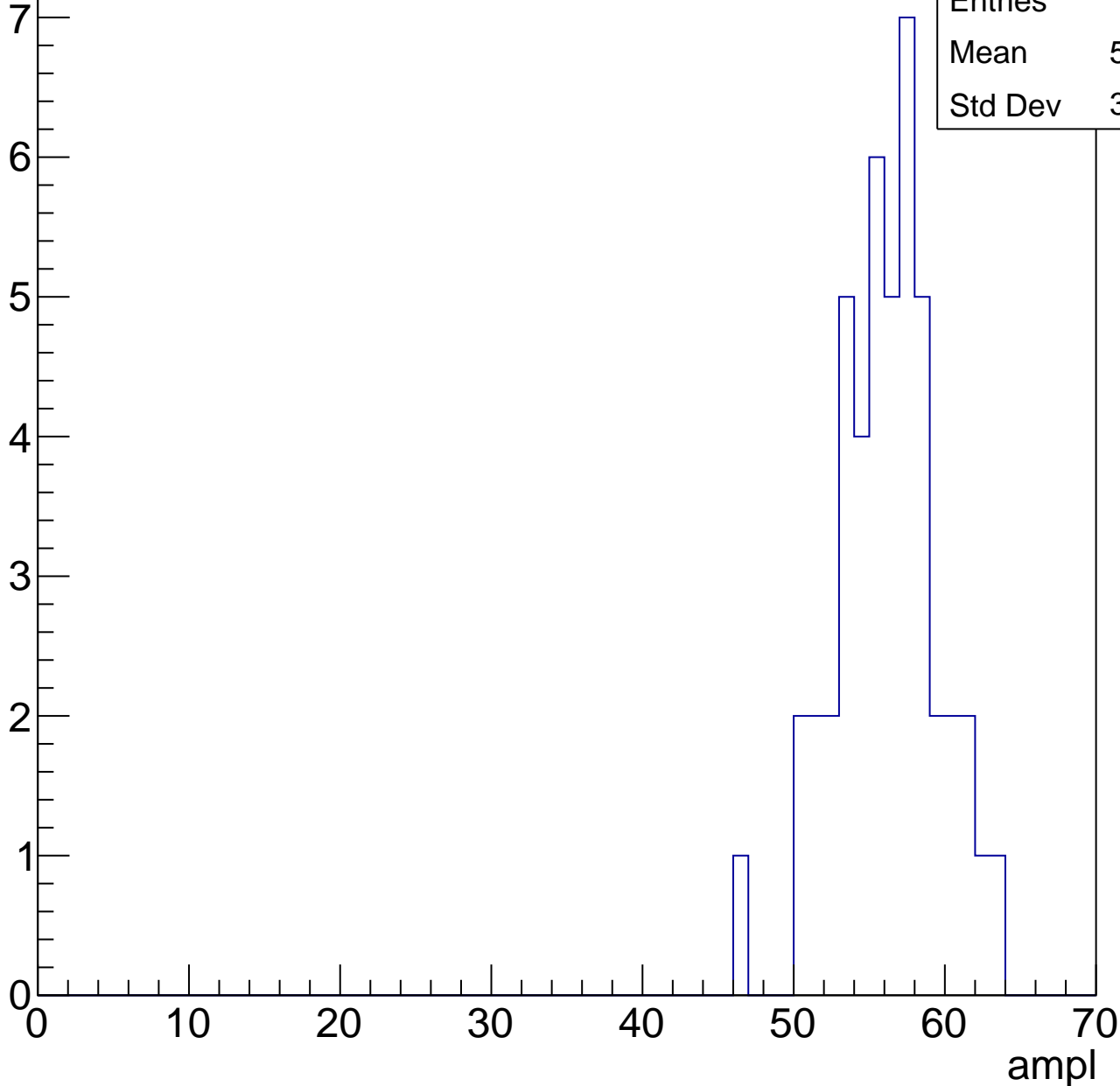


# B1L102S, U25-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.68
Std Dev	3.352



# B1L102S, U25-ch32, adc5

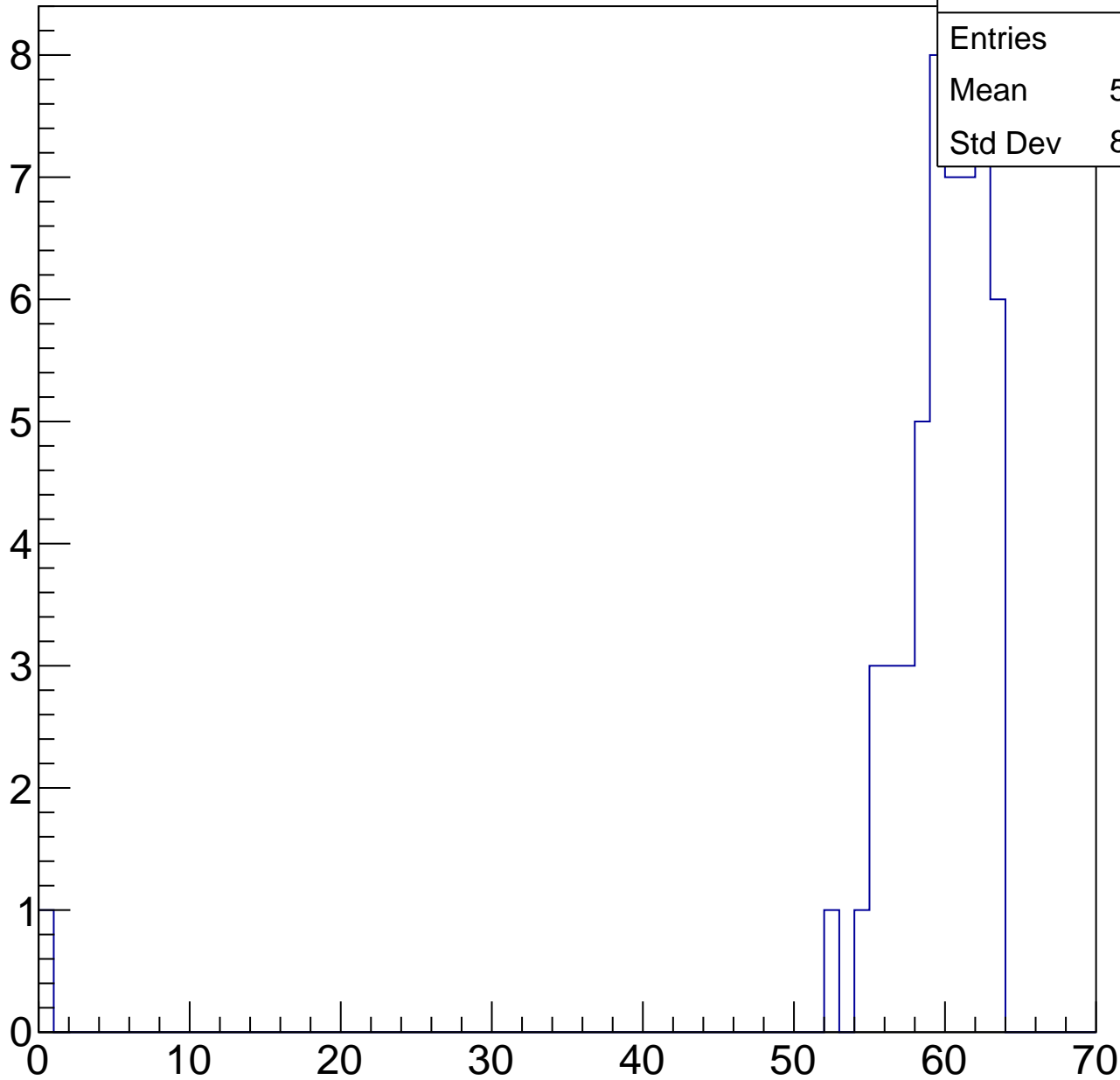
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.36
Std Dev	8.499

ampl



# B1L102S, U25-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch33, adc0

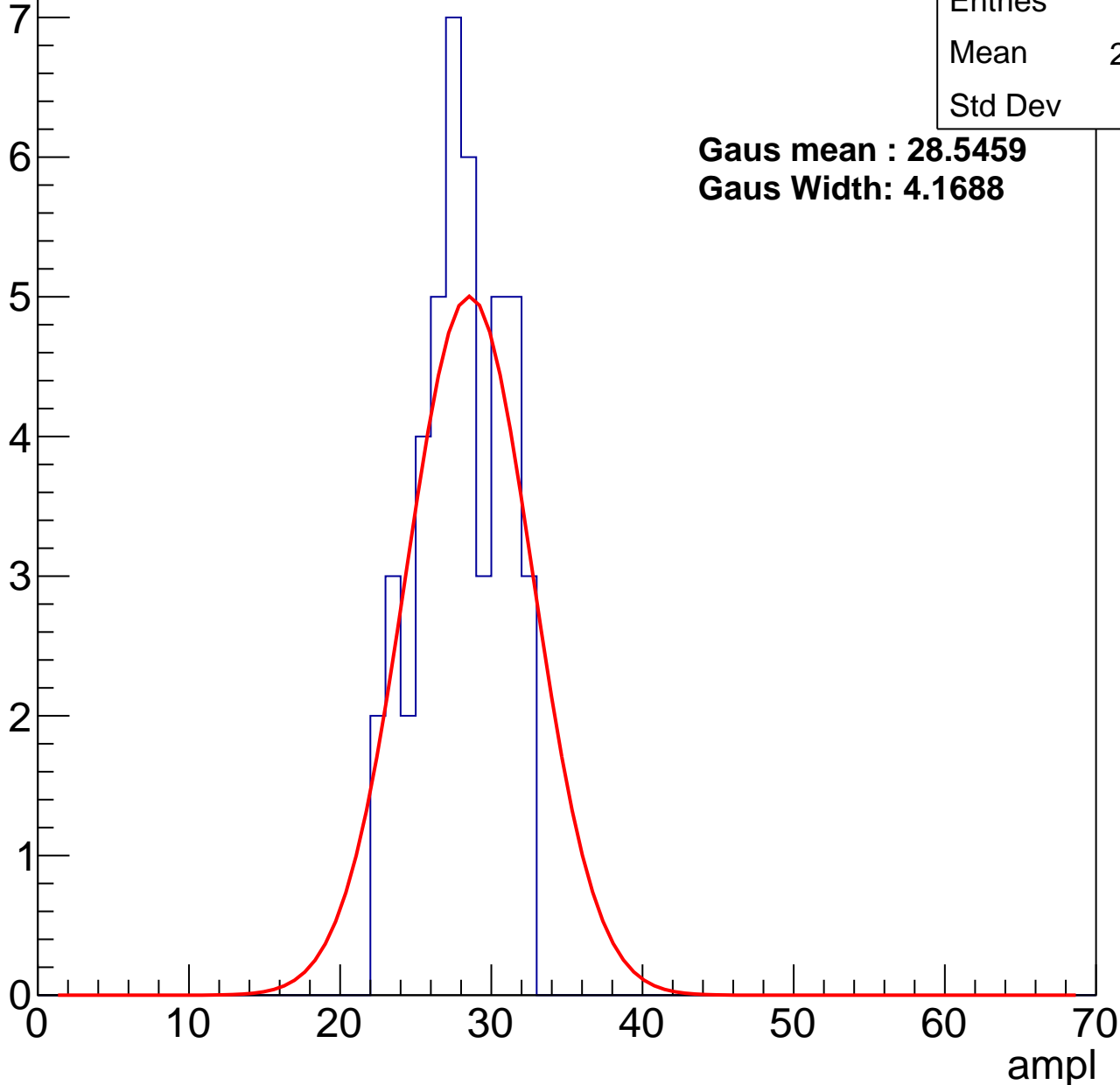
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	27.47
Std Dev	2.77

**Gaus mean : 28.5459**

**Gaus Width: 4.1688**



# B1L102S, U25-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	99
Mean	35.03
Std Dev	4.006

**Gaus mean : 35.0907**

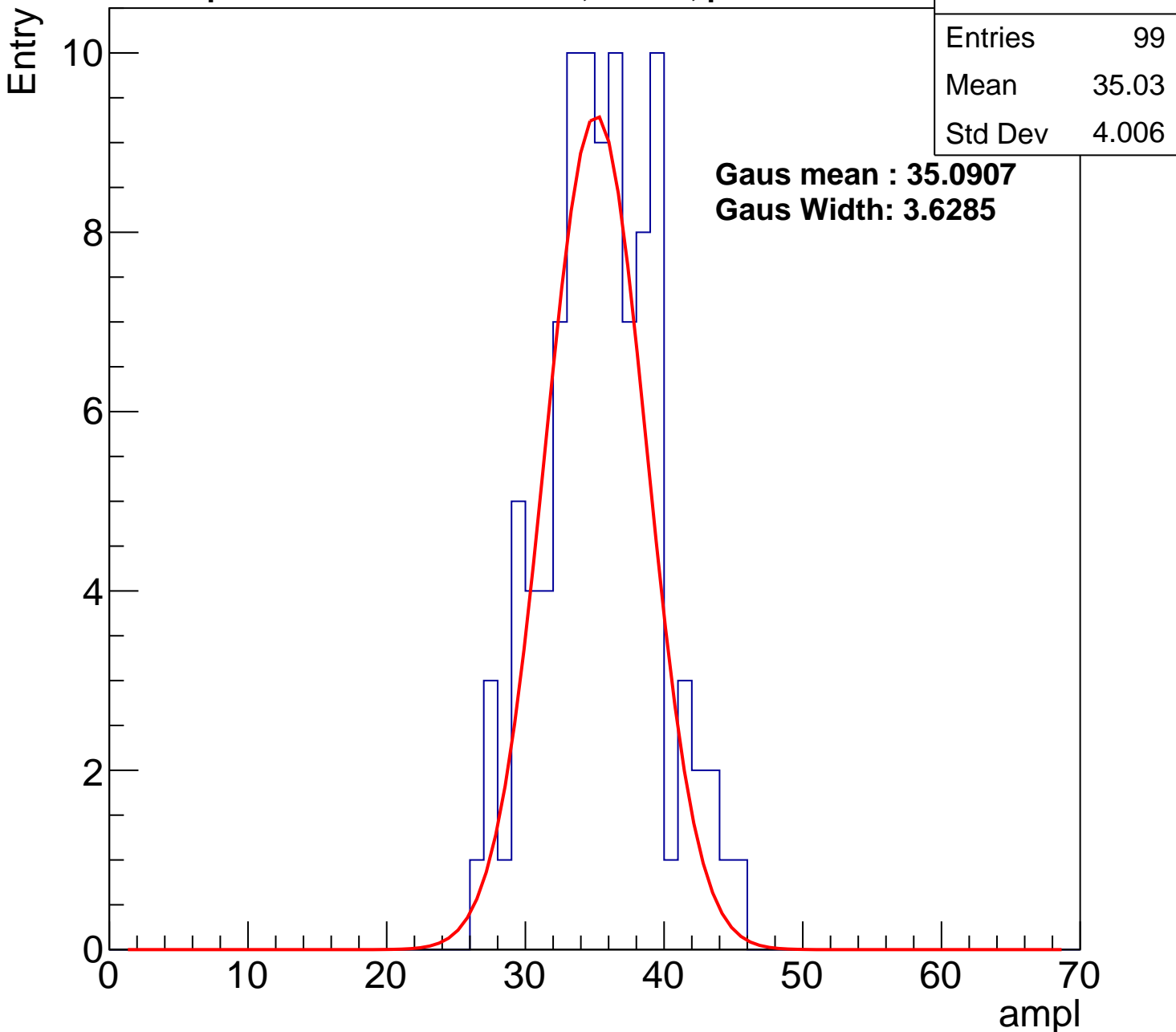
**Gaus Width: 3.6285**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch33, adc2

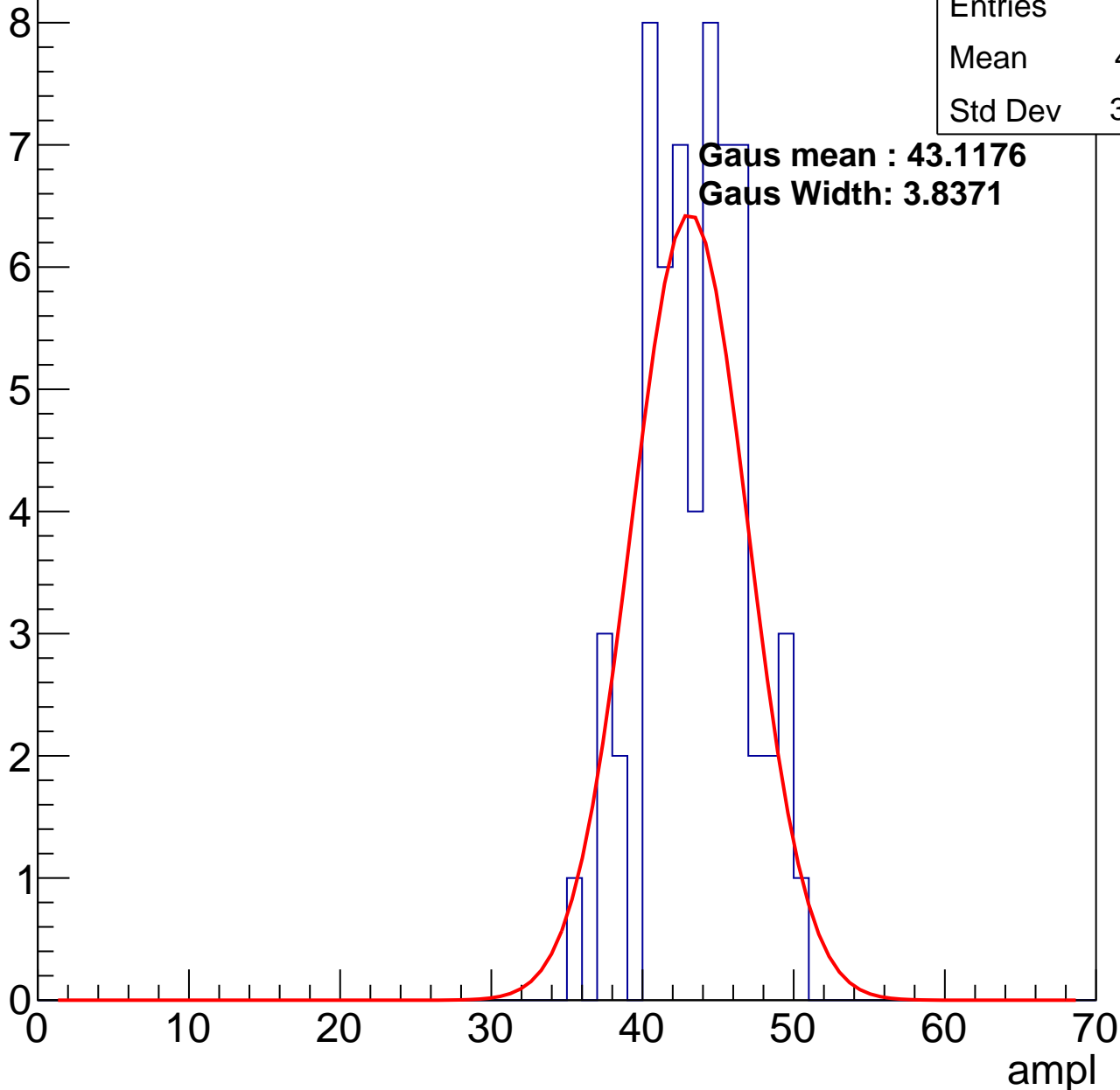
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.11
Std Dev	3.295

**Gaus mean : 43.1176**

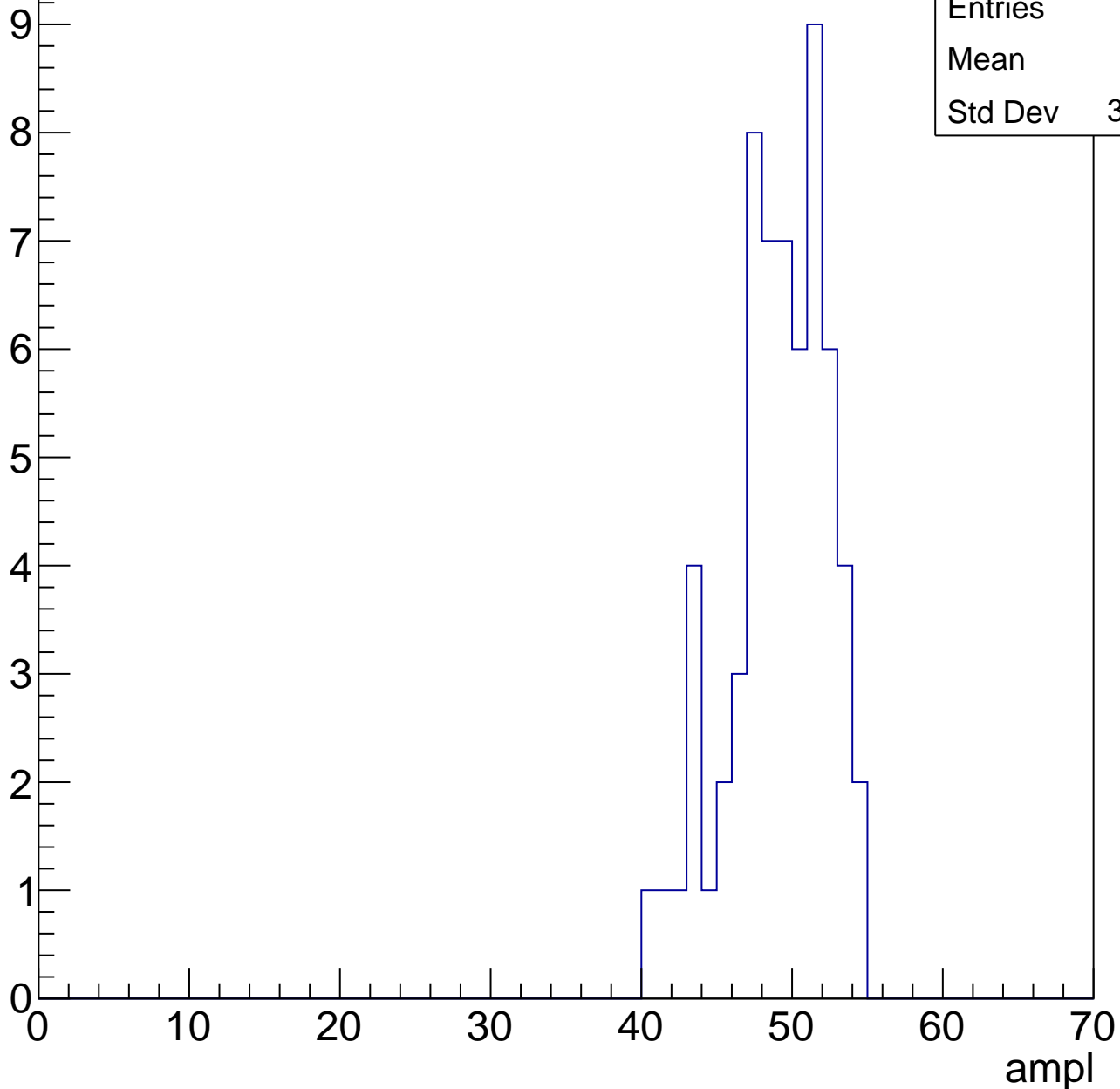
**Gaus Width: 3.8371**



# B1L102S, U25-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

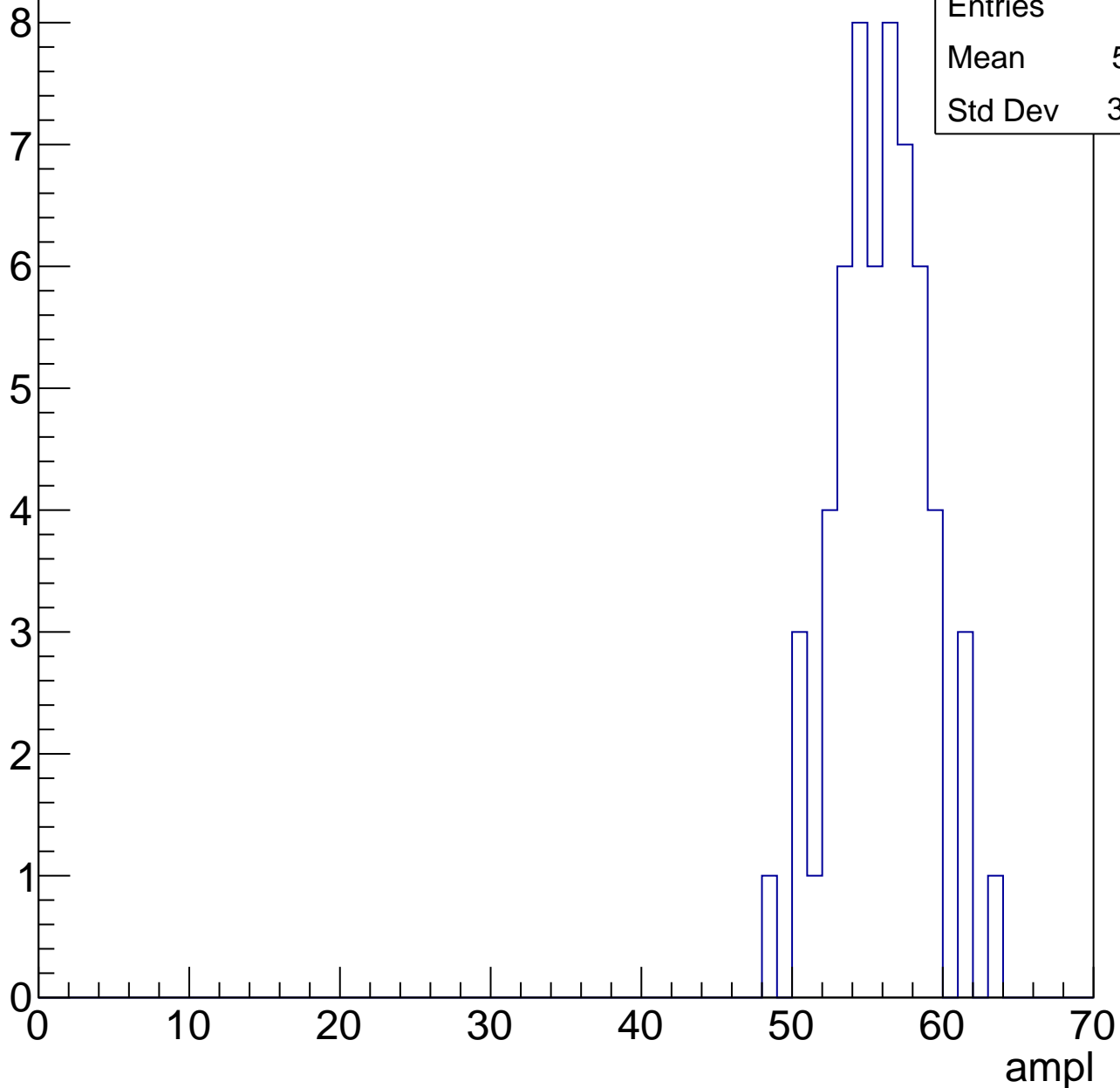


# B1L102S, U25-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.41
Std Dev	3.006

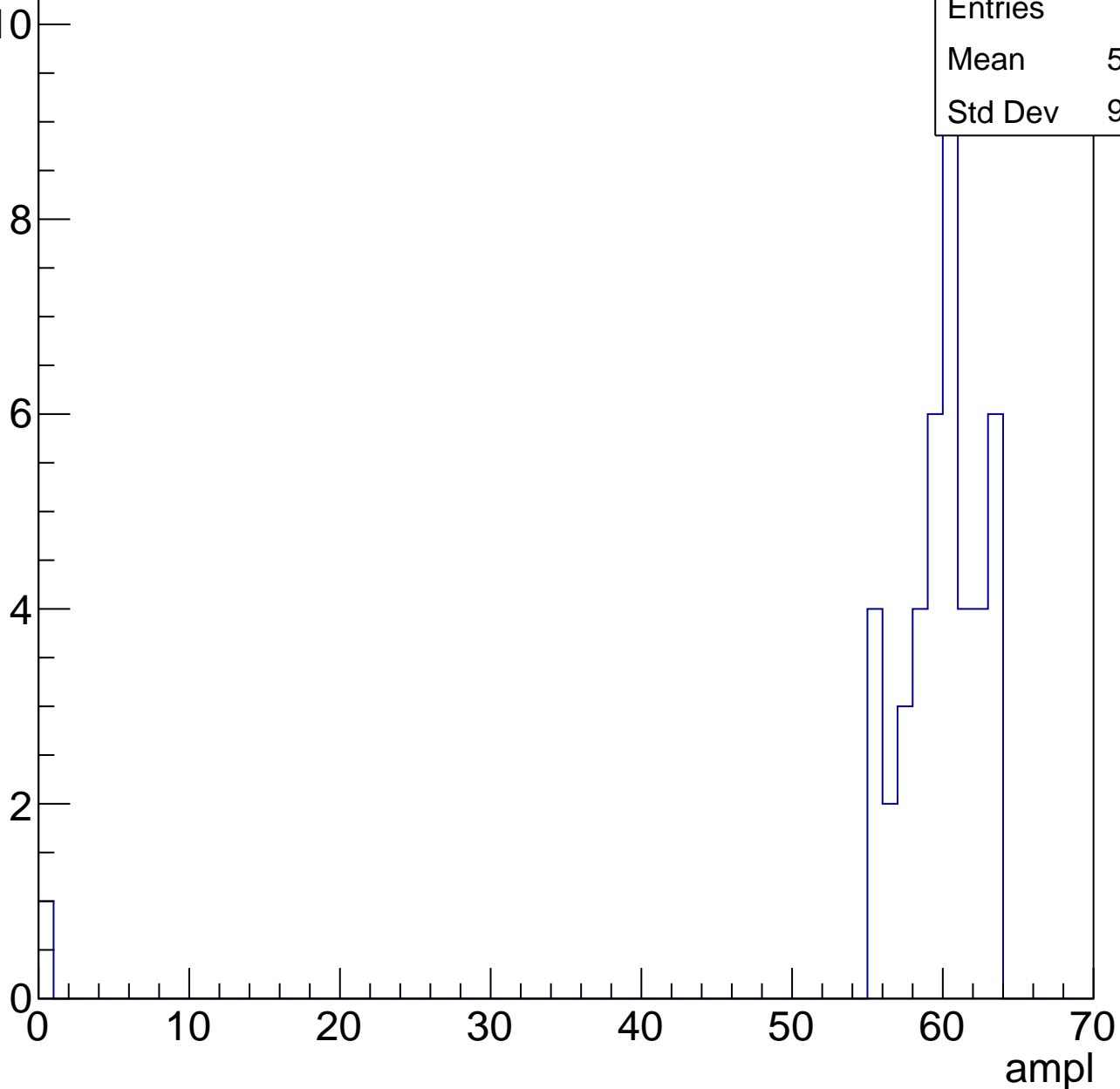


# B1L102S, U25-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.16
Std Dev	9.177

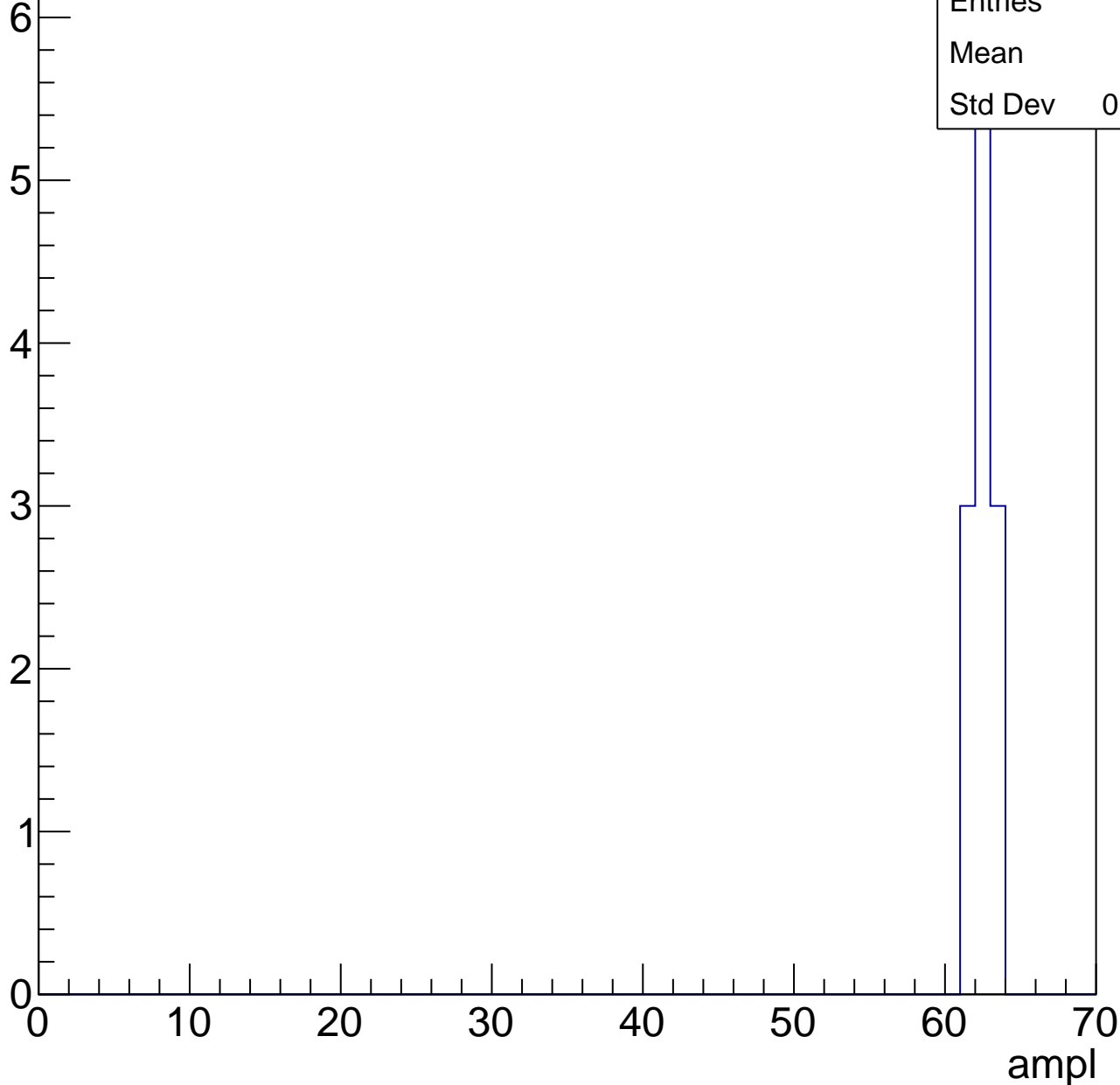


# B1L102S, U25-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62
Std Dev	0.7071





# B1L102S, U25-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L102S, U25-ch34, adc0

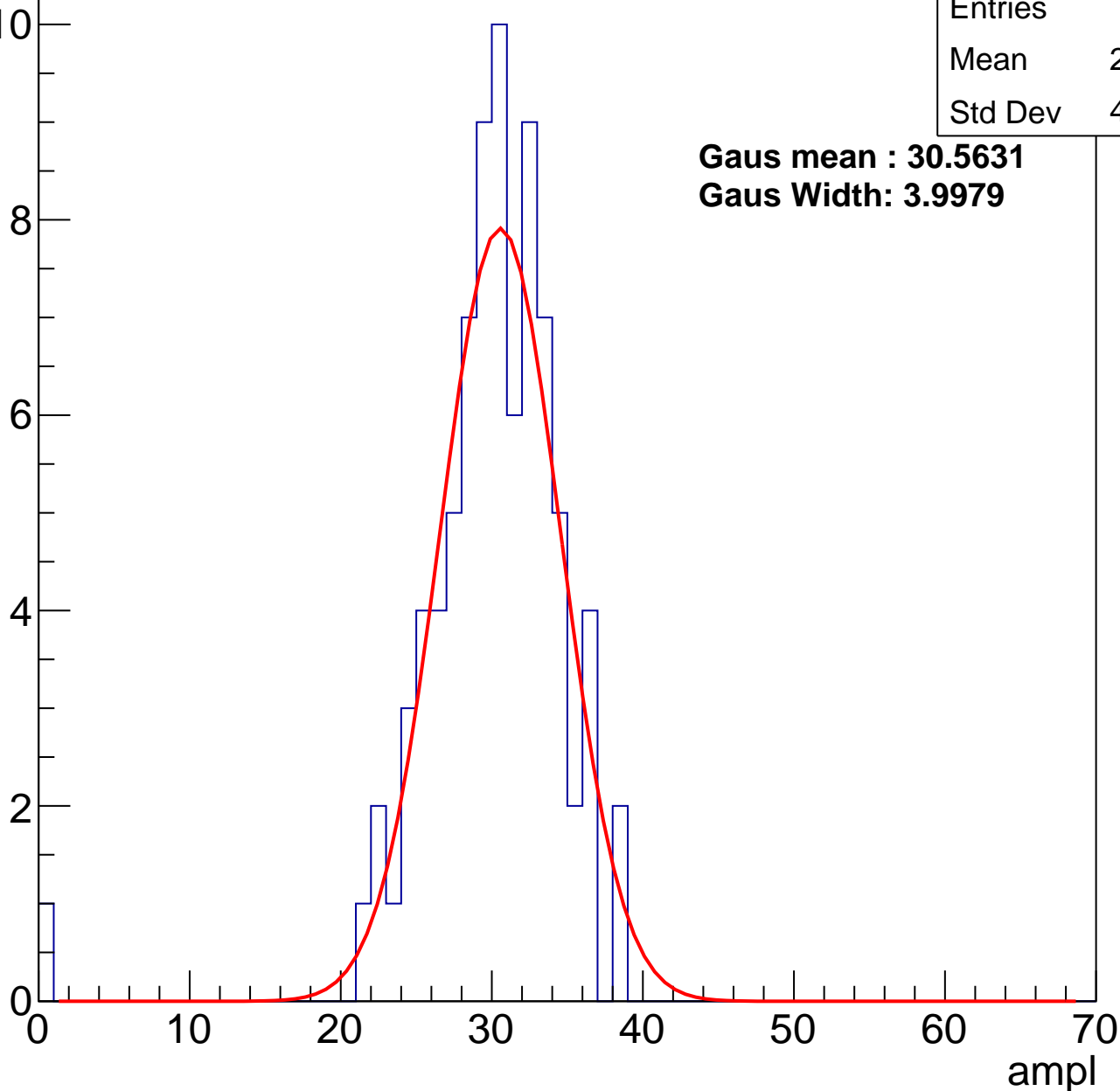
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	29.52
Std Dev	4.939

**Gaus mean : 30.5631**

**Gaus Width: 3.9979**



# B1L102S, U25-ch34, adc1

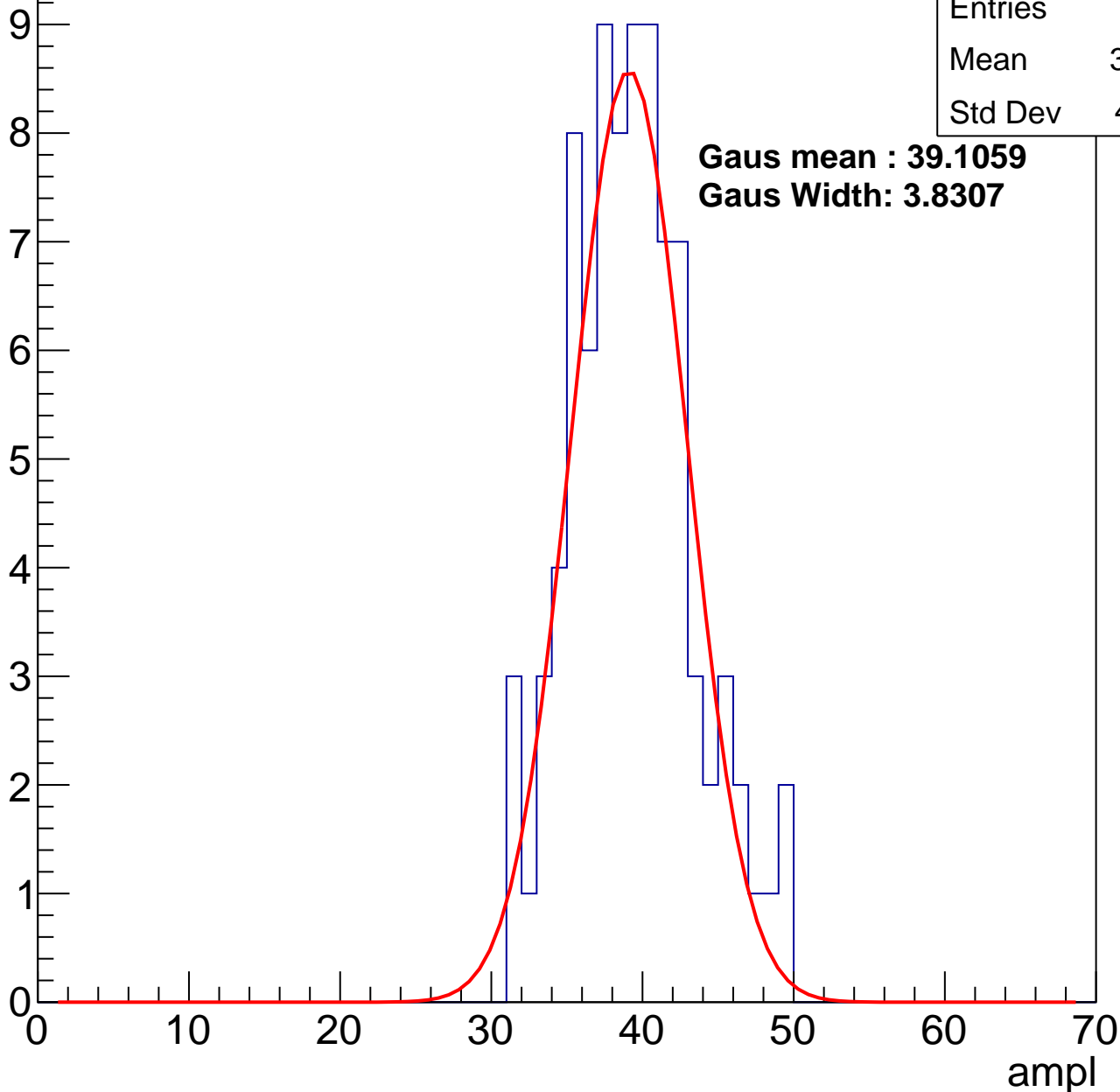
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	38.89
Std Dev	4.021

**Gaus mean : 39.1059**

**Gaus Width: 3.8307**



# B1L102S, U25-ch34, adc2

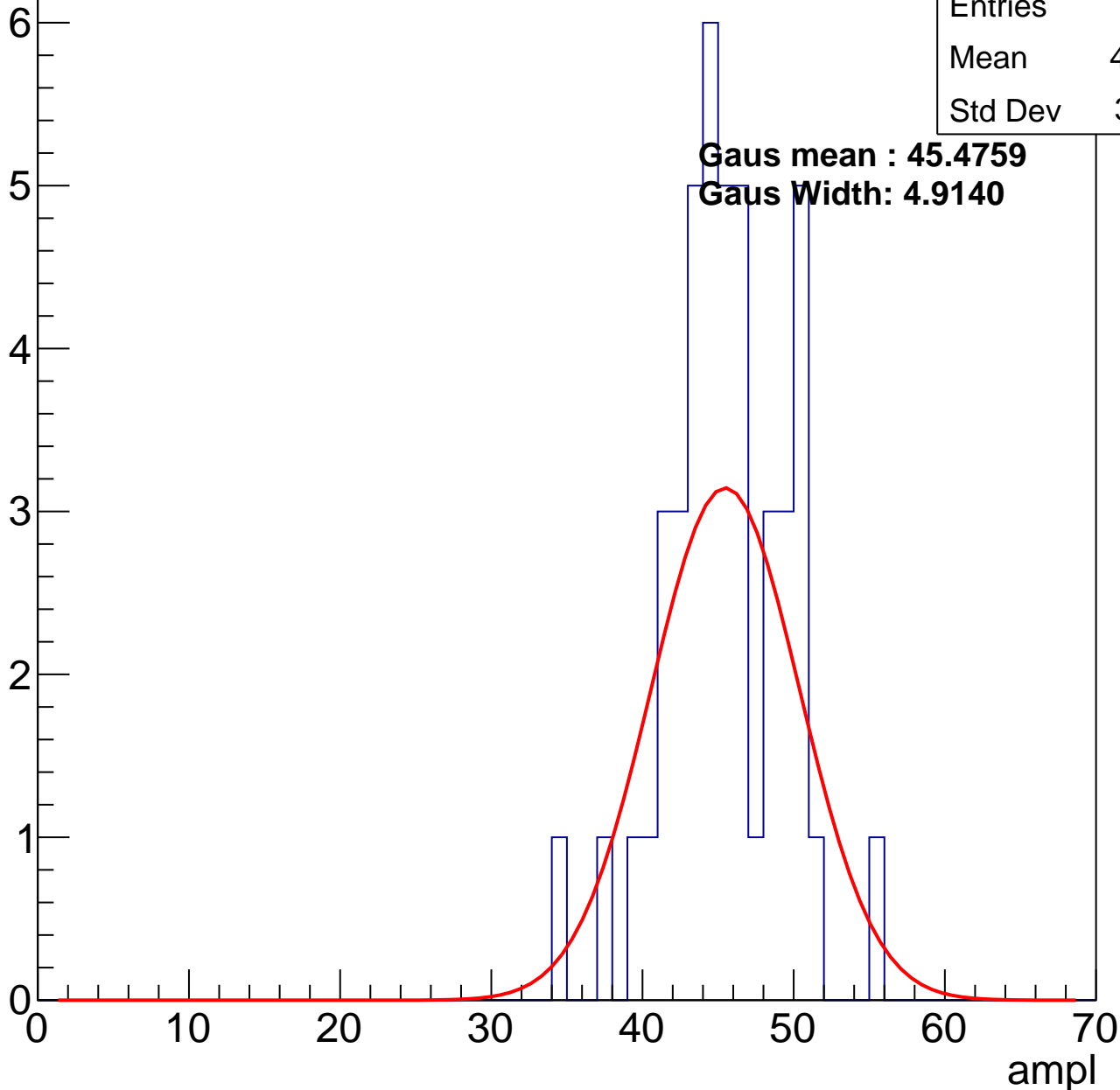
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	45.04
Std Dev	3.921

**Gaus mean : 45.4759**

**Gaus Width: 4.9140**

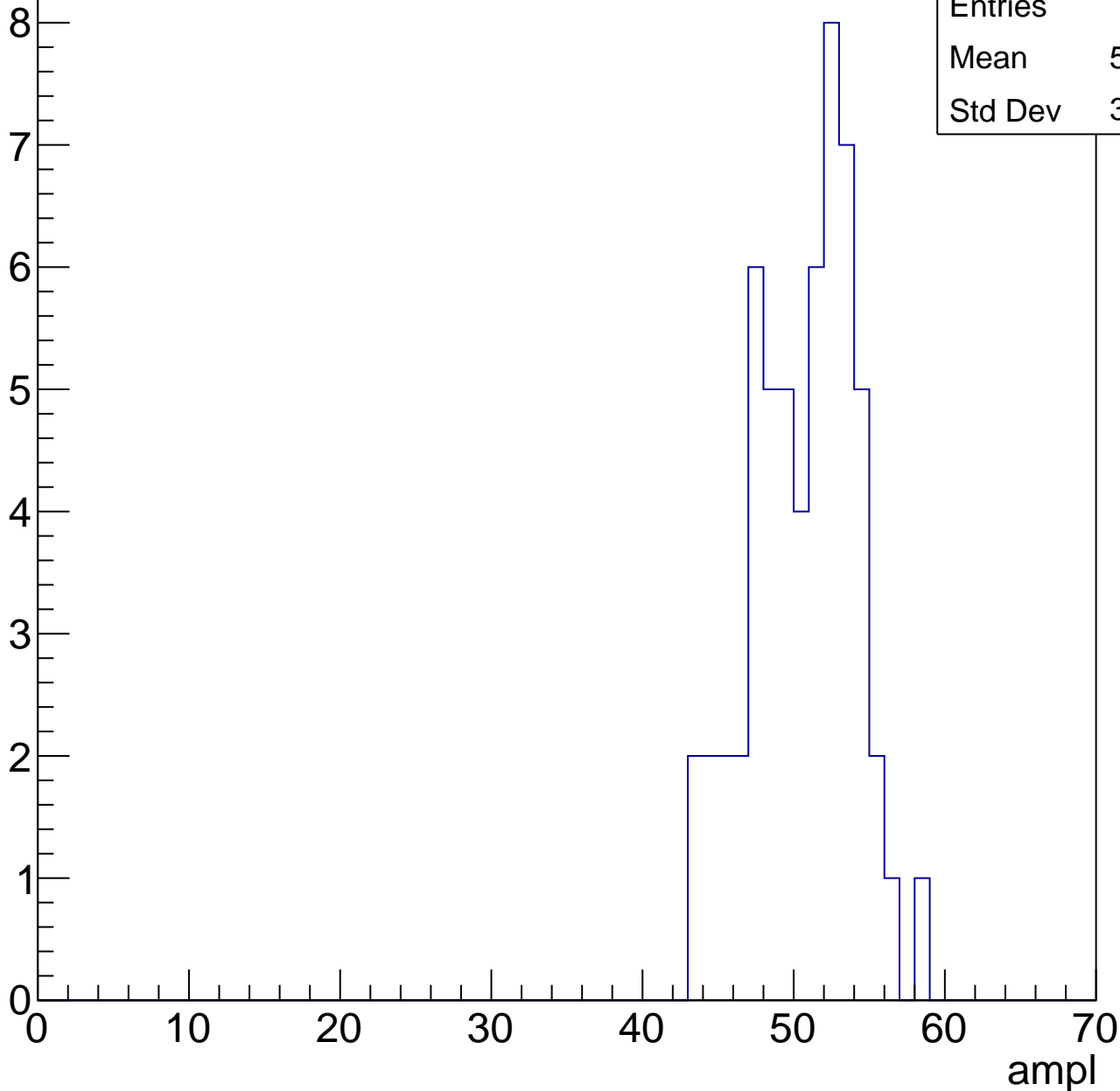


# B1L102S, U25-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

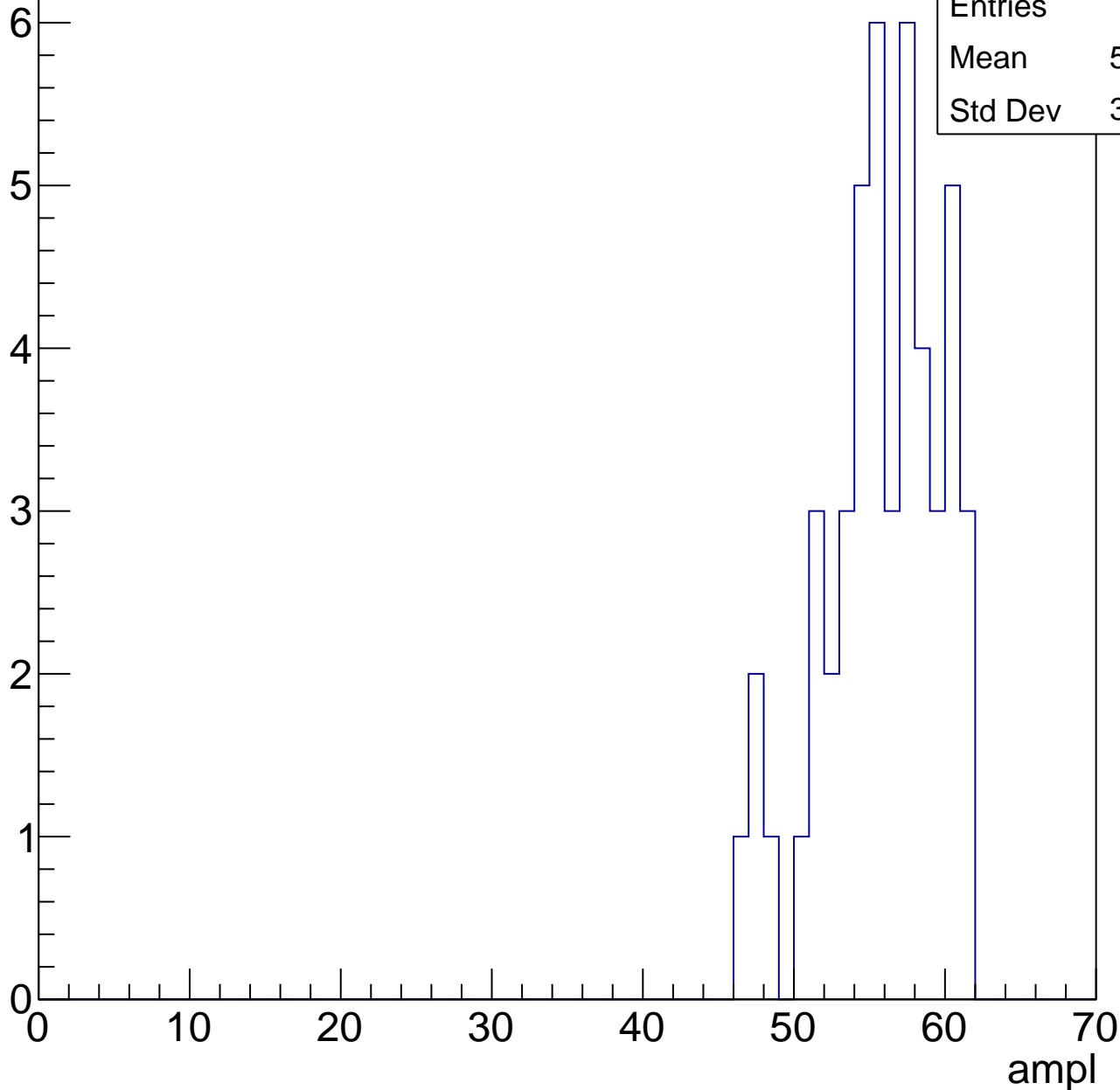
Entries	58
Mean	50.17
Std Dev	3.374



# B1L102S, U25-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

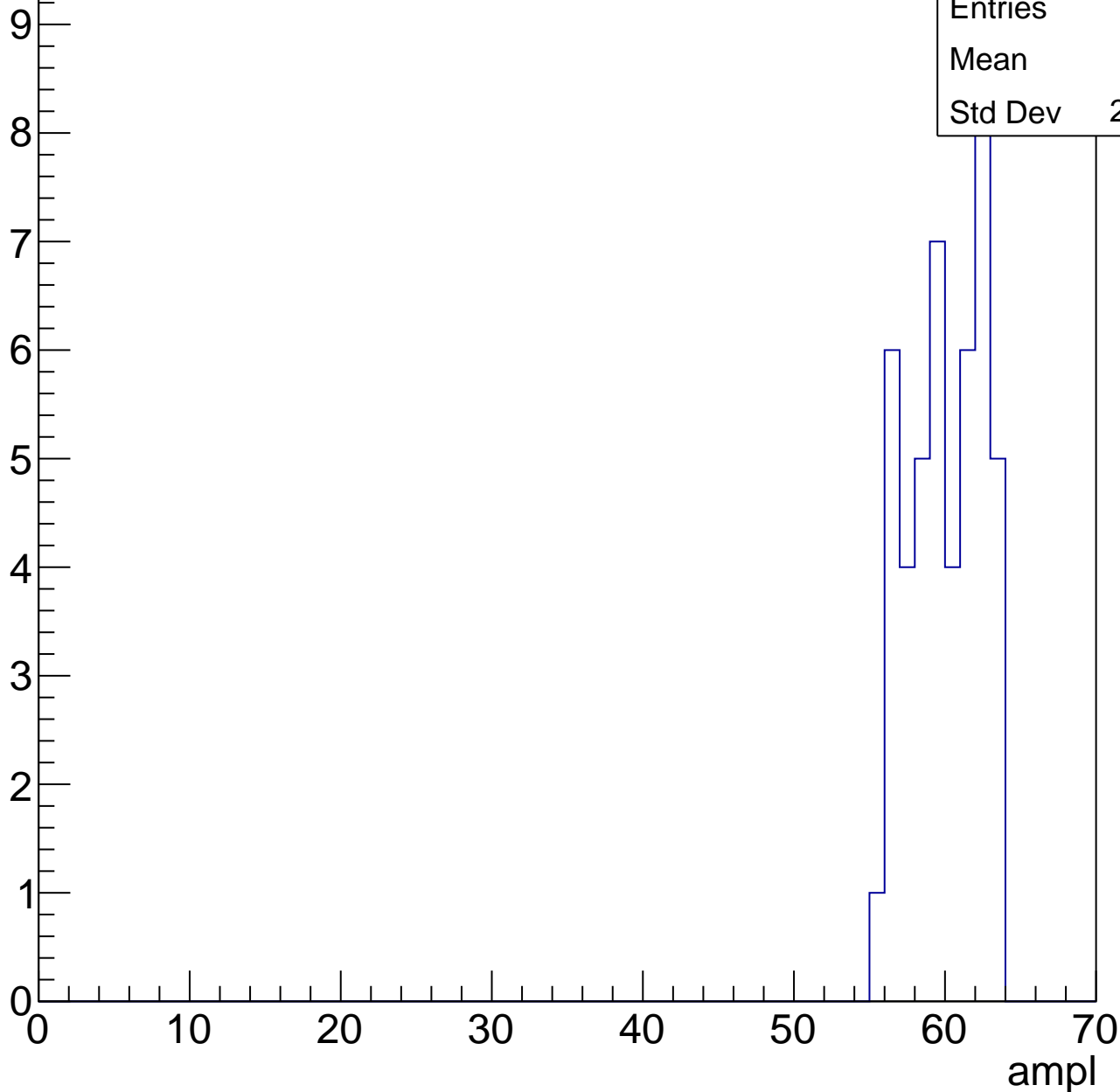


# B1L102S, U25-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.6
Std Dev	2.367

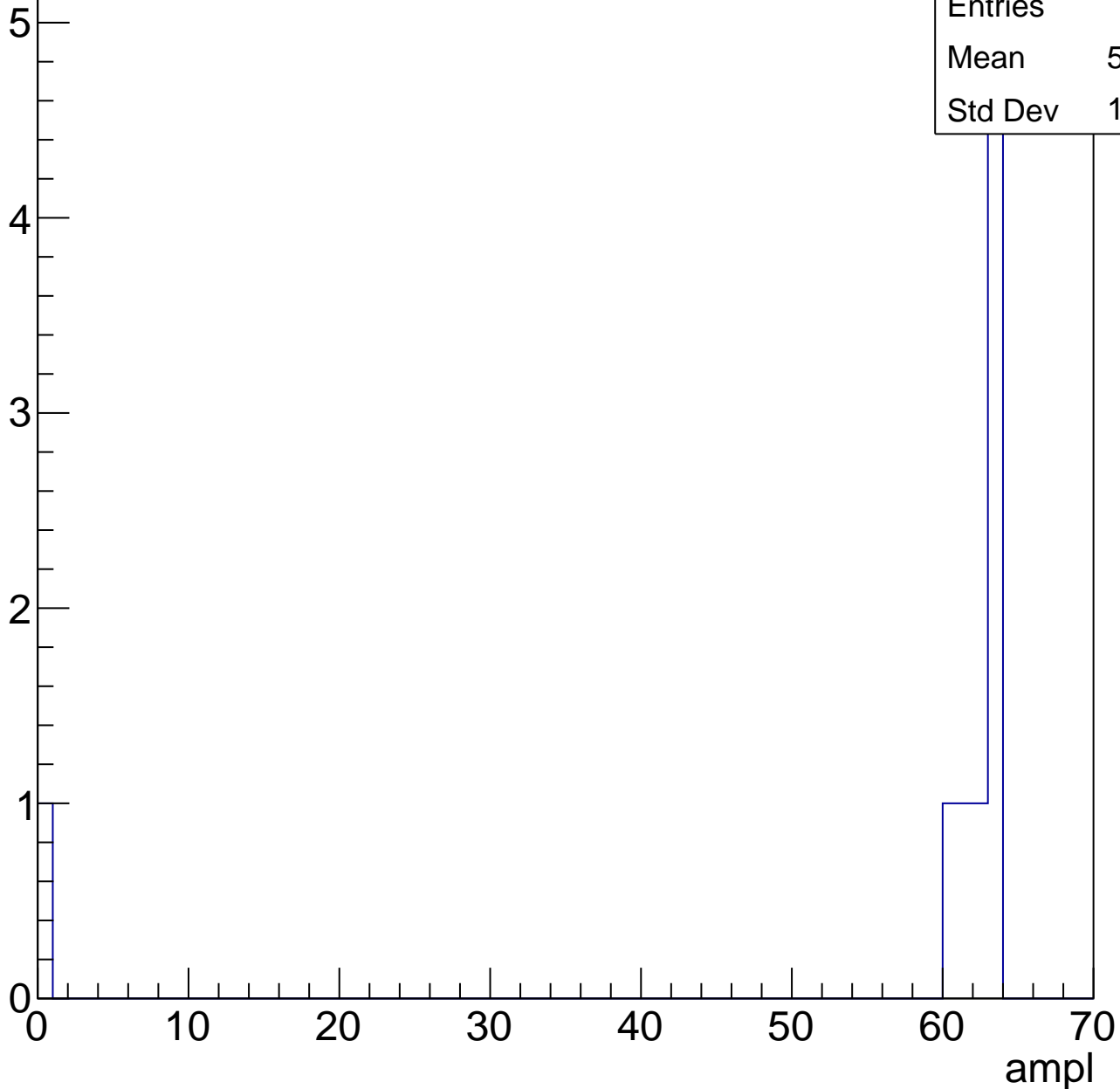


# B1L102S, U25-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	55.33
Std Dev	19.59





# B1L102S, U25-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch35, adc0

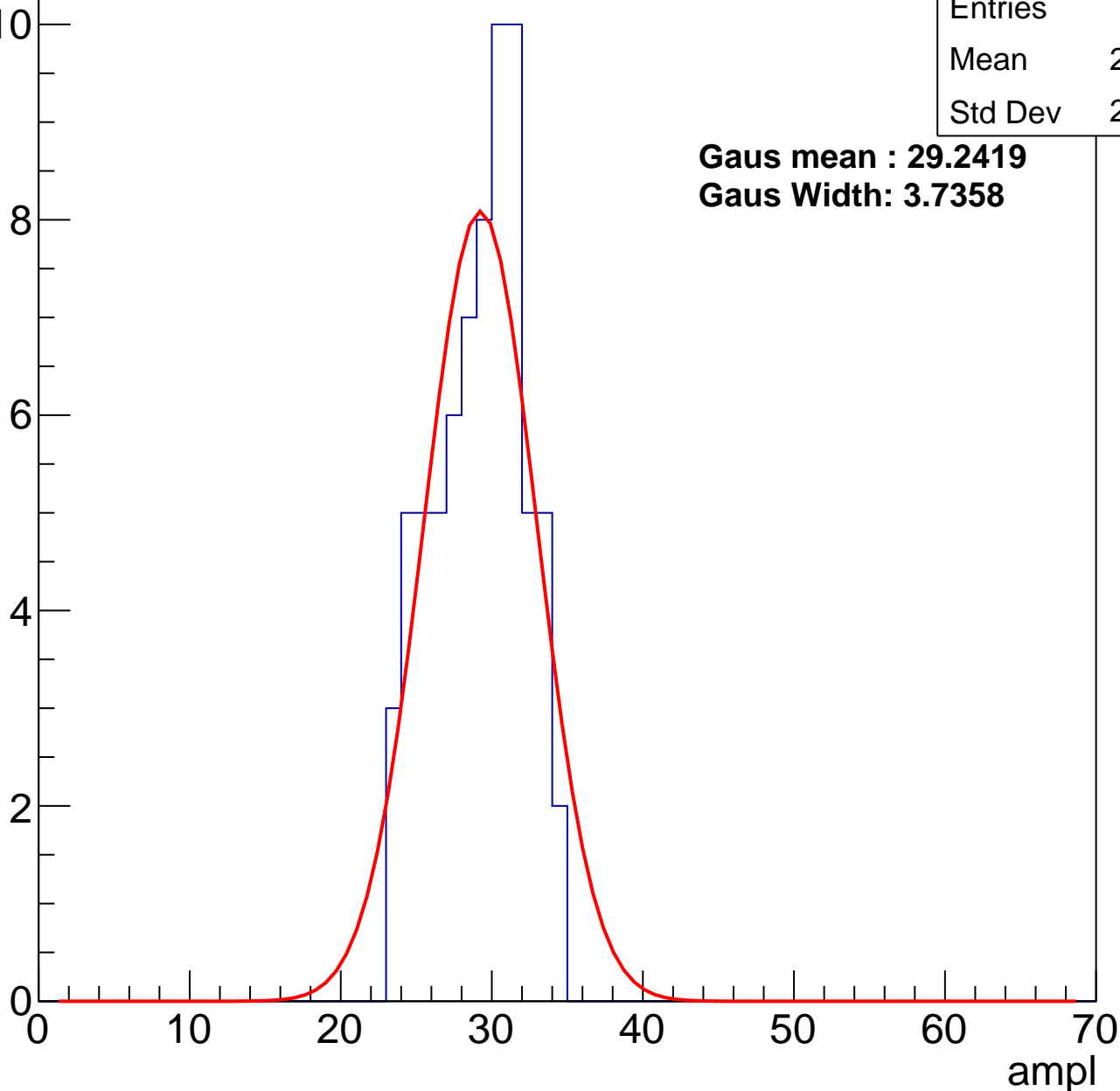
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.69
Std Dev	2.924

**Gaus mean : 29.2419**

**Gaus Width: 3.7358**



# B1L102S, U25-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	63
Mean	36.24
Std Dev	2.893

**Gaus mean : 36.3156**

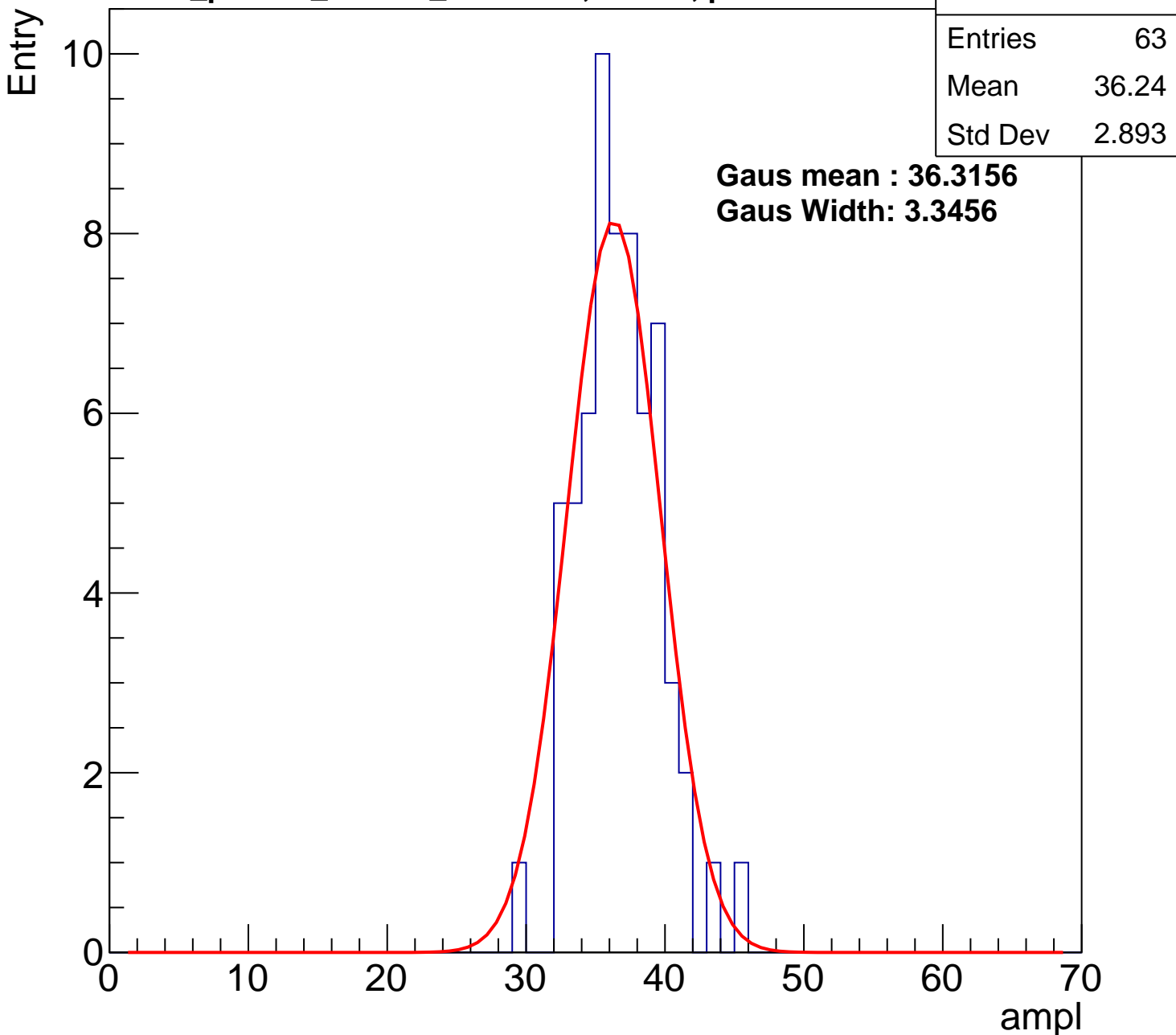
**Gaus Width: 3.3456**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U25-ch35, adc2

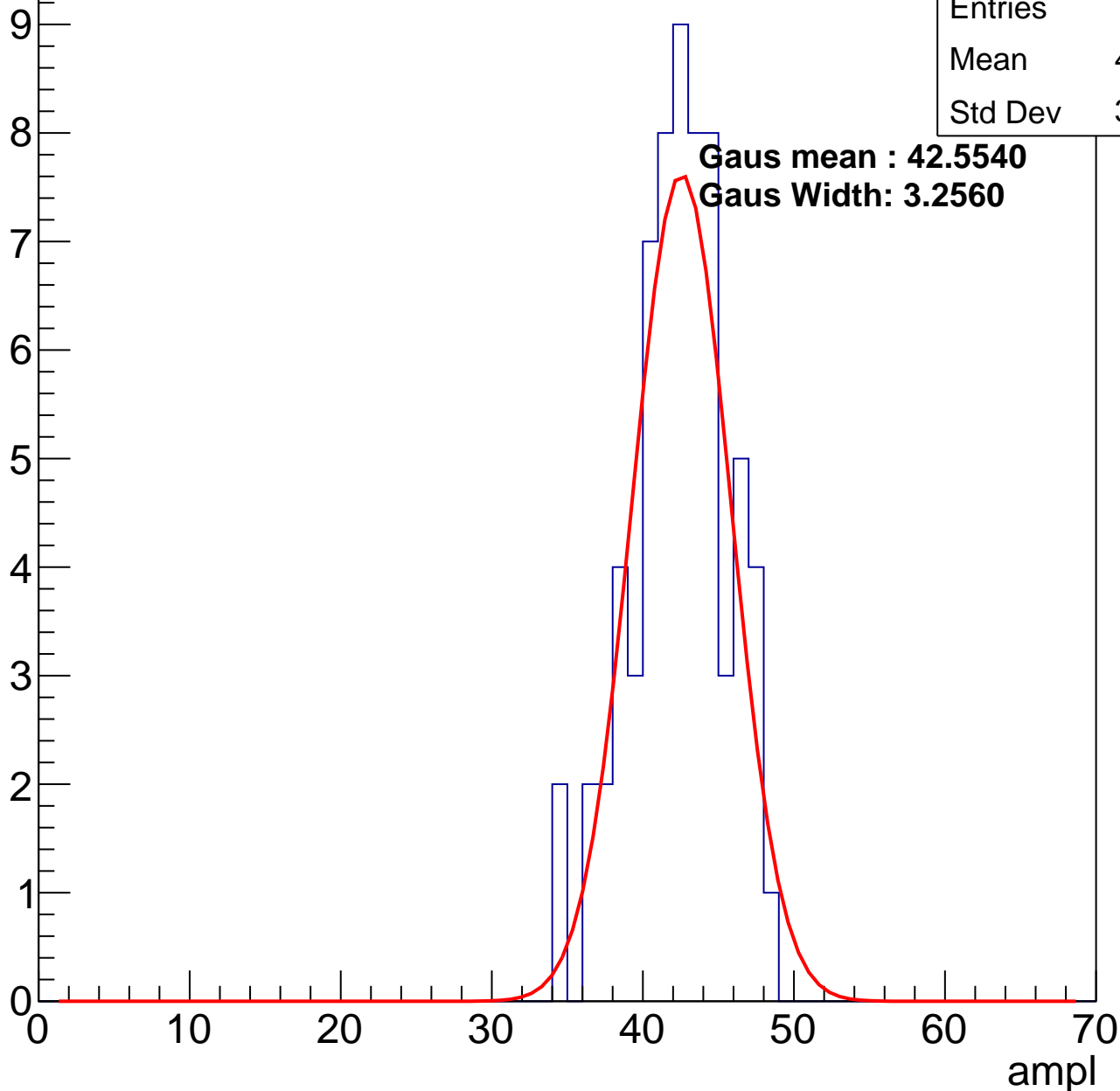
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.91
Std Dev	3.161

**Gaus mean : 42.5540**

**Gaus Width: 3.2560**

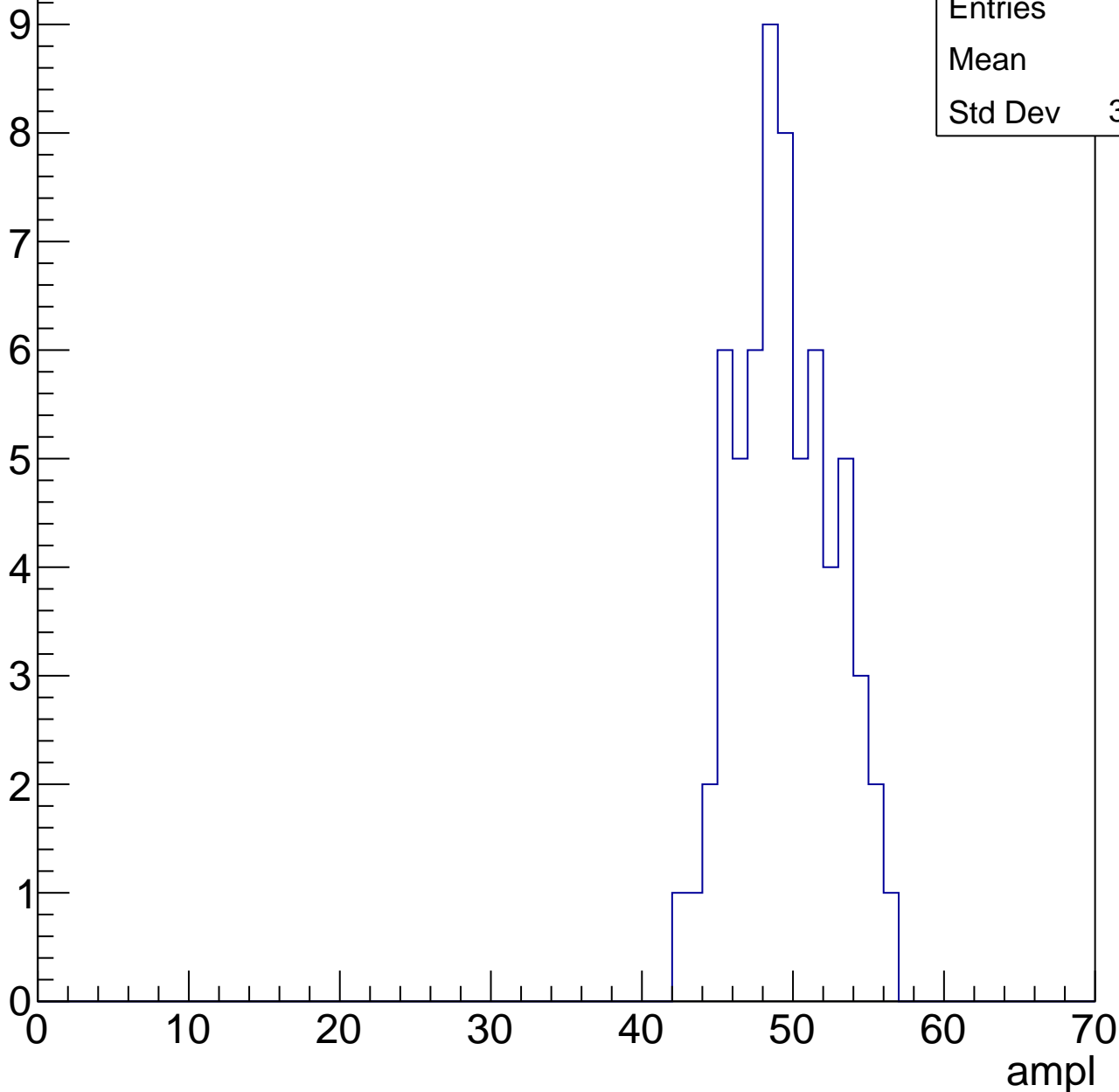


# B1L102S, U25-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

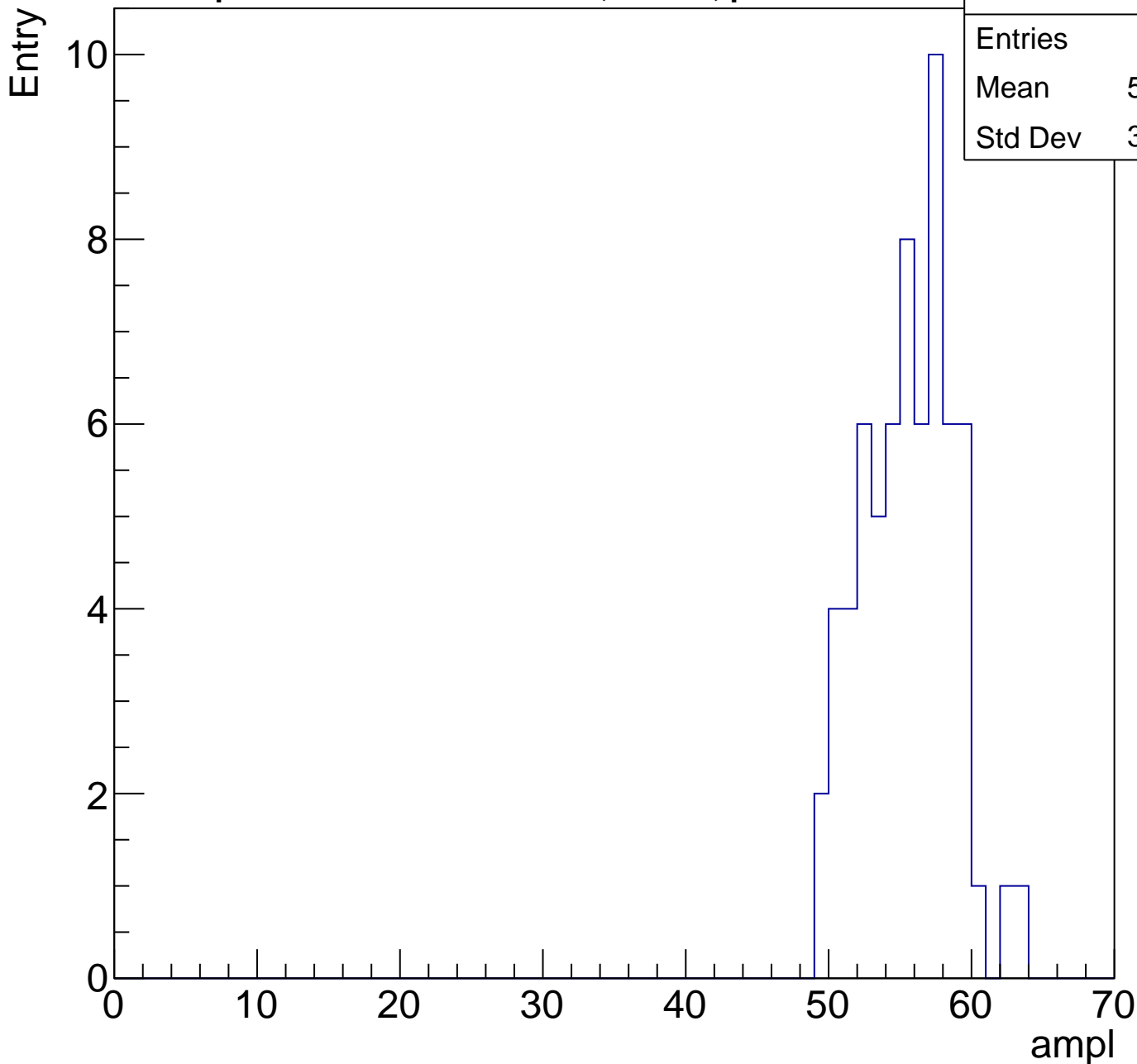
Entries	64
Mean	49
Std Dev	3.187



# B1L102S, U25-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	66
Mean	55.09
Std Dev	3.137

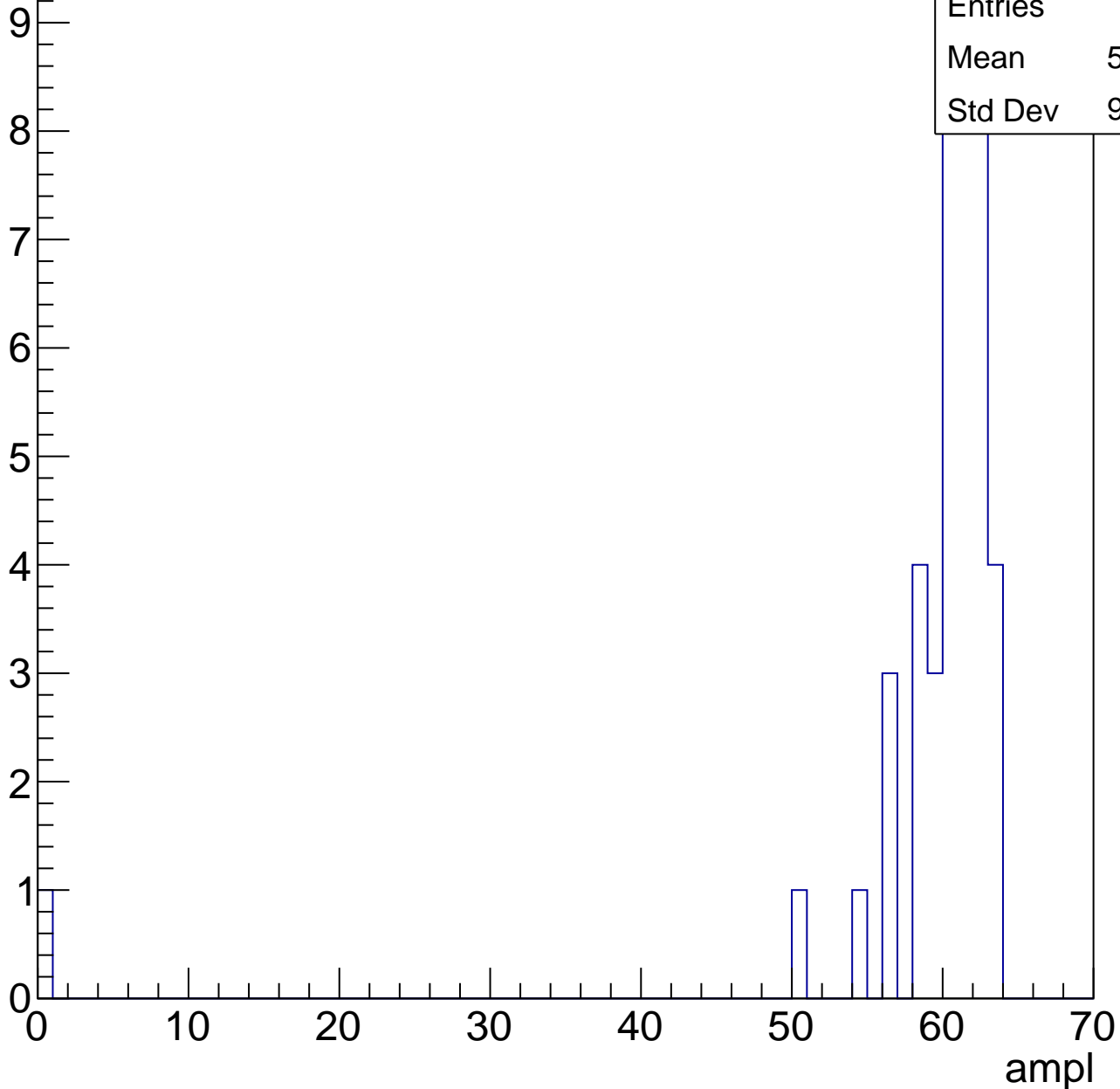


# B1L102S, U25-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.64
Std Dev	9.296

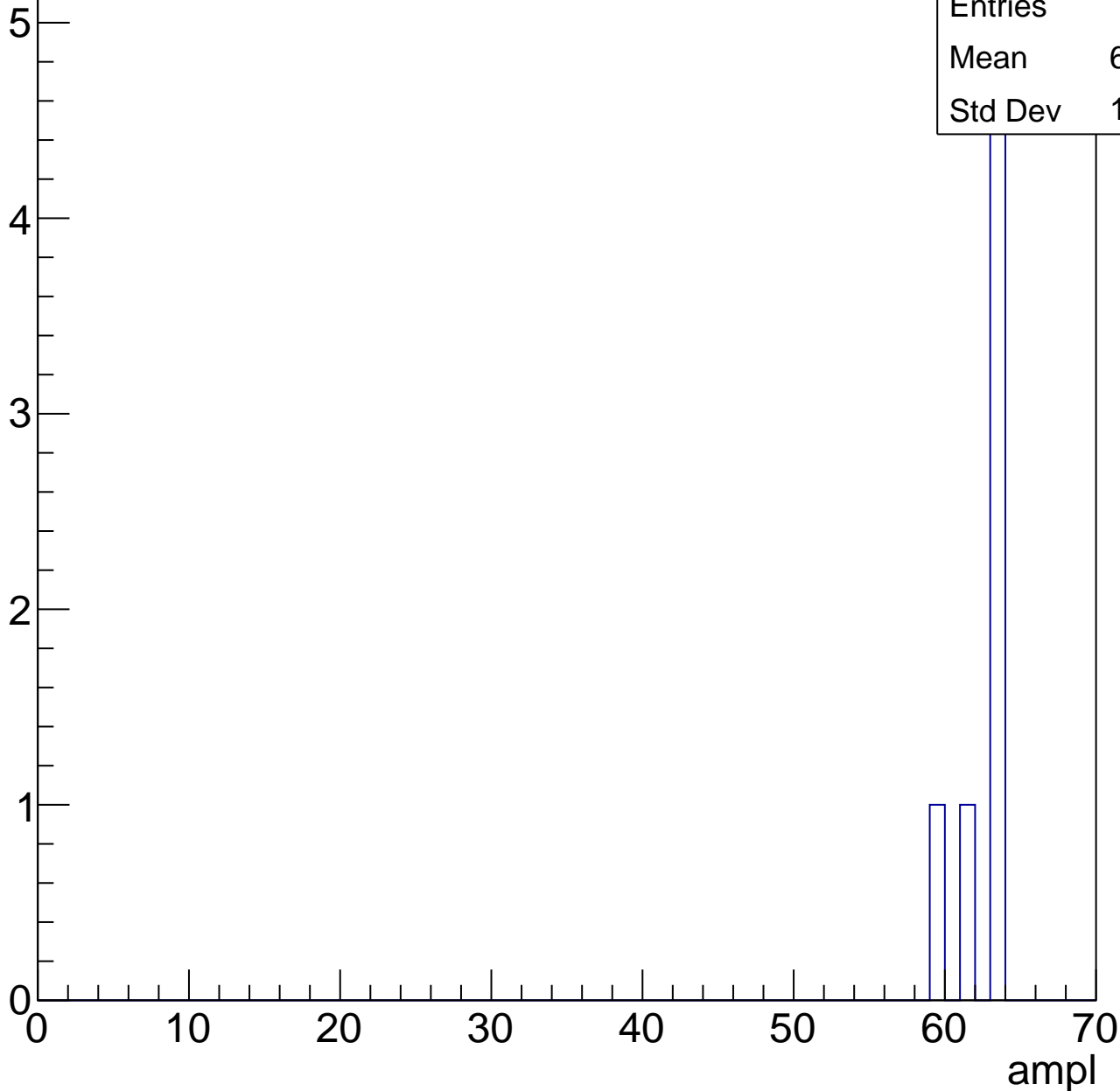


# B1L102S, U25-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.14
Std Dev	1.457





# B1L102S, U25-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch36, adc0

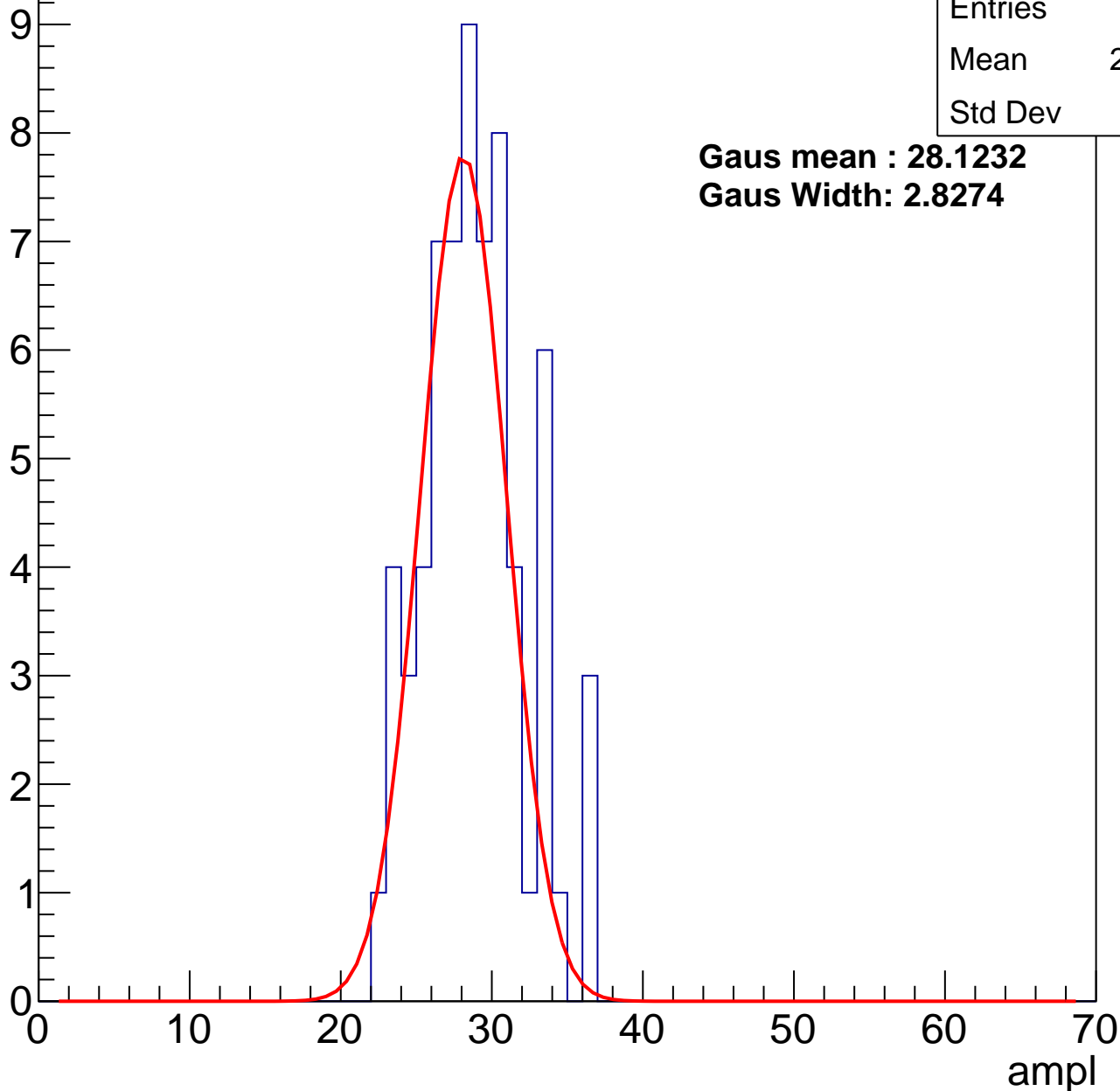
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.43
Std Dev	3.31

**Gaus mean : 28.1232**

**Gaus Width: 2.8274**



# B1L102S, U25-ch36, adc1

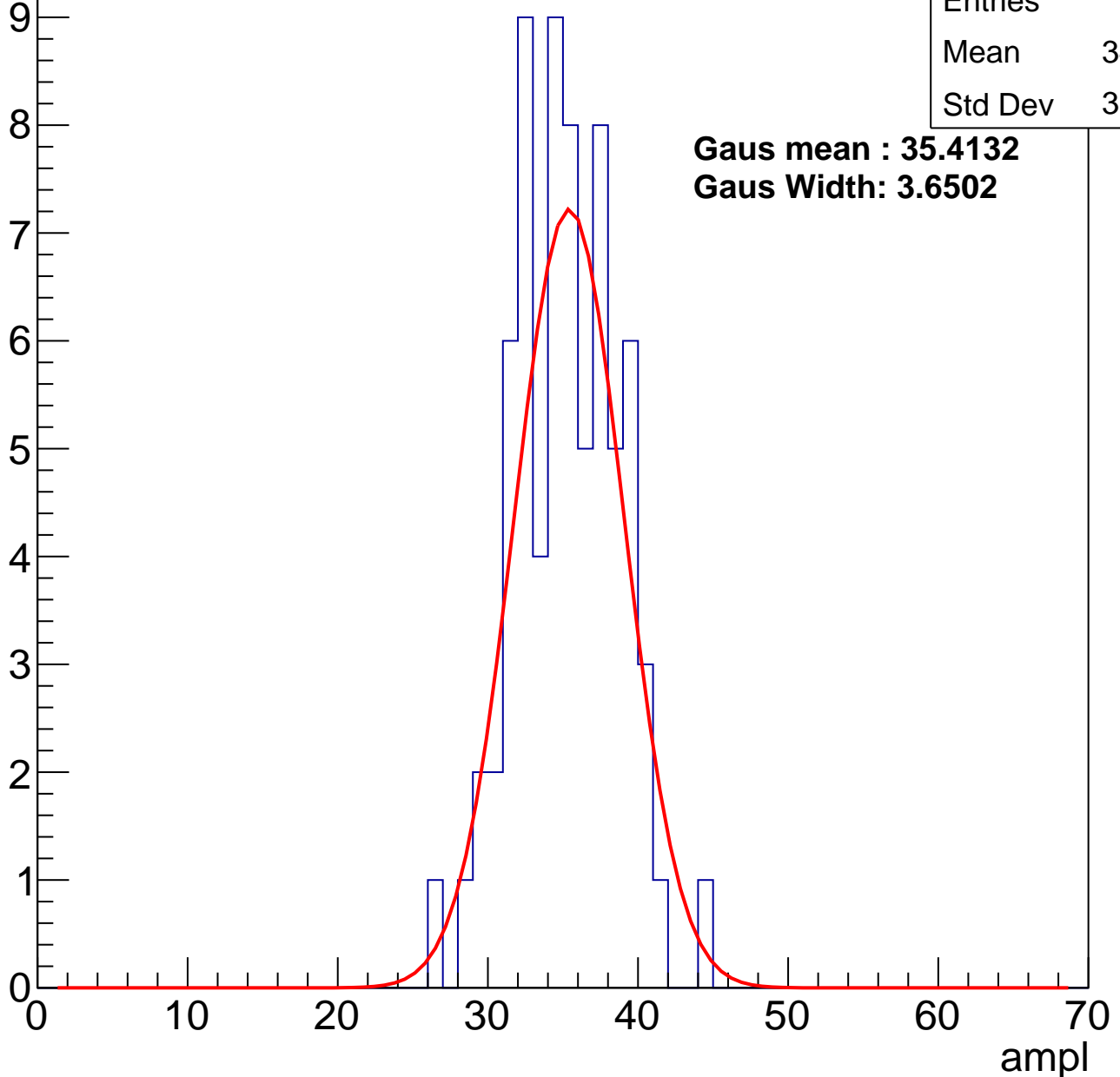
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.77
Std Dev	3.399

**Gaus mean : 35.4132**

**Gaus Width: 3.6502**



# B1L102S, U25-ch36, adc2

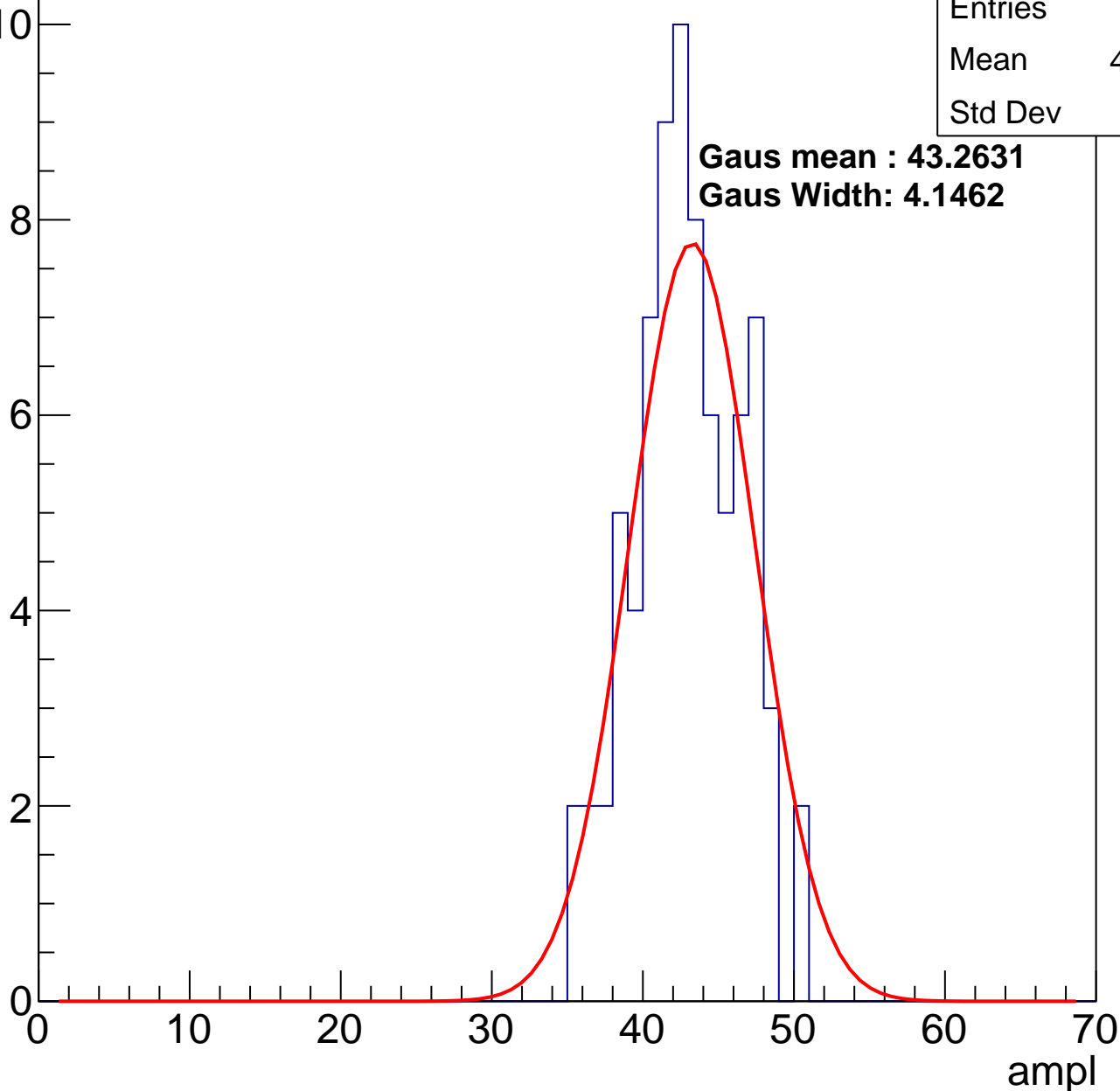
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.47
Std Dev	3.47

**Gaus mean : 43.2631**

**Gaus Width: 4.1462**

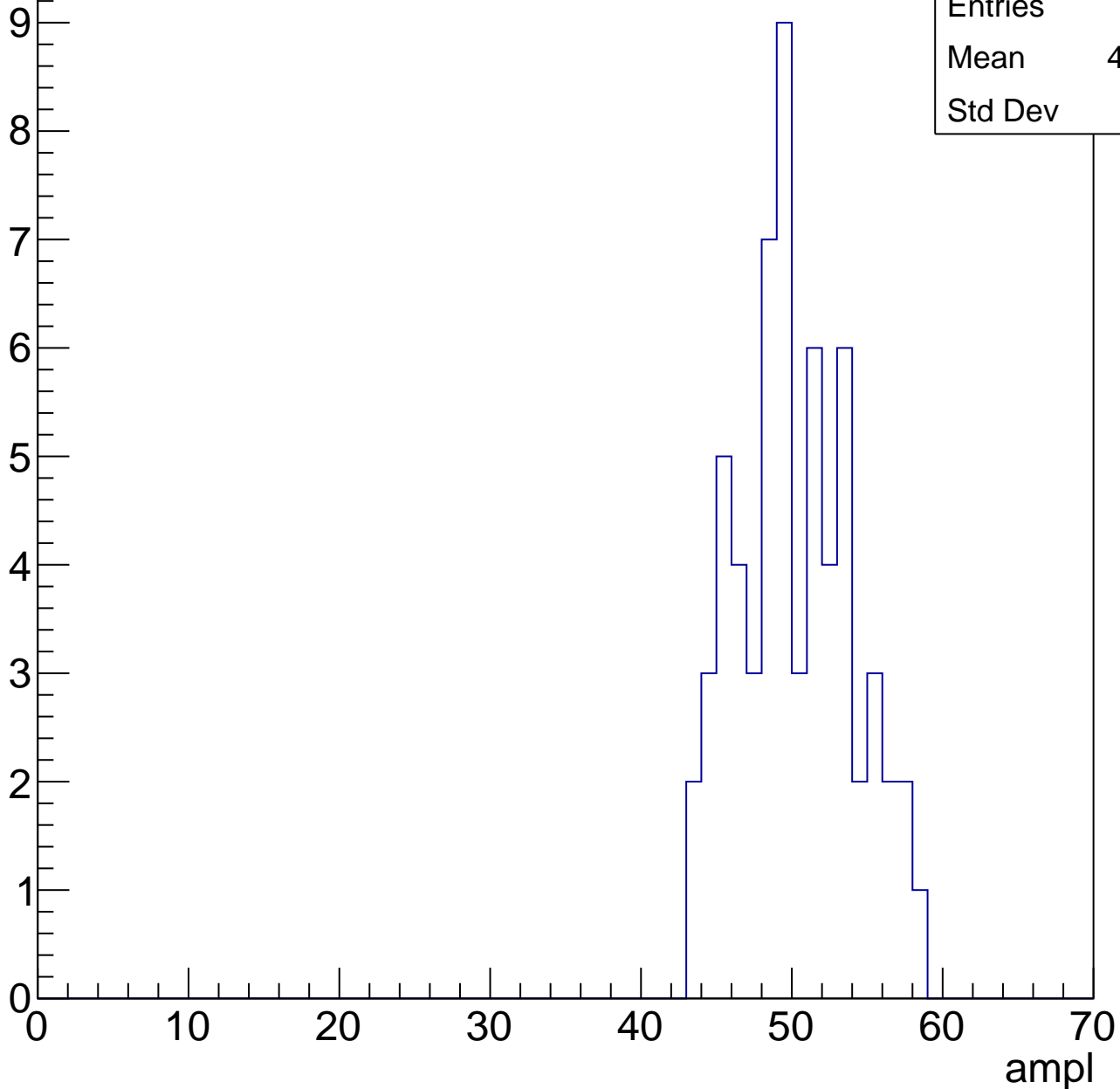


# B1L102S, U25-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.74
Std Dev	3.75

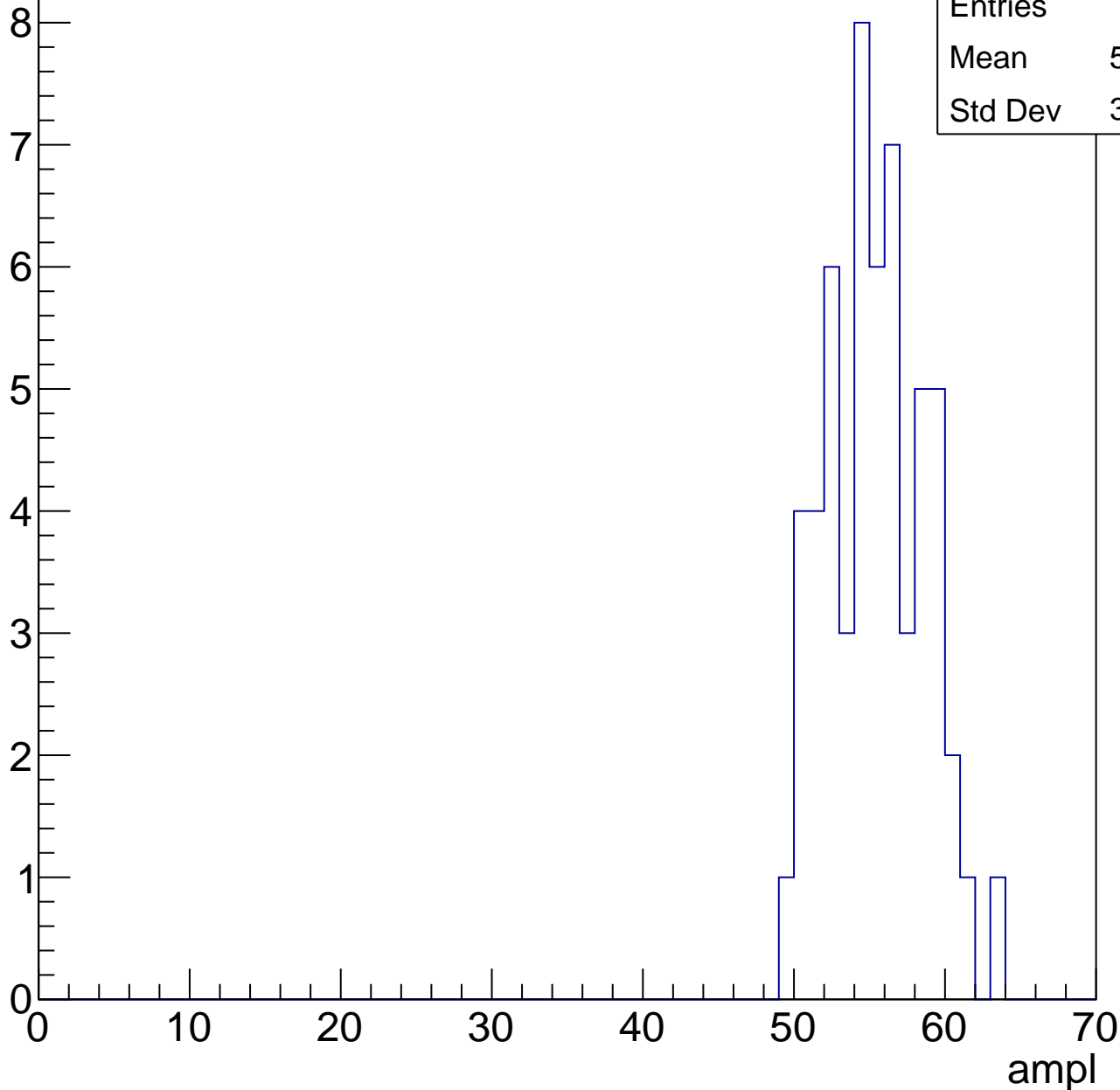


# B1L102S, U25-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.96
Std Dev	3.185

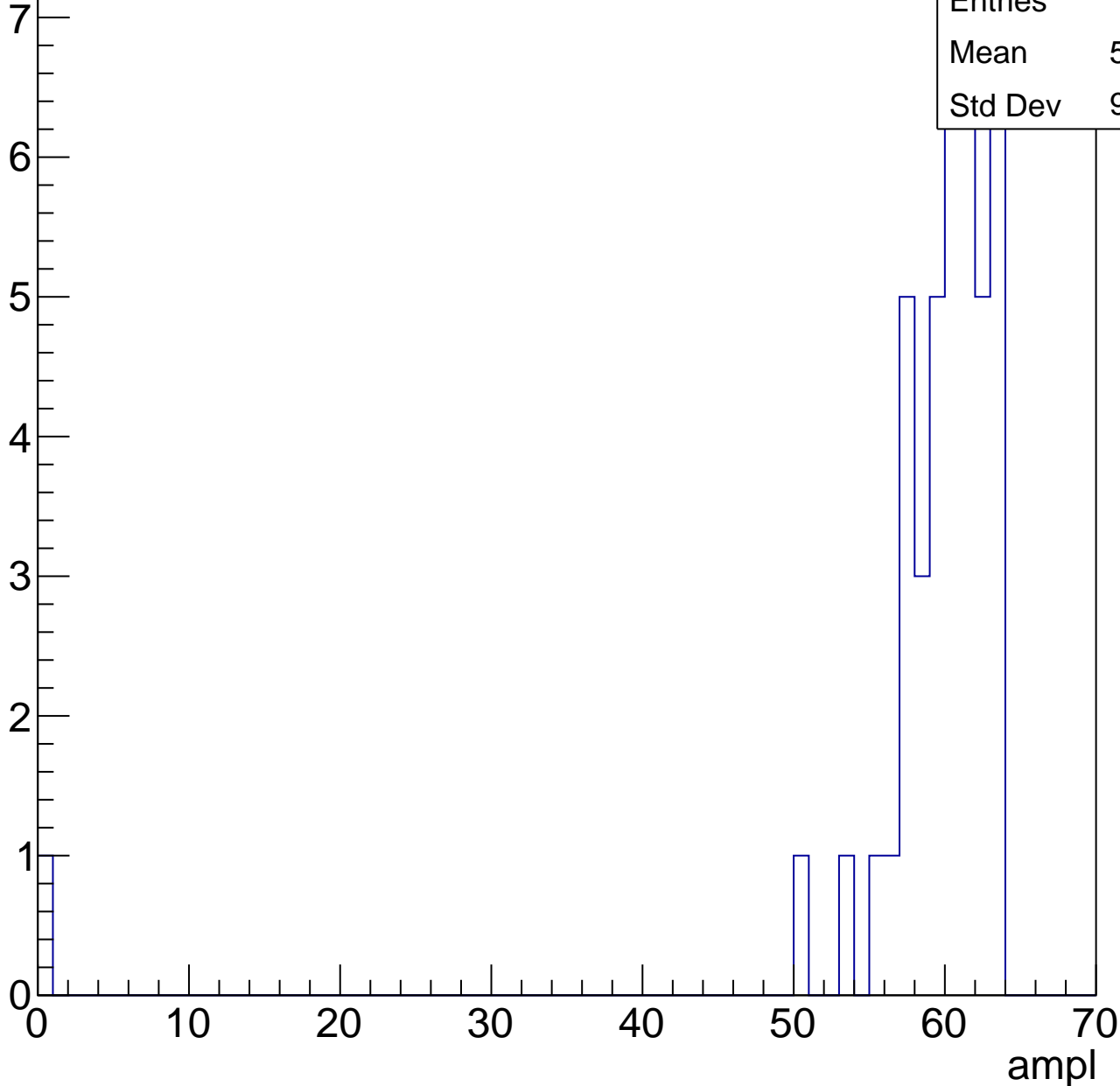


# B1L102S, U25-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.32
Std Dev	9.315

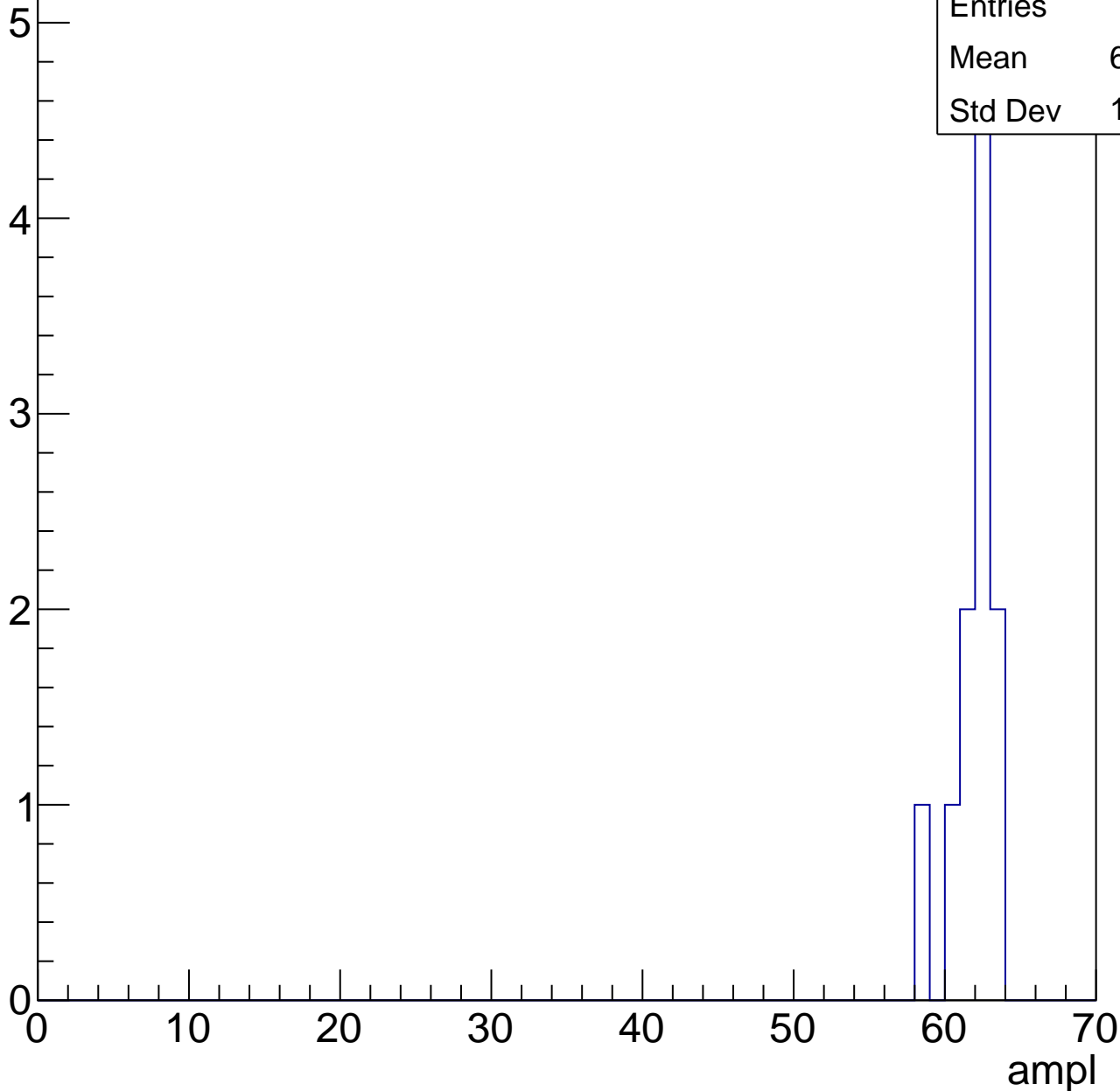


# B1L102S, U25-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.45
Std Dev	1.373





# B1L102S, U25-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch37, adc0

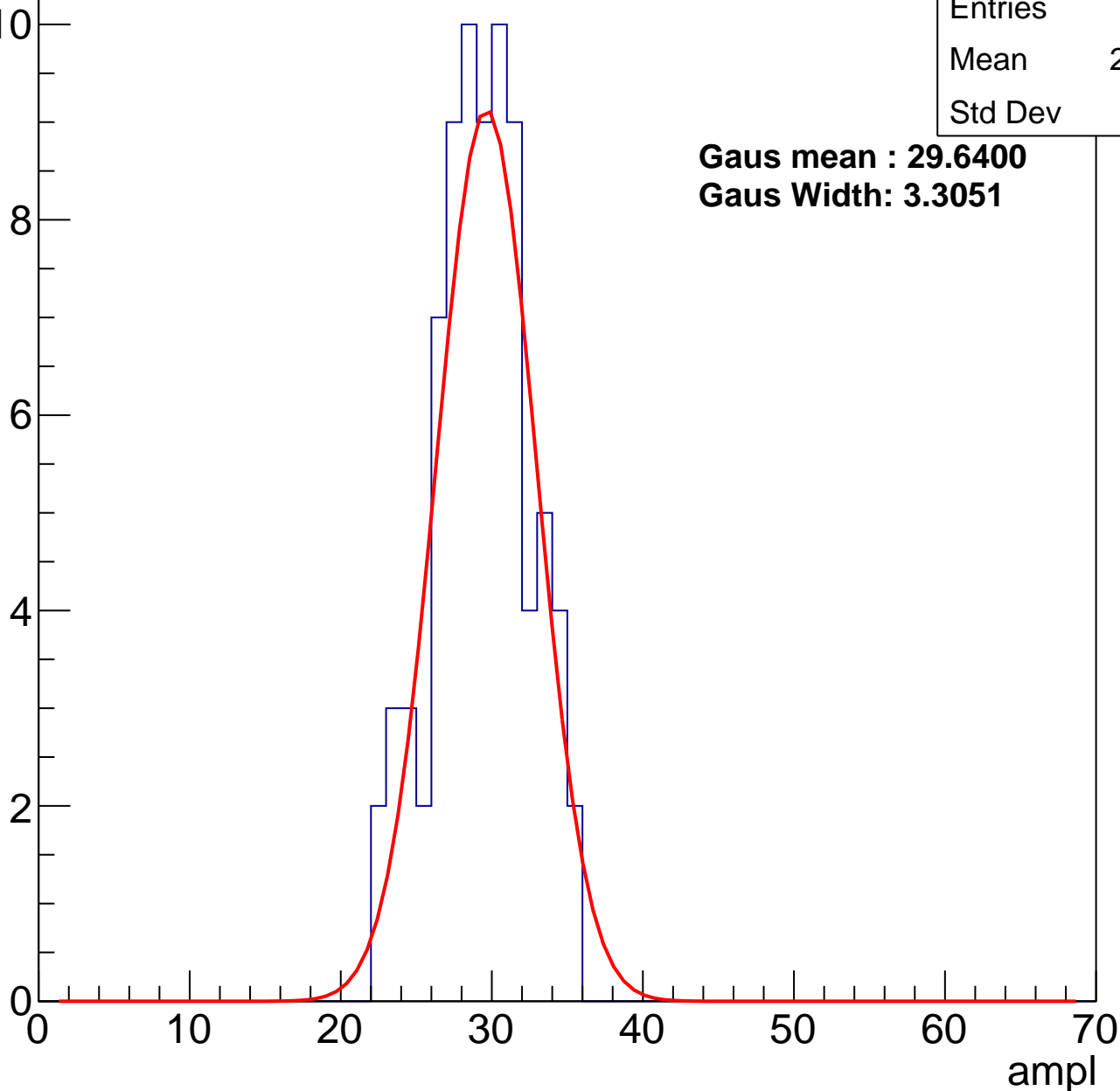
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.85
Std Dev	3.09

**Gaus mean : 29.6400**

**Gaus Width: 3.3051**



# B1L102S, U25-ch37, adc1

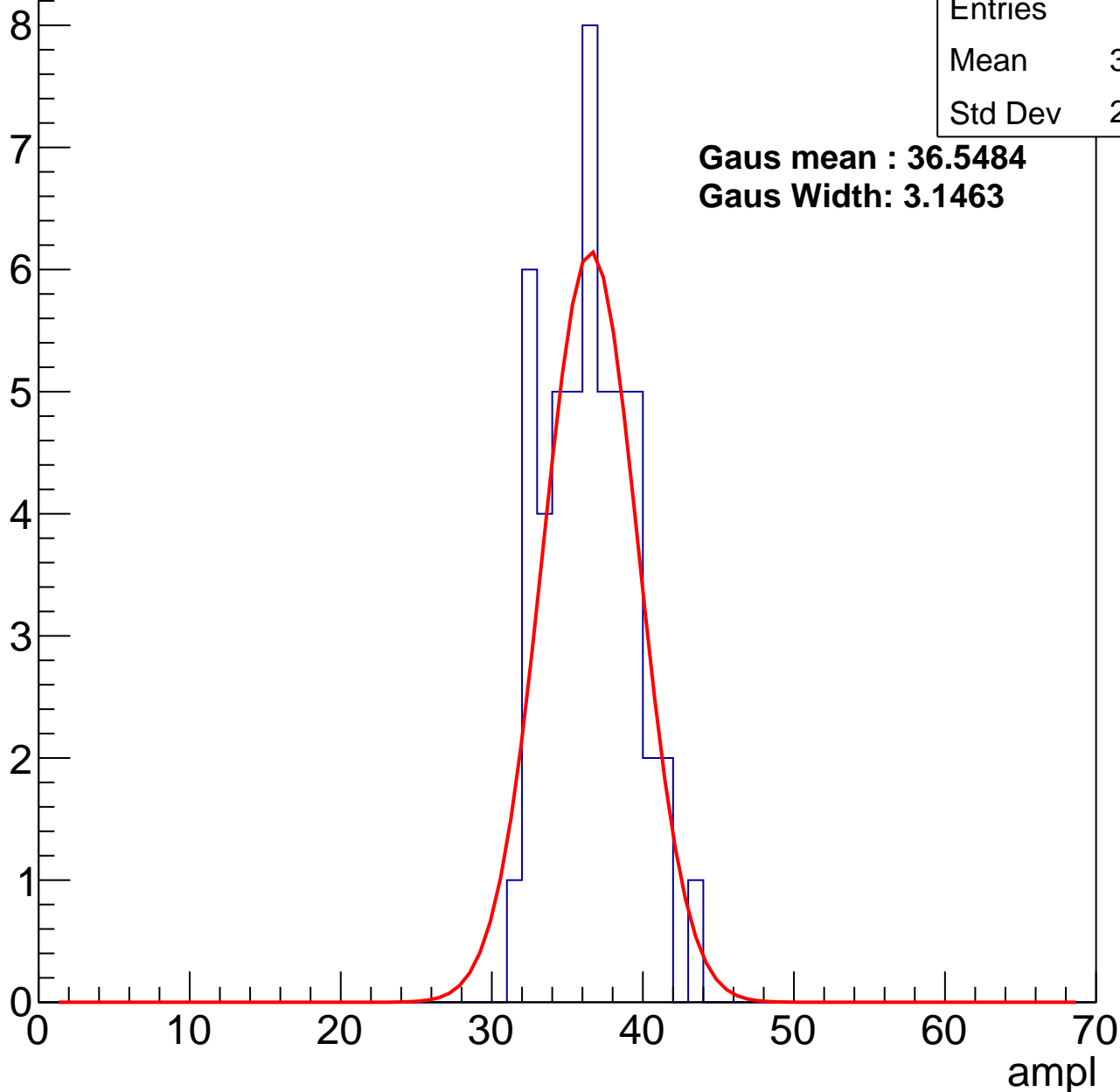
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	35.98
Std Dev	2.796

**Gaus mean : 36.5484**

**Gaus Width: 3.1463**



# B1L102S, U25-ch37, adc2

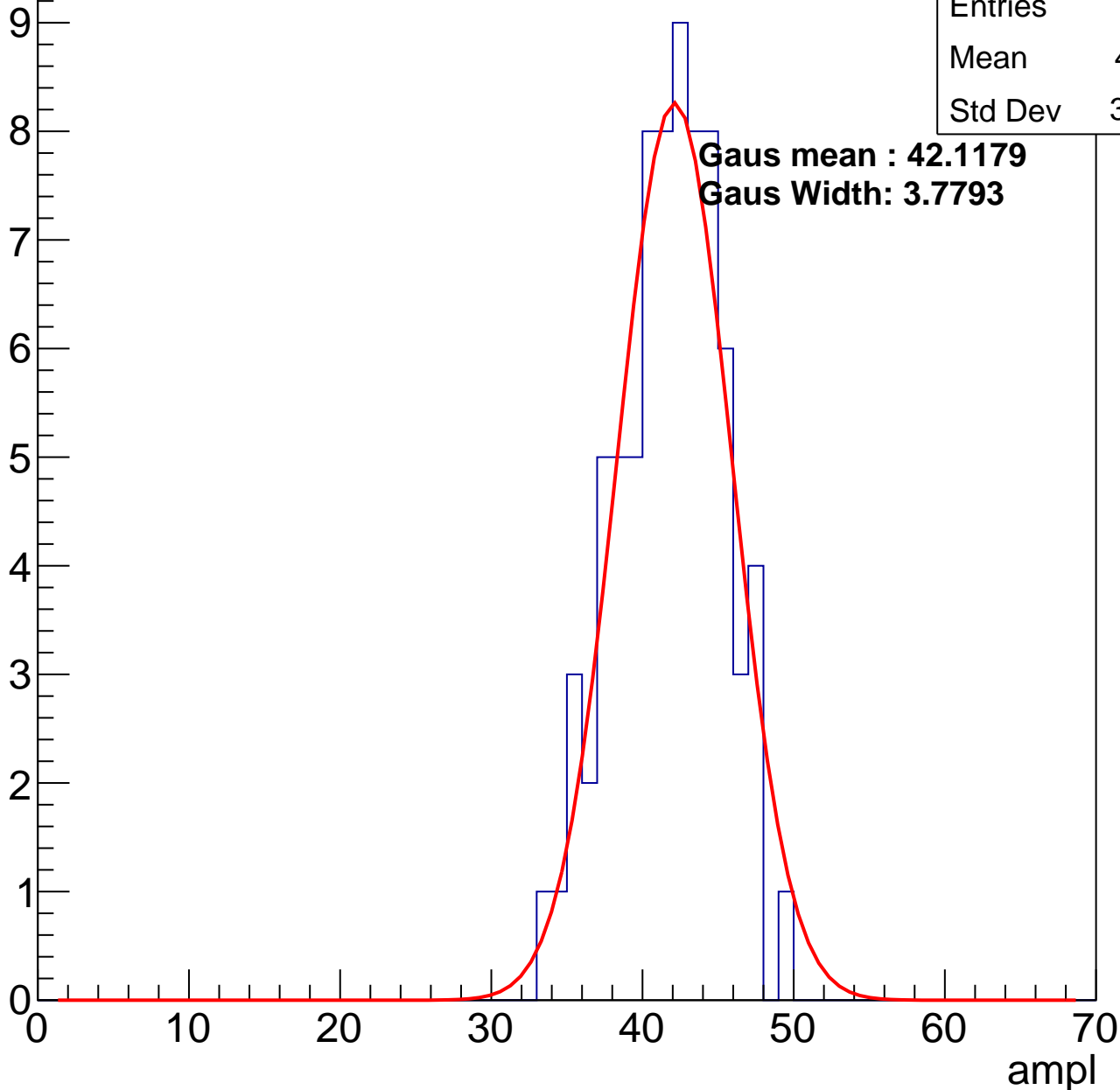
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.31
Std Dev	3.435

**Gaus mean : 42.1179**

**Gaus Width: 3.7793**

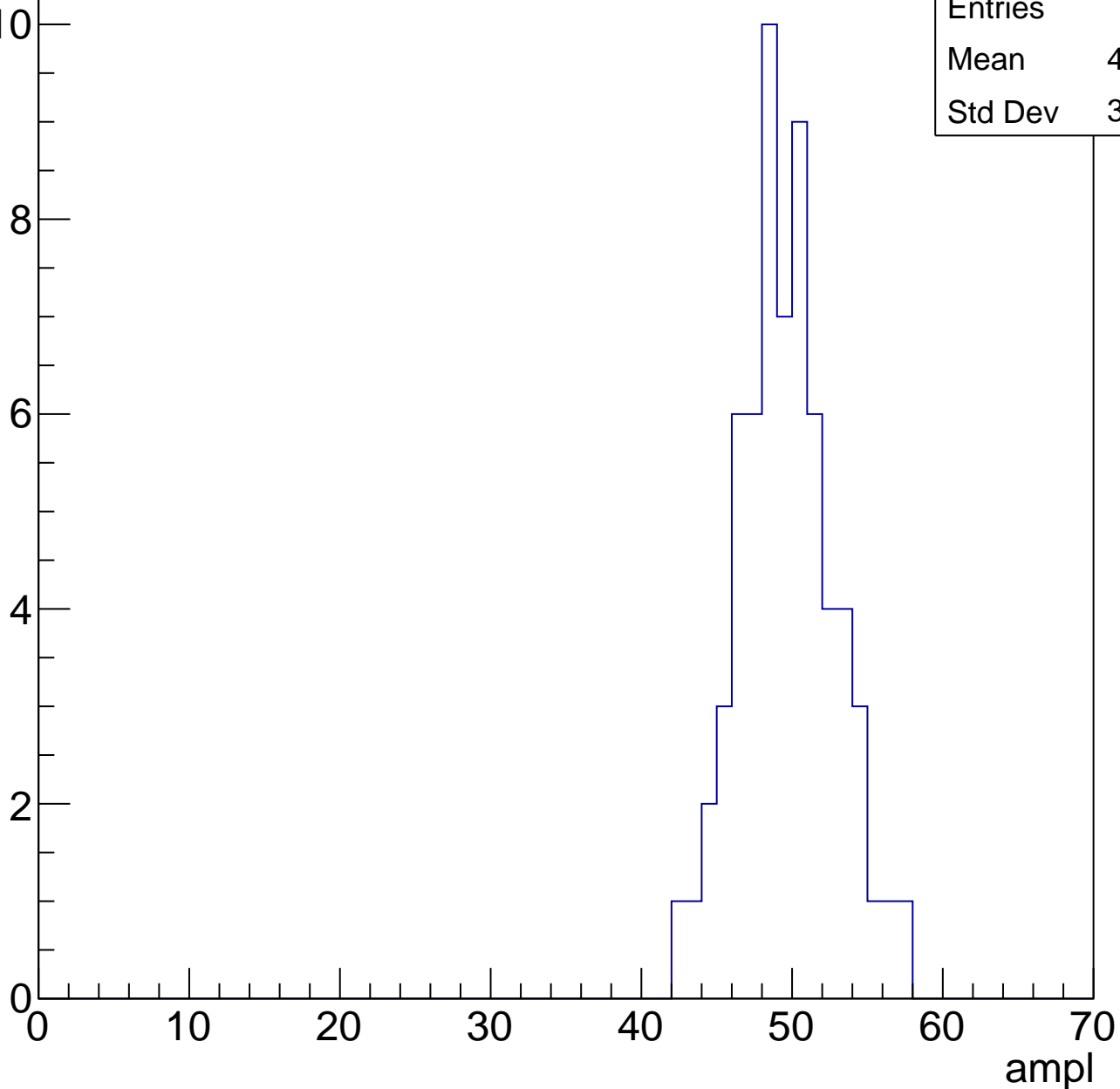


# B1L102S, U25-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.15
Std Dev	3.104

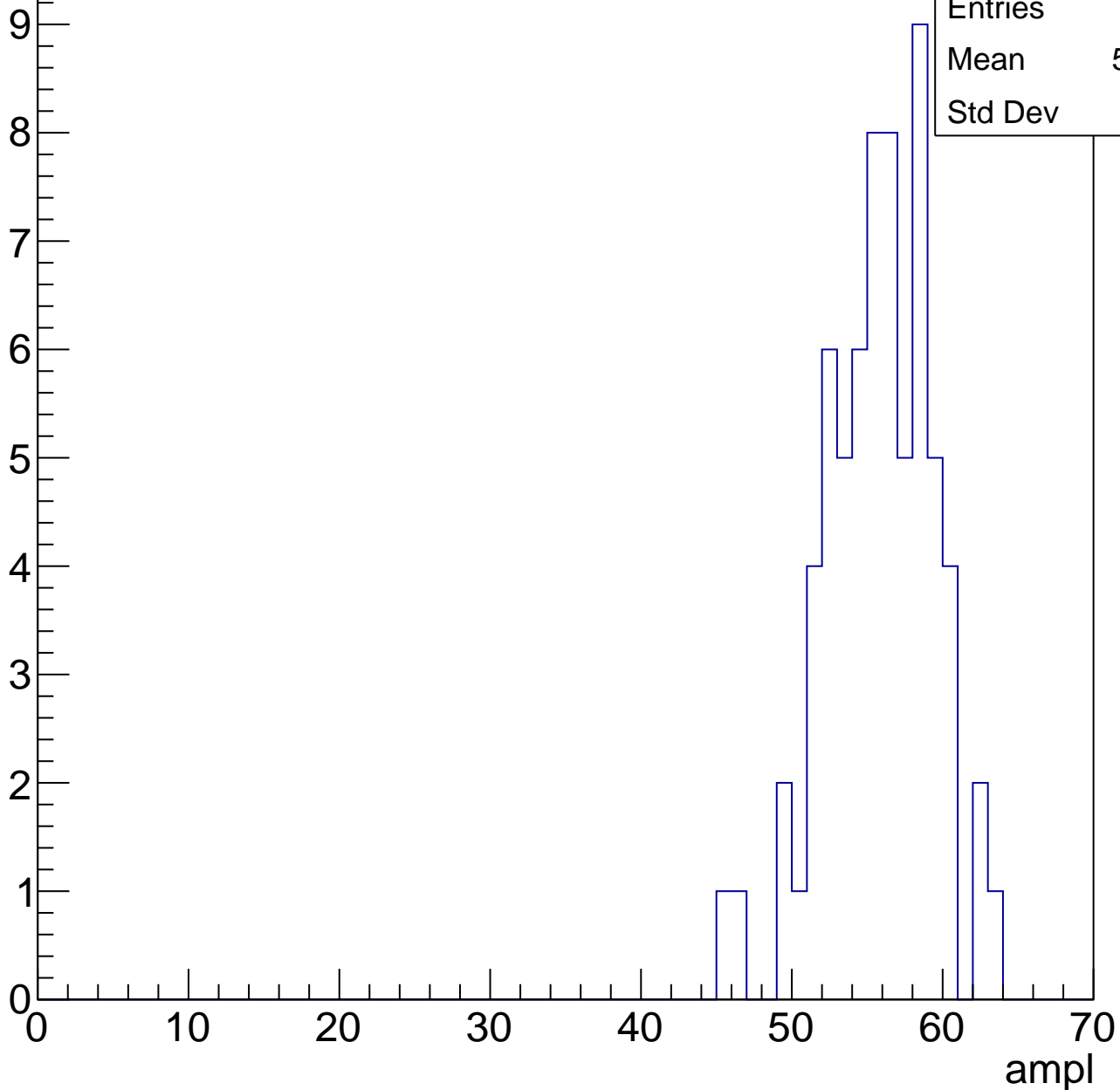


# B1L102S, U25-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.31
Std Dev	3.57



# B1L102S, U25-ch37, adc5

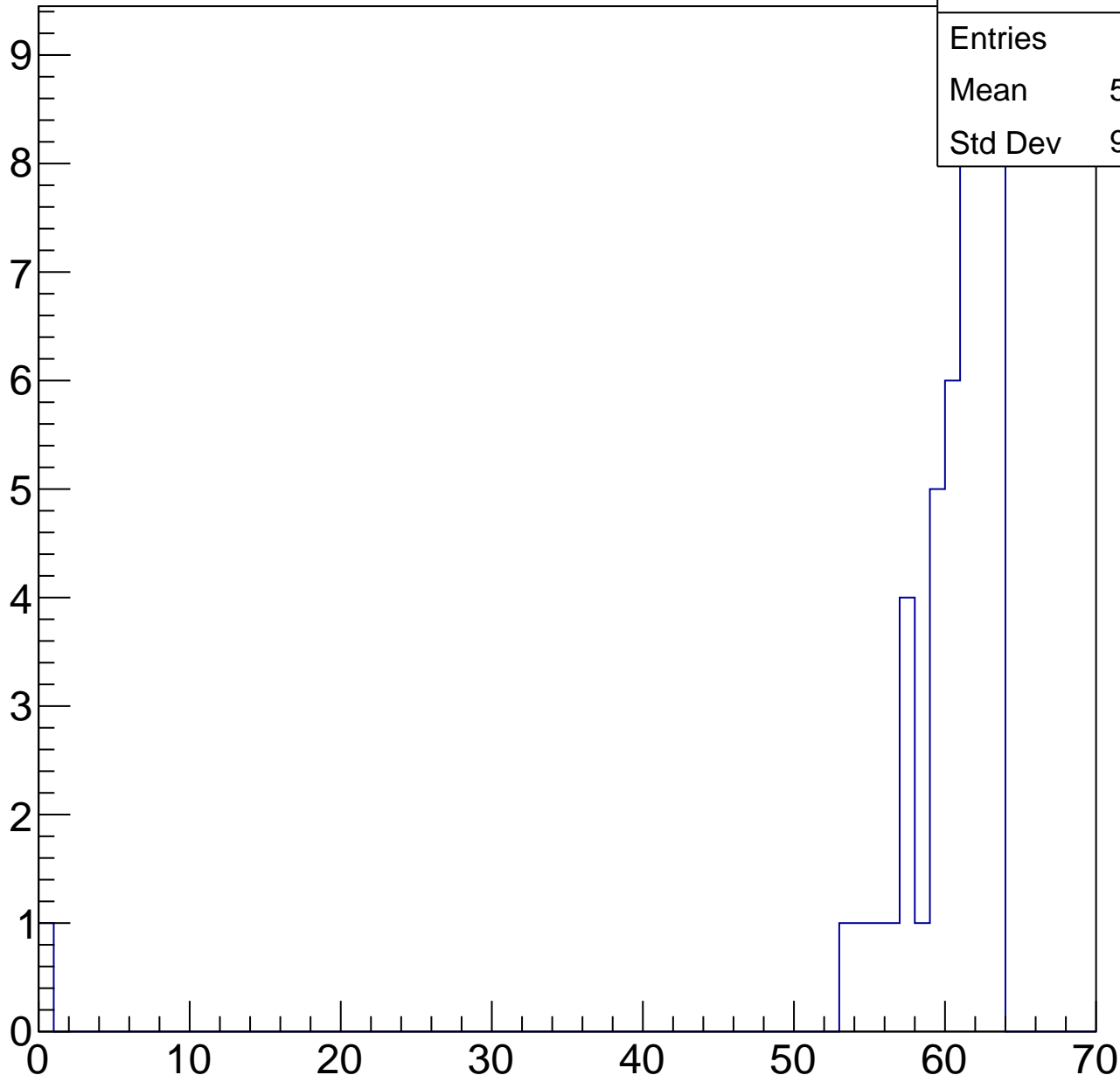
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.87
Std Dev	9.119

ampl



# B1L102S, U25-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch38, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	59
Mean	28.42
Std Dev	5.026

**Gaus mean : 29.8611**

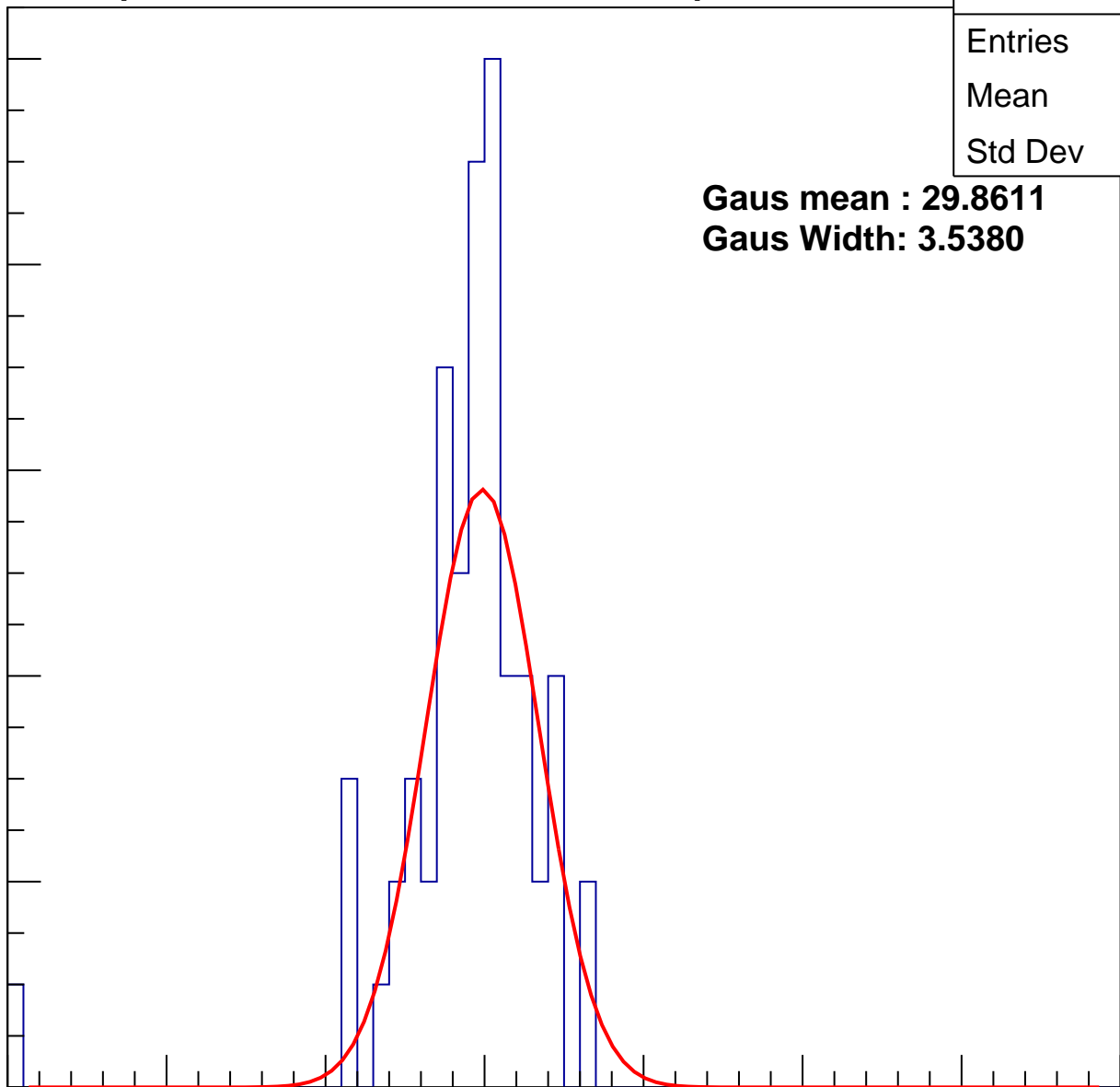
**Gaus Width: 3.5380**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch38, adc1

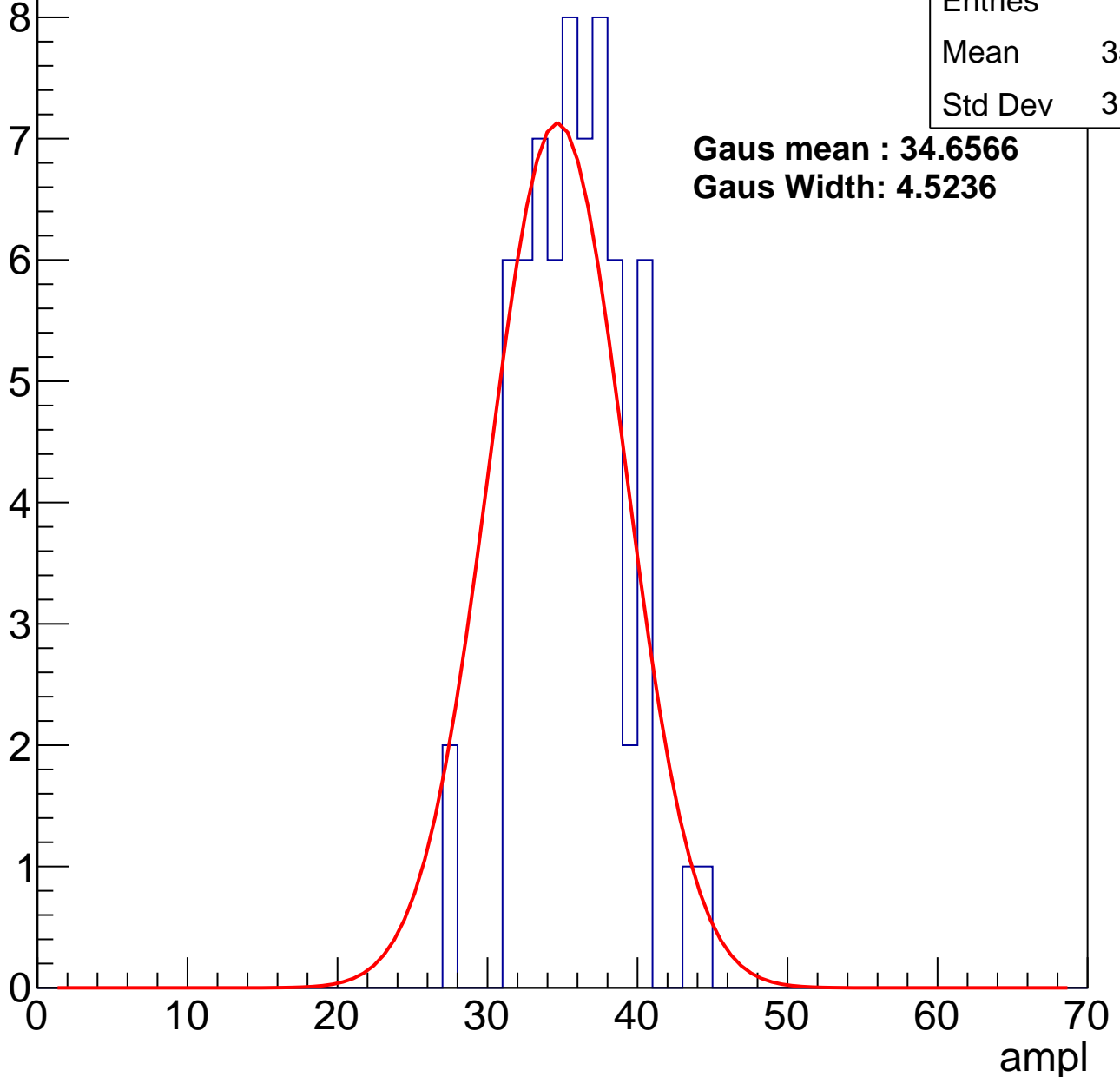
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.27
Std Dev	3.319

**Gaus mean : 34.6566**

**Gaus Width: 4.5236**



# B1L102S, U25-ch38, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.07
Std Dev	3.446

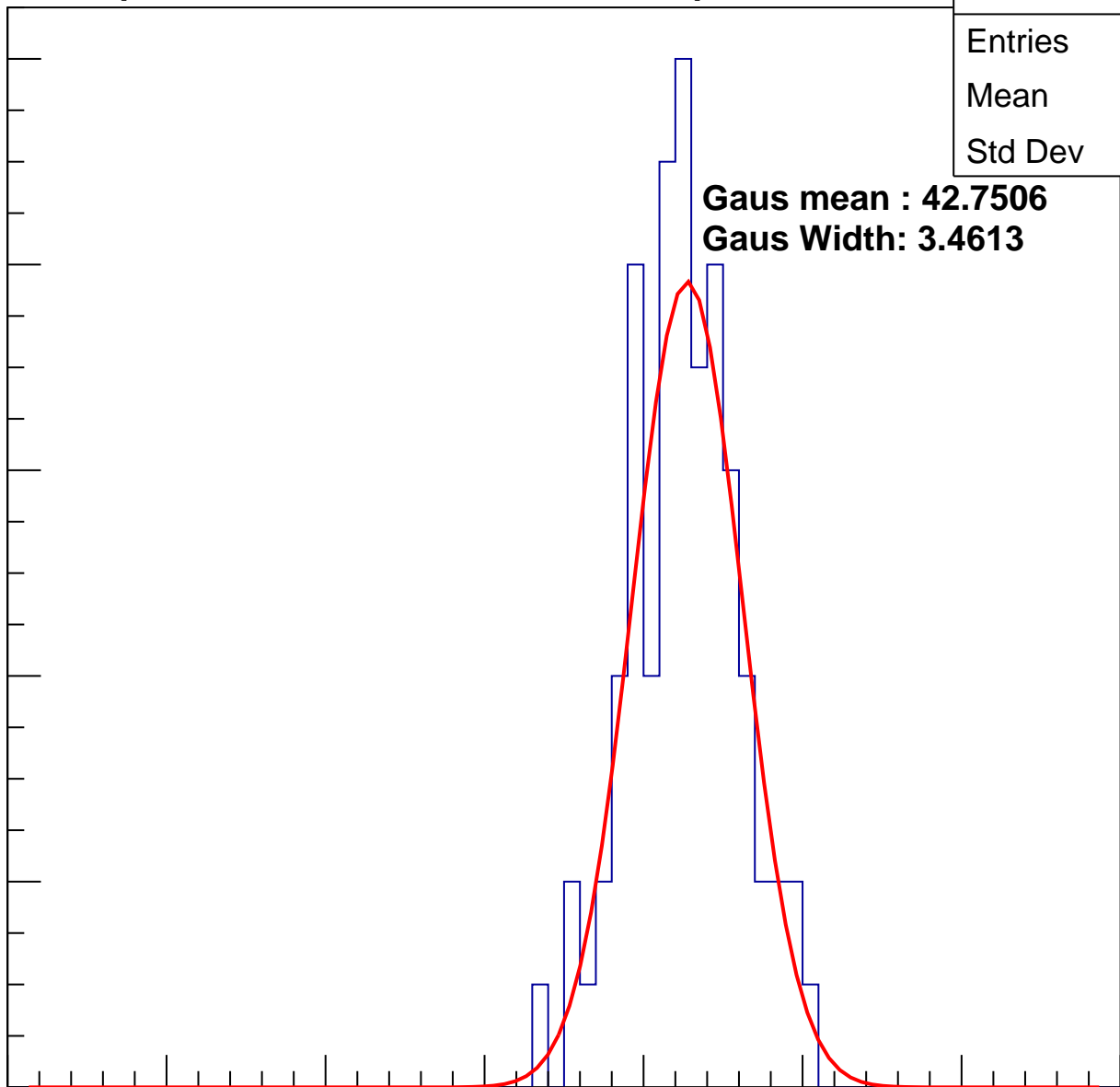
**Gaus mean : 42.7506**

**Gaus Width: 3.4613**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

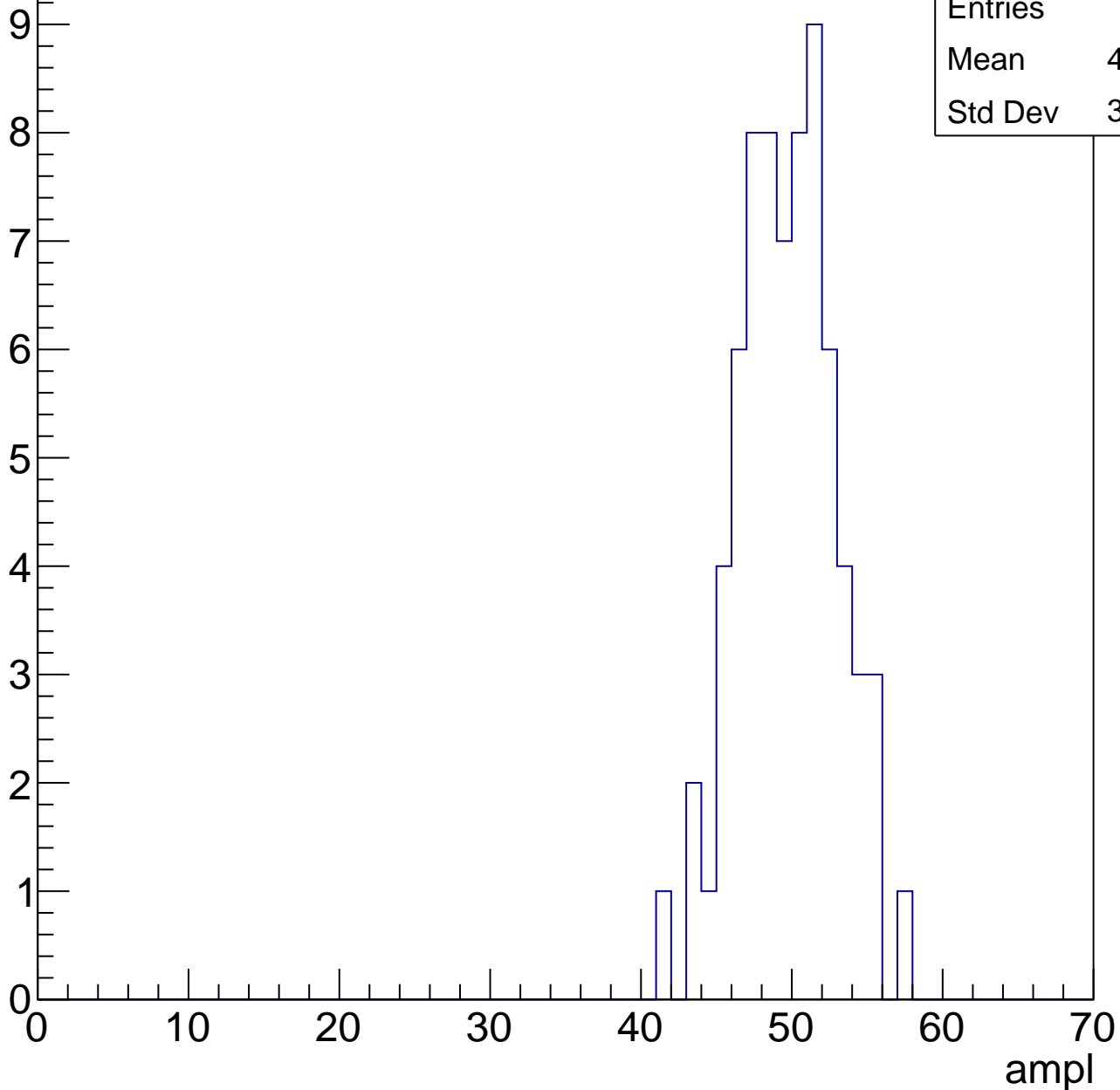


# B1L102S, U25-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.25
Std Dev	3.192

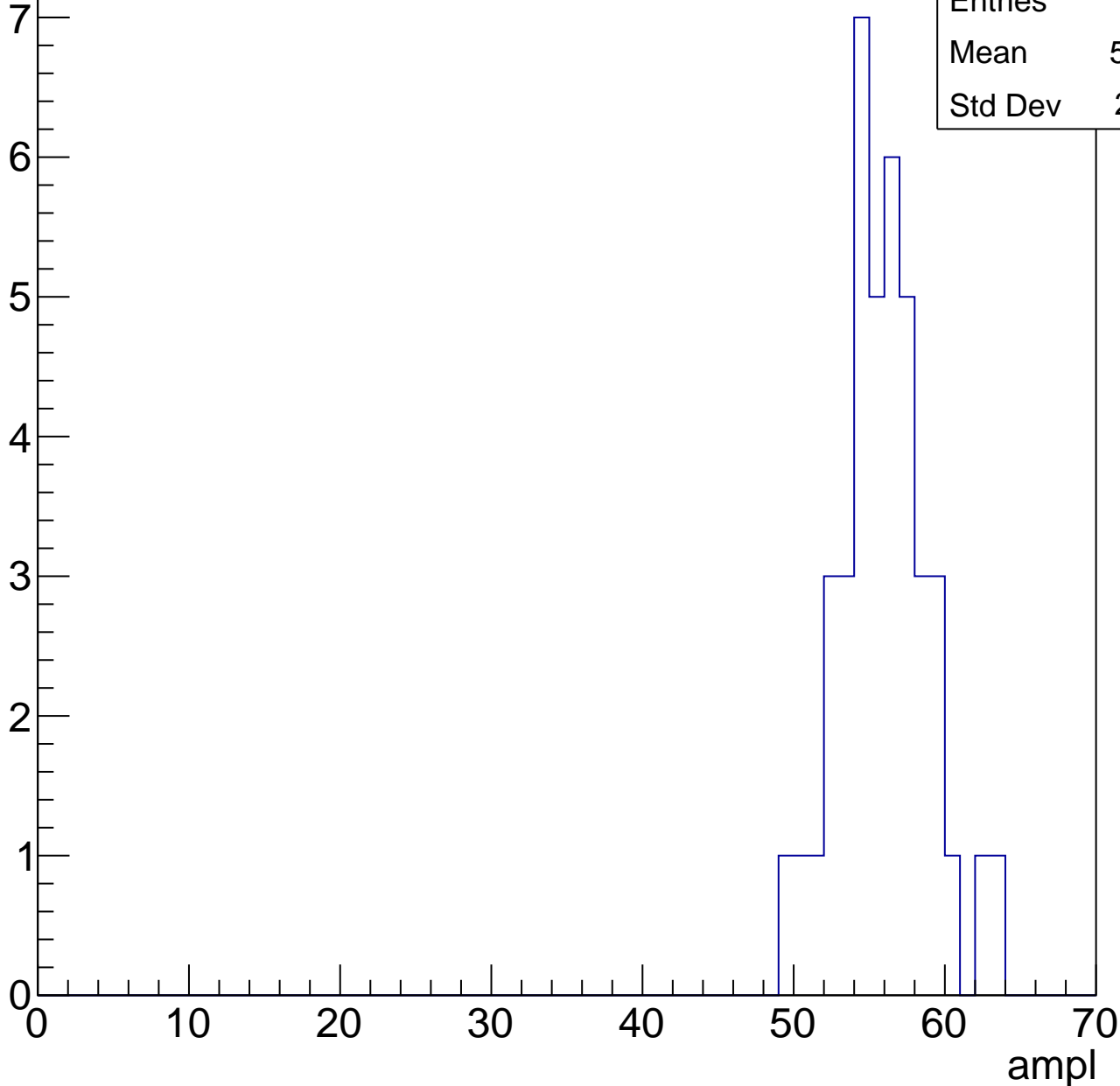


# B1L102S, U25-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	41
Mean	55.49
Std Dev	2.931

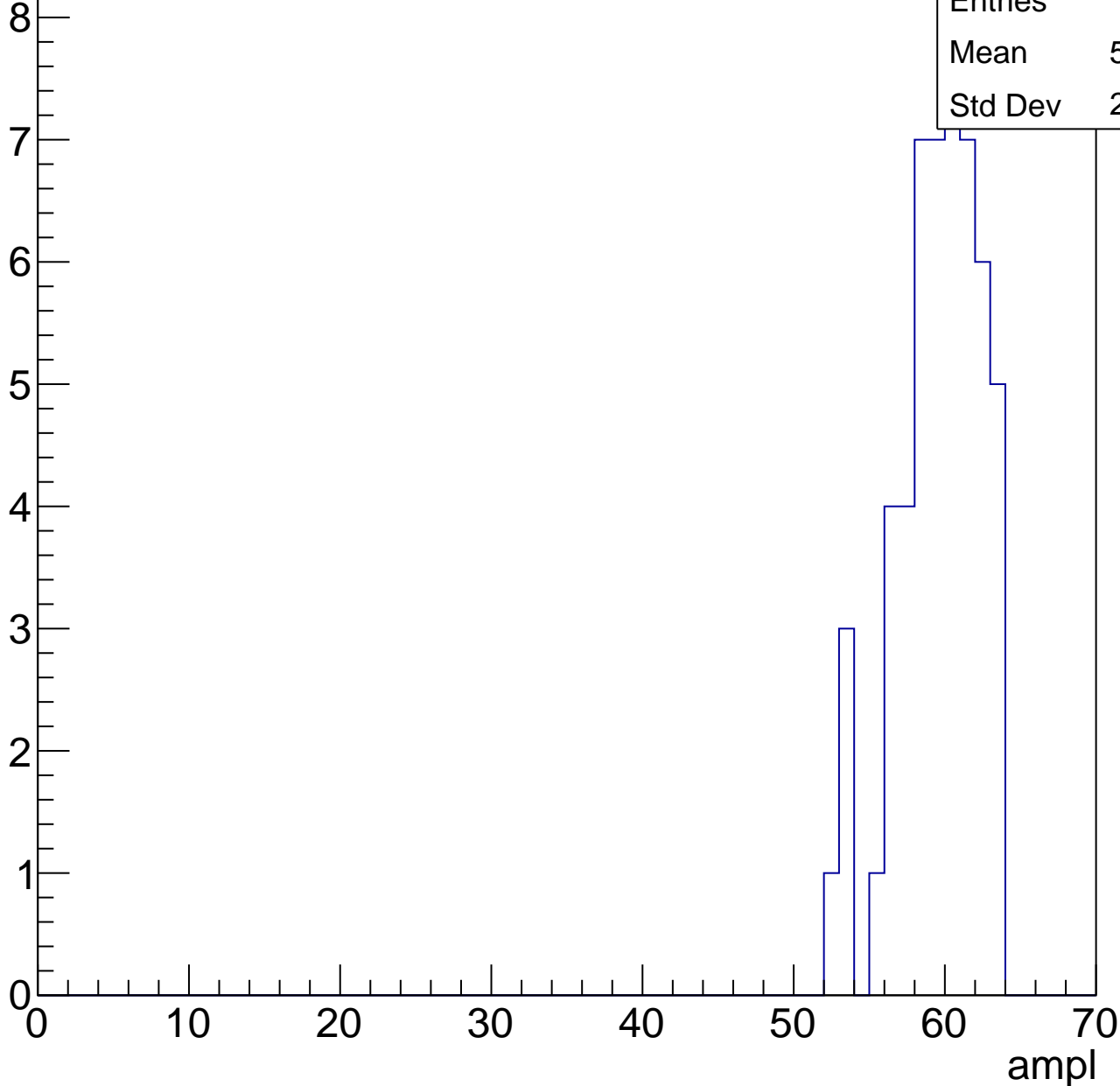


# B1L102S, U25-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

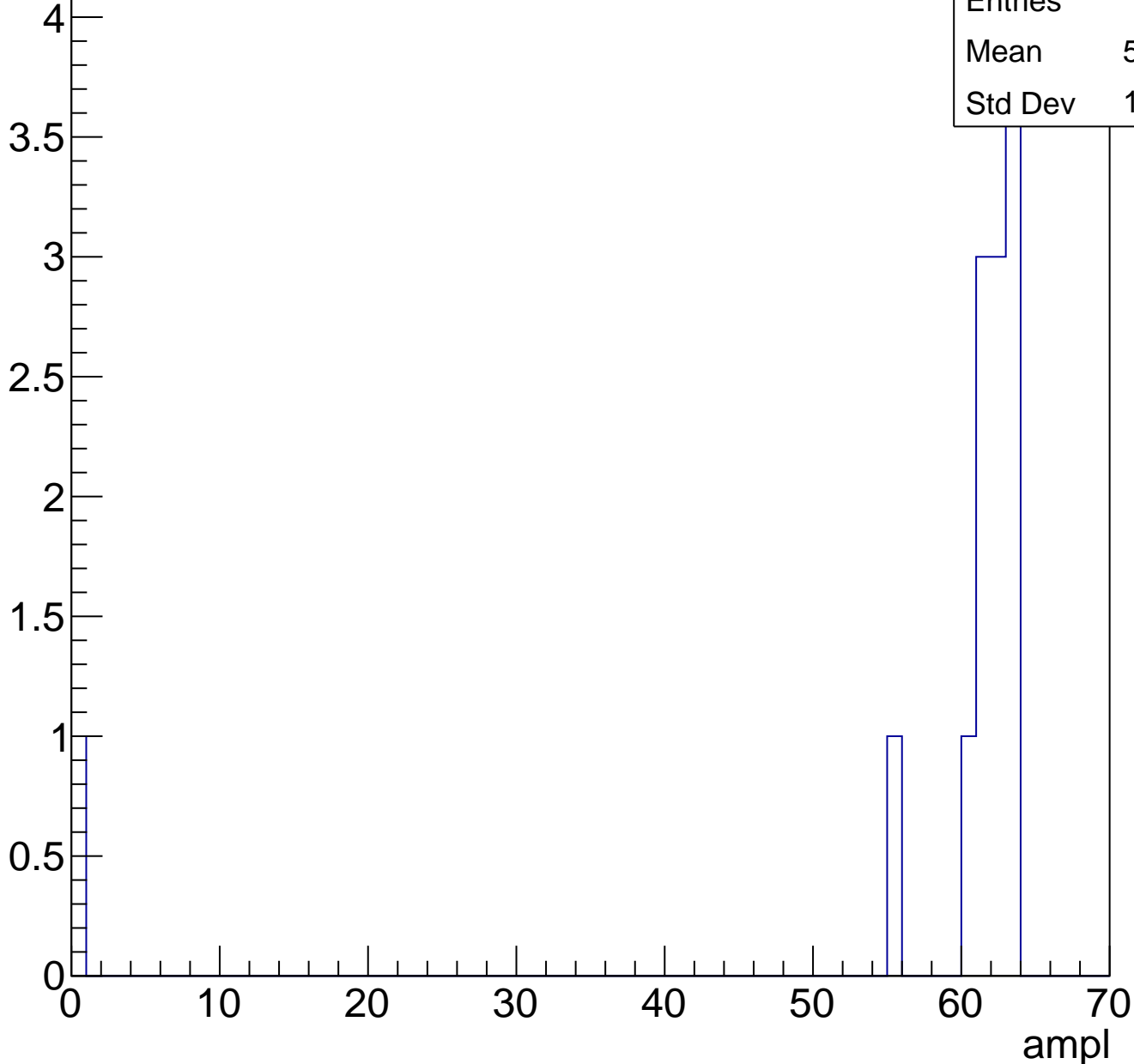
Entries	53
Mean	59.08
Std Dev	2.753



# B1L102S, U25-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	13
Mean	56.62
Std Dev	16.47



# B1L102S, U25-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch39, adc0

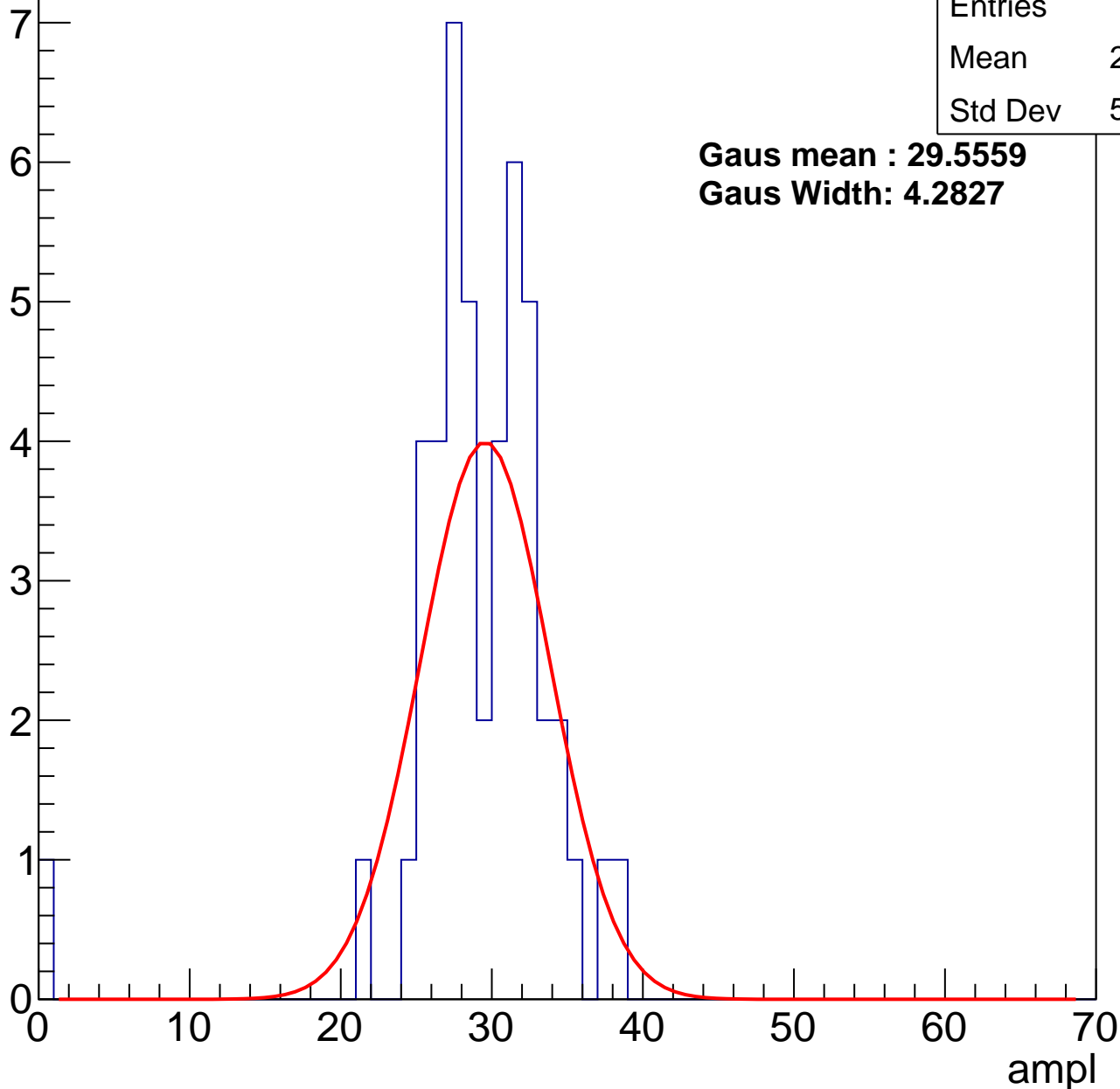
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	28.64
Std Dev	5.444

**Gaus mean : 29.5559**

**Gaus Width: 4.2827**



# B1L102S, U25-ch39, adc1

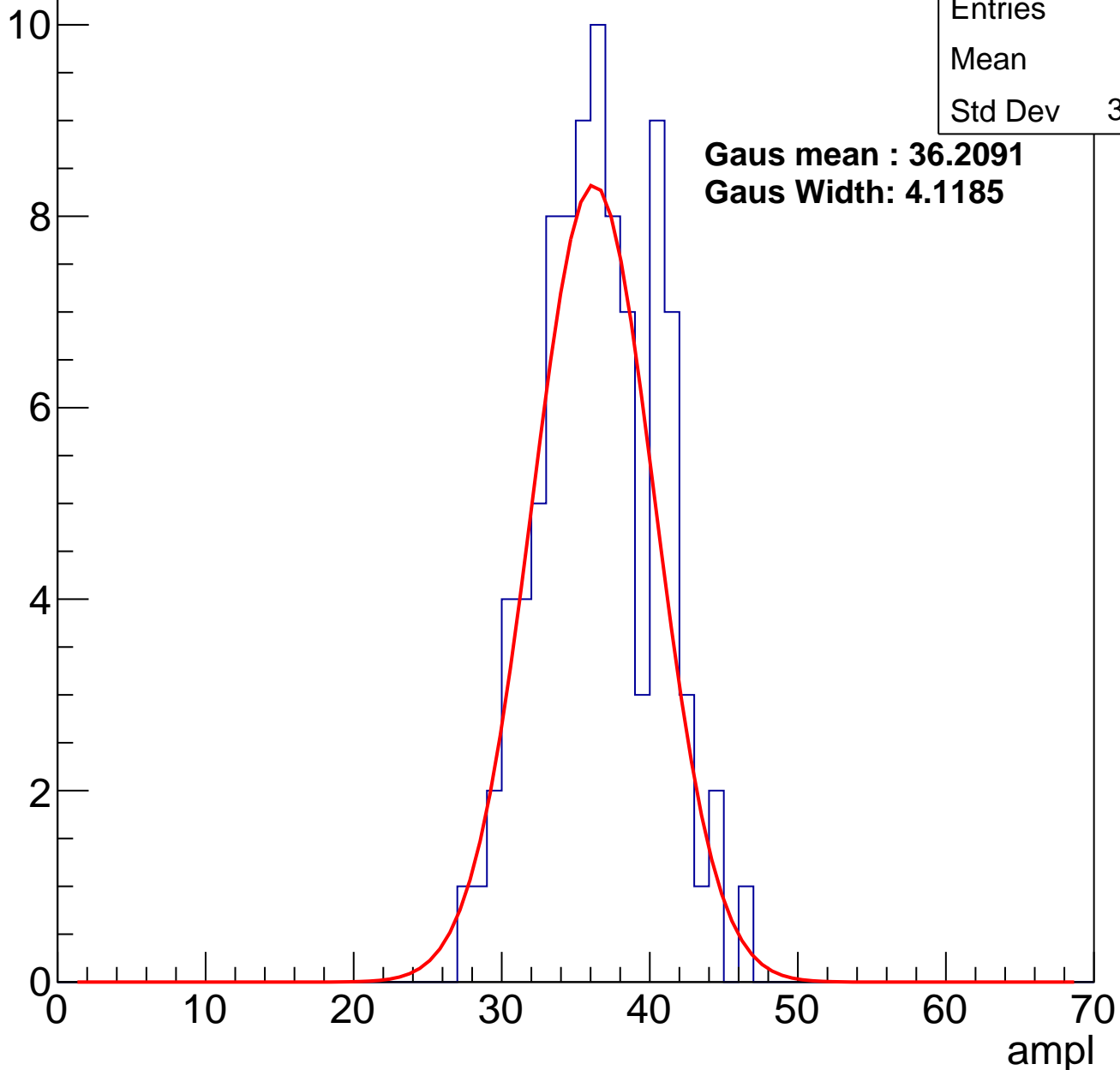
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	93
Mean	36.1
Std Dev	3.943

**Gaus mean : 36.2091**

**Gaus Width: 4.1185**

Entry



# B1L102S, U25-ch39, adc2

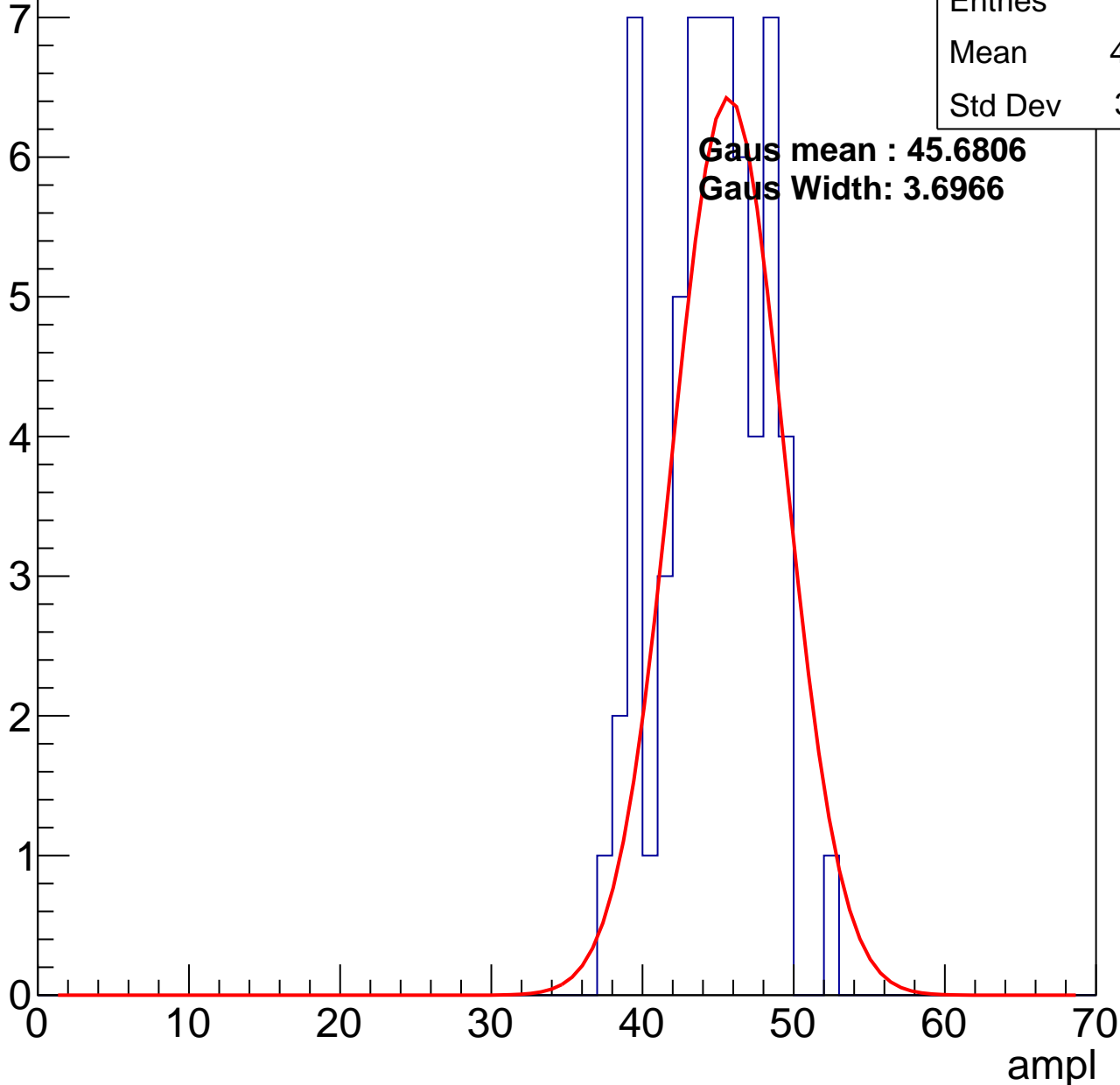
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.05
Std Dev	3.381

**Gaus mean : 45.6806**

**Gaus Width: 3.6966**



# B1L102S, U25-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	56
Mean	49.48
Std Dev	3.689

Entry

10

8

6

4

2

0

0

10

20

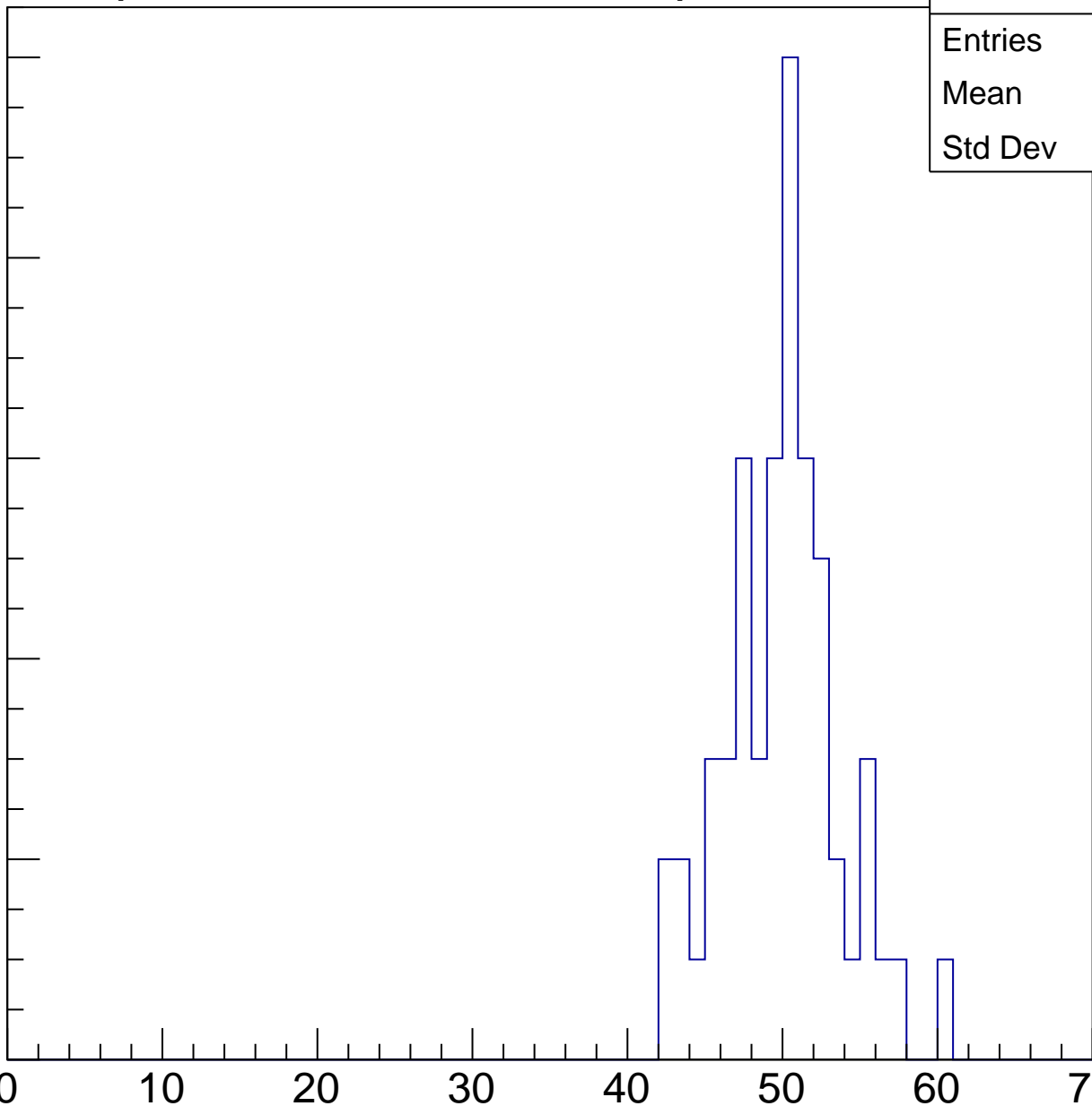
30

40

50

60

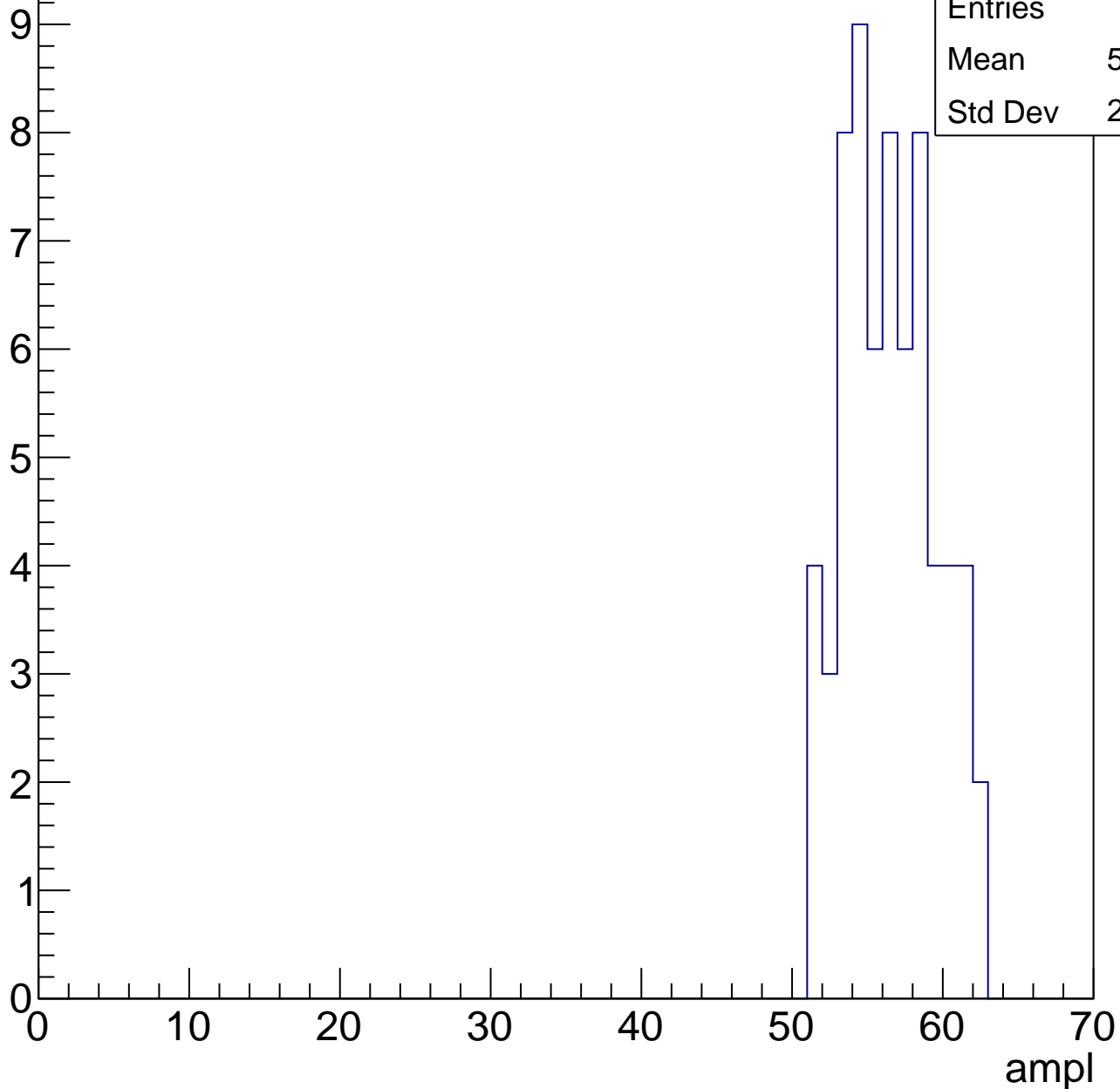
ampl



# B1L102S, U25-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

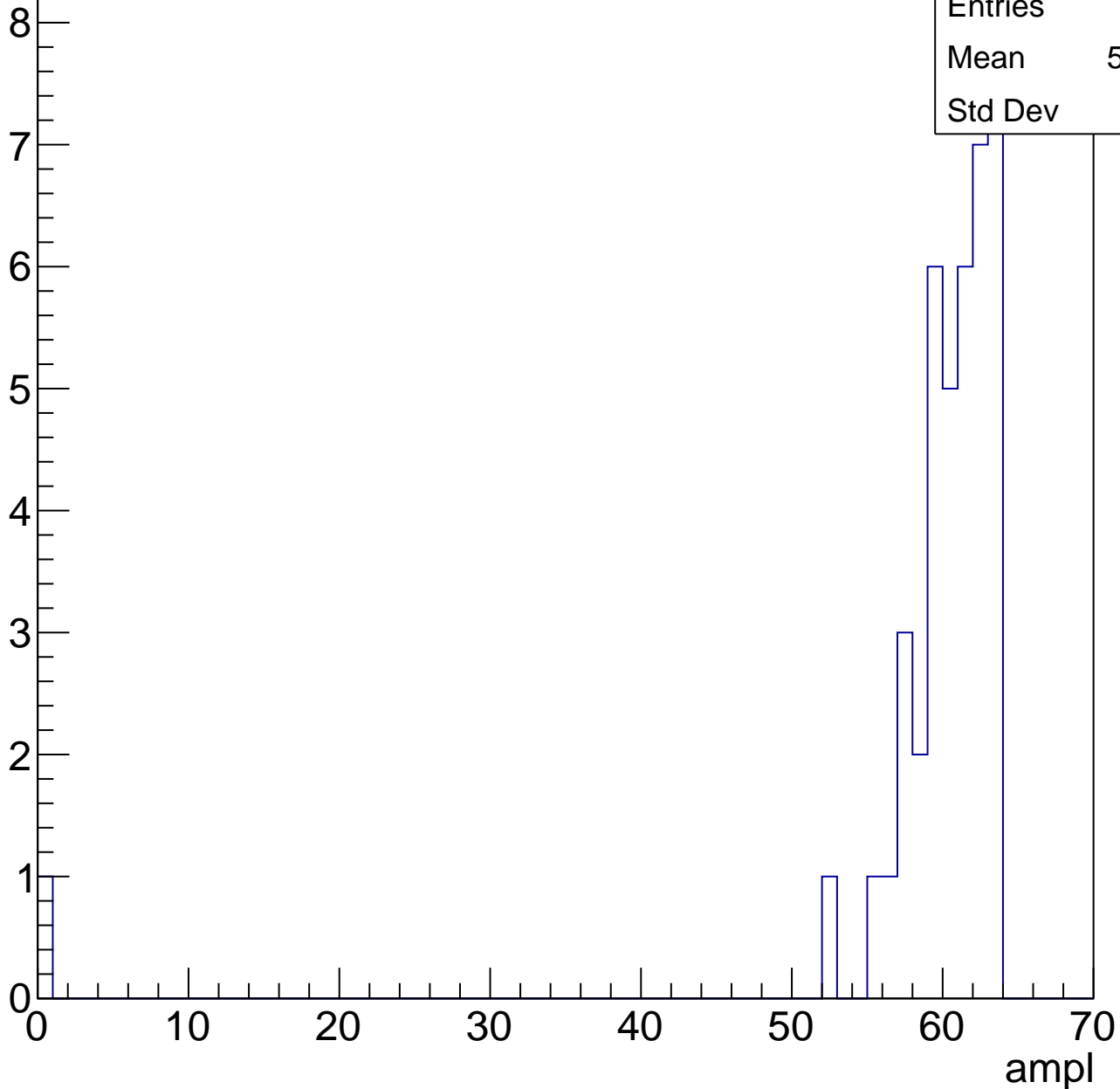


# B1L102S, U25-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.73
Std Dev	9.61



# B1L102S, U25-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch40, adc0

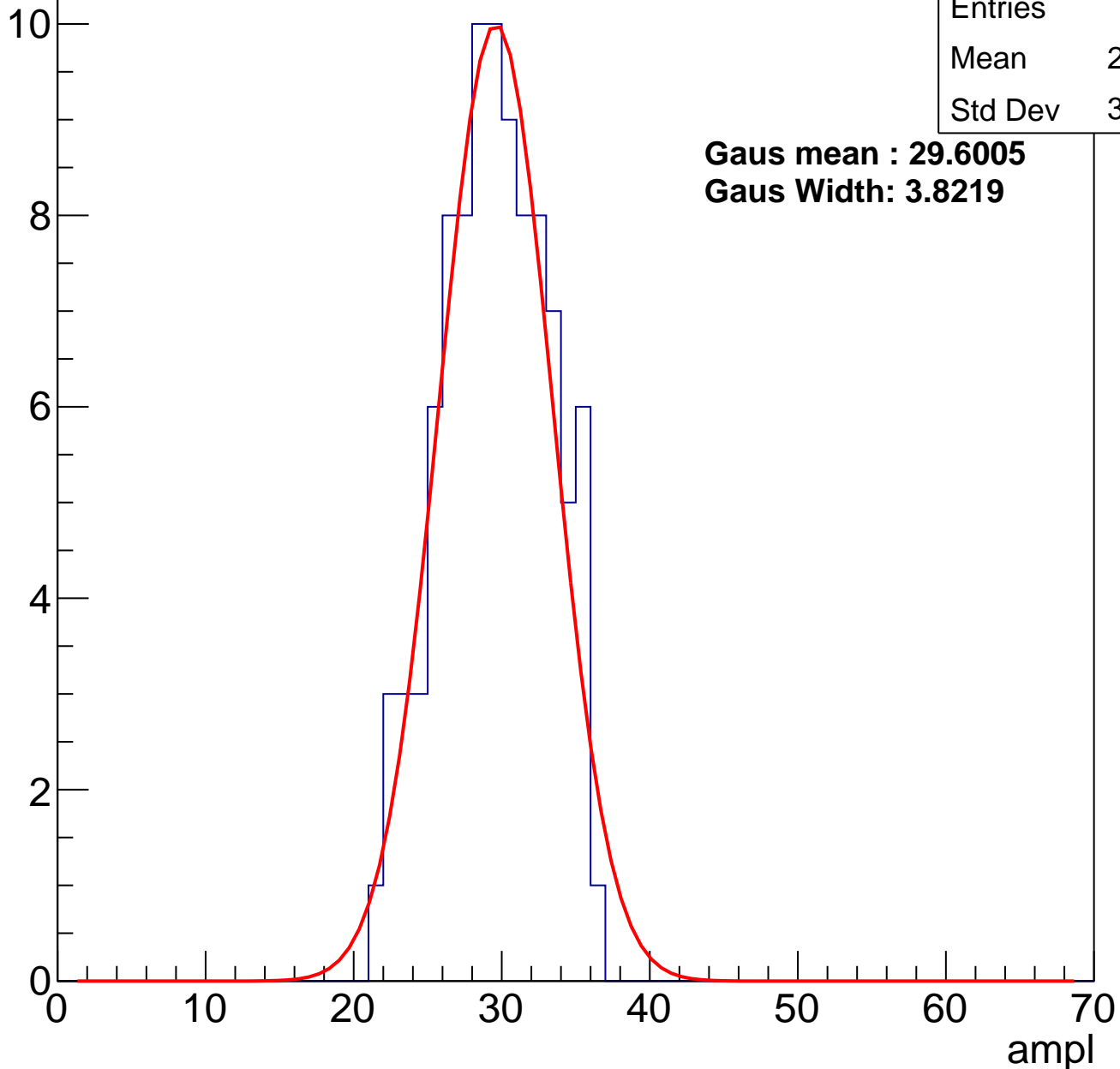
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	96
Mean	29.09
Std Dev	3.562

**Gaus mean : 29.6005**

**Gaus Width: 3.8219**

Entry



# B1L102S, U25-ch40, adc1

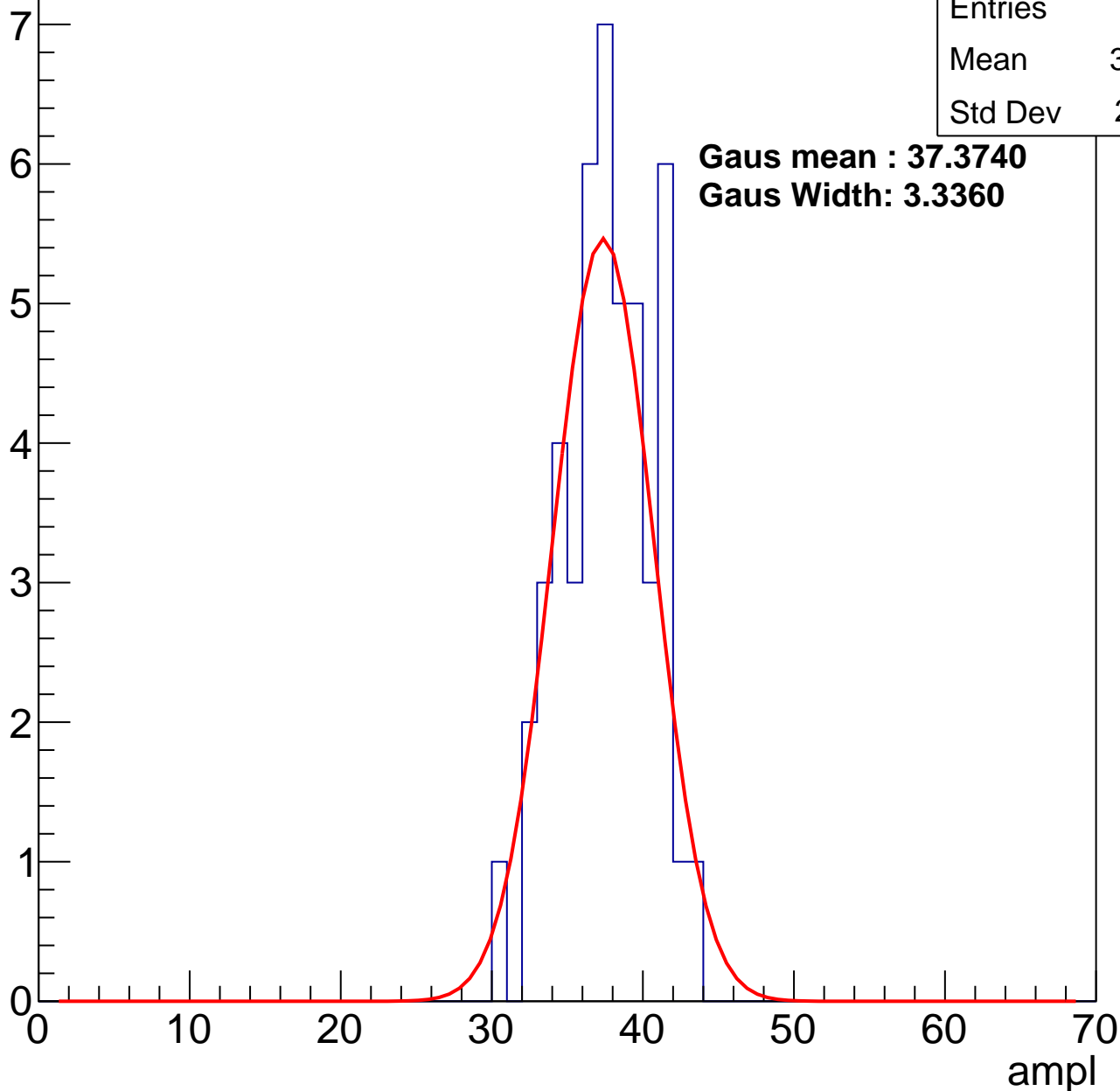
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	37.13
Std Dev	2.951

**Gaus mean : 37.3740**

**Gaus Width: 3.3360**



# B1L102S, U25-ch40, adc2

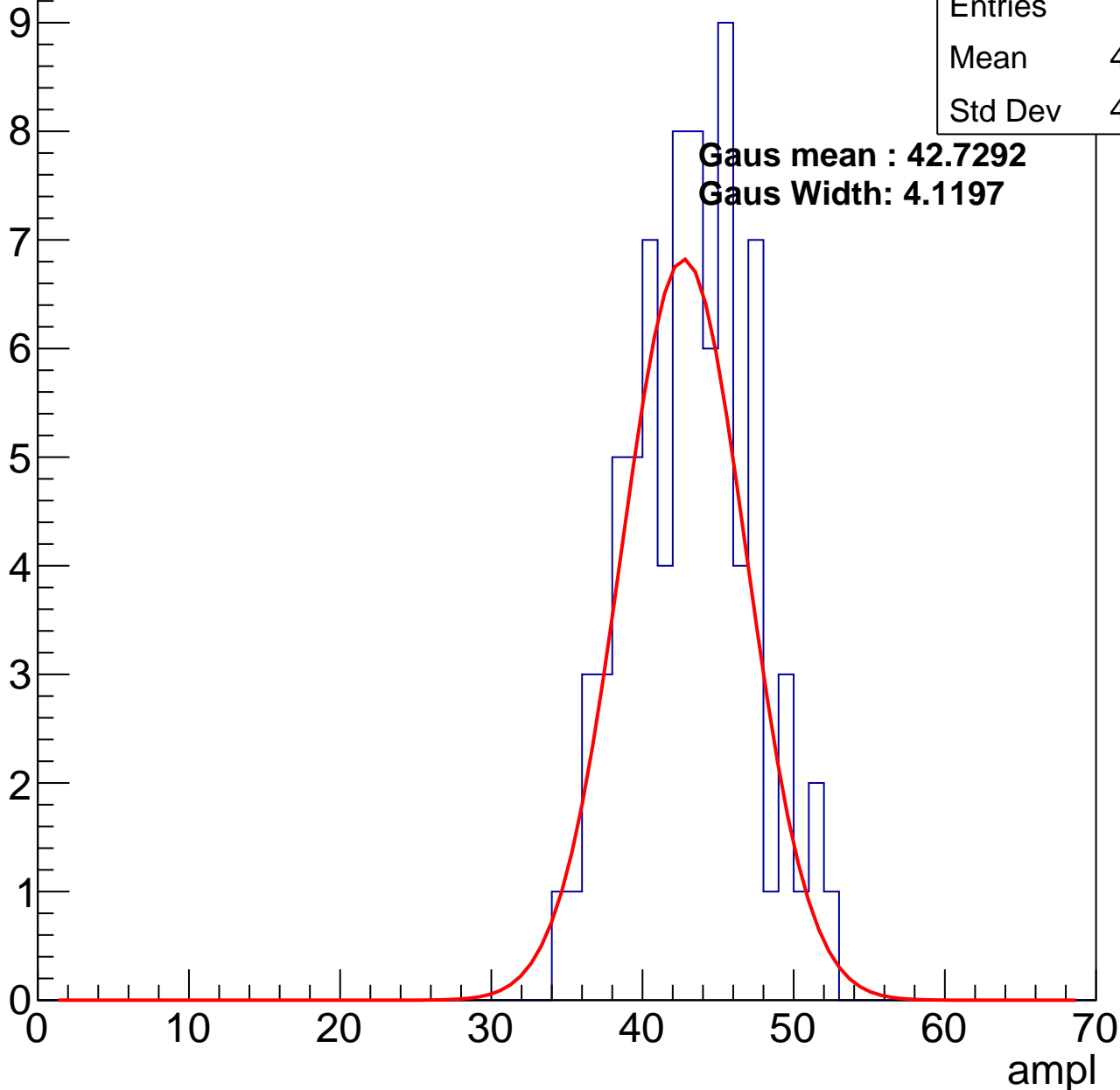
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	42.76
Std Dev	4.007

**Gaus mean : 42.7292**

**Gaus Width: 4.1197**

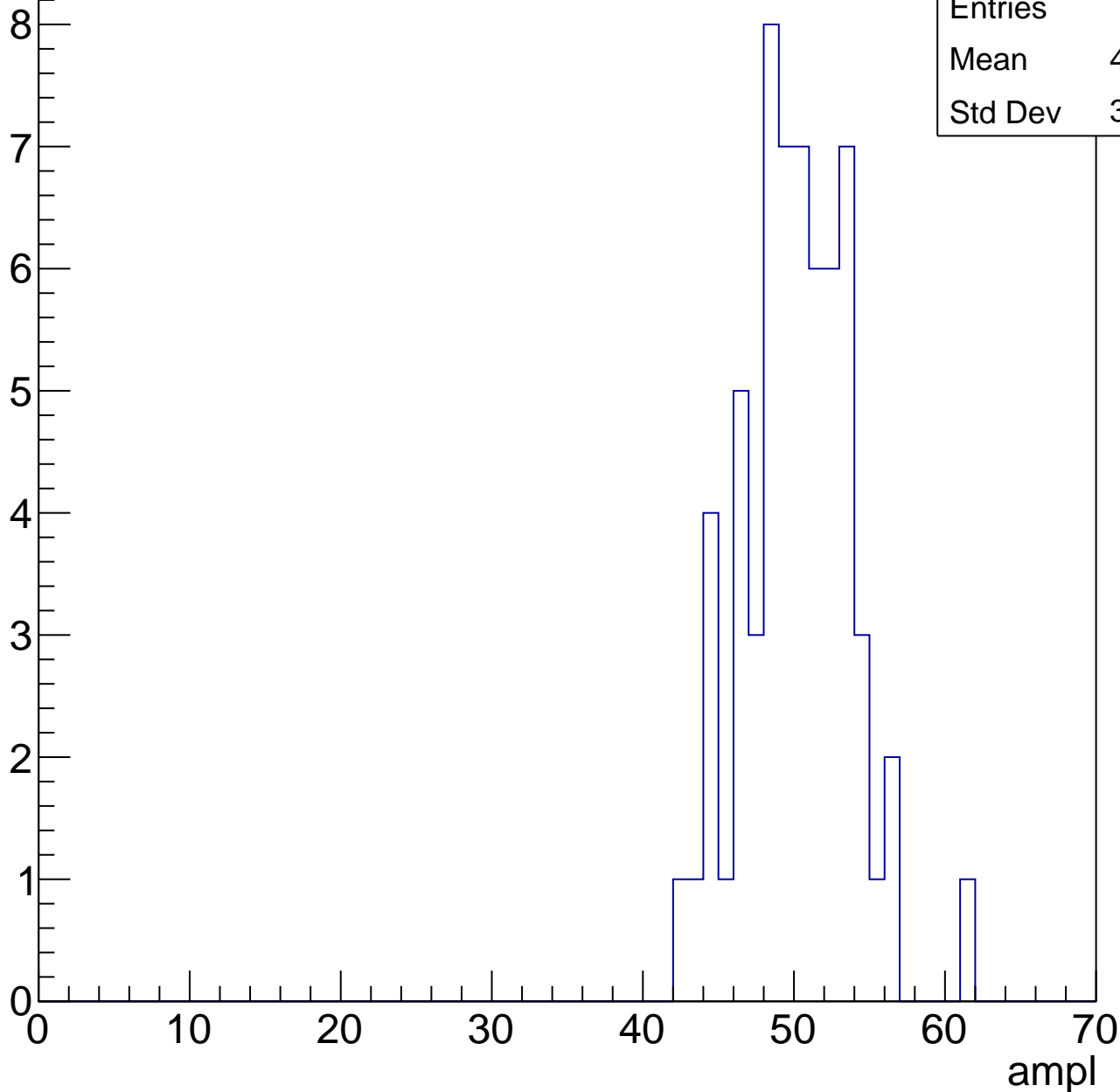


# B1L102S, U25-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.73
Std Dev	3.524

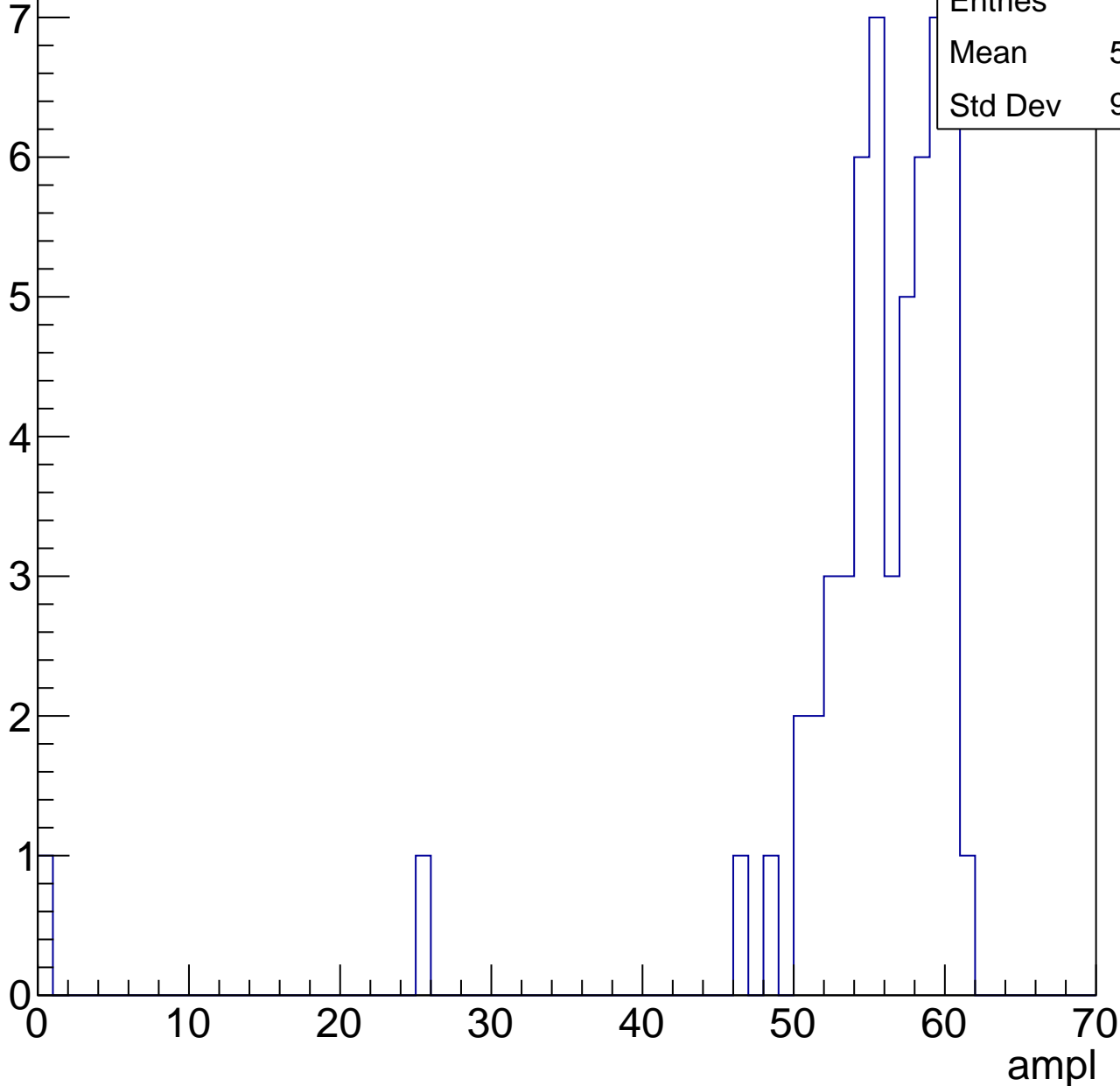


# B1L102S, U25-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.29
Std Dev	9.017

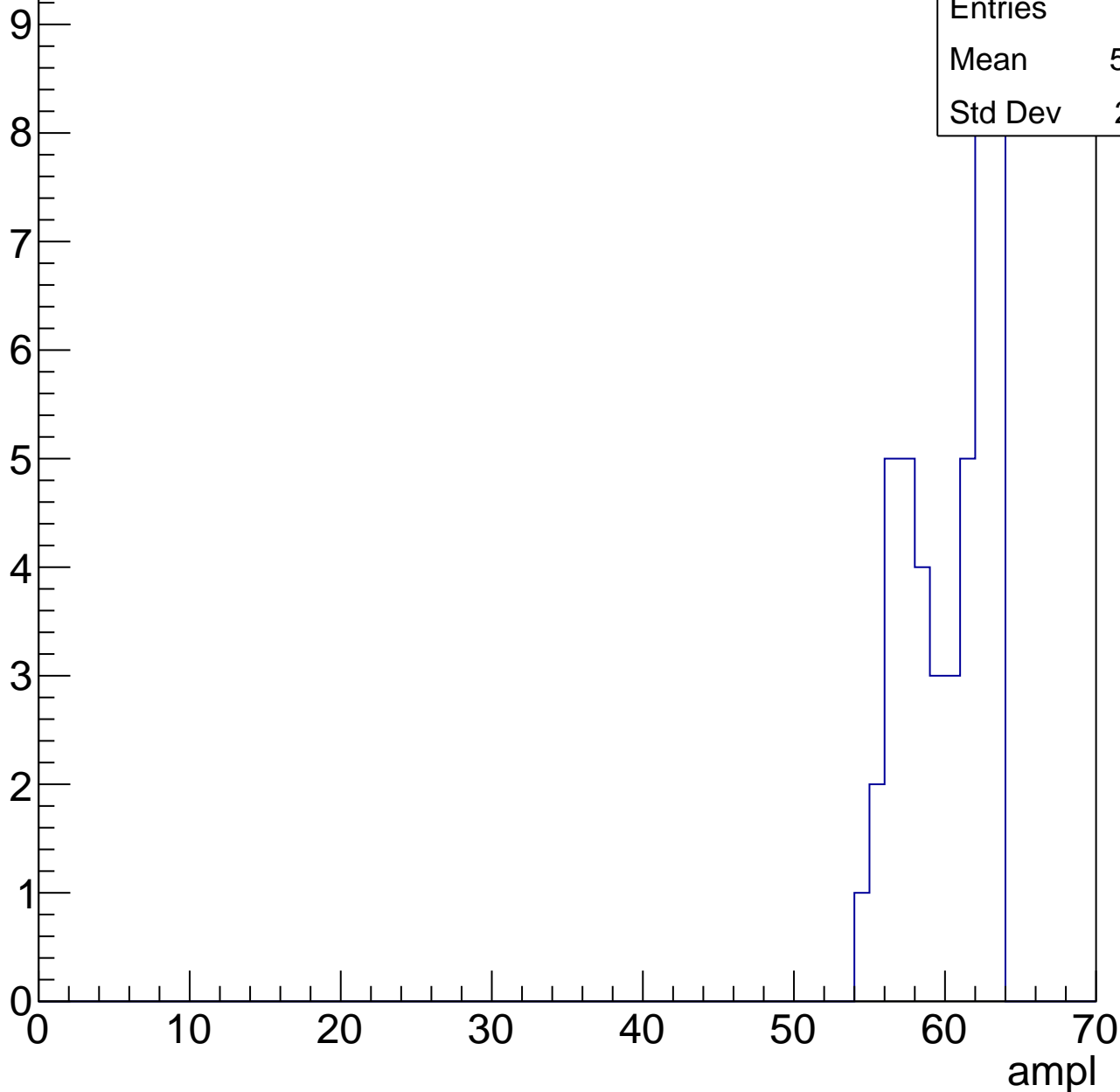


# B1L102S, U25-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

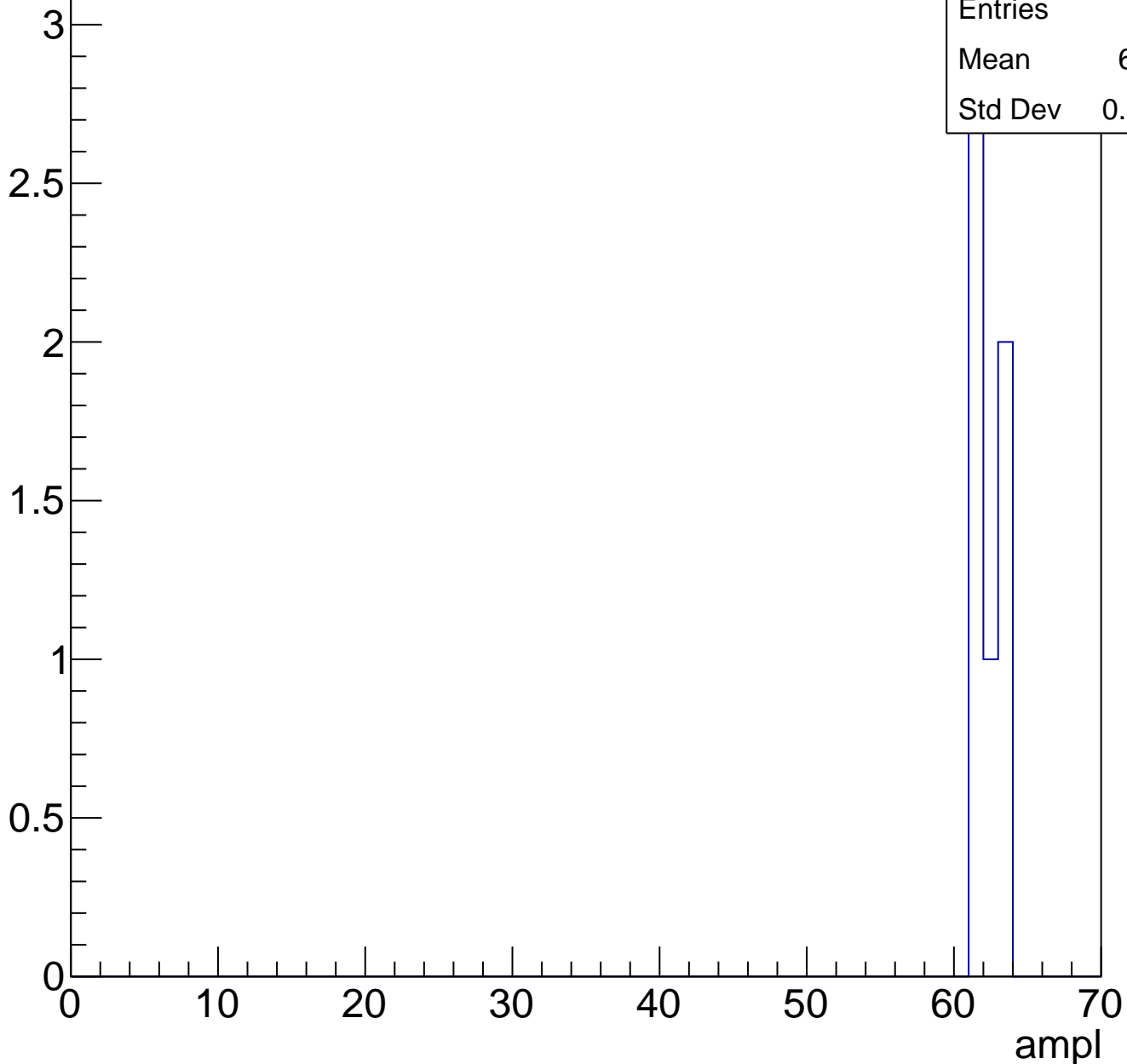
Entries	45
Mean	59.67
Std Dev	2.741



# B1L102S, U25-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch41, adc0

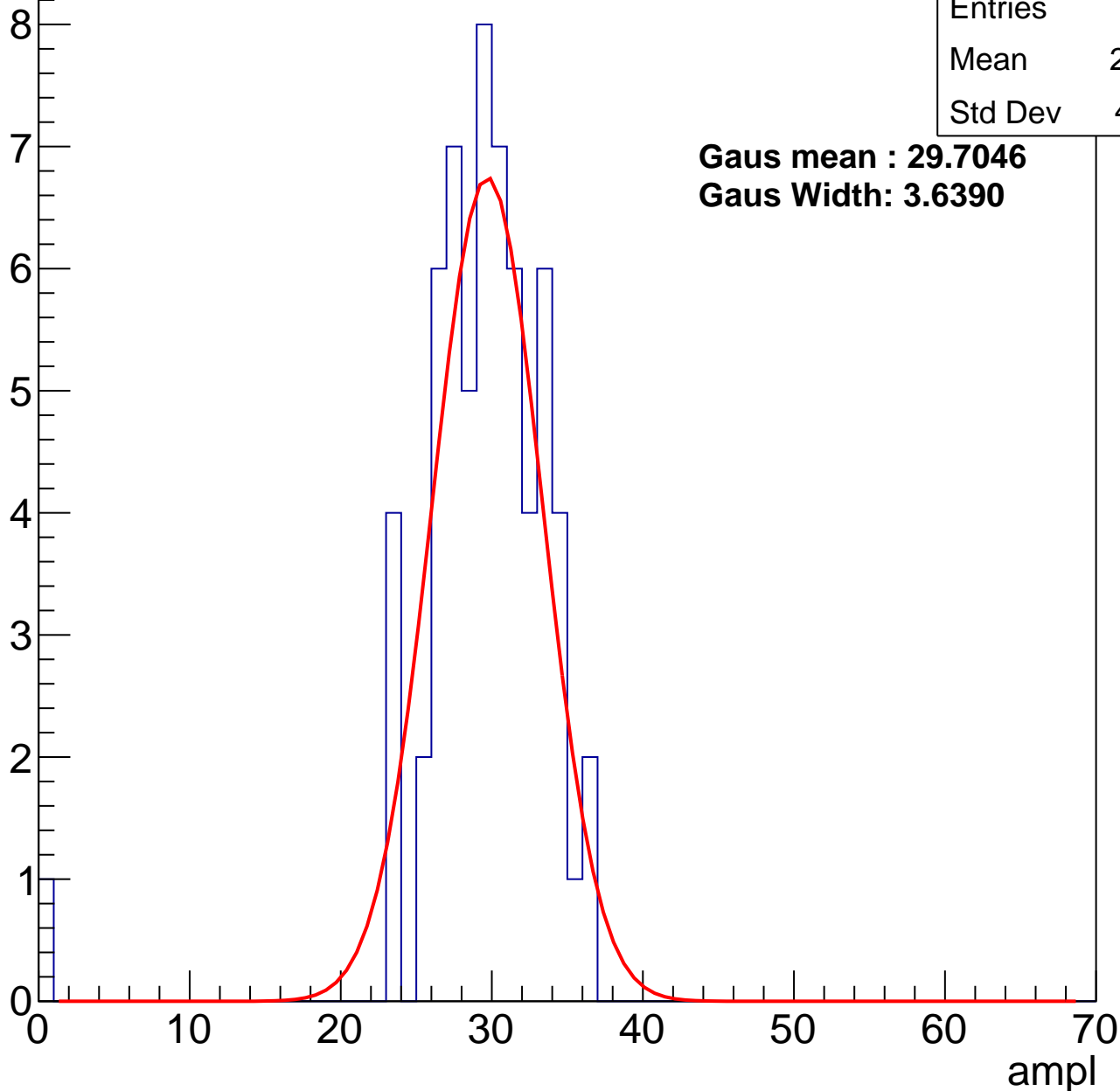
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.95
Std Dev	4.881

**Gaus mean : 29.7046**

**Gaus Width: 3.6390**



# B1L102S, U25-ch41, adc1

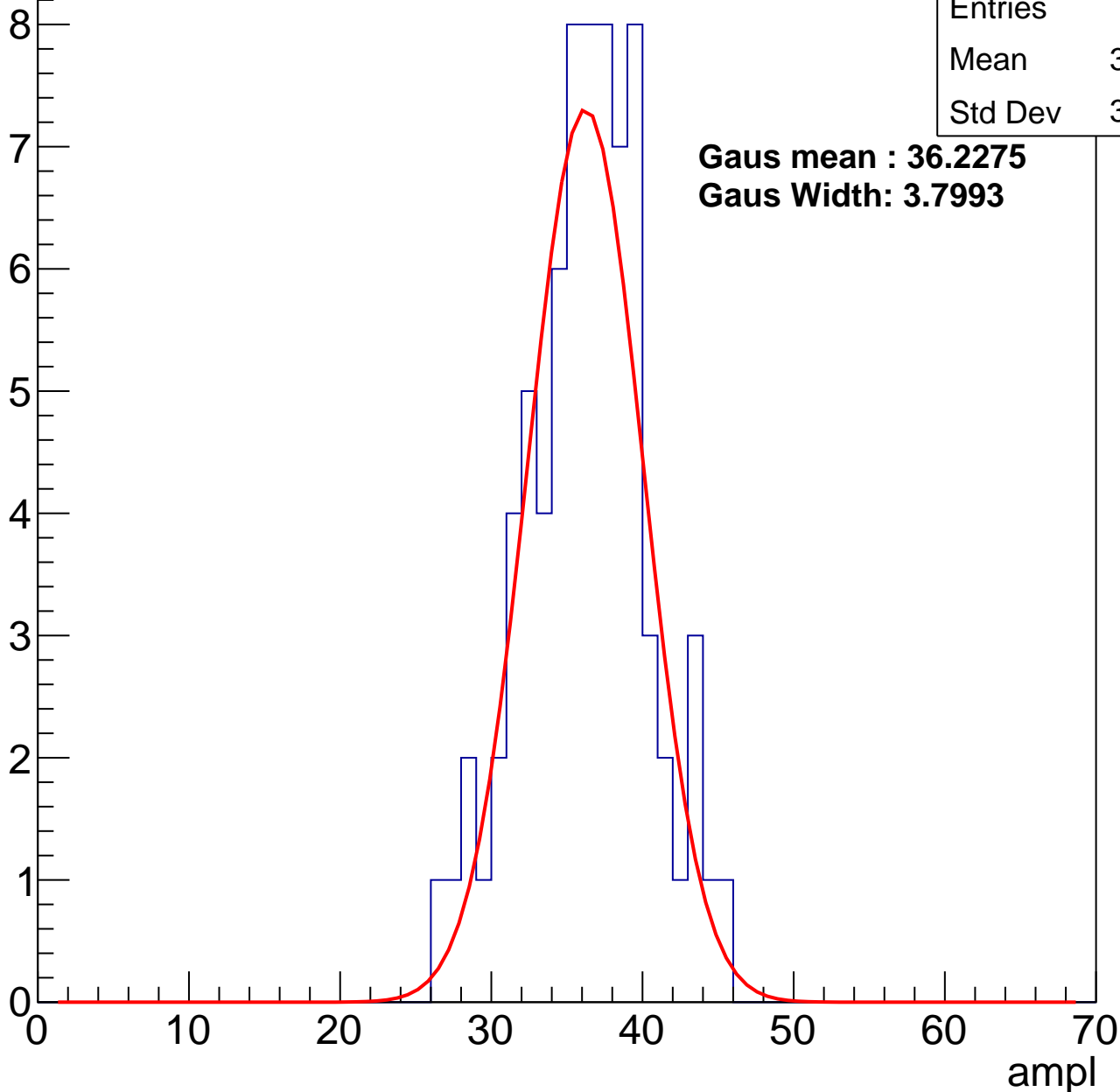
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.82
Std Dev	3.989

**Gaus mean : 36.2275**

**Gaus Width: 3.7993**



# B1L102S, U25-ch41, adc2

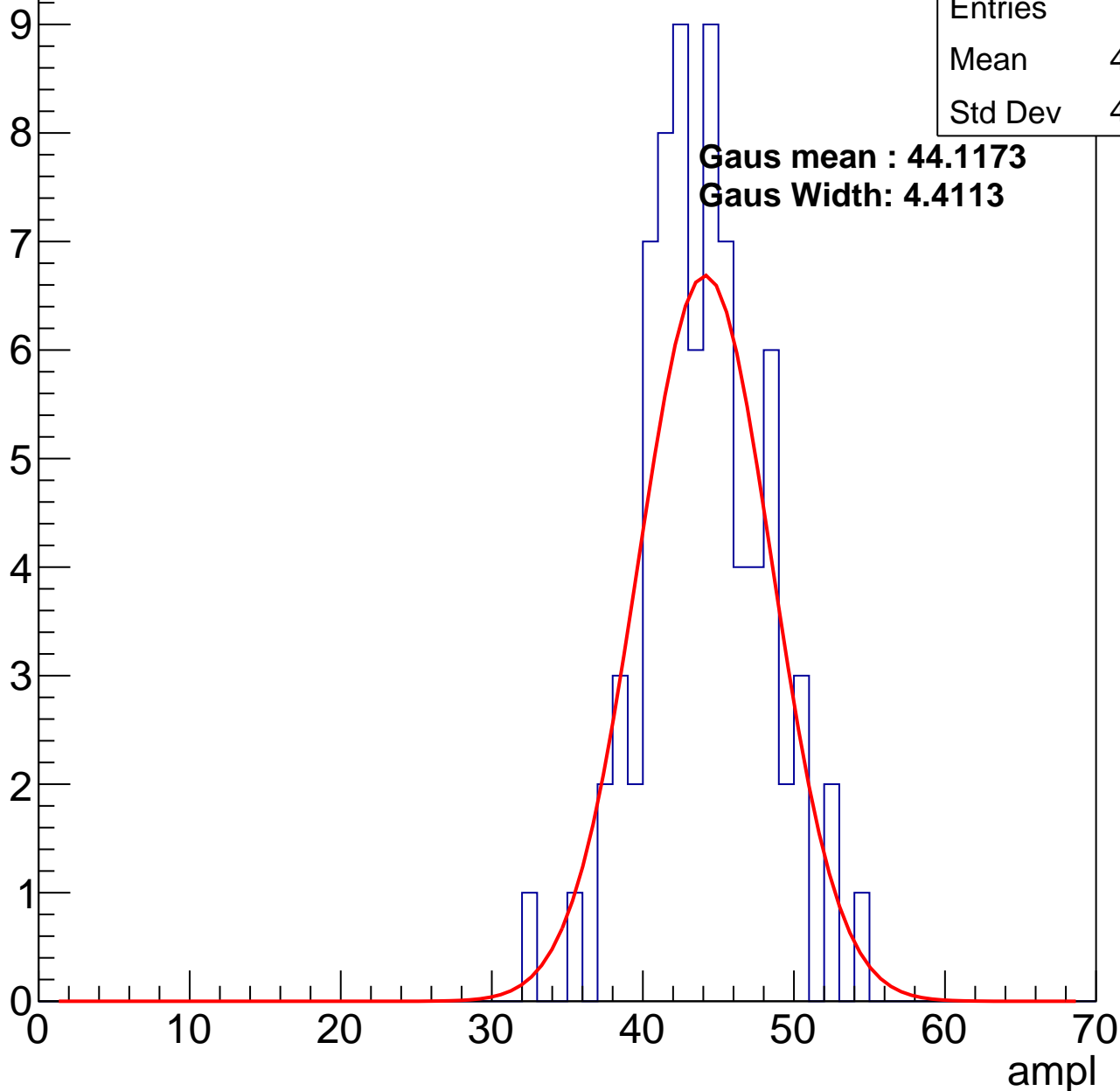
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	43.56
Std Dev	4.002

**Gaus mean : 44.1173**

**Gaus Width: 4.4113**

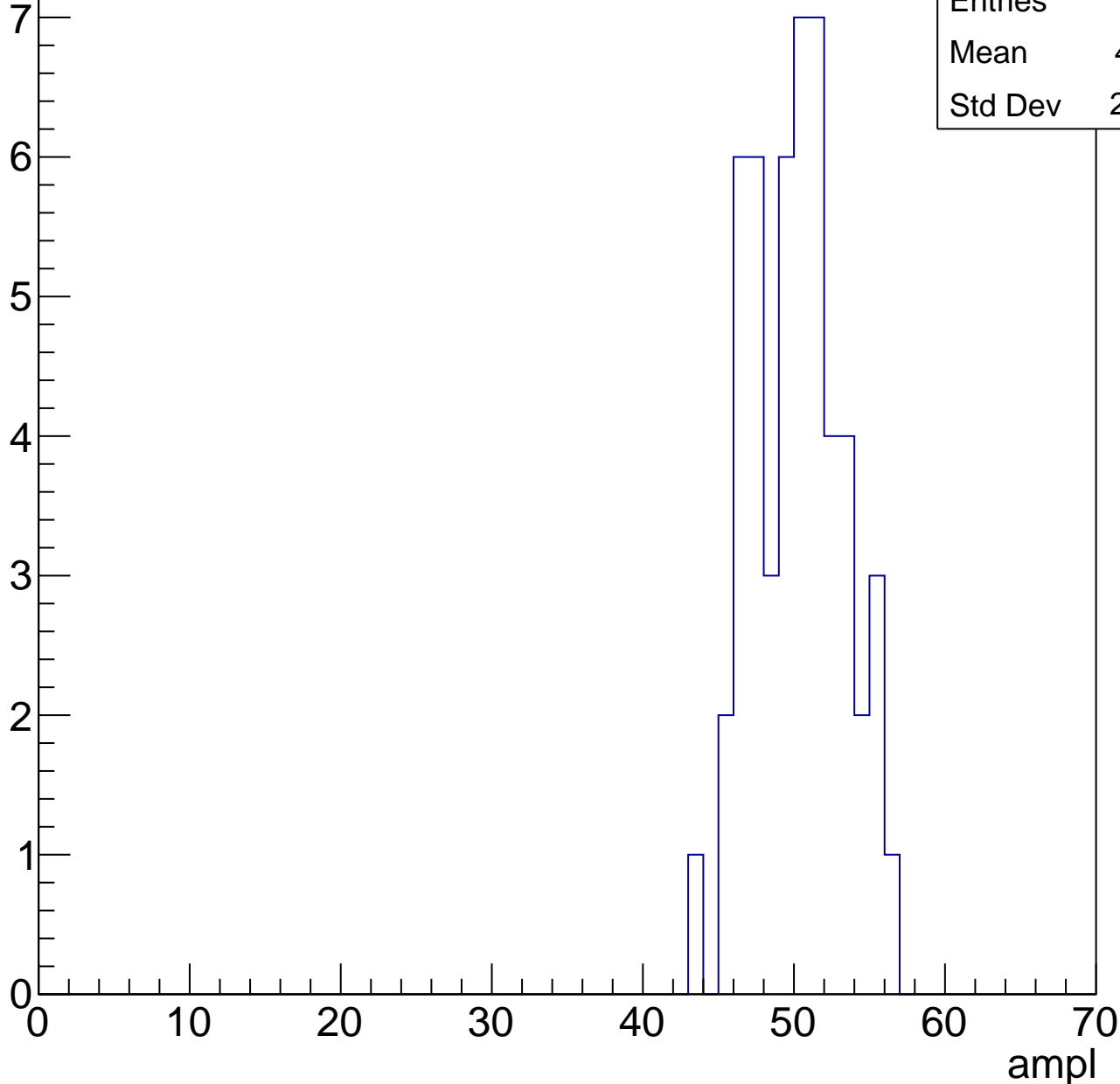


# B1L102S, U25-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.71
Std Dev	2.989

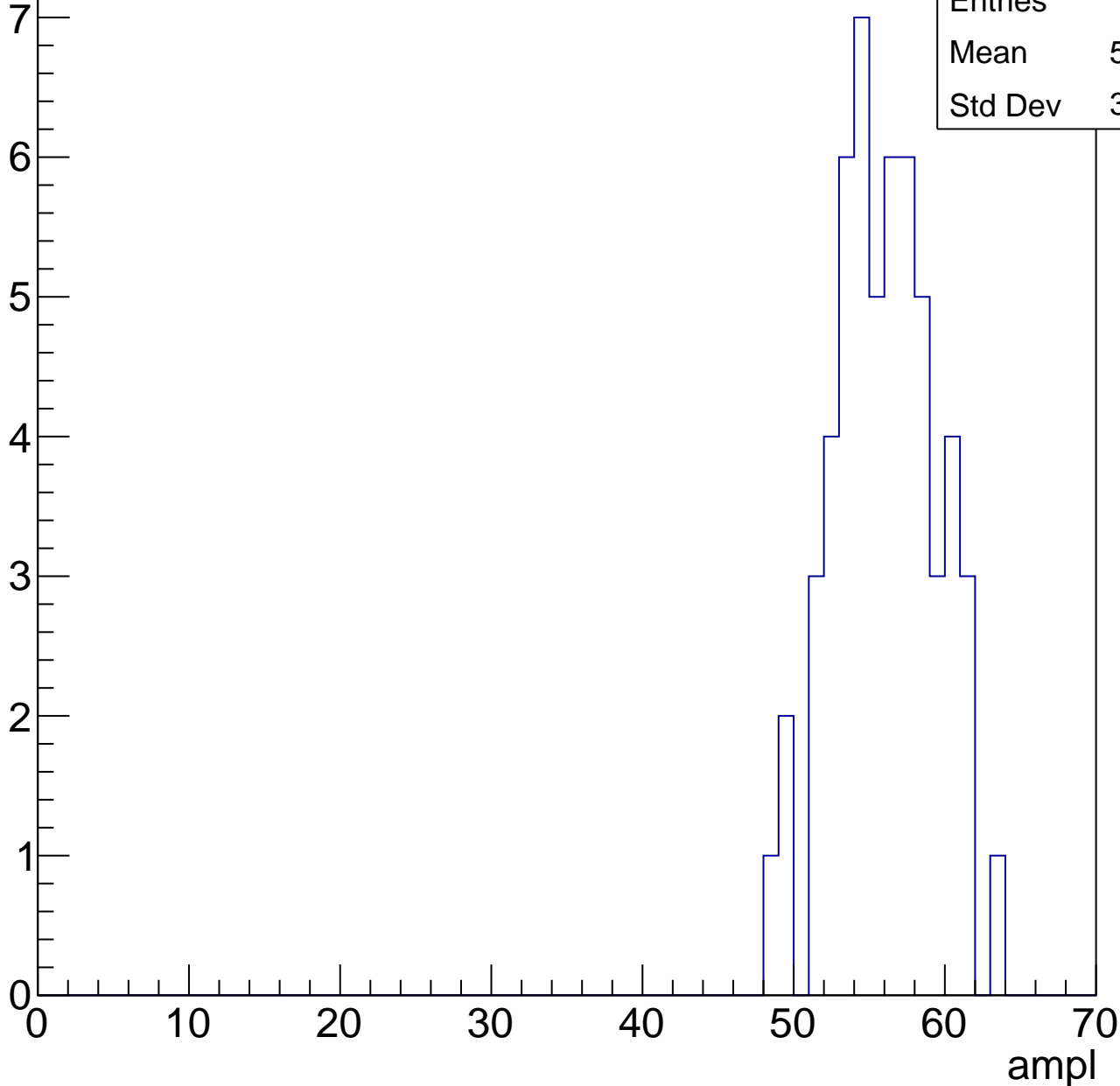


# B1L102S, U25-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.52
Std Dev	3.317

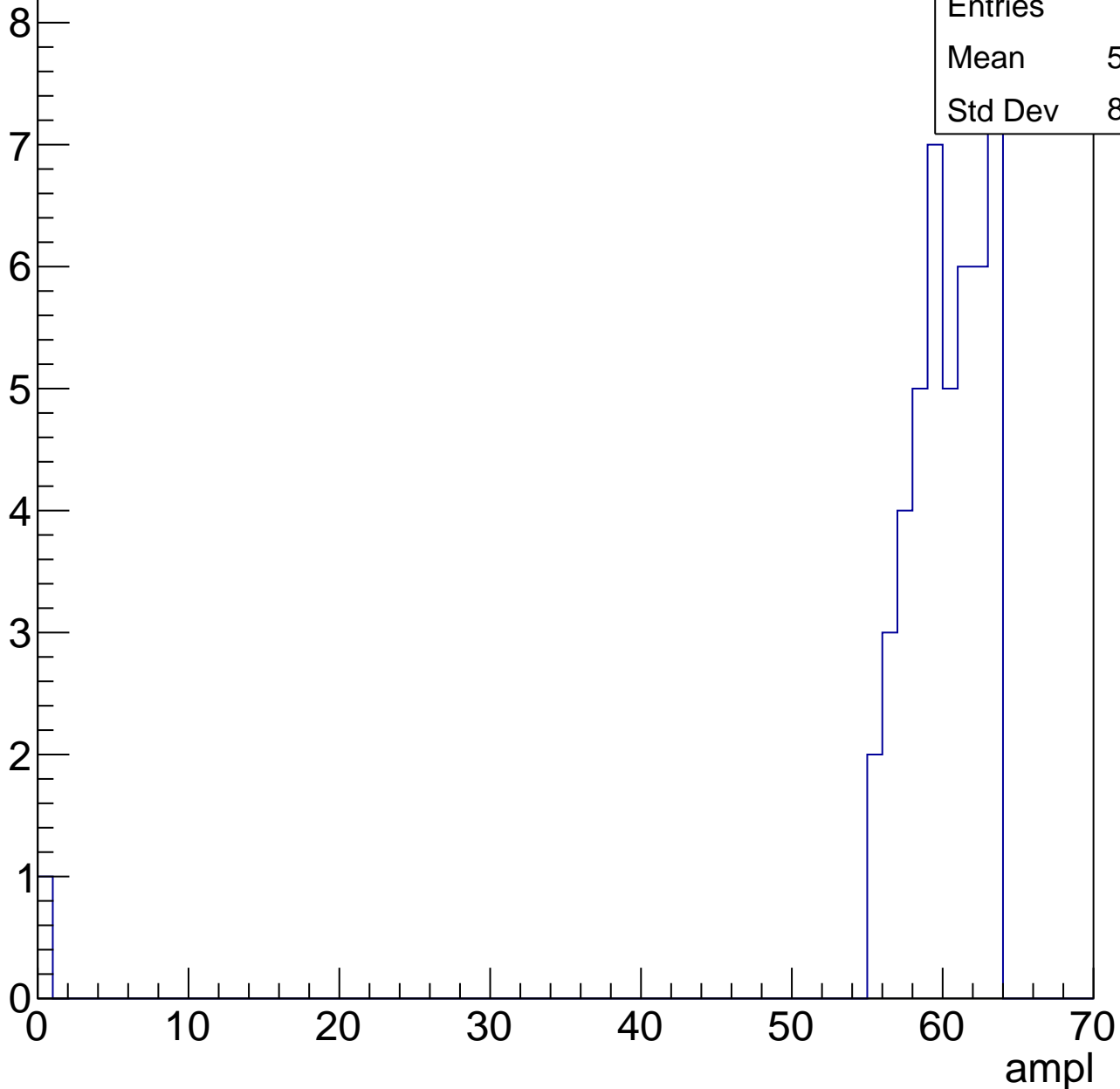


# B1L102S, U25-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

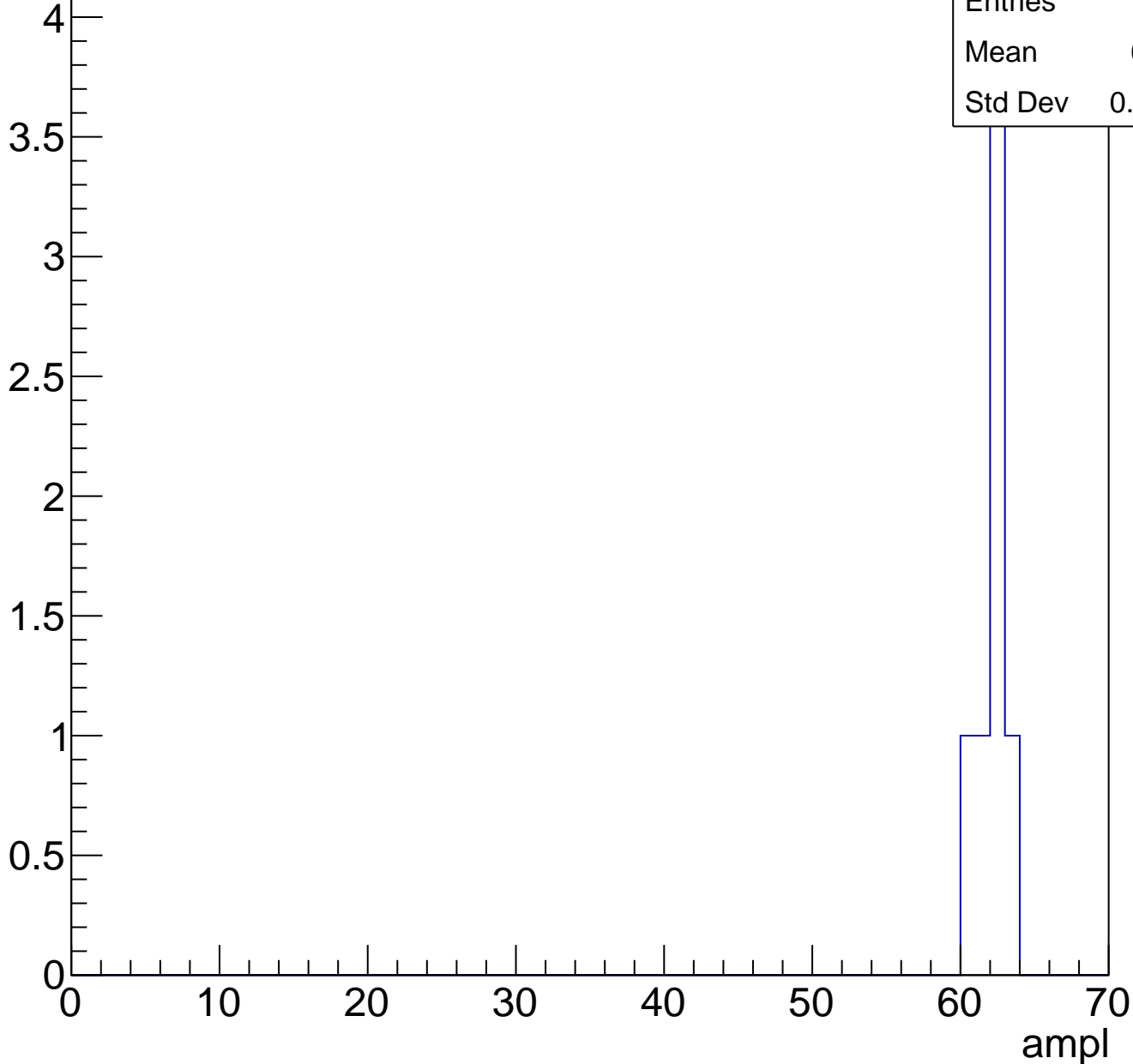
Entries	47
Mean	58.53
Std Dev	8.946



# B1L102S, U25-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9
Std Dev	9

# B1L102S, U25-ch42, adc0

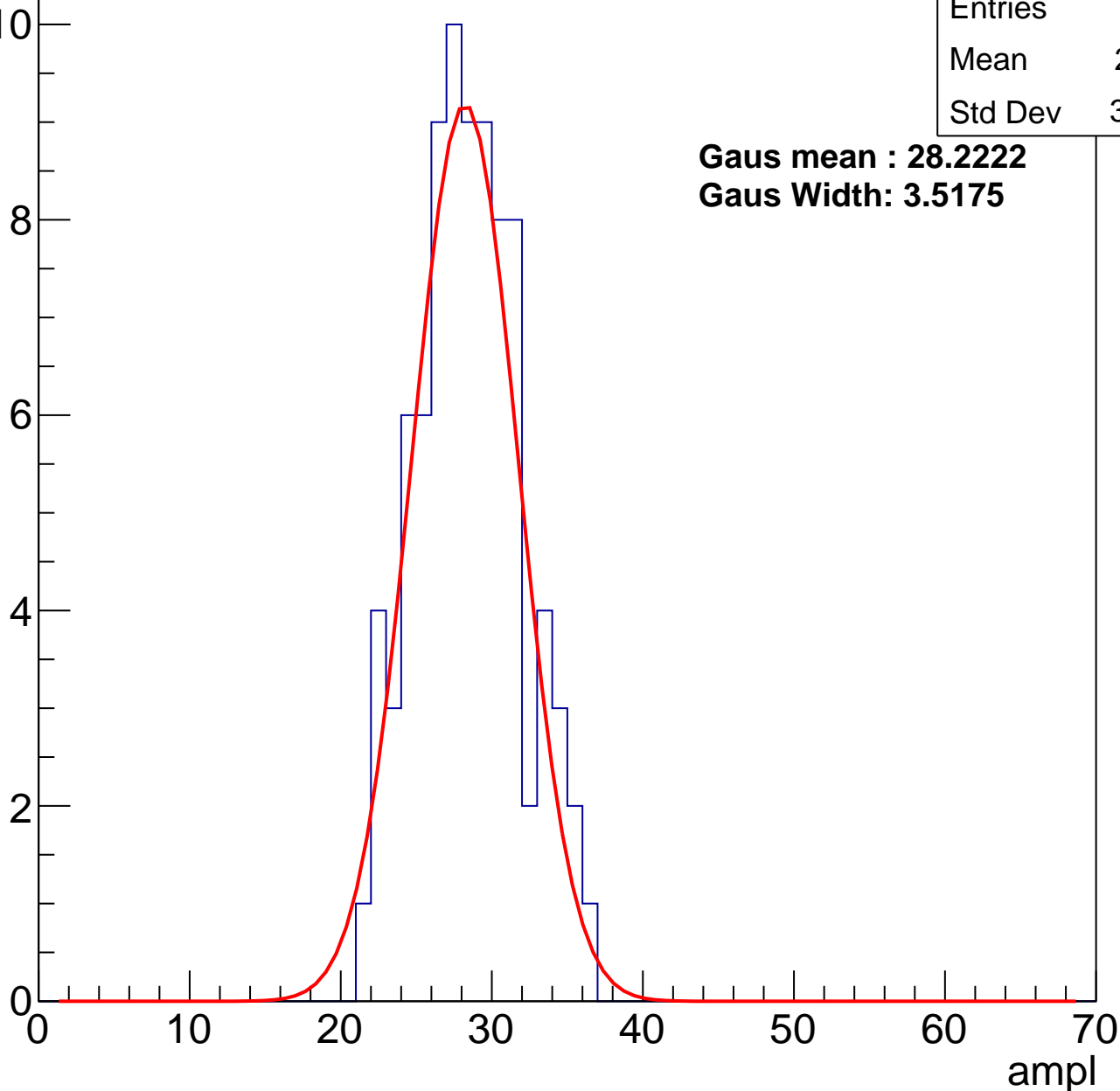
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	28.01
Std Dev	3.394

**Gaus mean : 28.2222**

**Gaus Width: 3.5175**



# B1L102S, U25-ch42, adc1

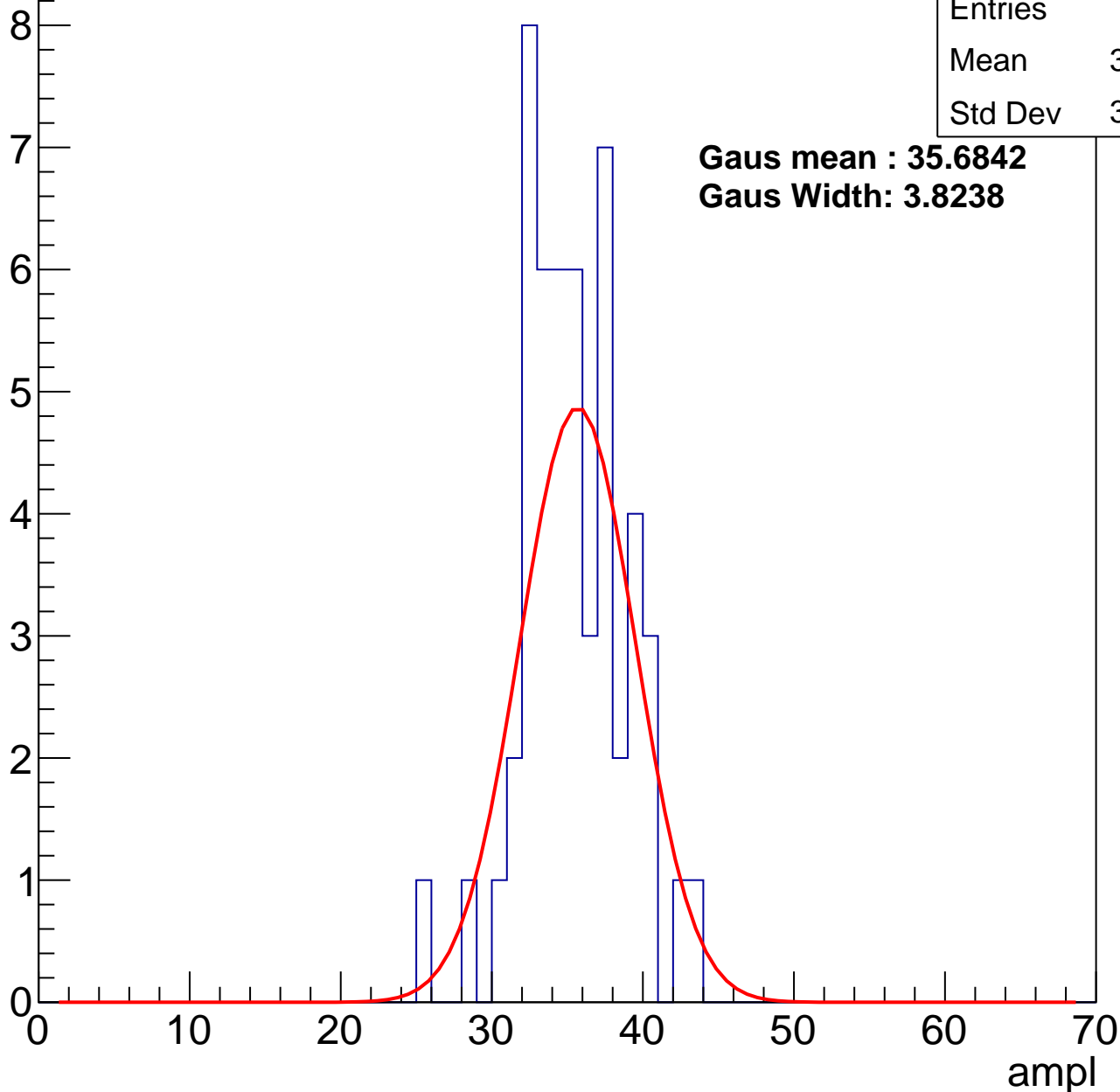
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.94
Std Dev	3.422

**Gaus mean : 35.6842**

**Gaus Width: 3.8238**



# B1L102S, U25-ch42, adc2

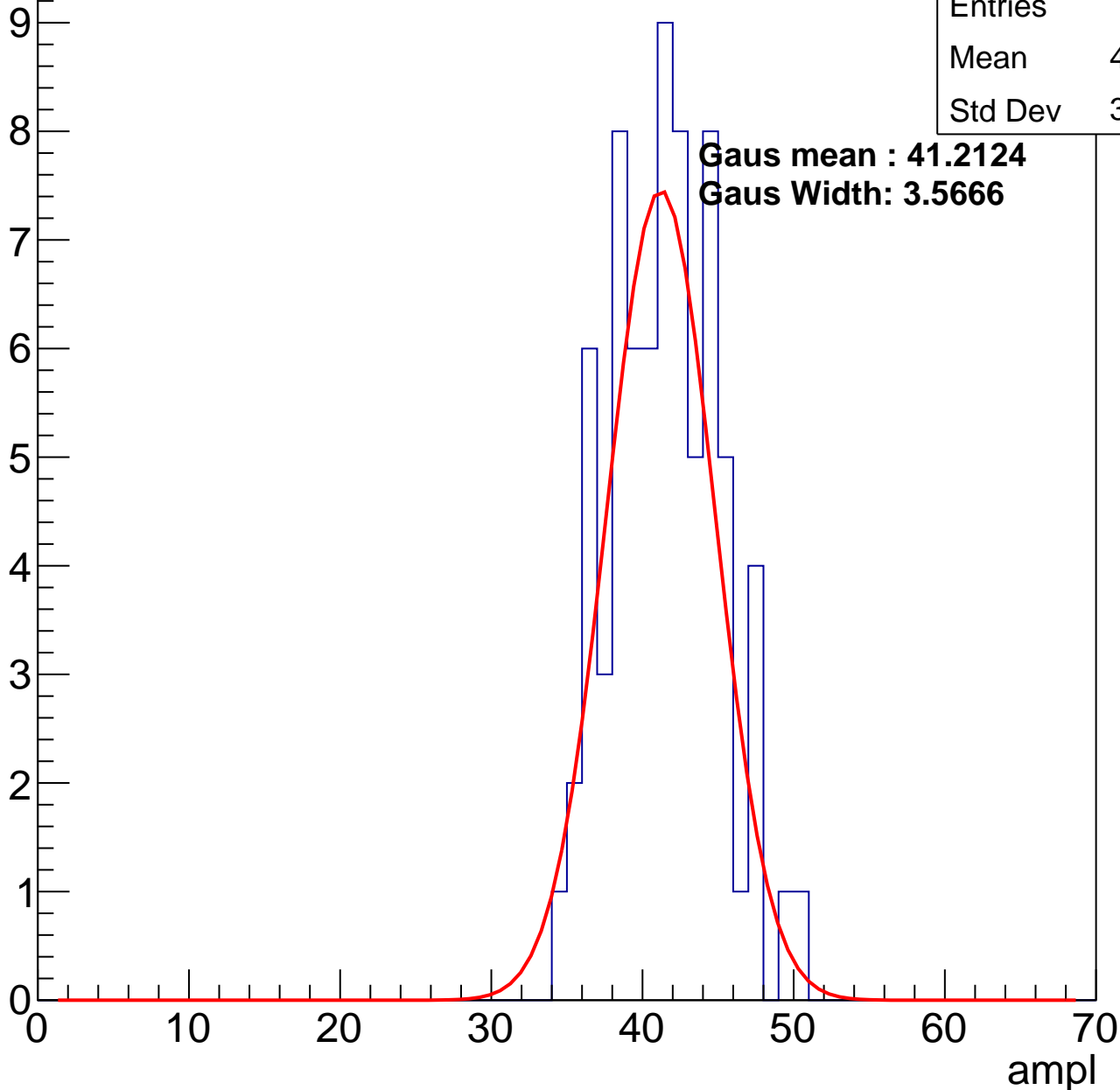
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.07
Std Dev	3.516

**Gaus mean : 41.2124**

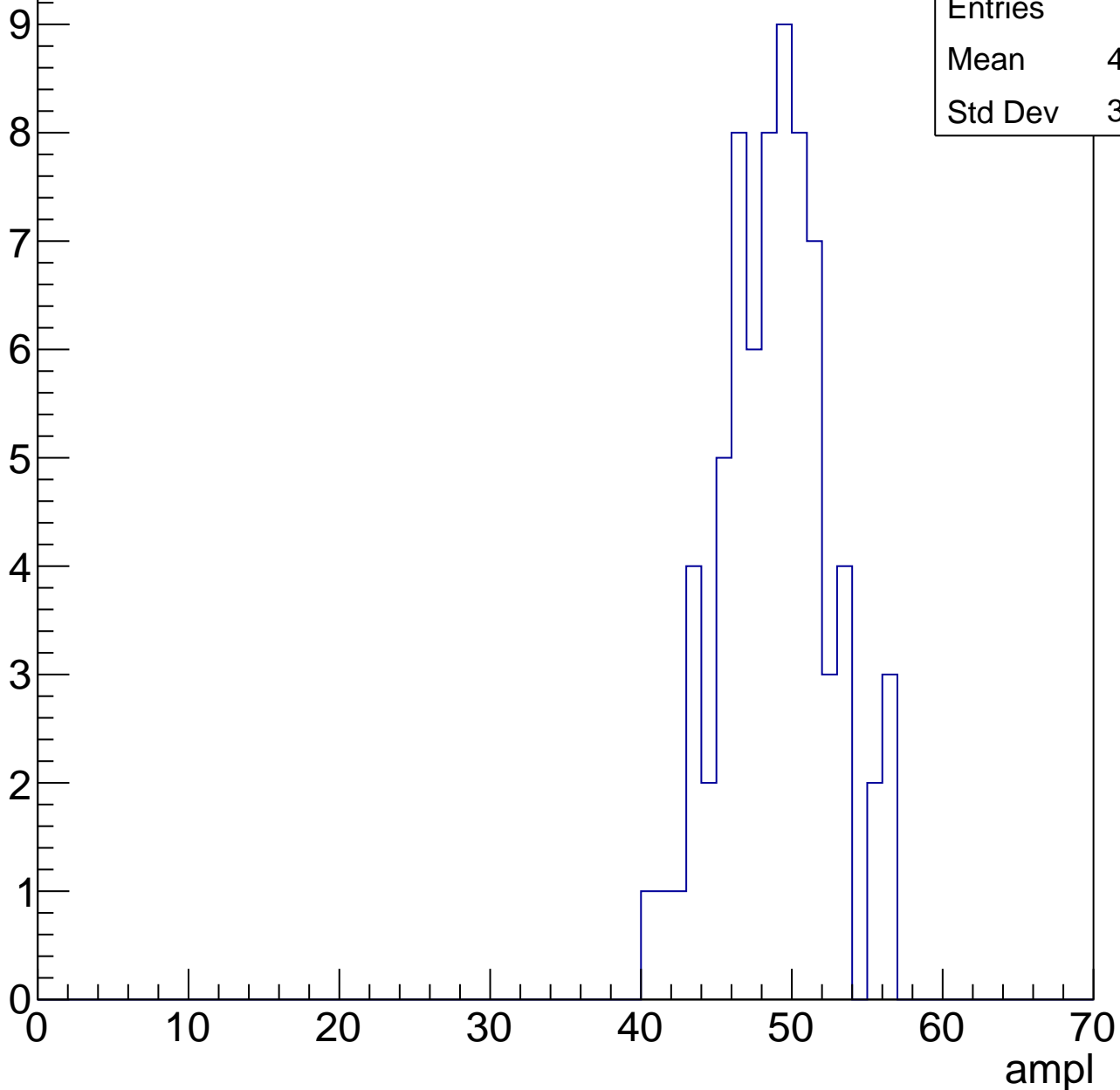
**Gaus Width: 3.5666**



# B1L102S, U25-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



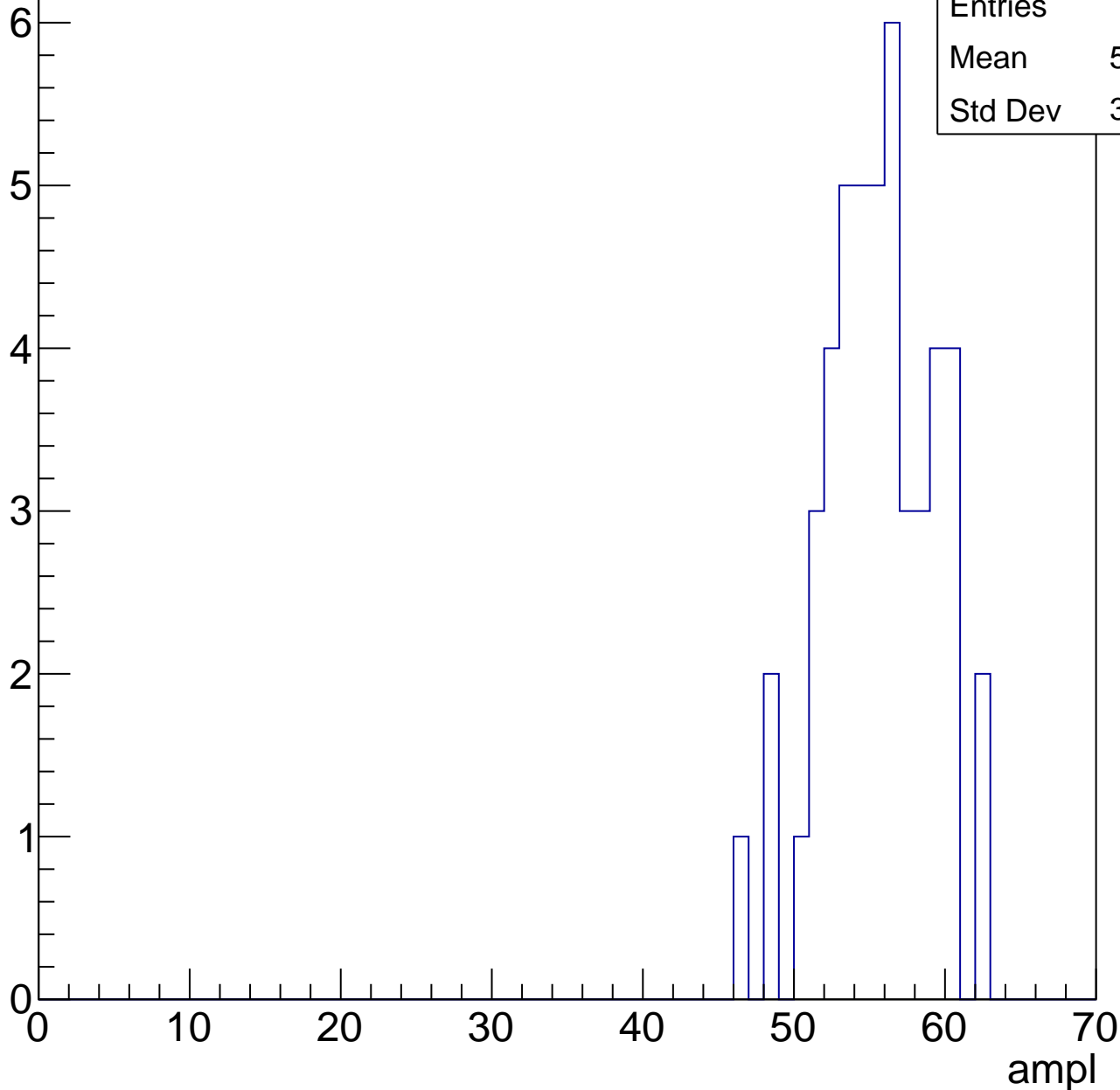
Entries	72
Mean	48.42
Std Dev	3.523

# B1L102S, U25-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.08
Std Dev	3.593

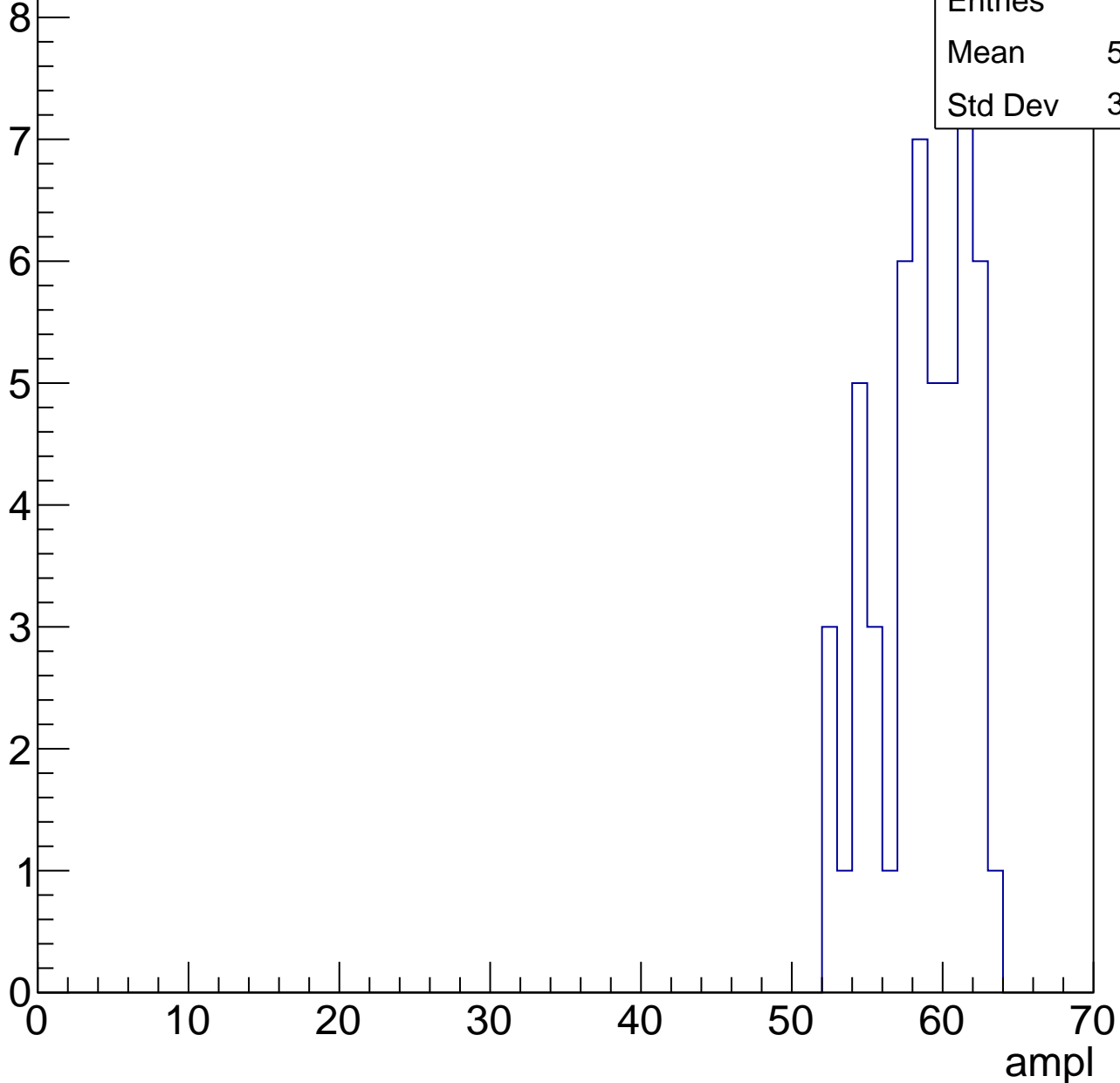


# B1L102S, U25-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.16
Std Dev	3.025

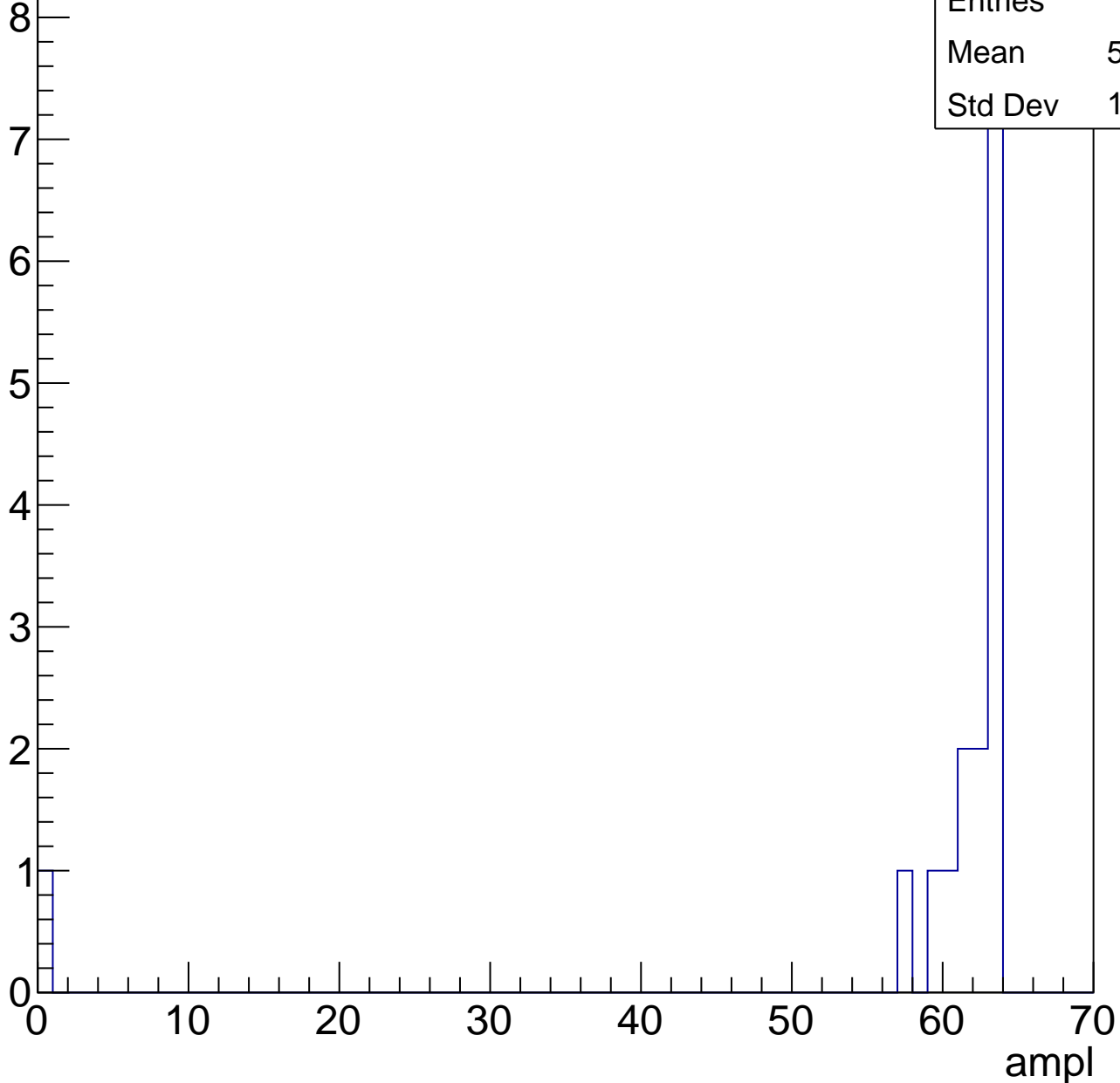


# B1L102S, U25-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.88
Std Dev	15.04





# B1L102S, U25-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L102S, U25-ch43, adc0

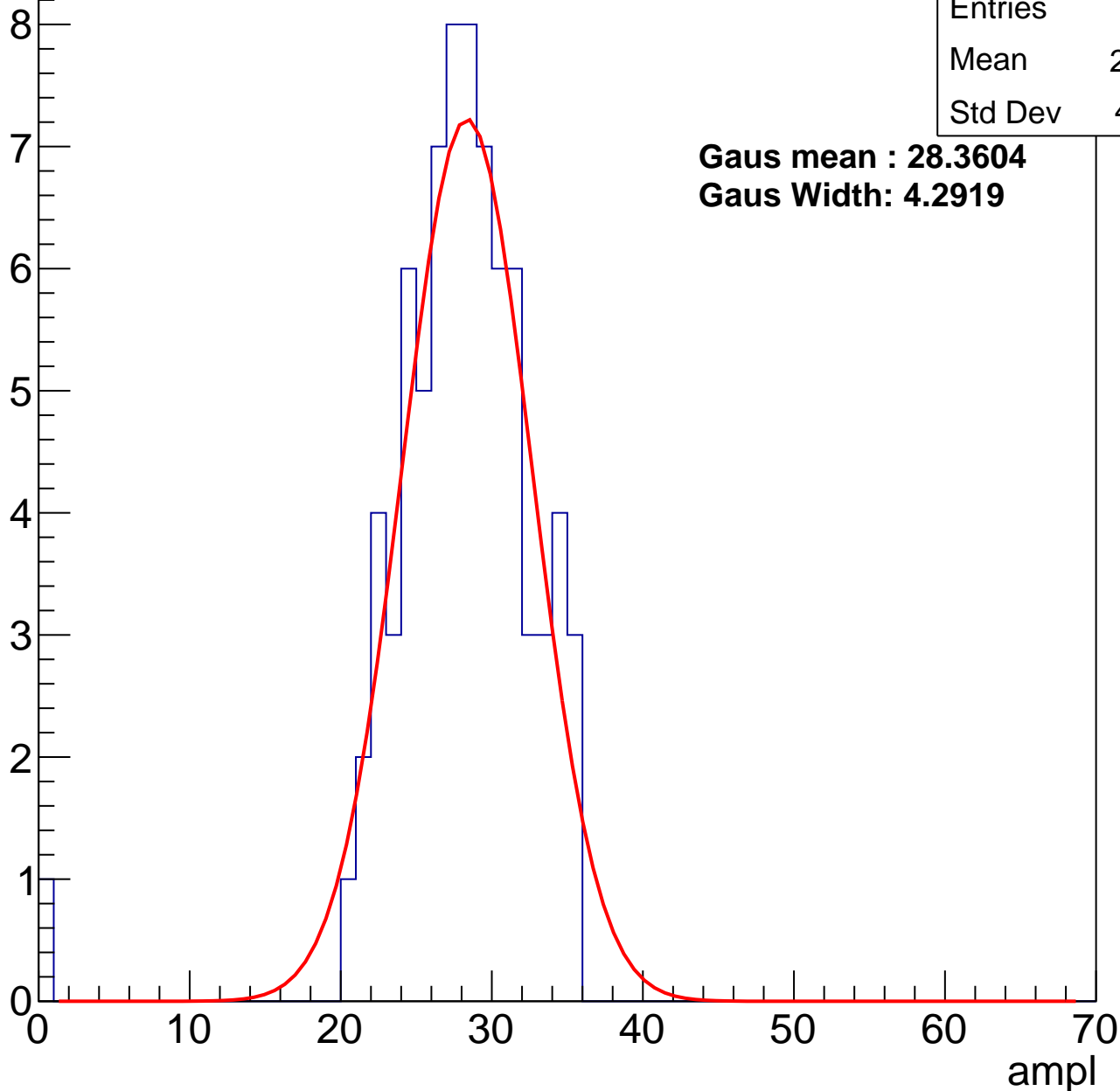
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.47
Std Dev	4.861

**Gaus mean : 28.3604**

**Gaus Width: 4.2919**



# B1L102S, U25-ch43, adc1

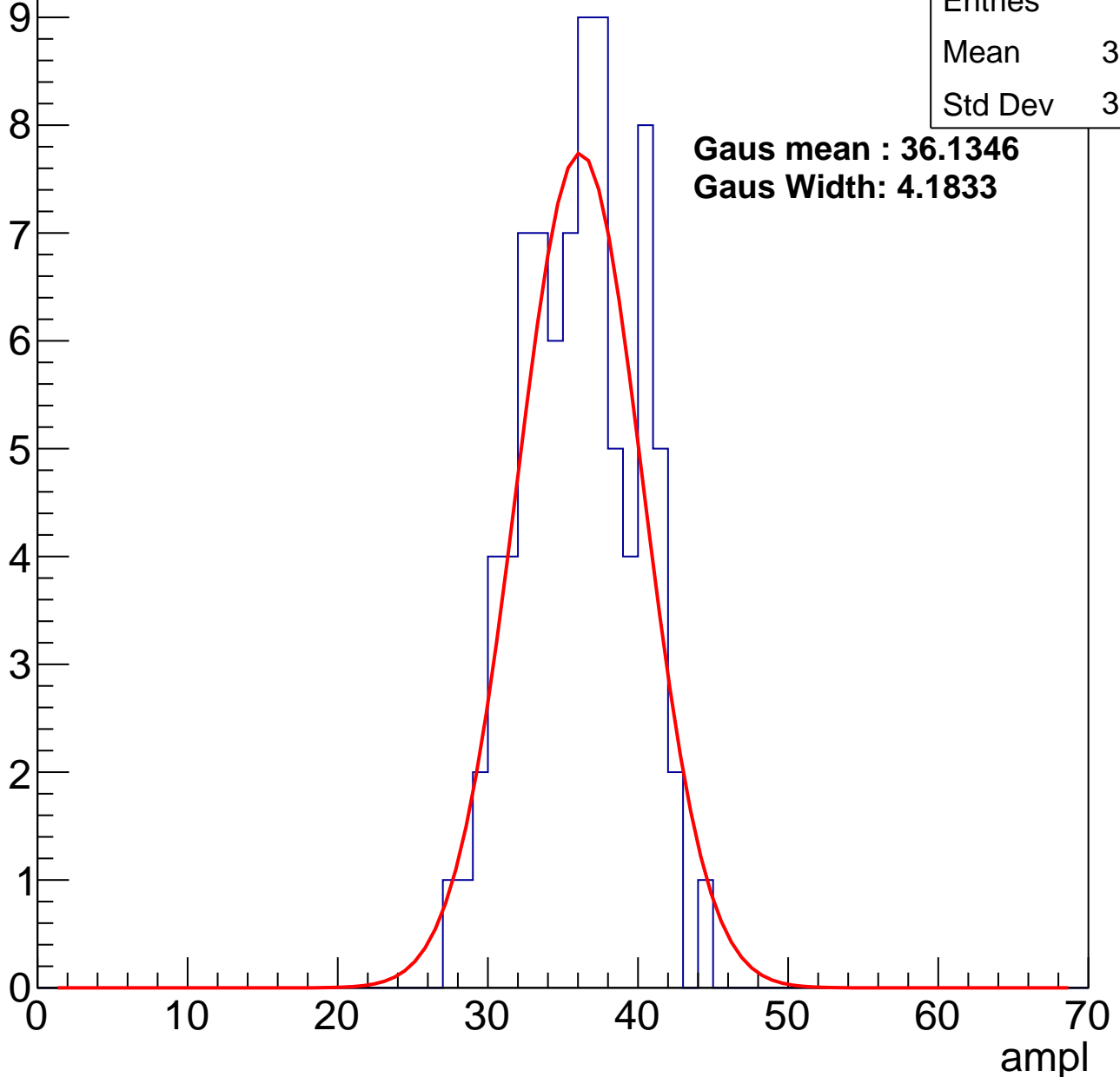
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.57
Std Dev	3.716

**Gaus mean : 36.1346**

**Gaus Width: 4.1833**



# B1L102S, U25-ch43, adc2

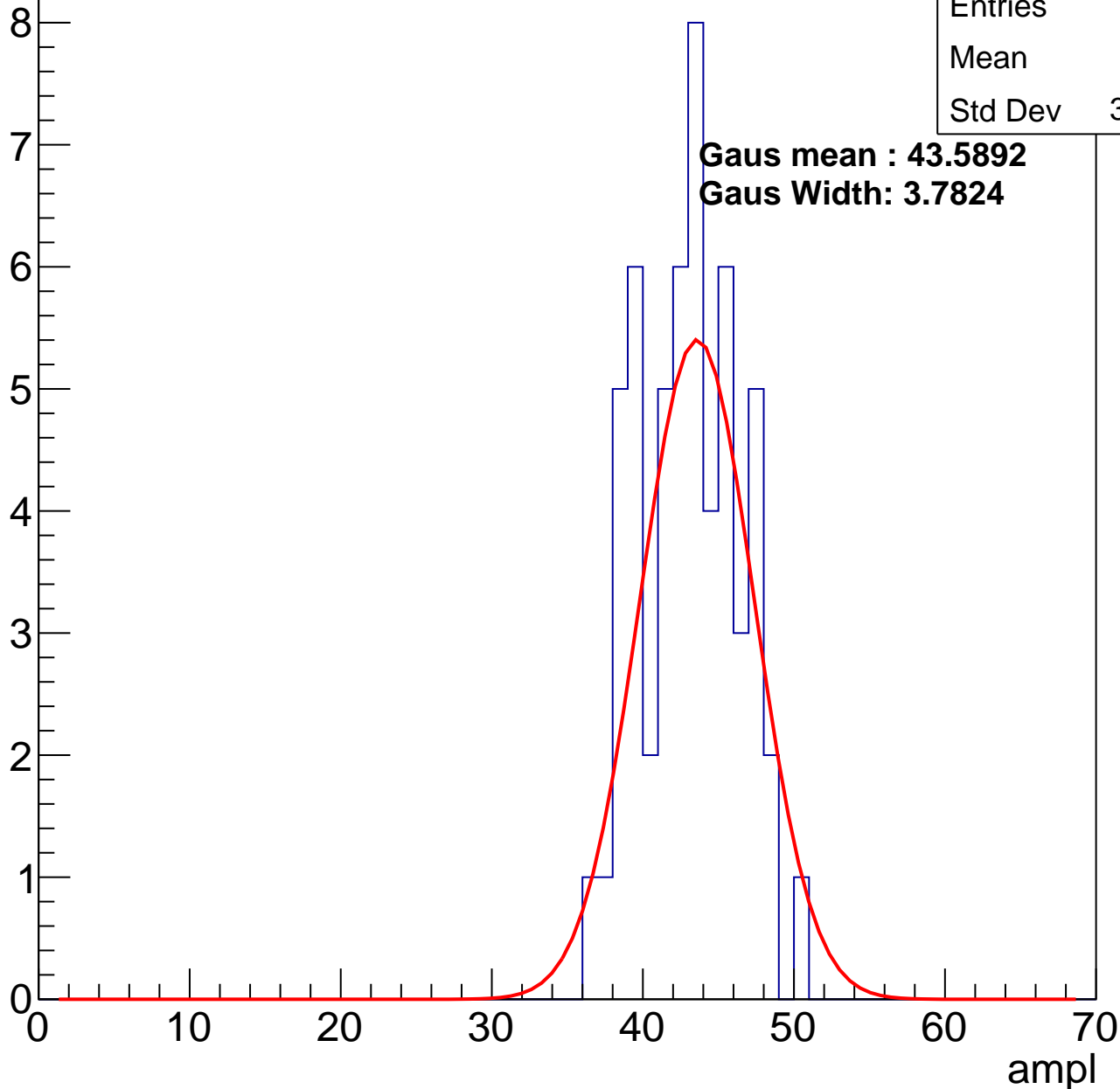
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.6
Std Dev	3.234

**Gaus mean : 43.5892**

**Gaus Width: 3.7824**

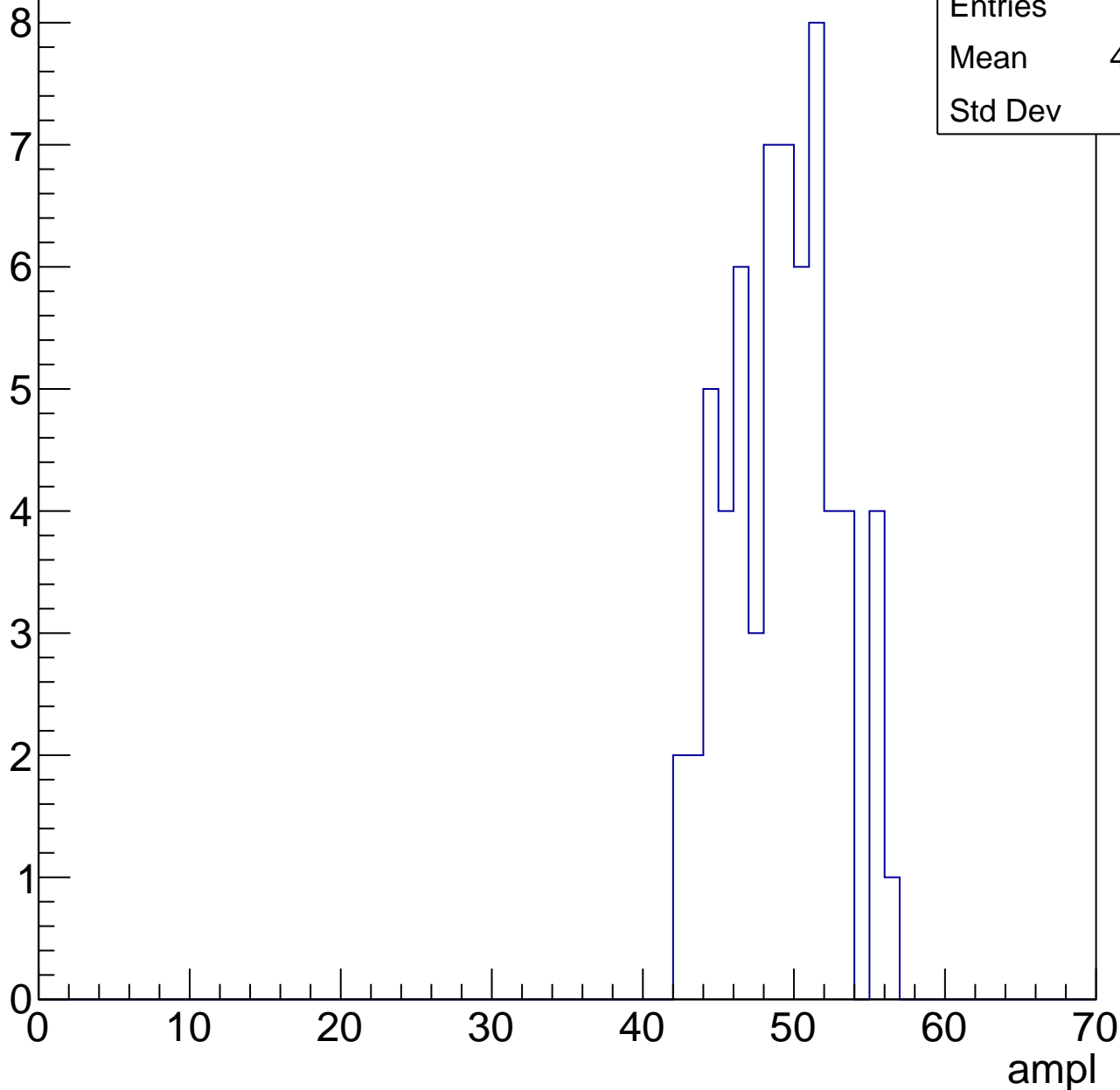


# B1L102S, U25-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.73
Std Dev	3.47



# B1L102S, U25-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

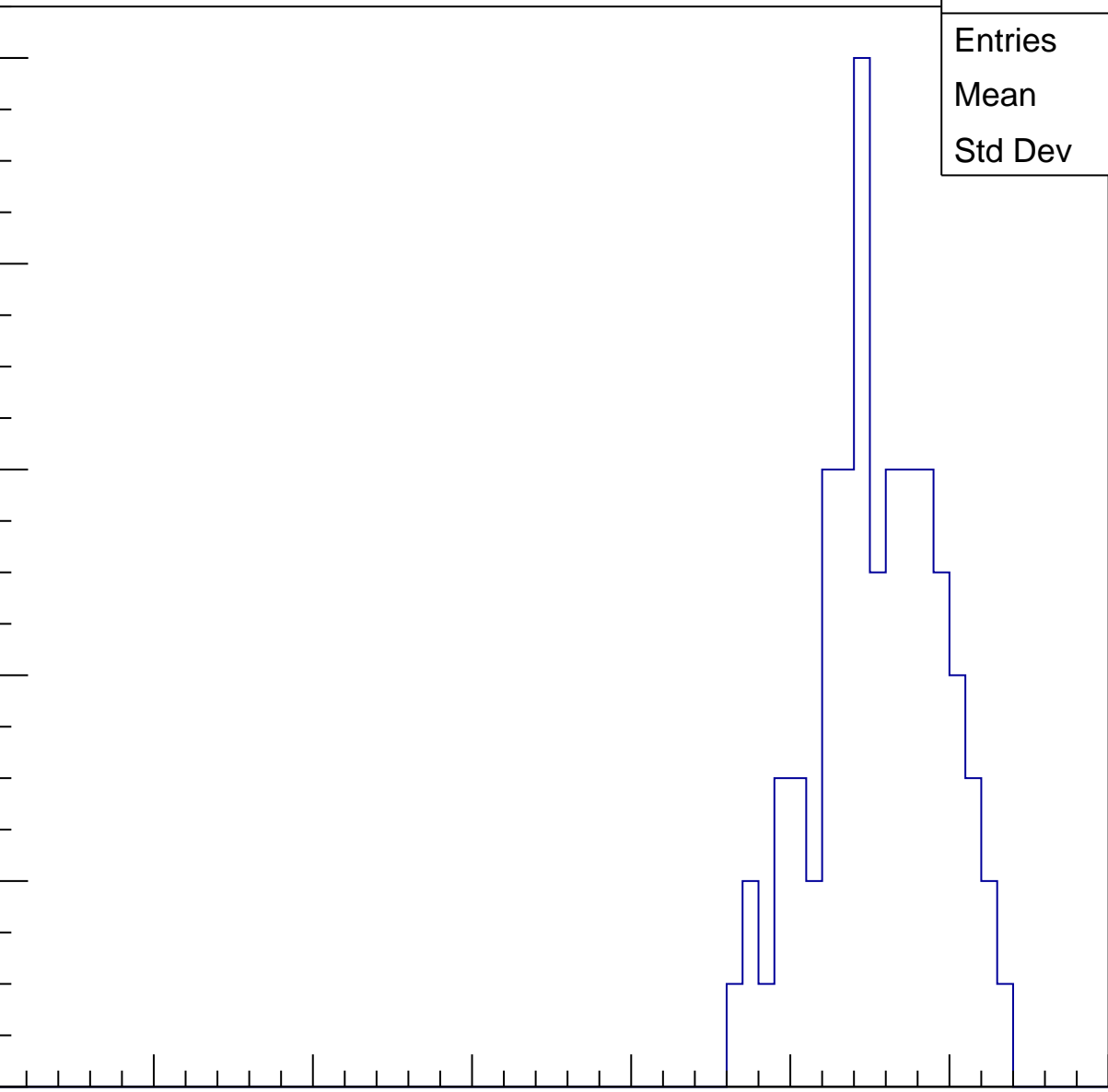
Entries	72
Mean	55.04
Std Dev	3.9

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

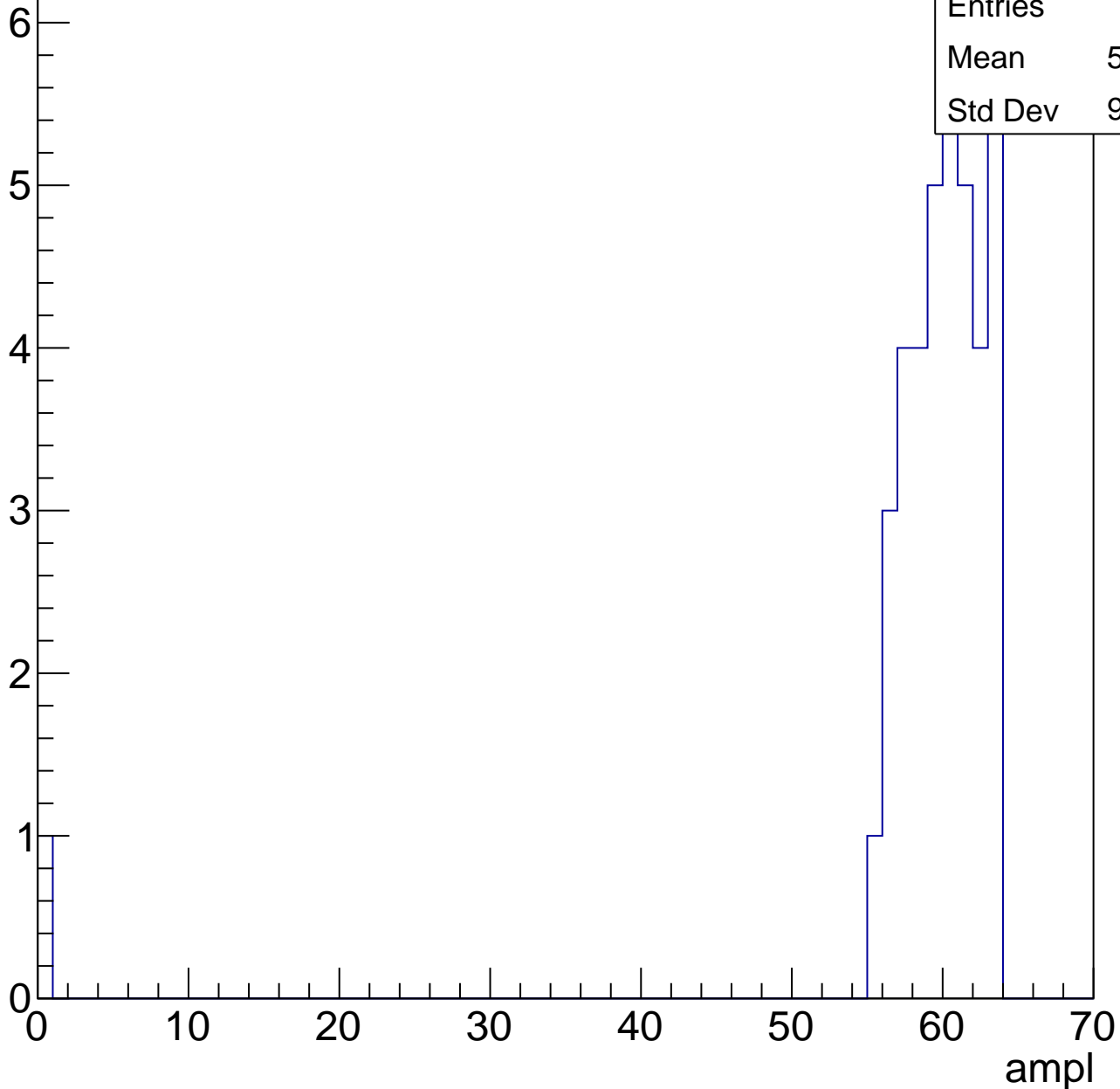


# B1L102S, U25-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

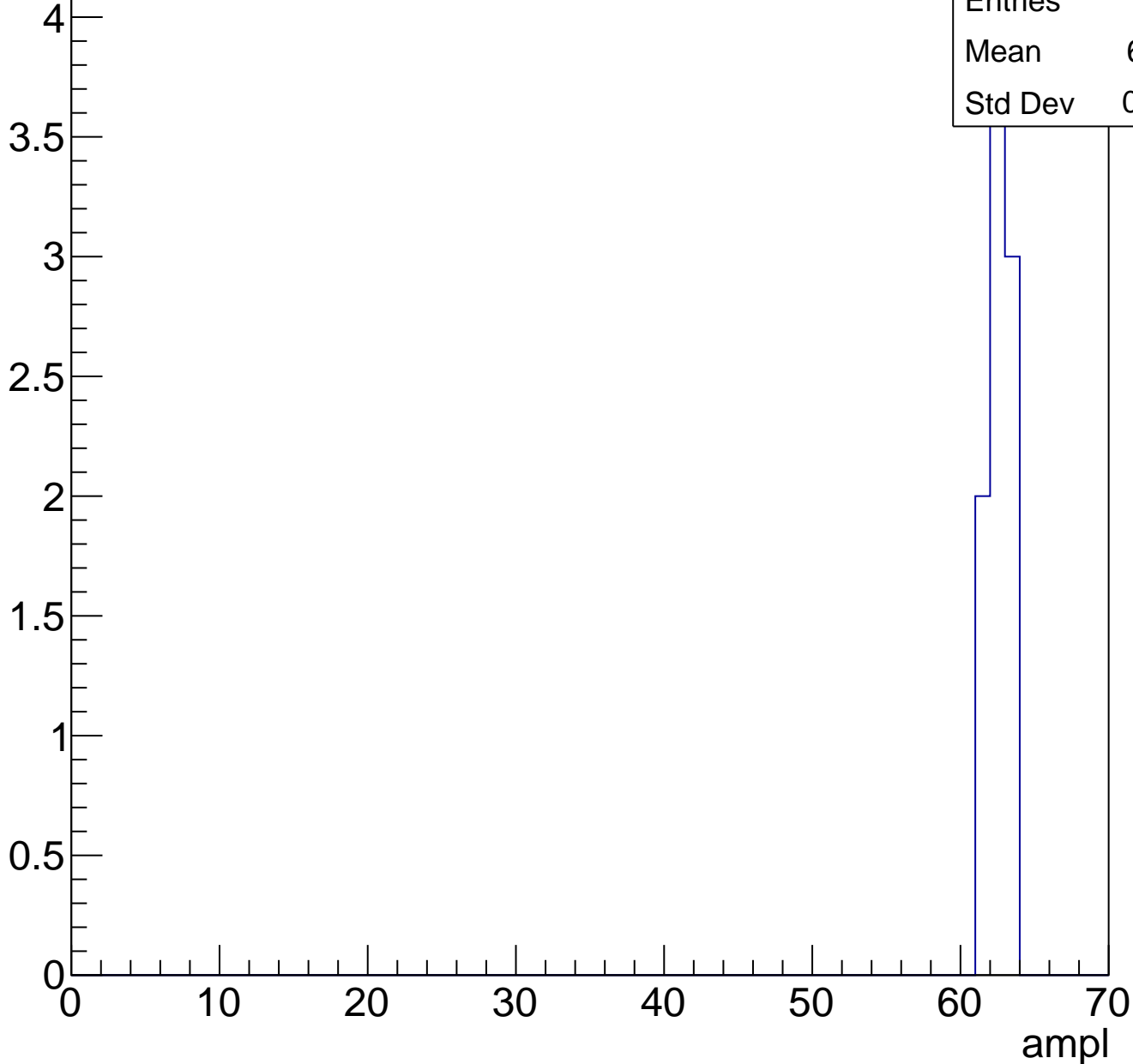
Entries	39
Mean	58.18
Std Dev	9.708



# B1L102S, U25-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

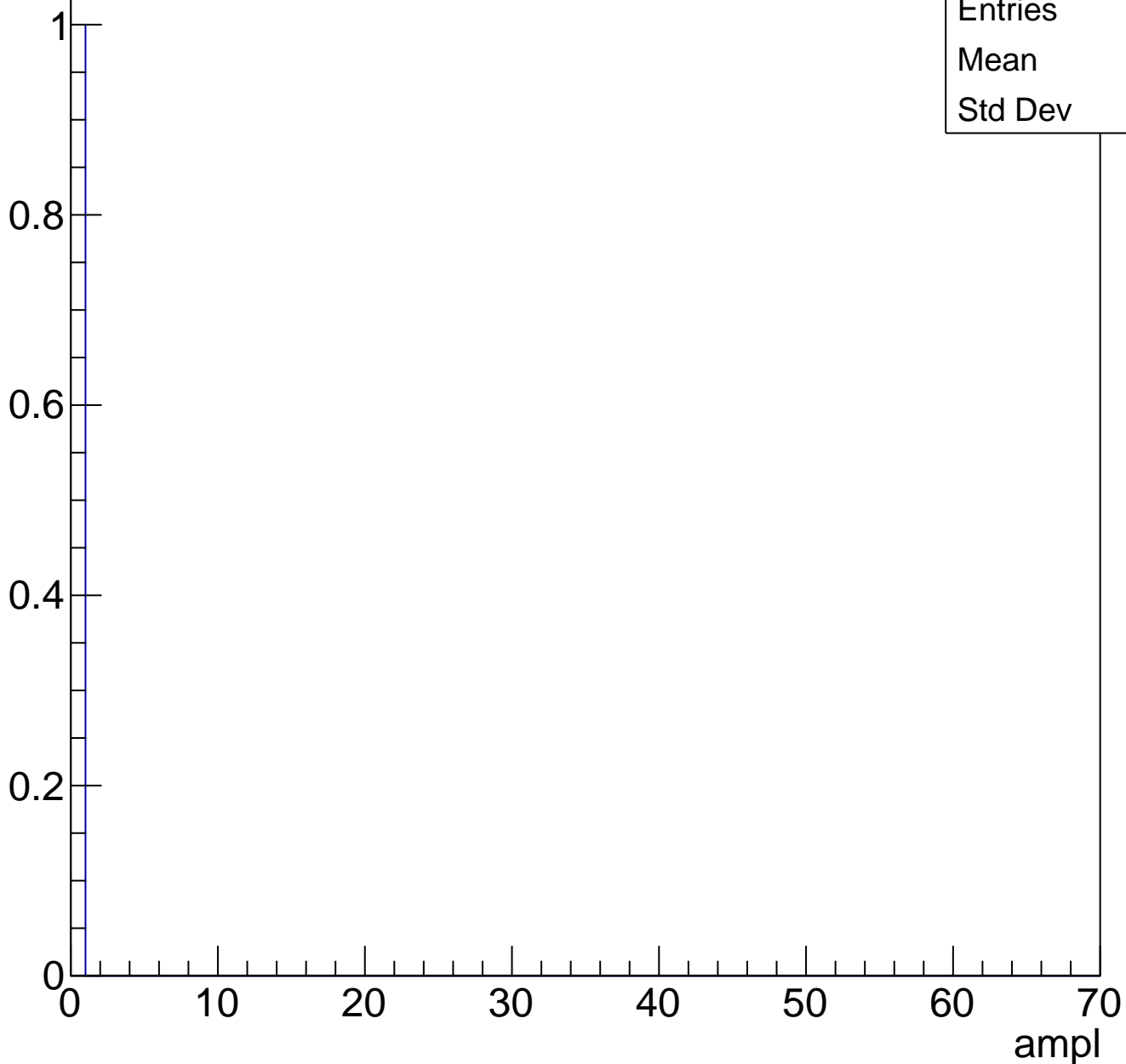




# B1L102S, U25-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch44, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	65
Mean	28.23
Std Dev	3.441

**Gaus mean : 29.4726**

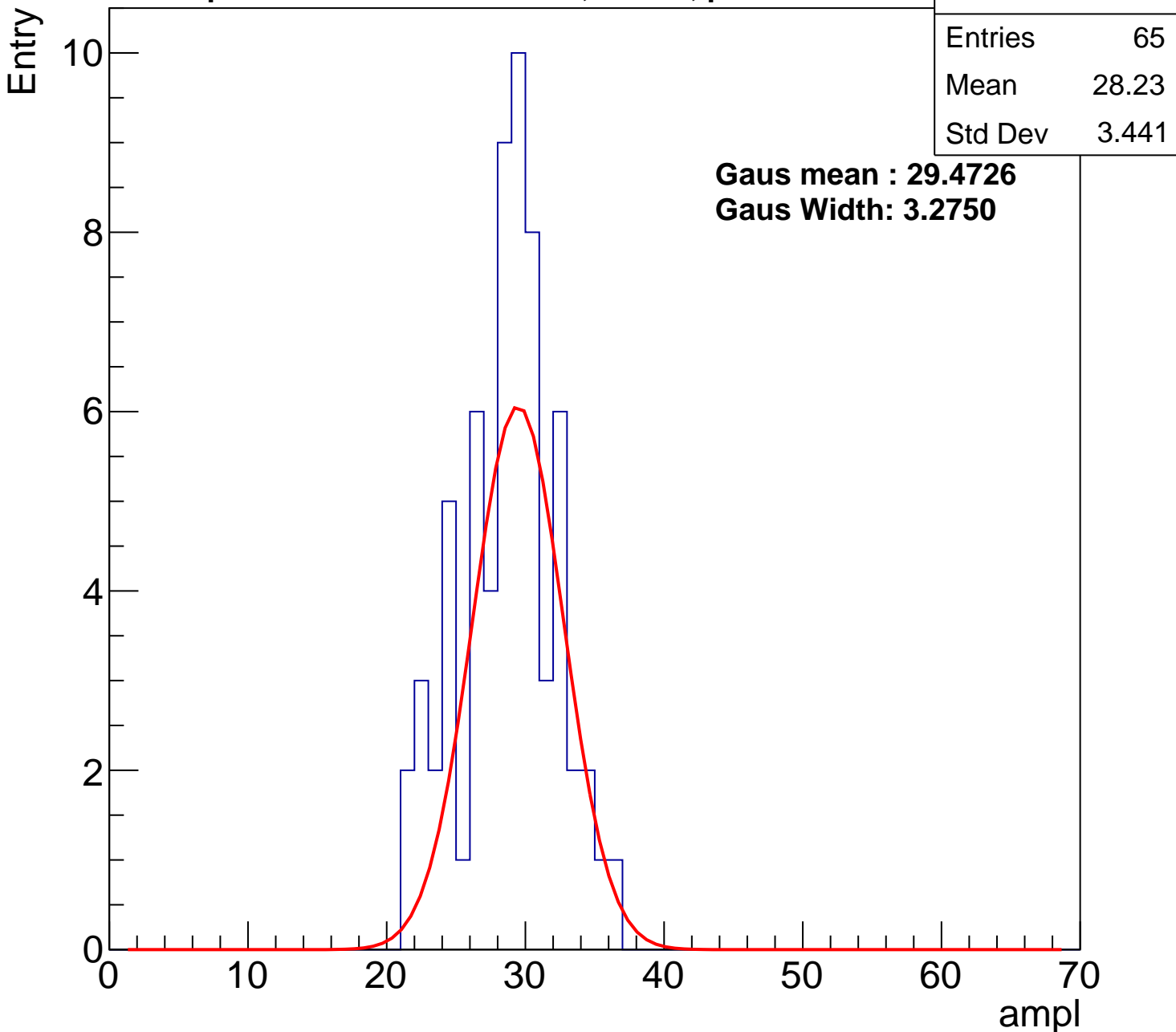
**Gaus Width: 3.2750**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch44, adc1

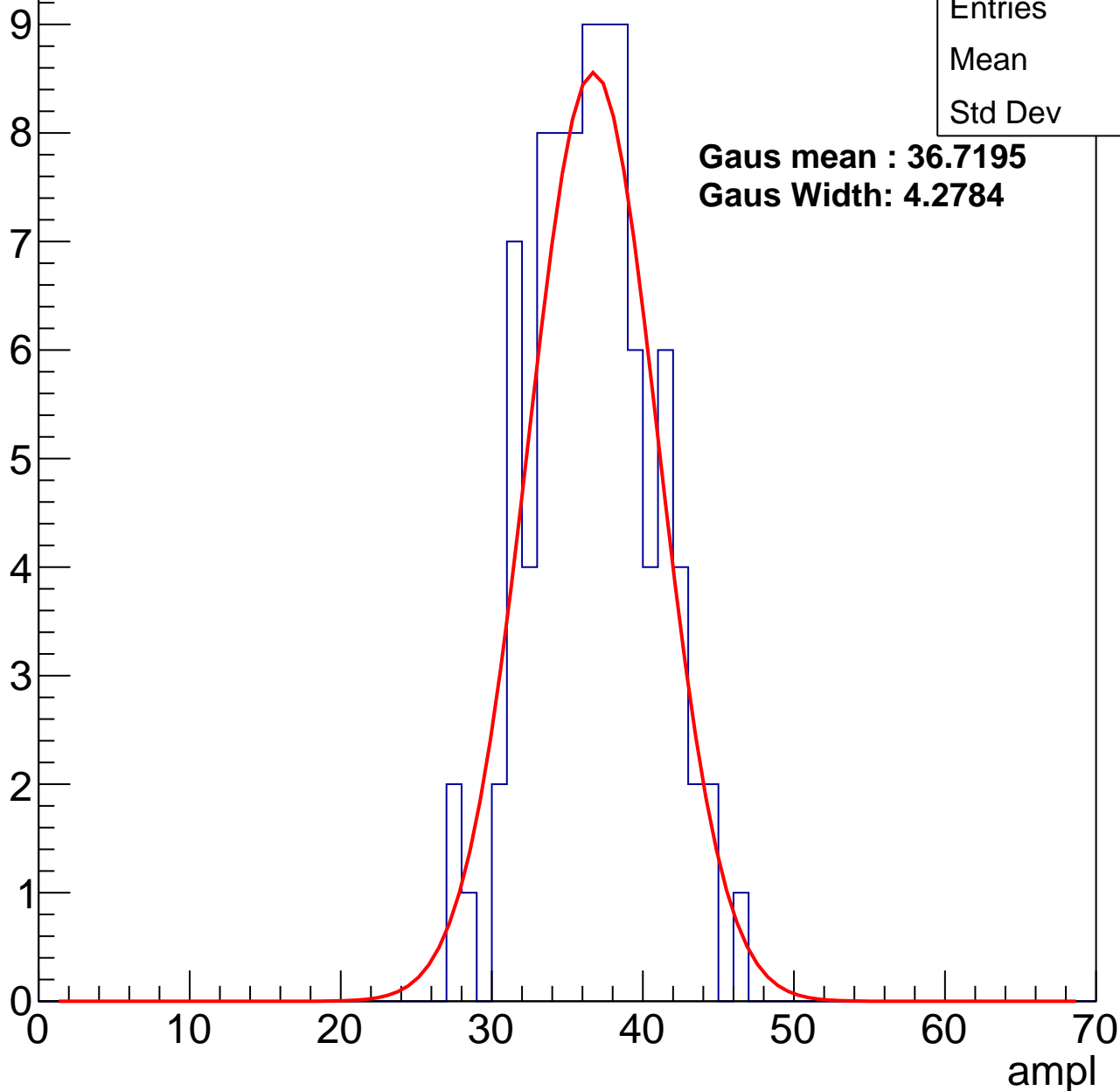
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	92
Mean	36.2
Std Dev	3.94

**Gaus mean : 36.7195**

**Gaus Width: 4.2784**



# B1L102S, U25-ch44, adc2

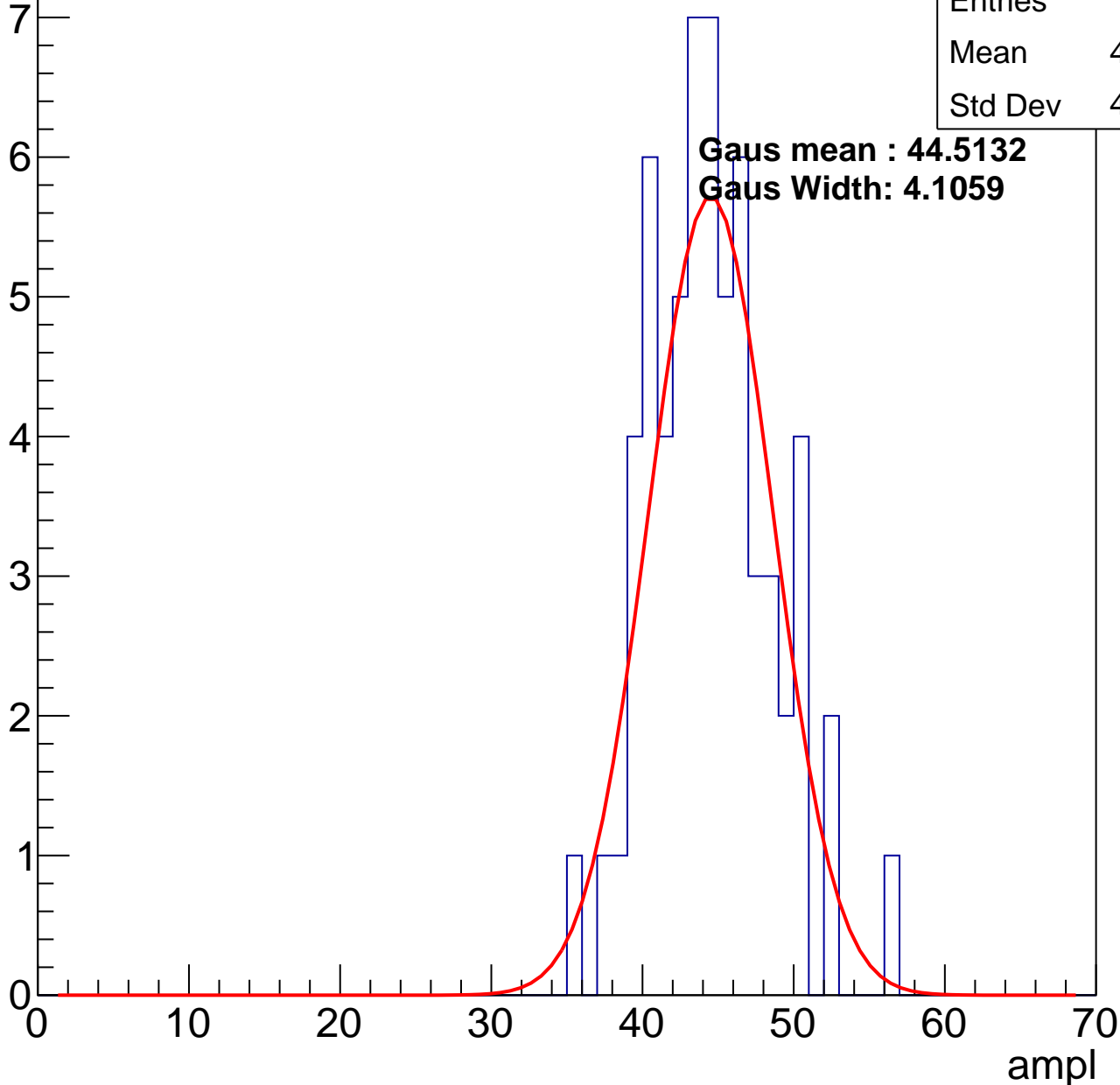
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.08
Std Dev	4.005

**Gaus mean : 44.5132**

**Gaus Width: 4.1059**

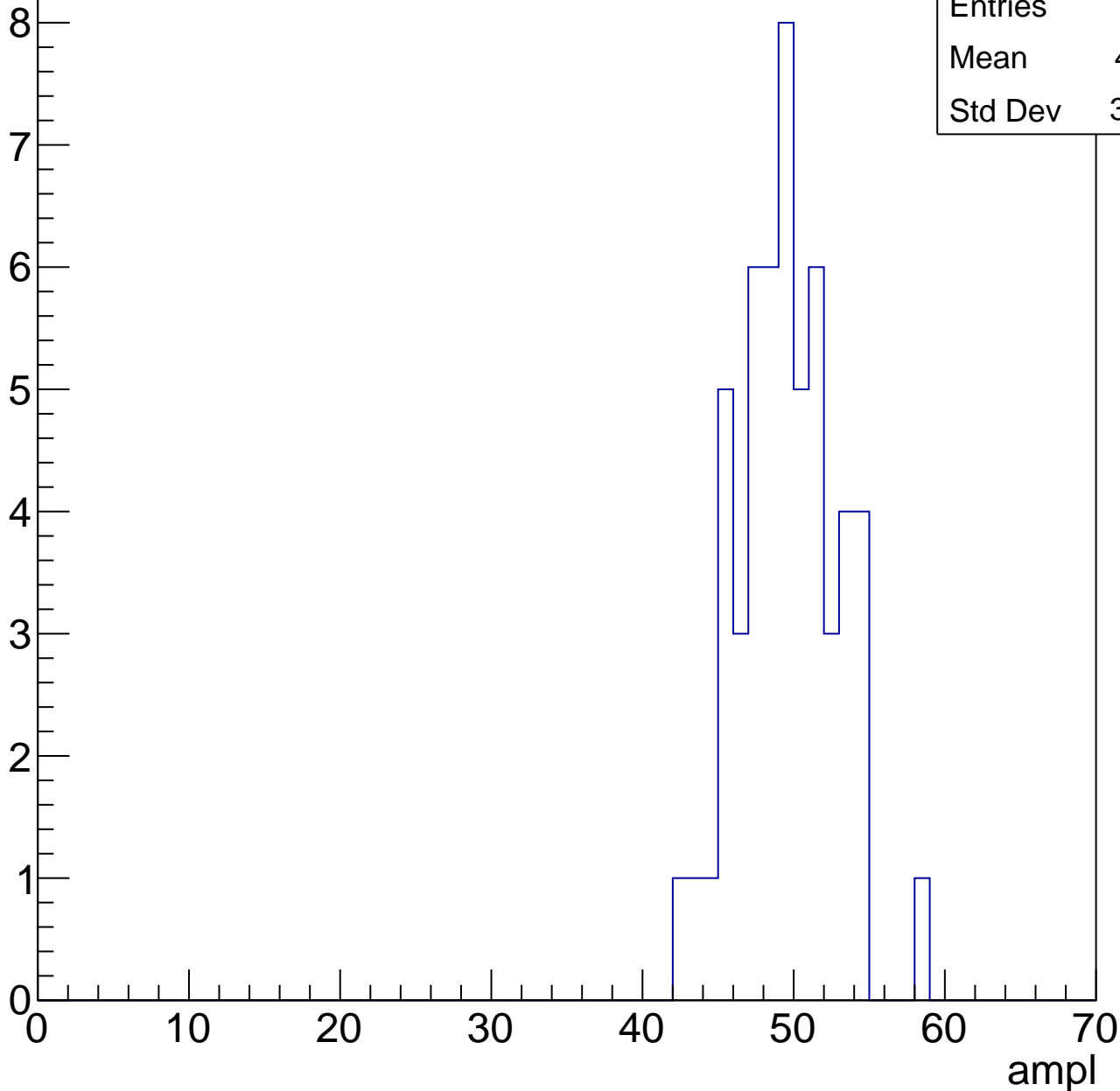


# B1L102S, U25-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	49.11
Std Dev	3.184

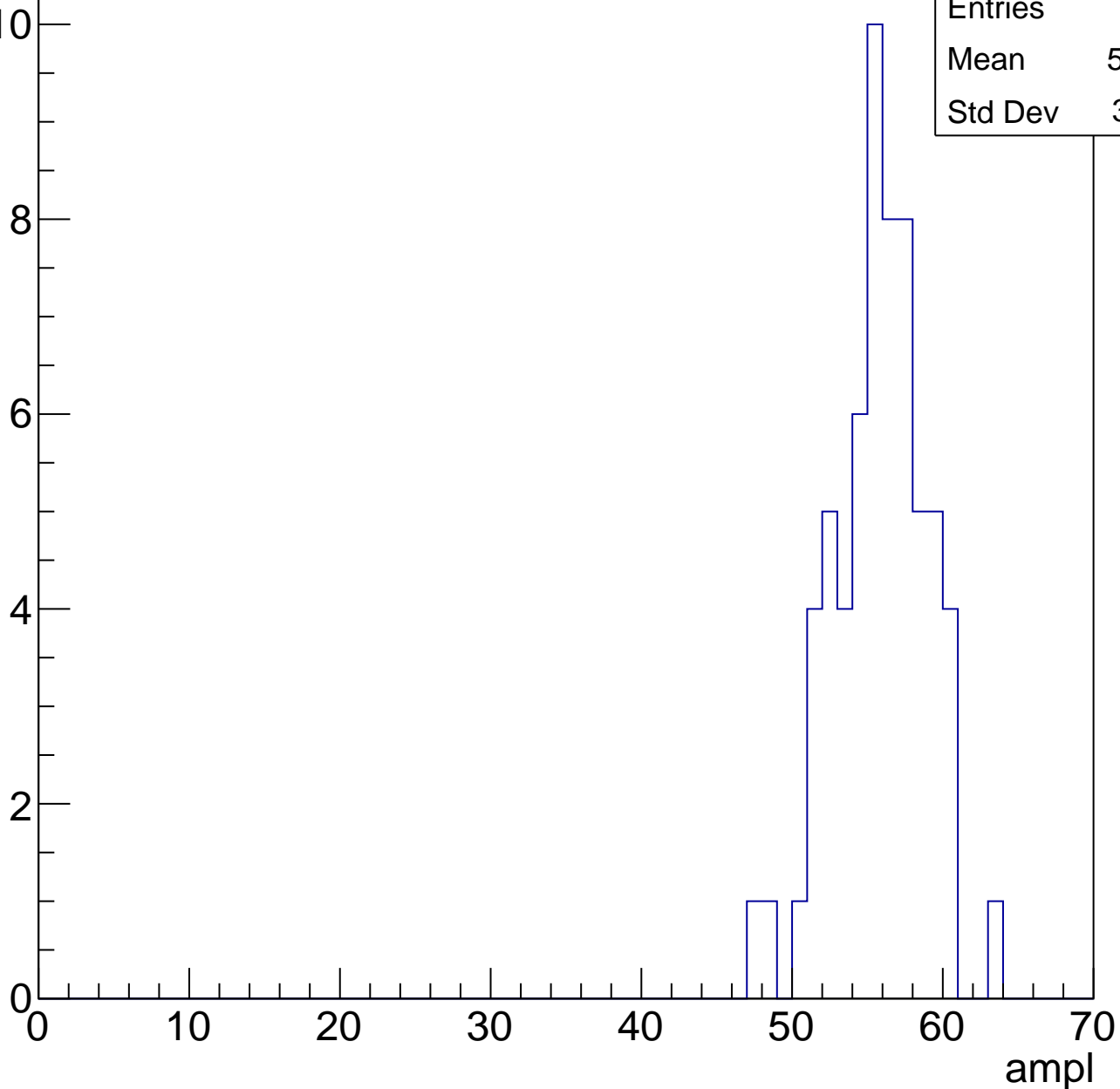


# B1L102S, U25-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.35
Std Dev	3.061

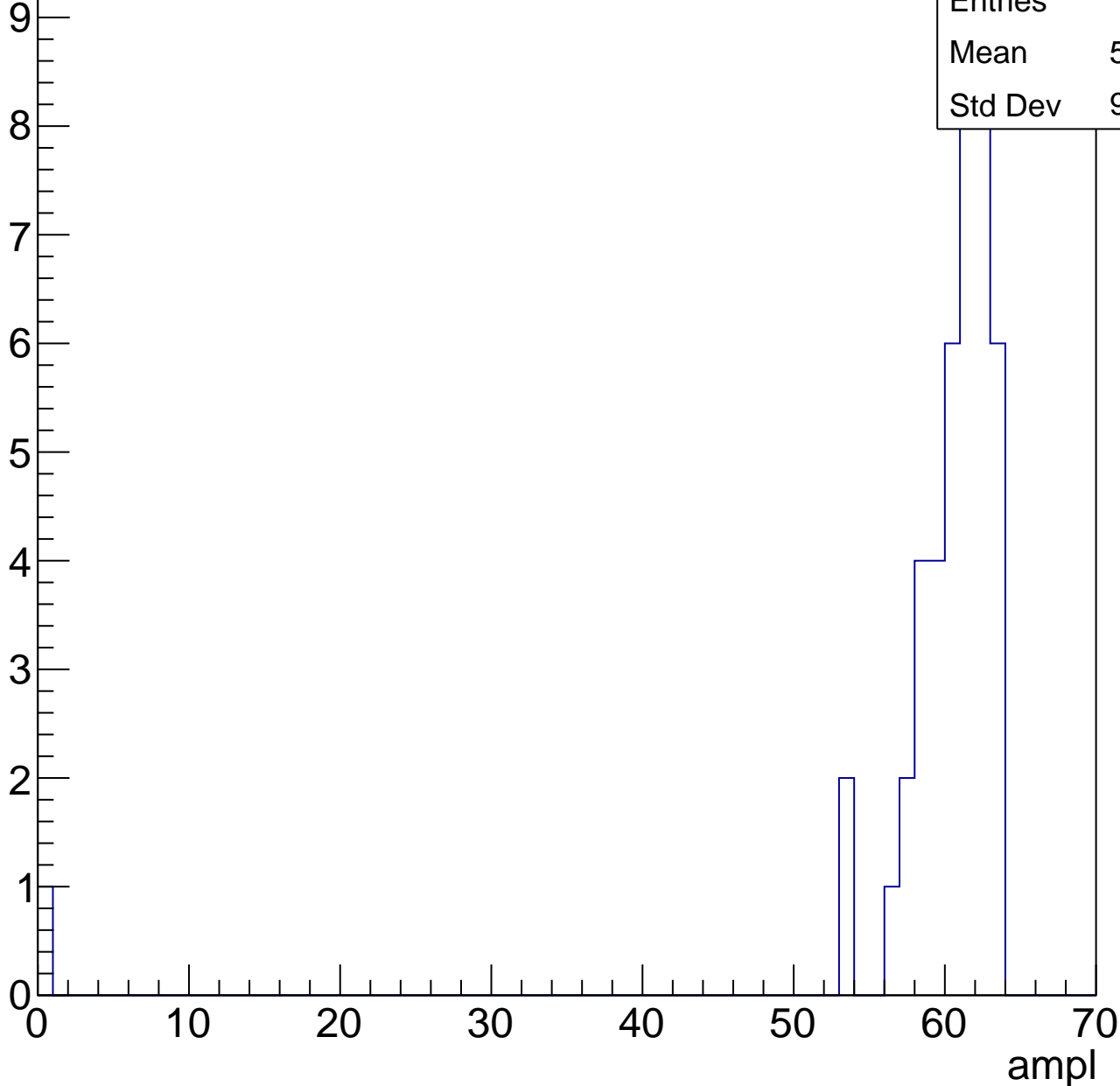


# B1L102S, U25-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

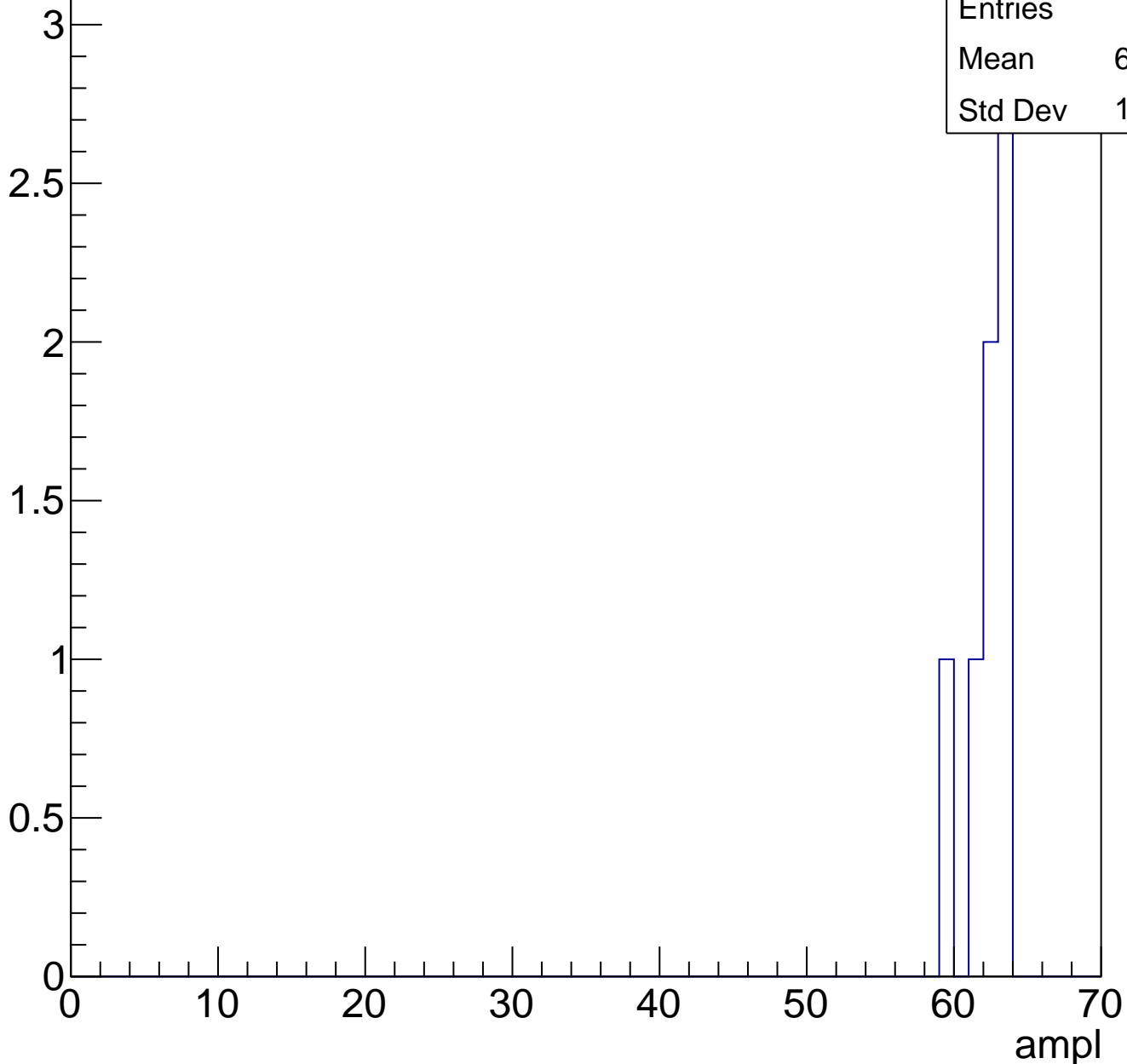
Entries	43
Mean	58.77
Std Dev	9.378



# B1L102S, U25-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L102S, U25-ch45, adc0

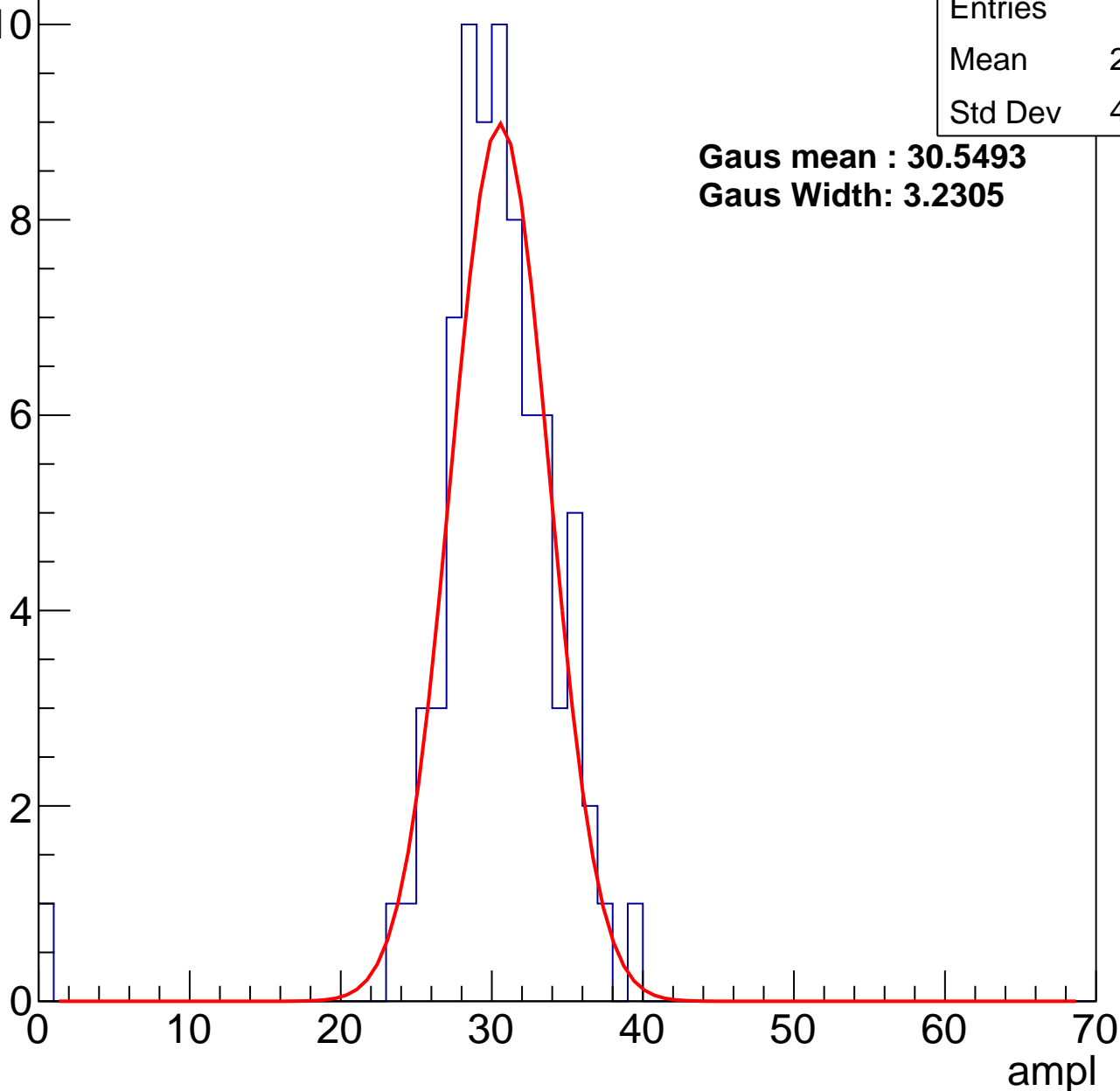
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.78
Std Dev	4.667

**Gaus mean : 30.5493**

**Gaus Width: 3.2305**



# B1L102S, U25-ch45, adc1

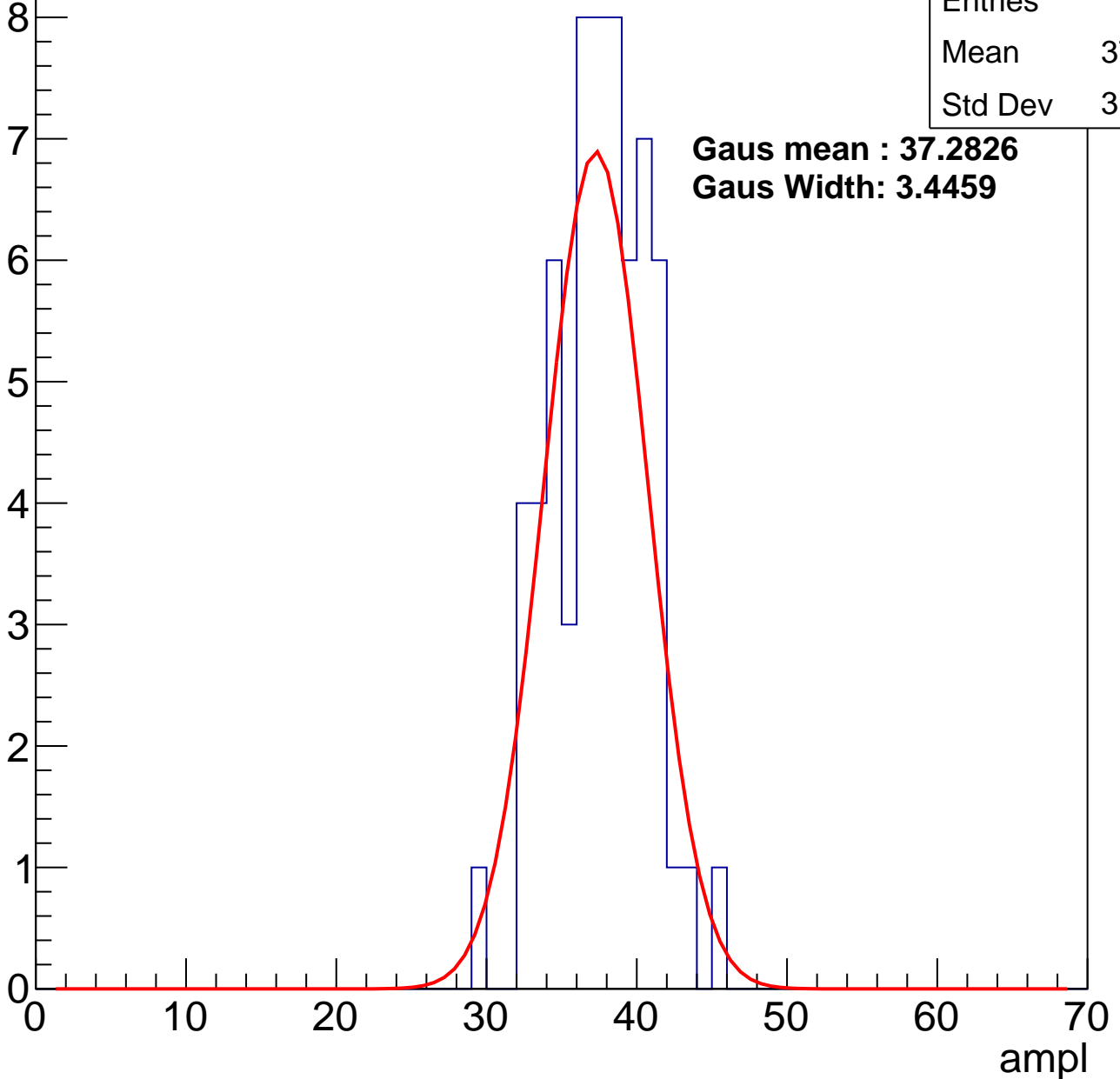
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.12
Std Dev	3.105

**Gaus mean : 37.2826**

**Gaus Width: 3.4459**



# B1L102S, U25-ch45, adc2

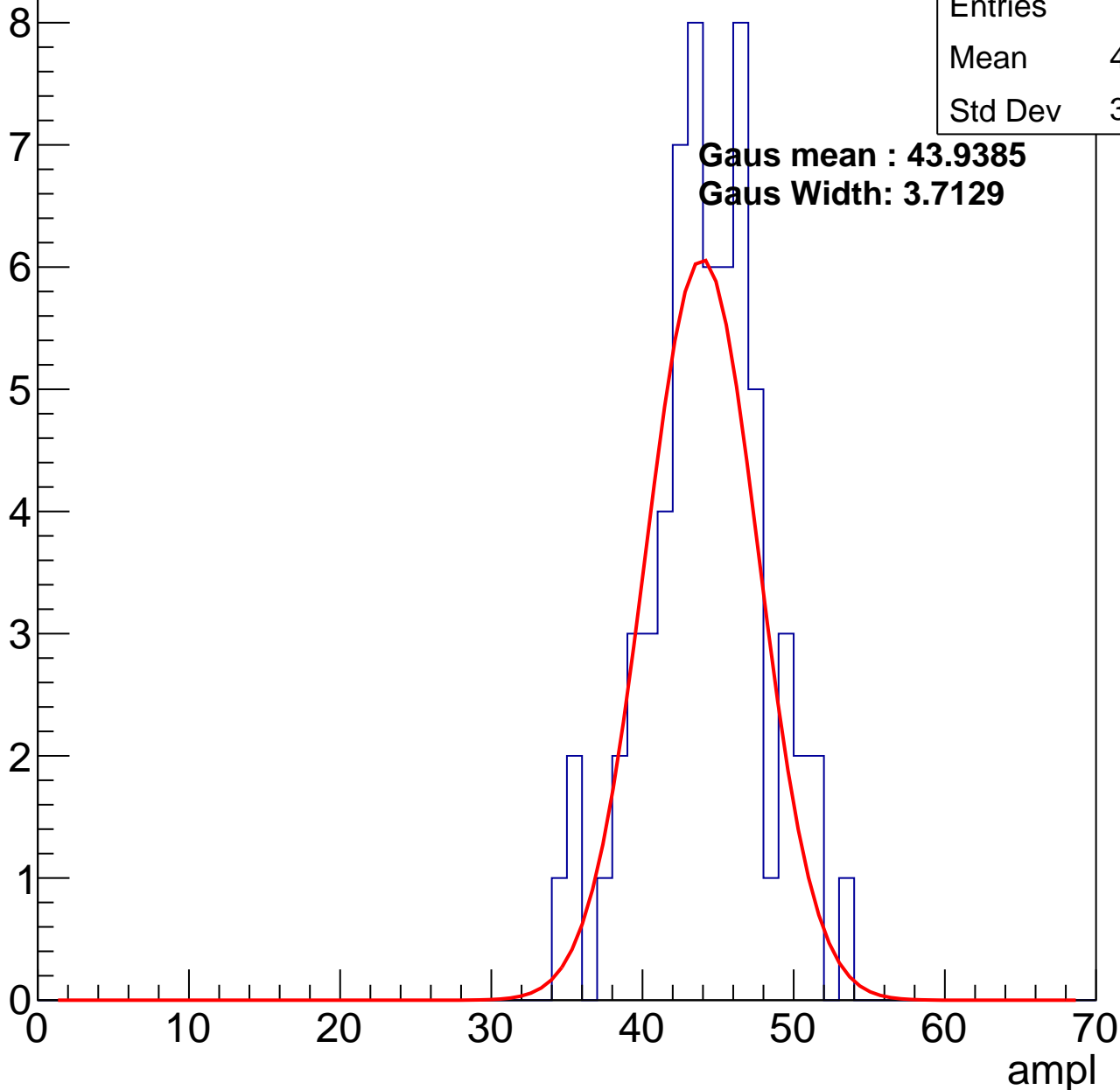
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.74
Std Dev	3.919

**Gaus mean : 43.9385**

**Gaus Width: 3.7129**

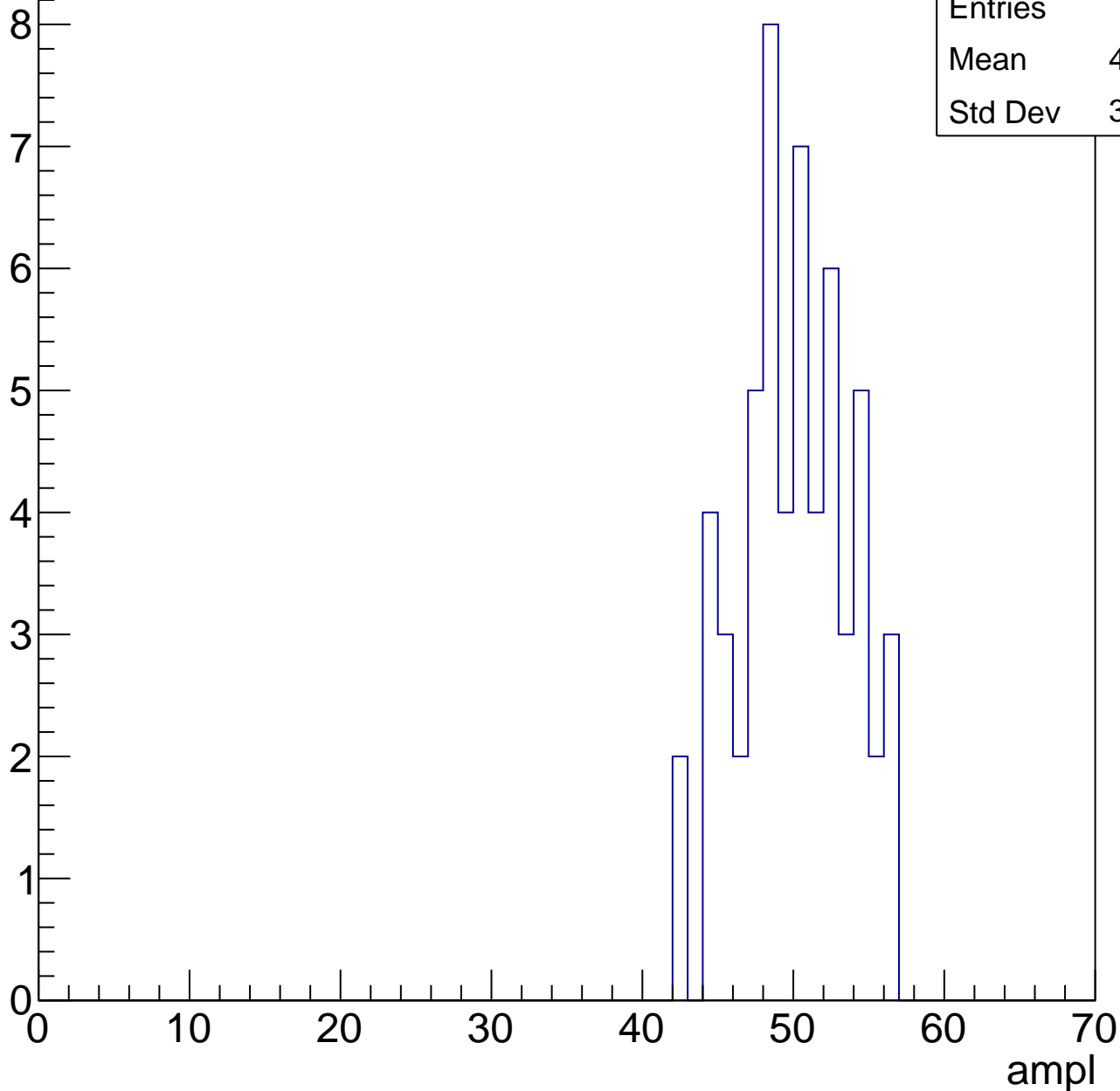


# B1L102S, U25-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

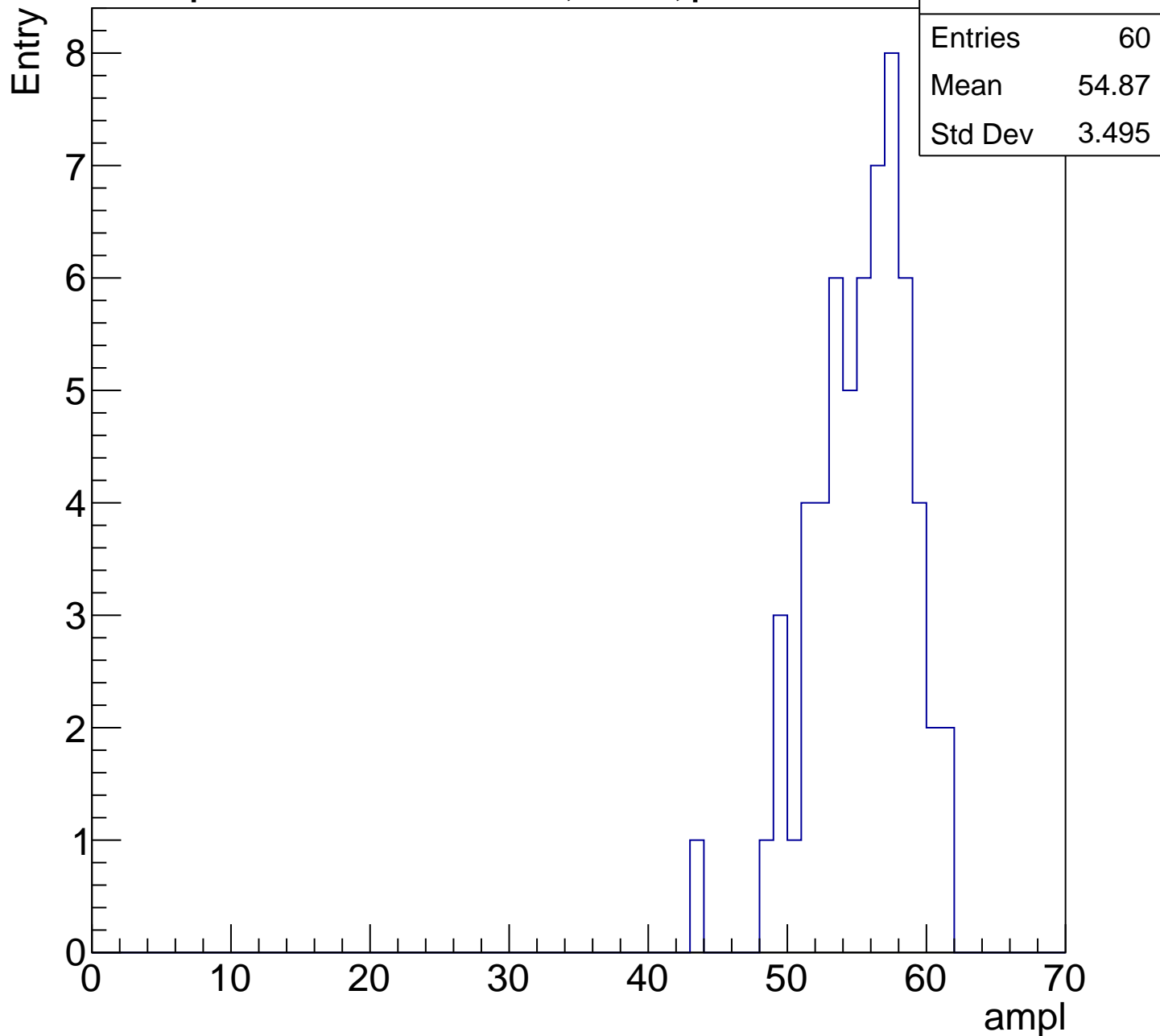
Entry

Entries	58
Mean	49.57
Std Dev	3.577



# B1L102S, U25-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

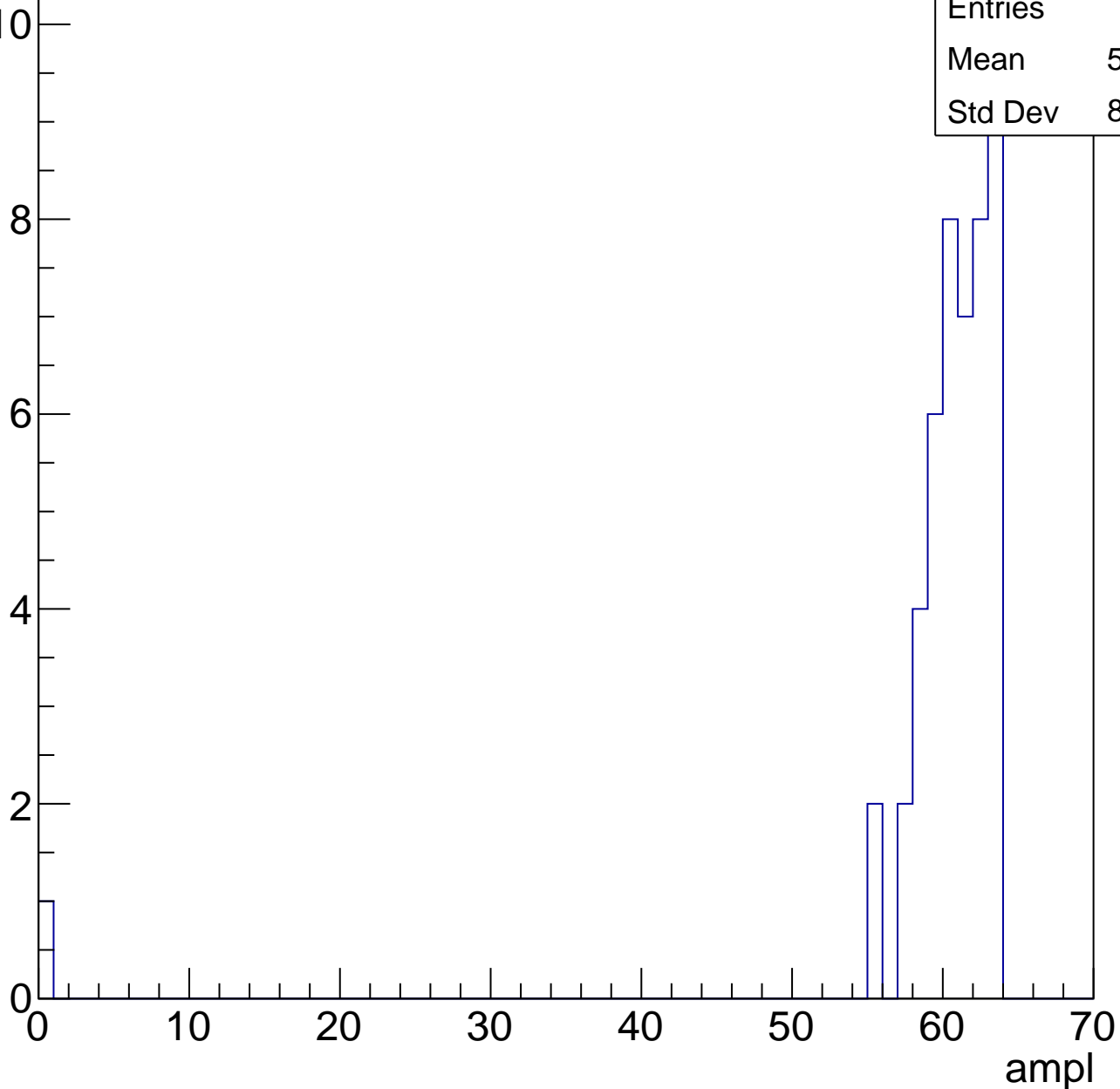


# B1L102S, U25-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	59.23
Std Dev	8.886



# B1L102S, U25-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch46, adc0

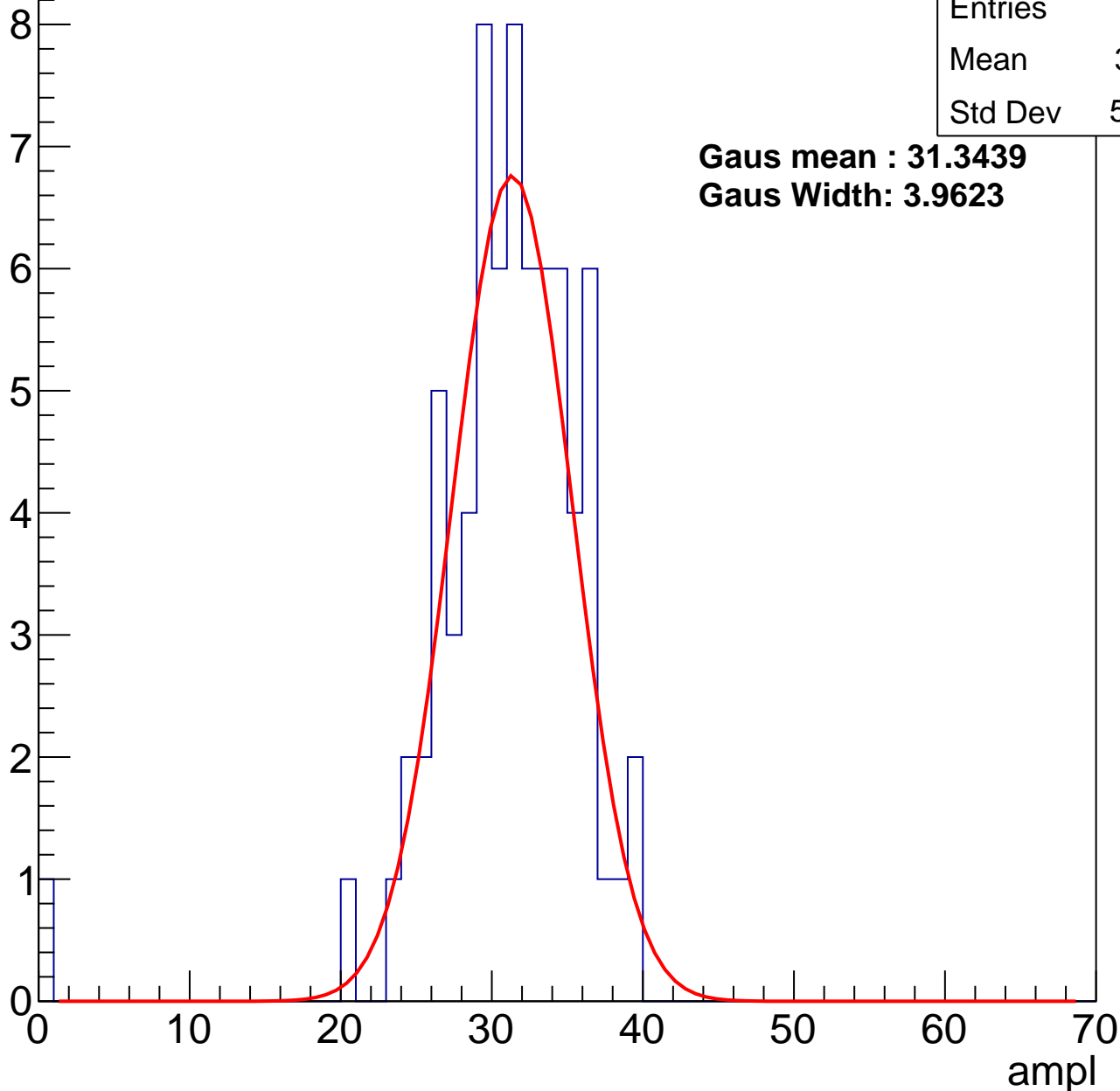
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.51
Std Dev	5.307

**Gaus mean : 31.3439**

**Gaus Width: 3.9623**



# B1L102S, U25-ch46, adc1

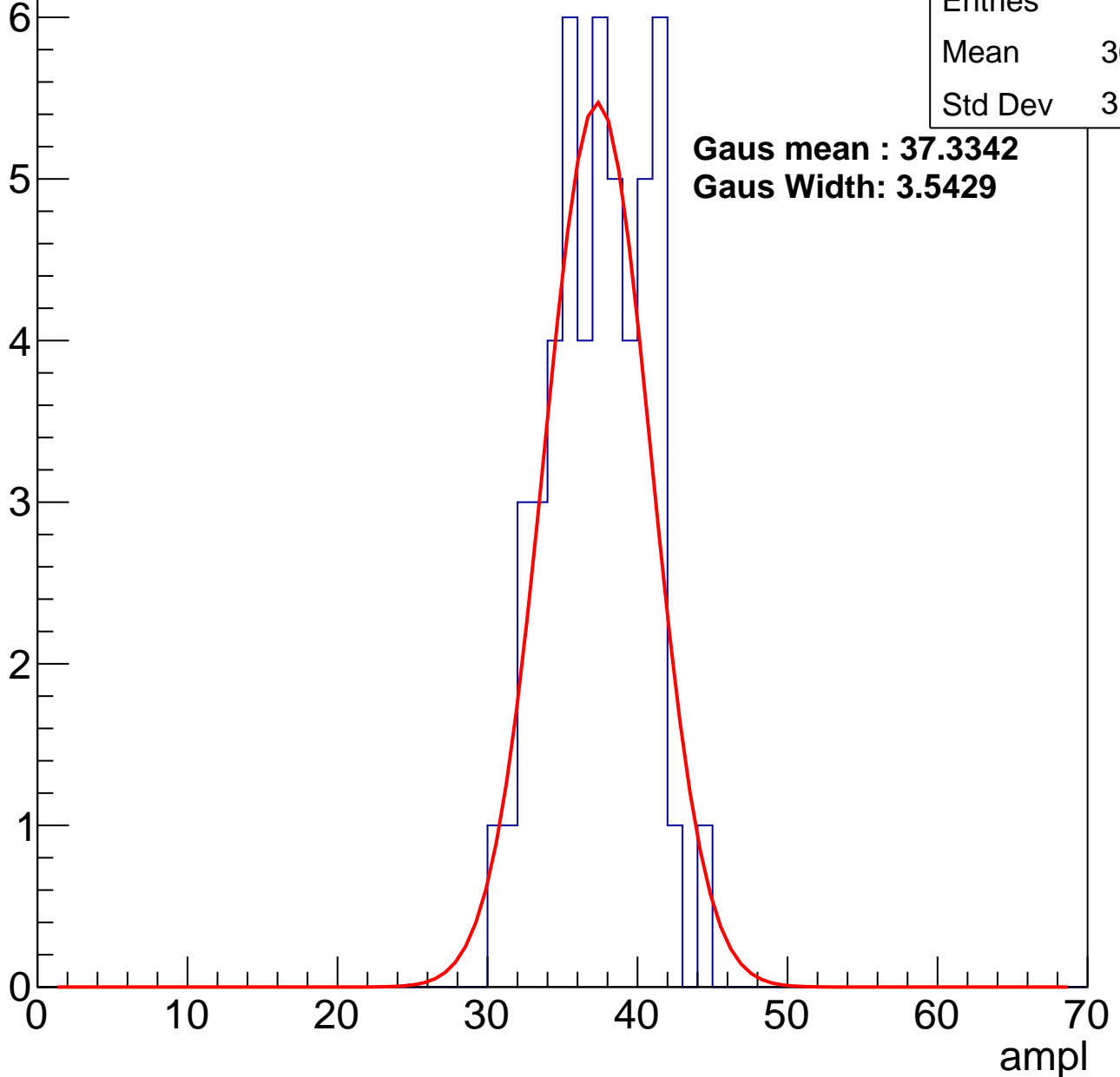
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	36.92
Std Dev	3.186

**Gaus mean : 37.3342**

**Gaus Width: 3.5429**



# B1L102S, U25-ch46, adc2

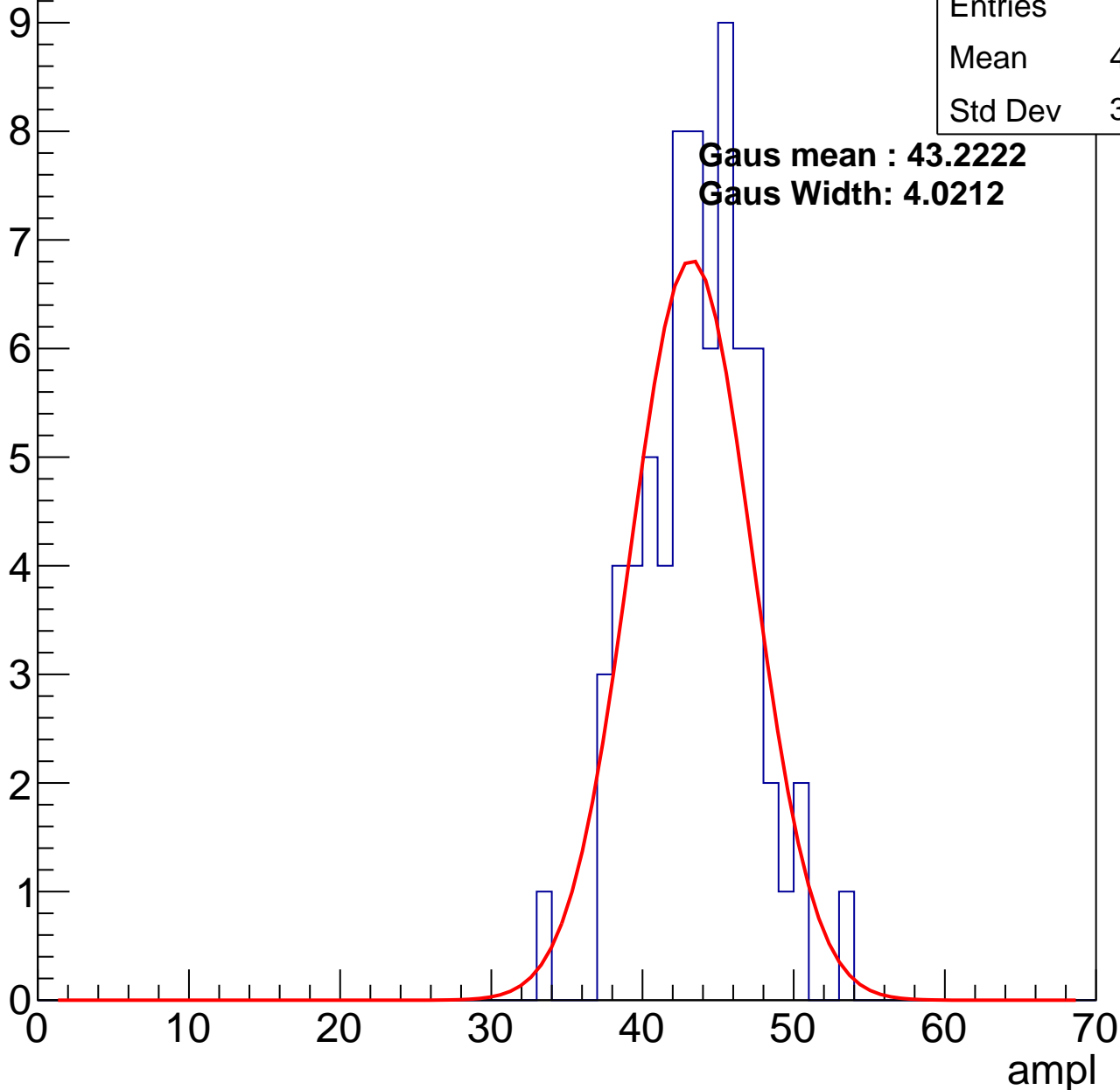
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.16
Std Dev	3.616

**Gaus mean : 43.2222**

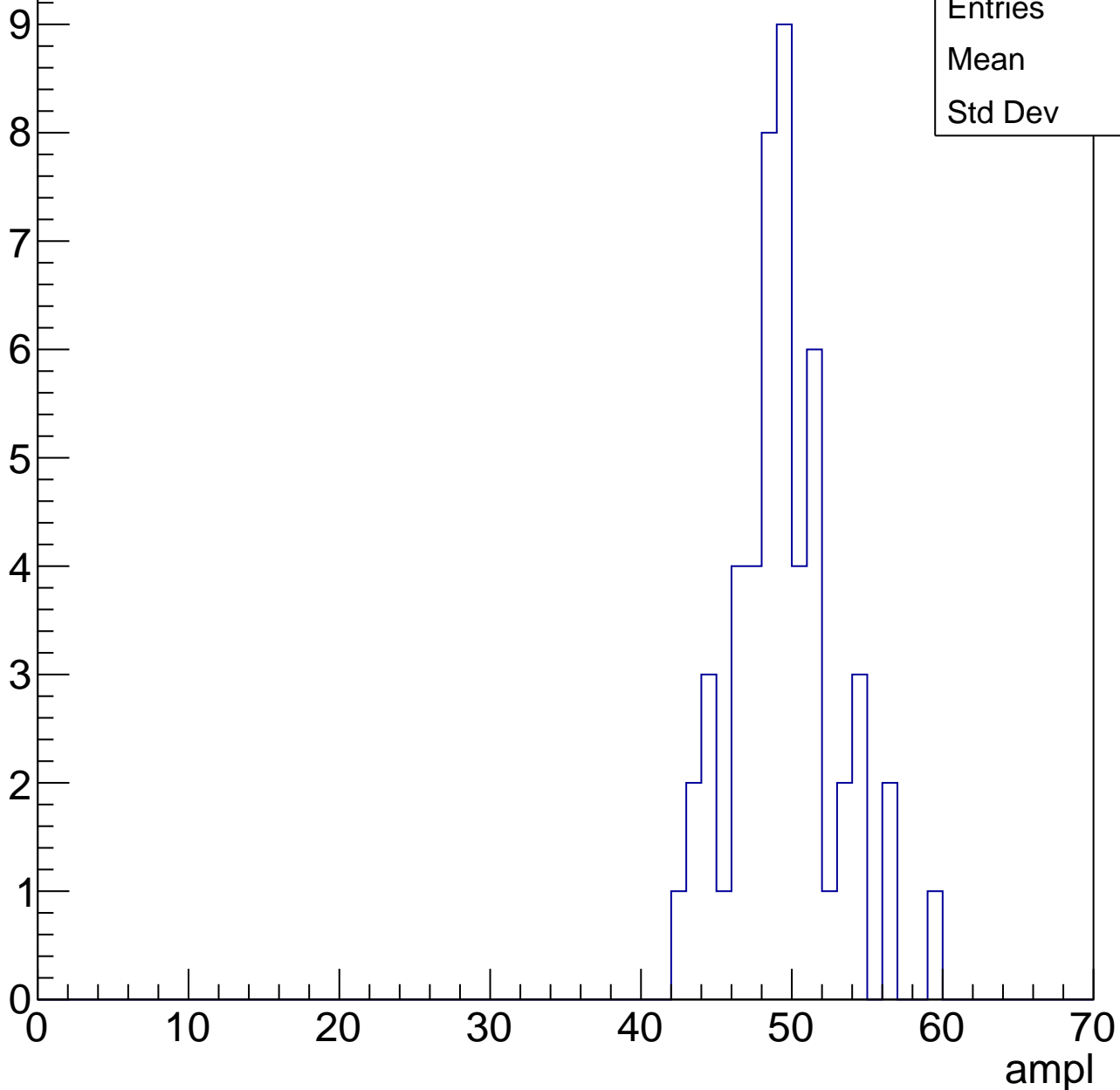
**Gaus Width: 4.0212**



# B1L102S, U25-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	83
Mean	56.01
Std Dev	3.524

Entry

10

8

6

4

2

0

0

10

20

30

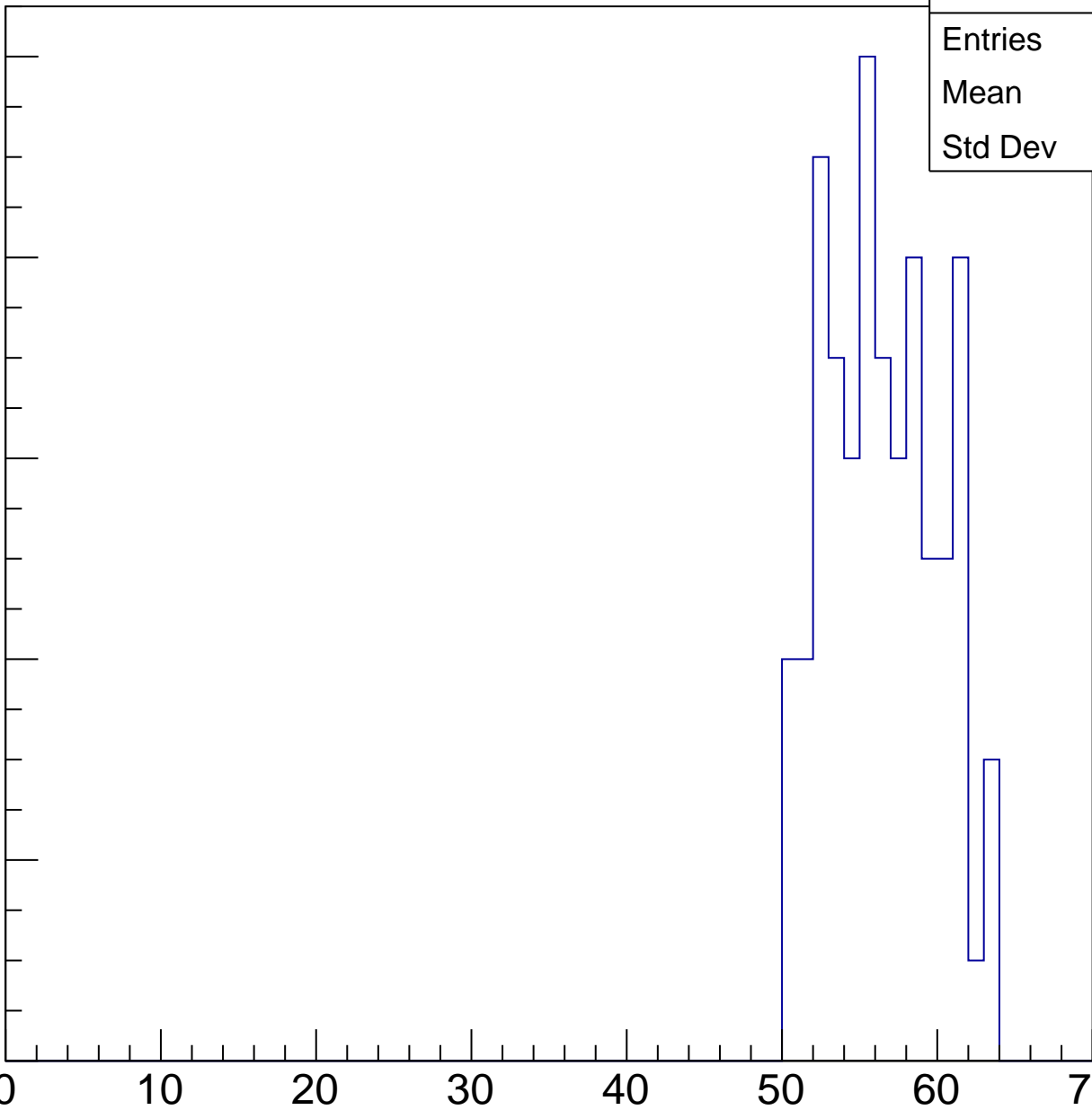
40

50

60

ampl

70

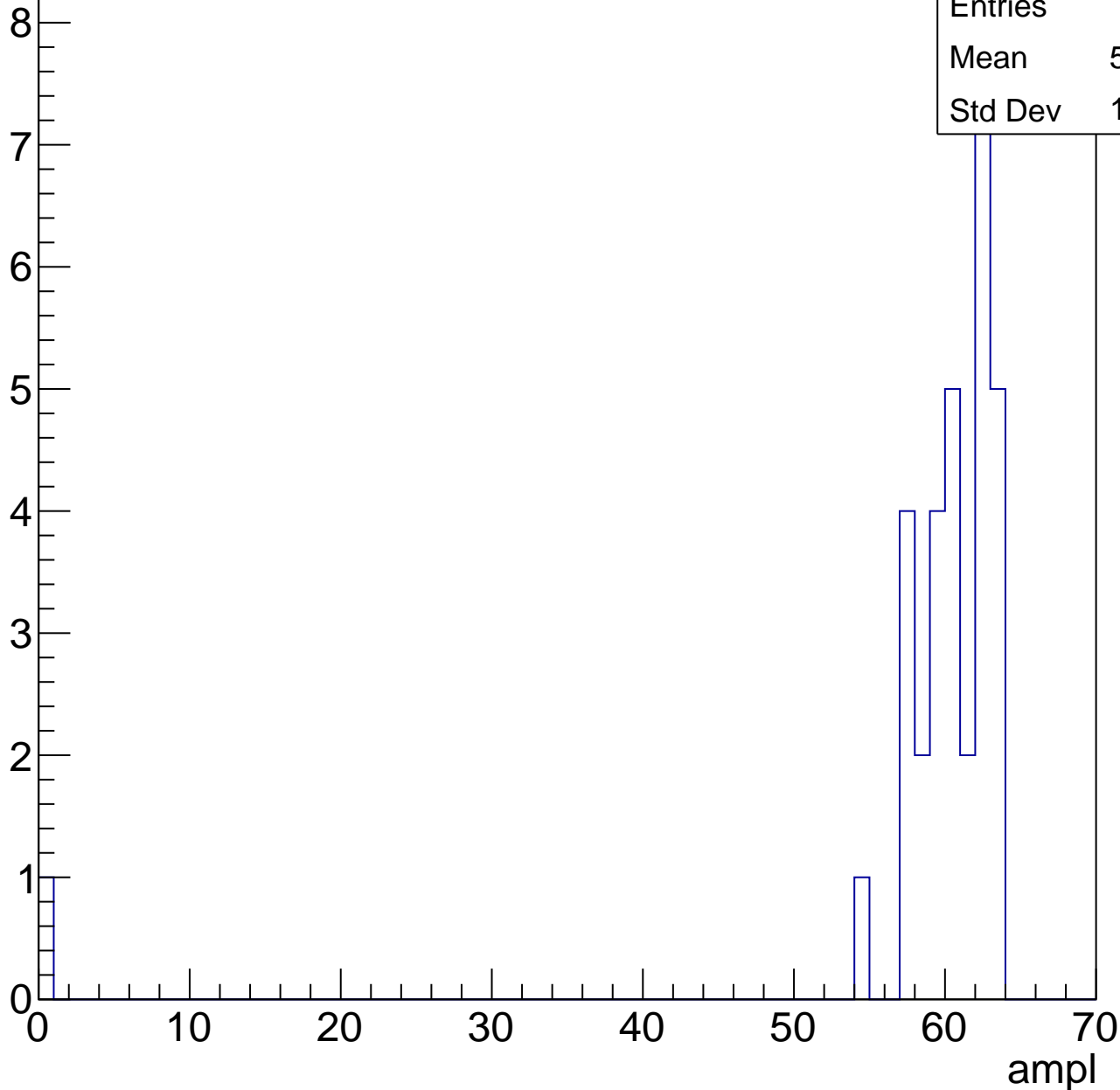


# B1L102S, U25-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	32
Mean	58.34
Std Dev	10.72



# B1L102S, U25-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61
Std Dev	2.915

ampl

0 10 20 30 40 50 60 70

0.2  
0.4  
0.6  
0.8  
1  
1.2  
1.4  
1.6  
1.8  
2



# B1L102S, U25-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch47, adc0

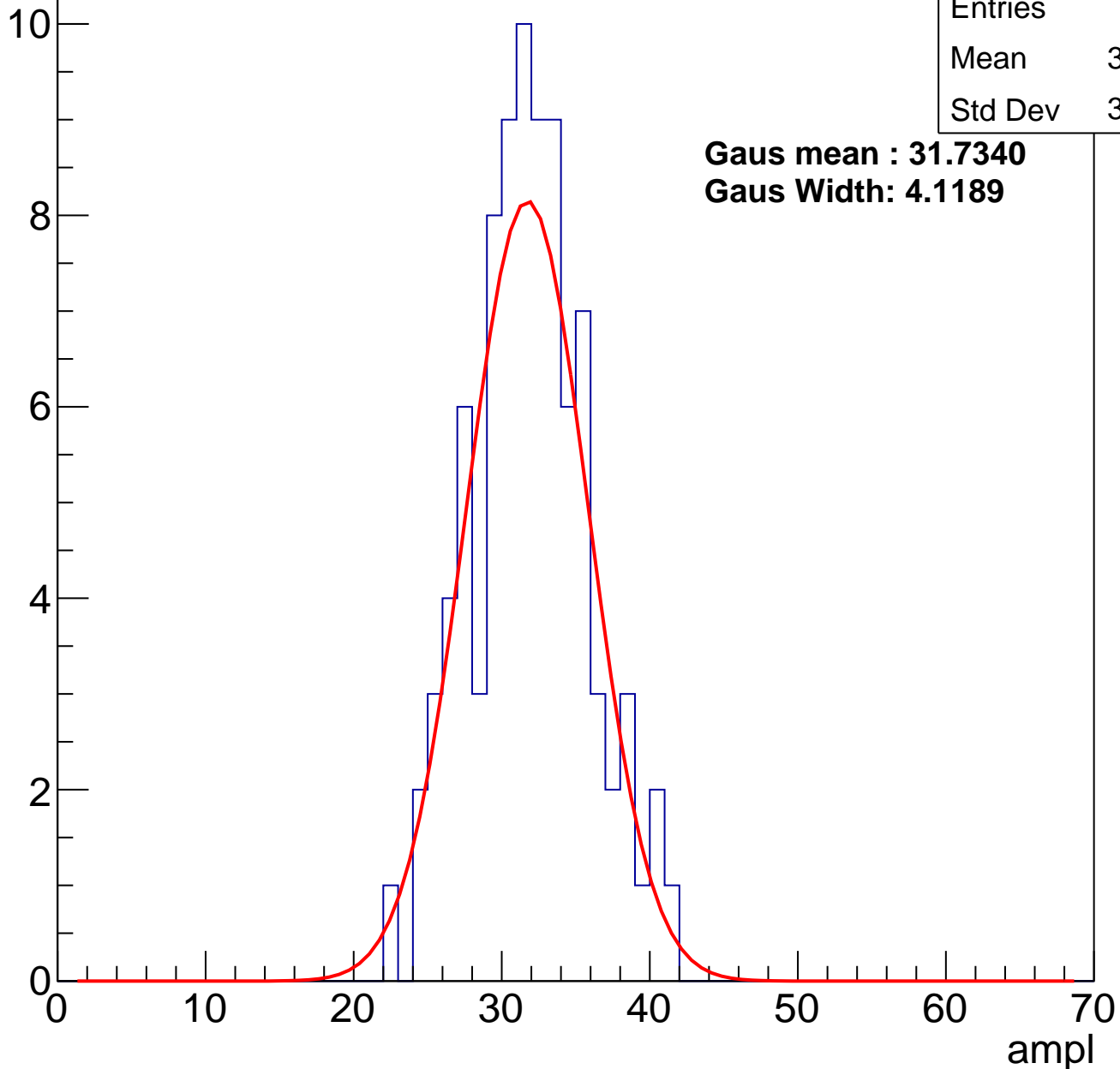
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	89
Mean	31.43
Std Dev	3.917

**Gaus mean : 31.7340**

**Gaus Width: 4.1189**

Entry



# B1L102S, U25-ch47, adc1

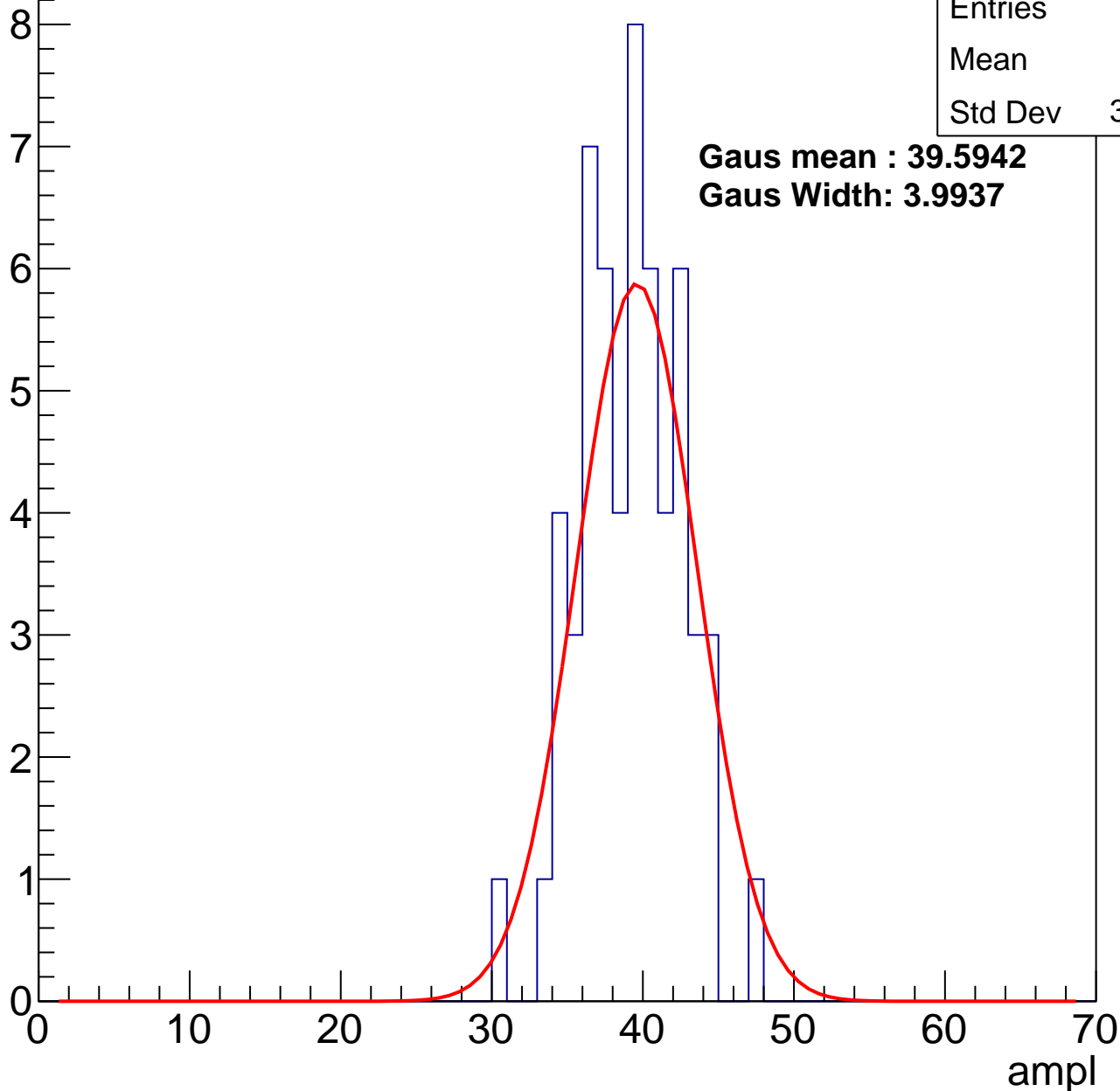
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	38.7
Std Dev	3.282

**Gaus mean : 39.5942**

**Gaus Width: 3.9937**



# B1L102S, U25-ch47, adc2

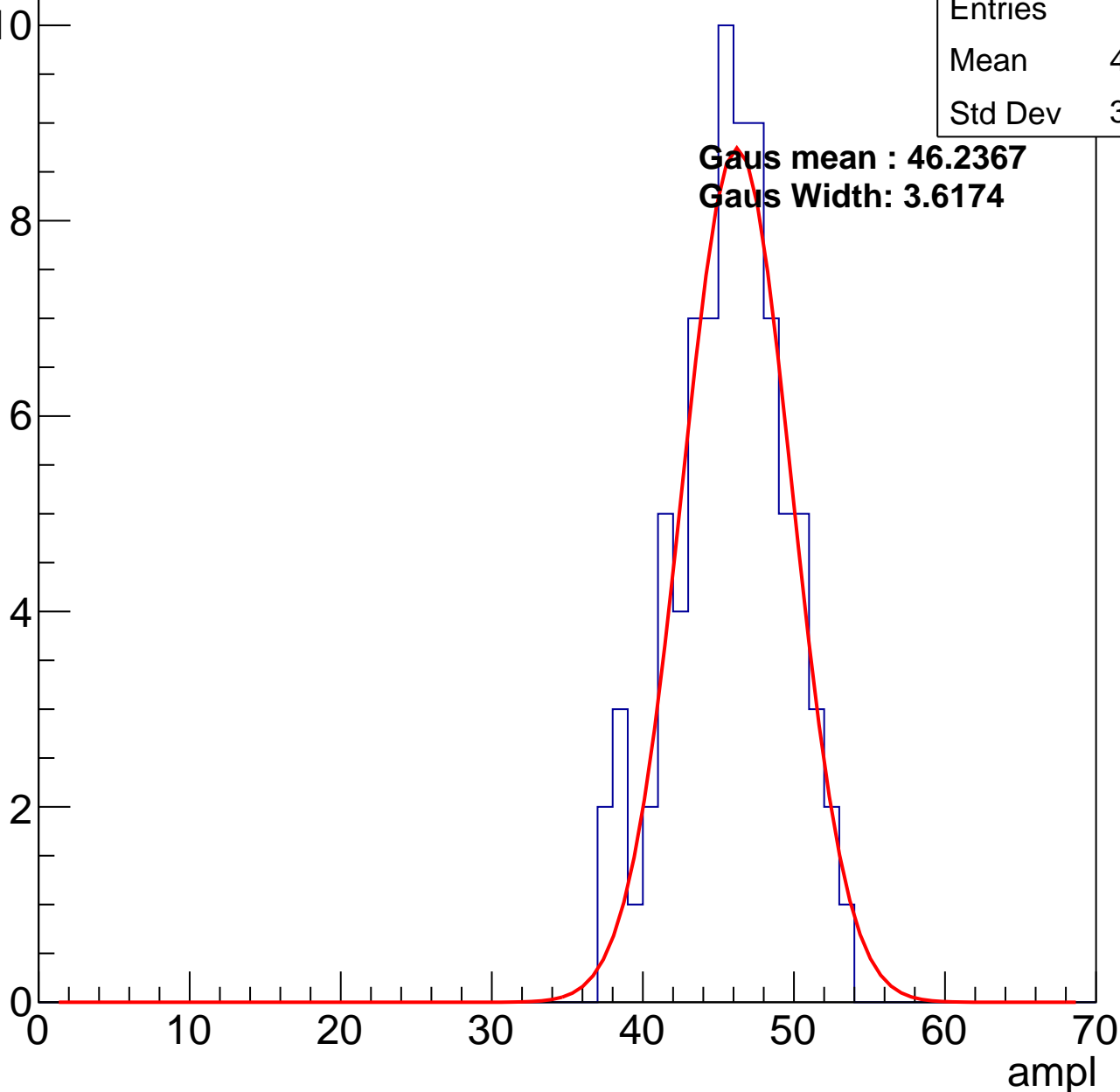
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	45.33
Std Dev	3.639

**Gaus mean : 46.2367**

**Gaus Width: 3.6174**

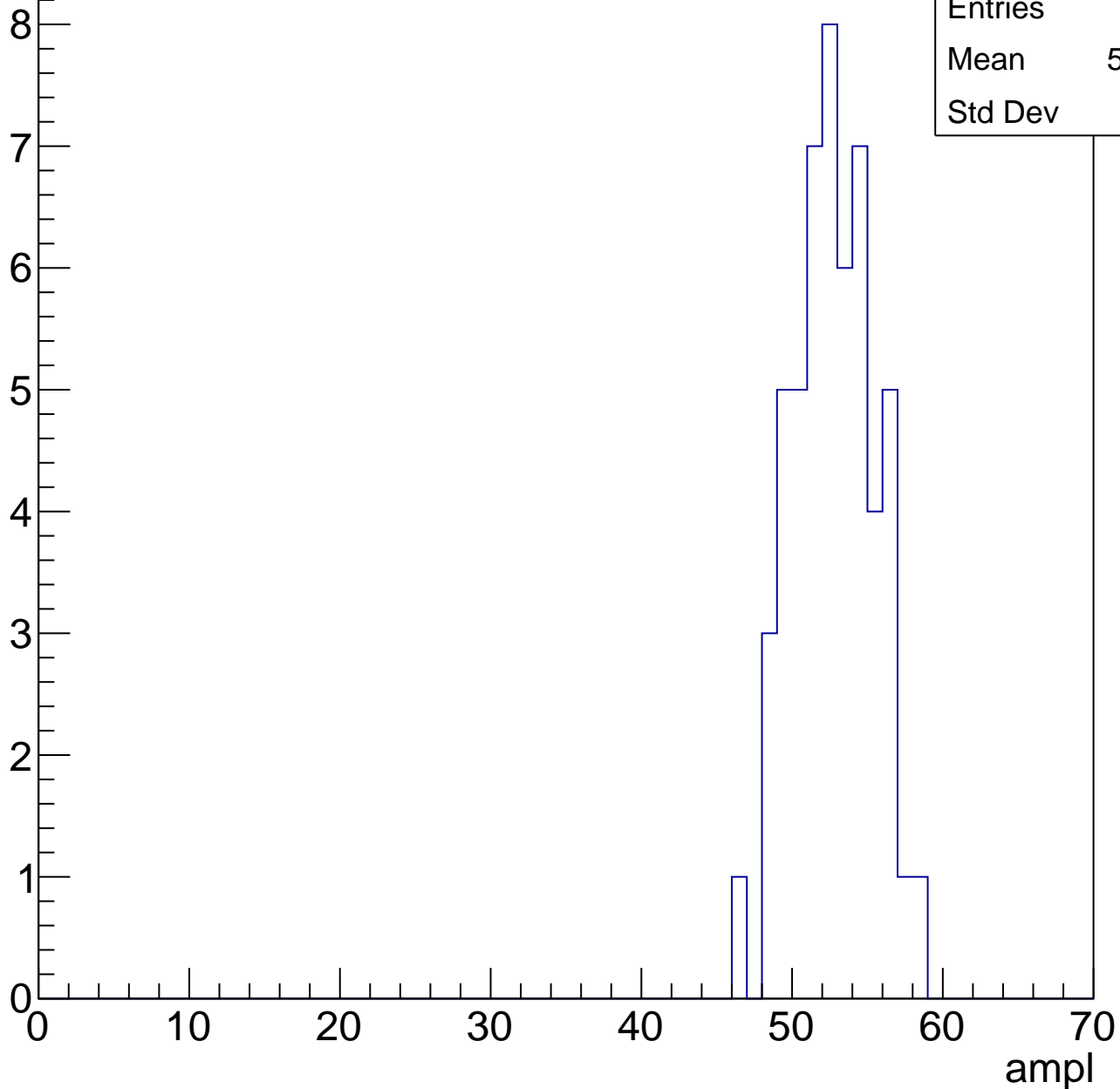


# B1L102S, U25-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	52.25
Std Dev	2.62

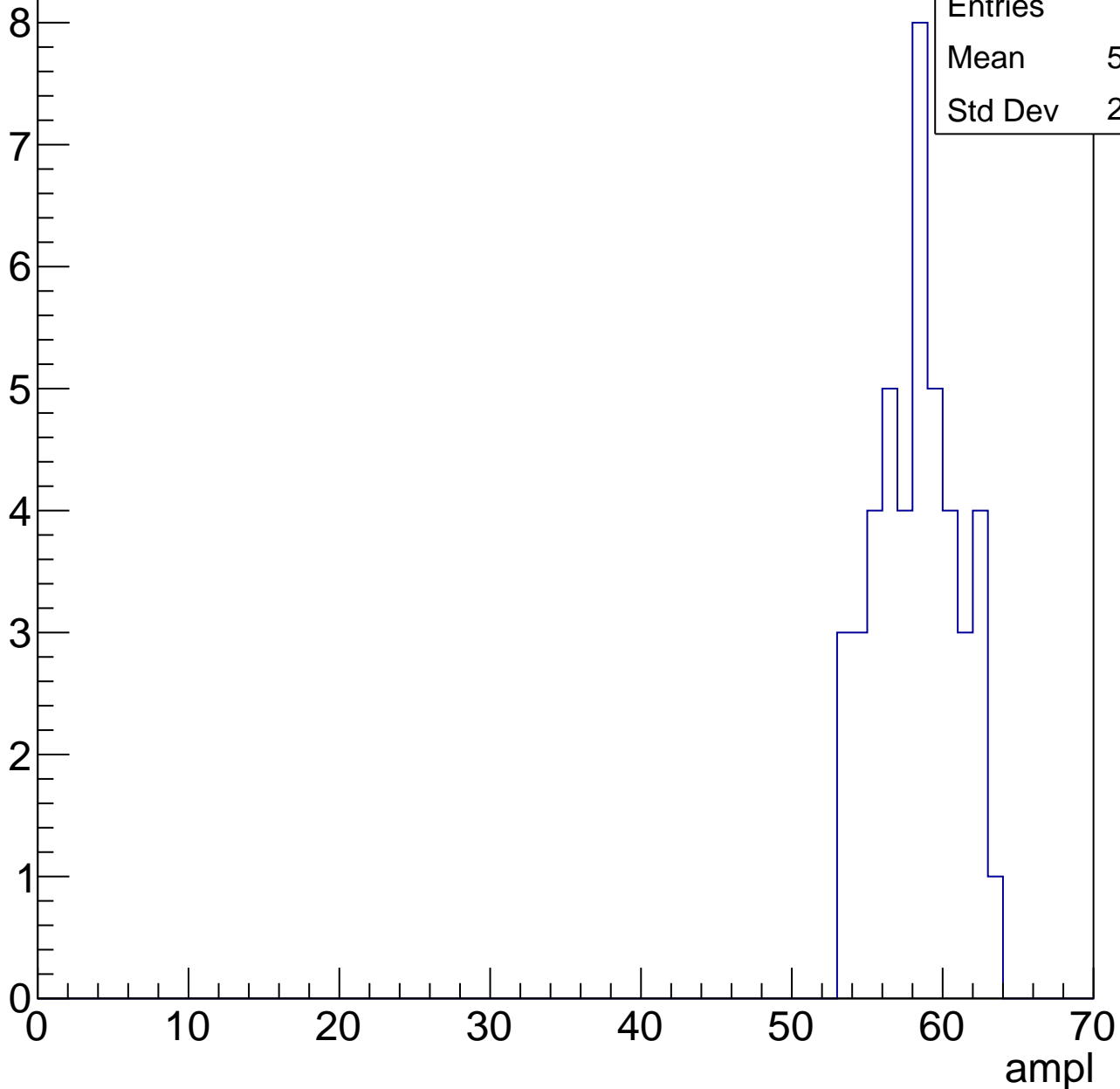


# B1L102S, U25-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.77
Std Dev	2.687

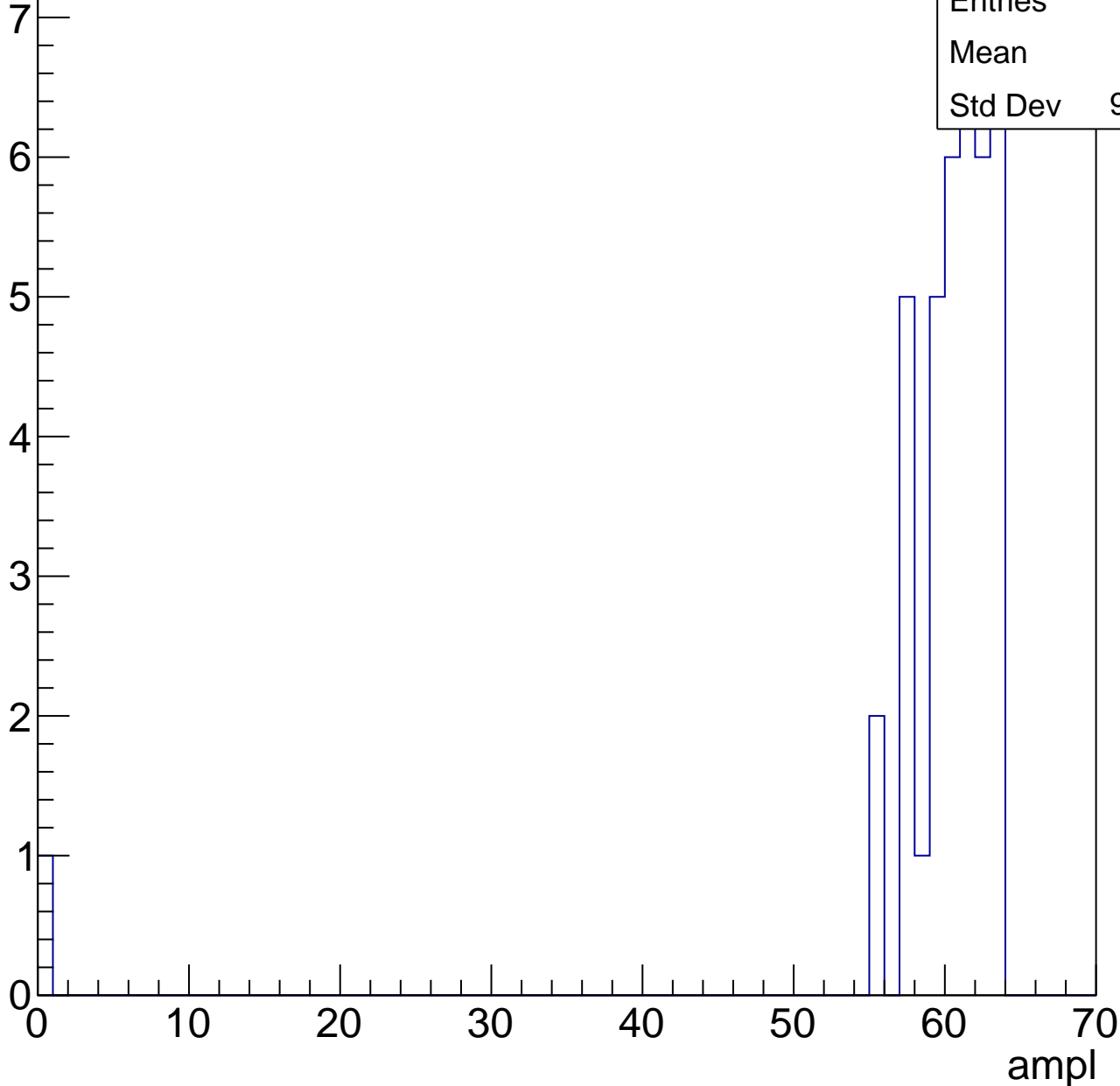


# B1L102S, U25-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.7
Std Dev	9.657



# B1L102S, U25-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U25-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch48, adc0

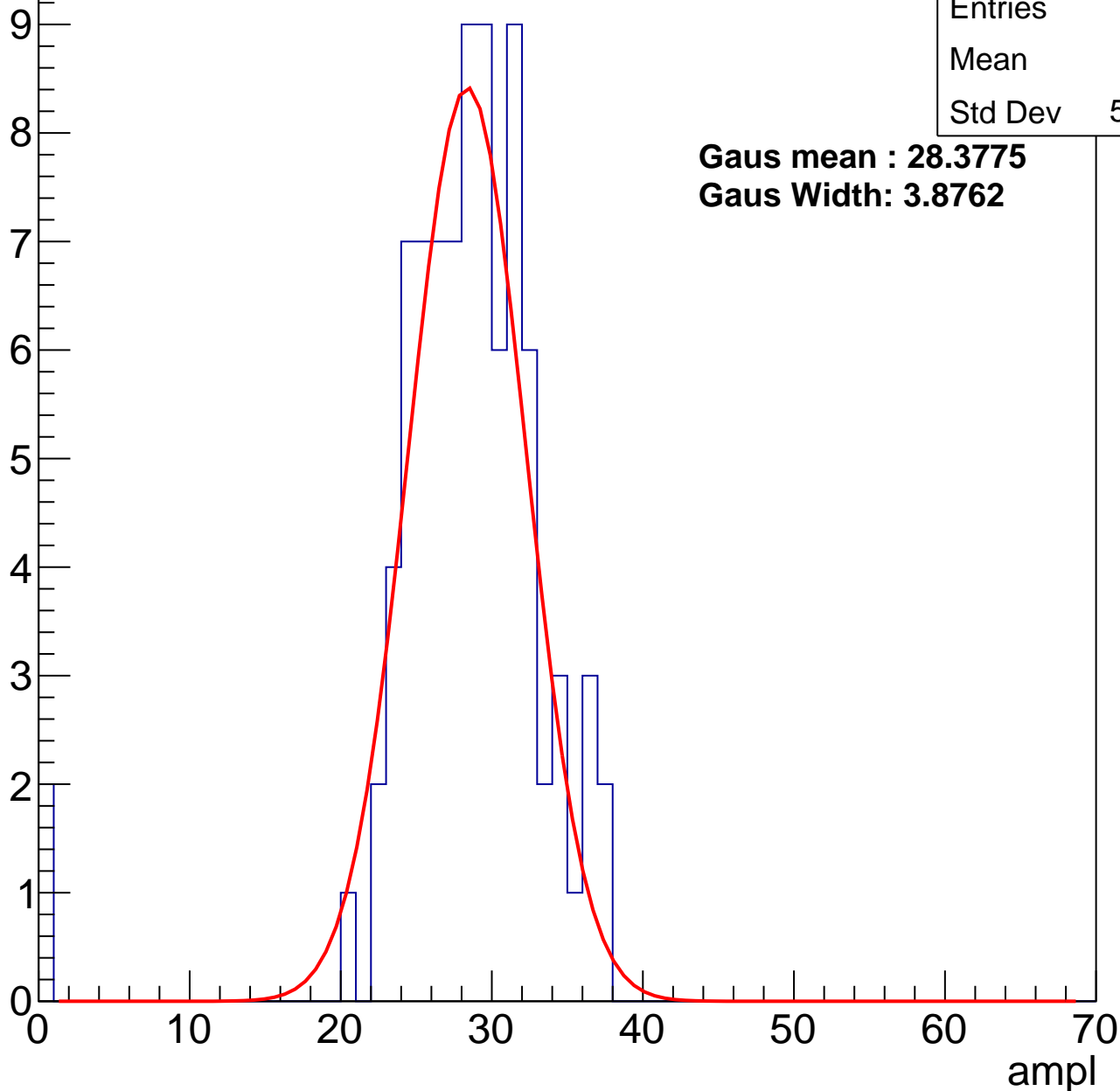
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	87
Mean	27.8
Std Dev	5.654

**Gaus mean : 28.3775**

**Gaus Width: 3.8762**



# B1L102S, U25-ch48, adc1

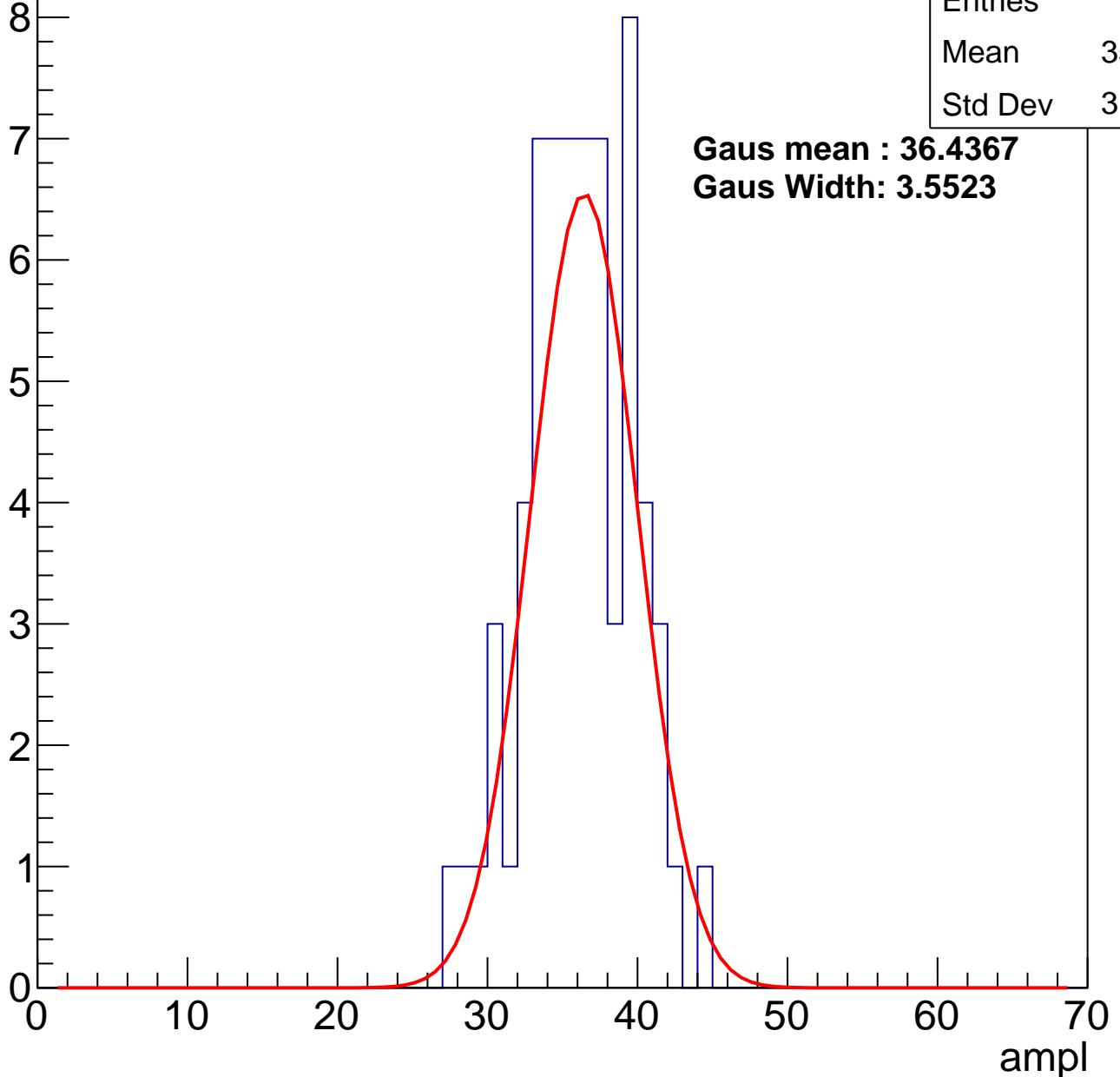
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.65
Std Dev	3.505

**Gaus mean : 36.4367**

**Gaus Width: 3.5523**



# B1L102S, U25-ch48, adc2

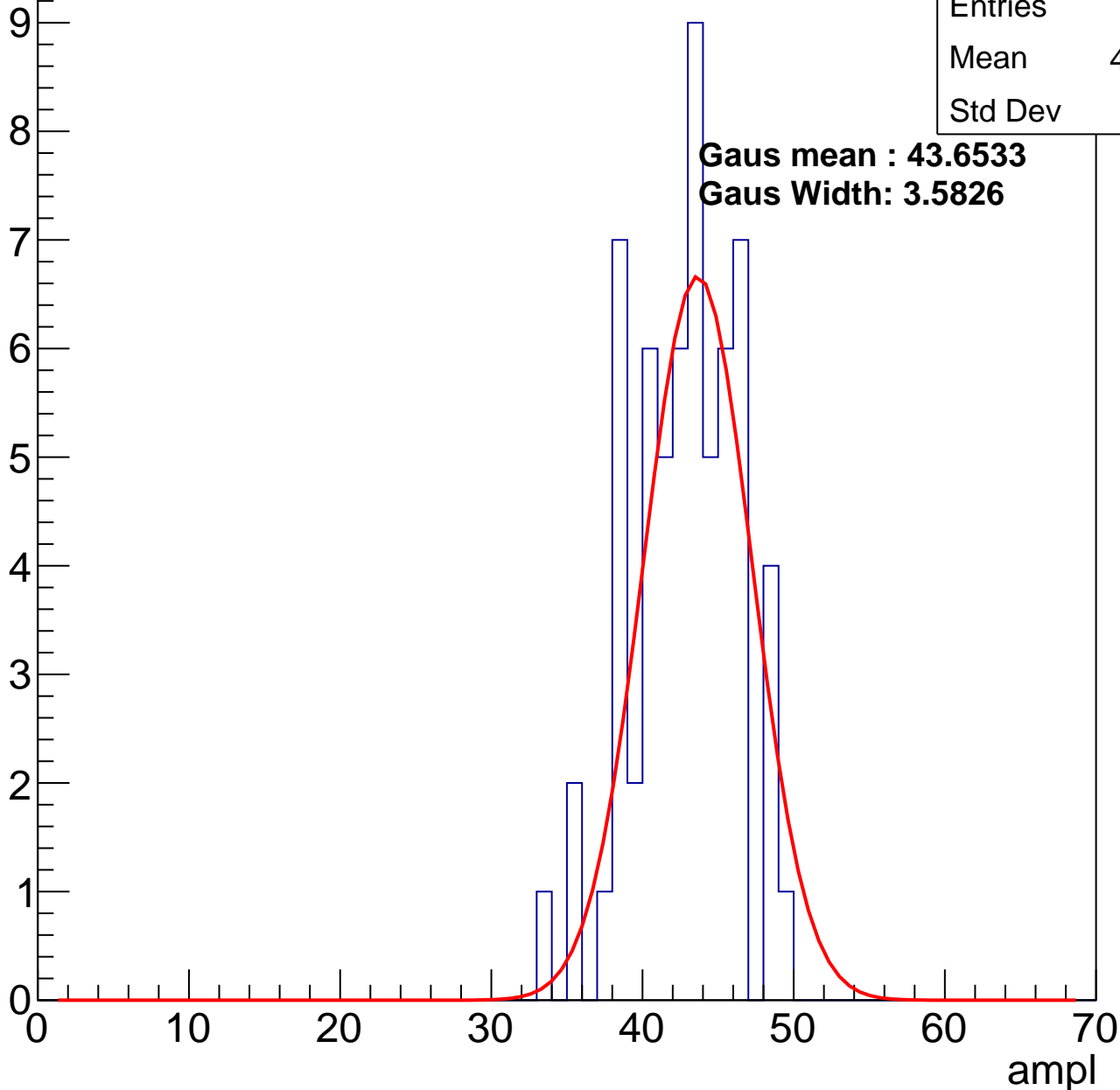
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.27
Std Dev	3.47

**Gaus mean : 43.6533**

**Gaus Width: 3.5826**

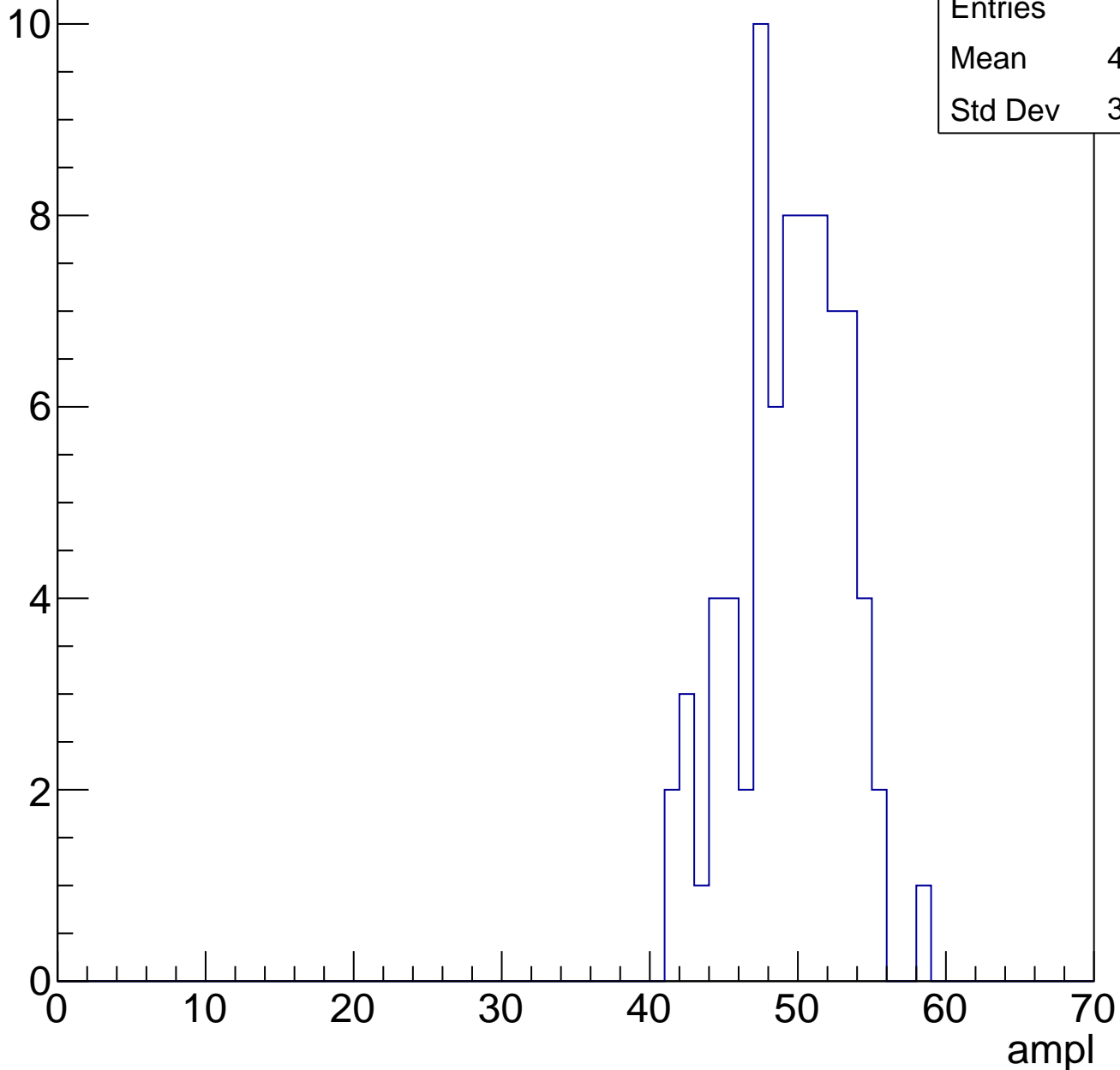


# B1L102S, U25-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	49.04
Std Dev	3.616

Entry

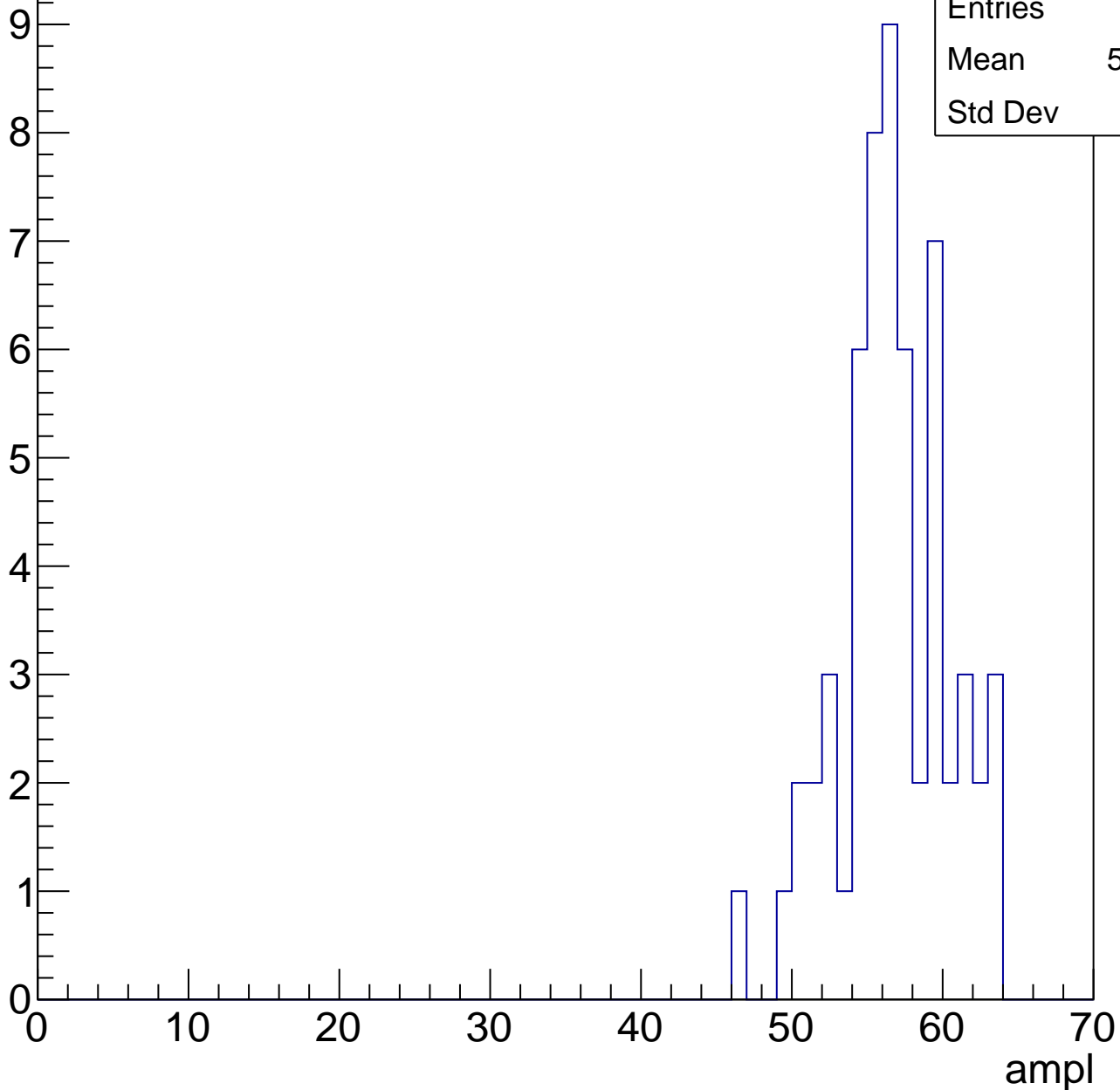


# B1L102S, U25-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.22
Std Dev	3.62

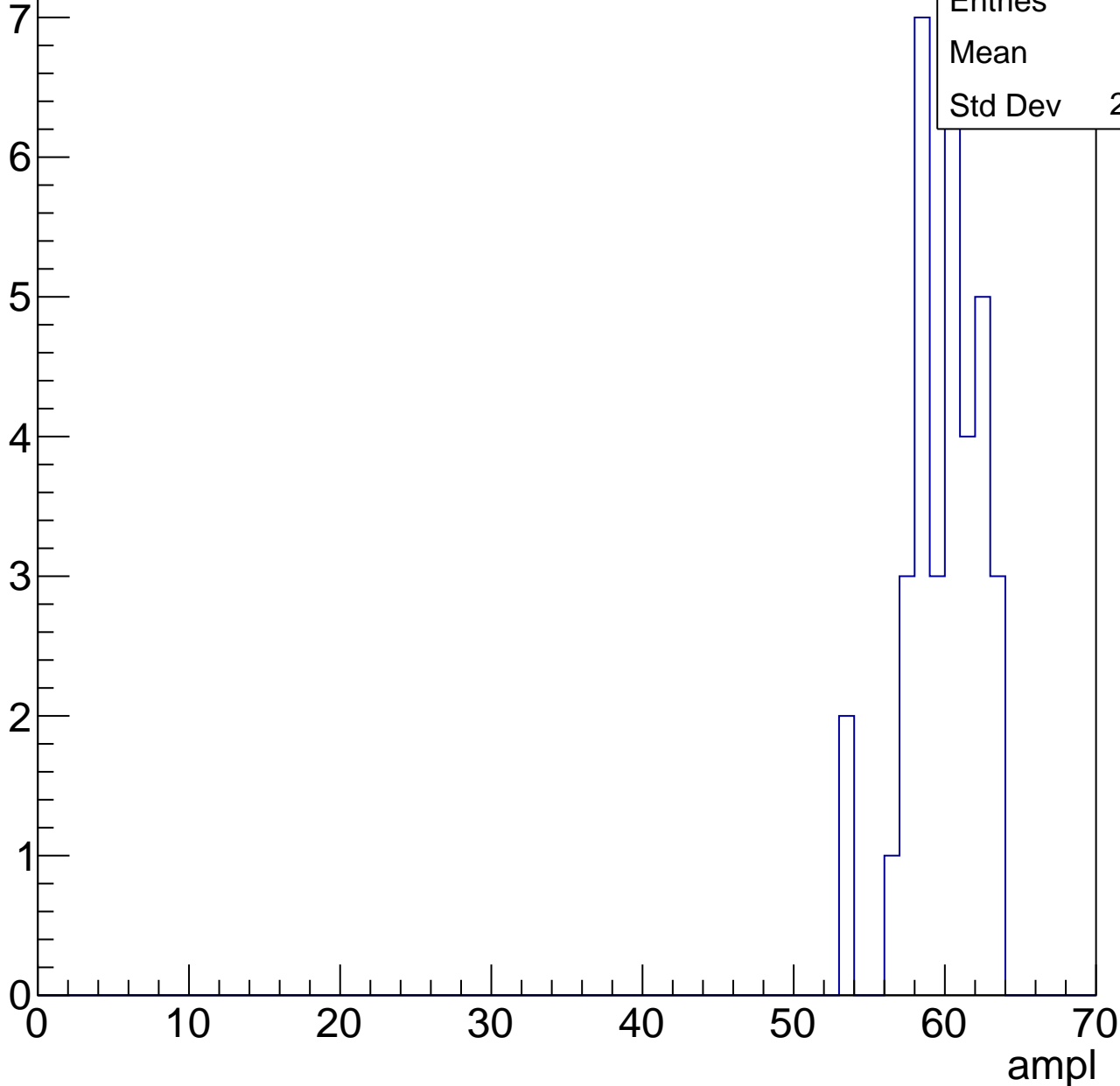


# B1L102S, U25-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

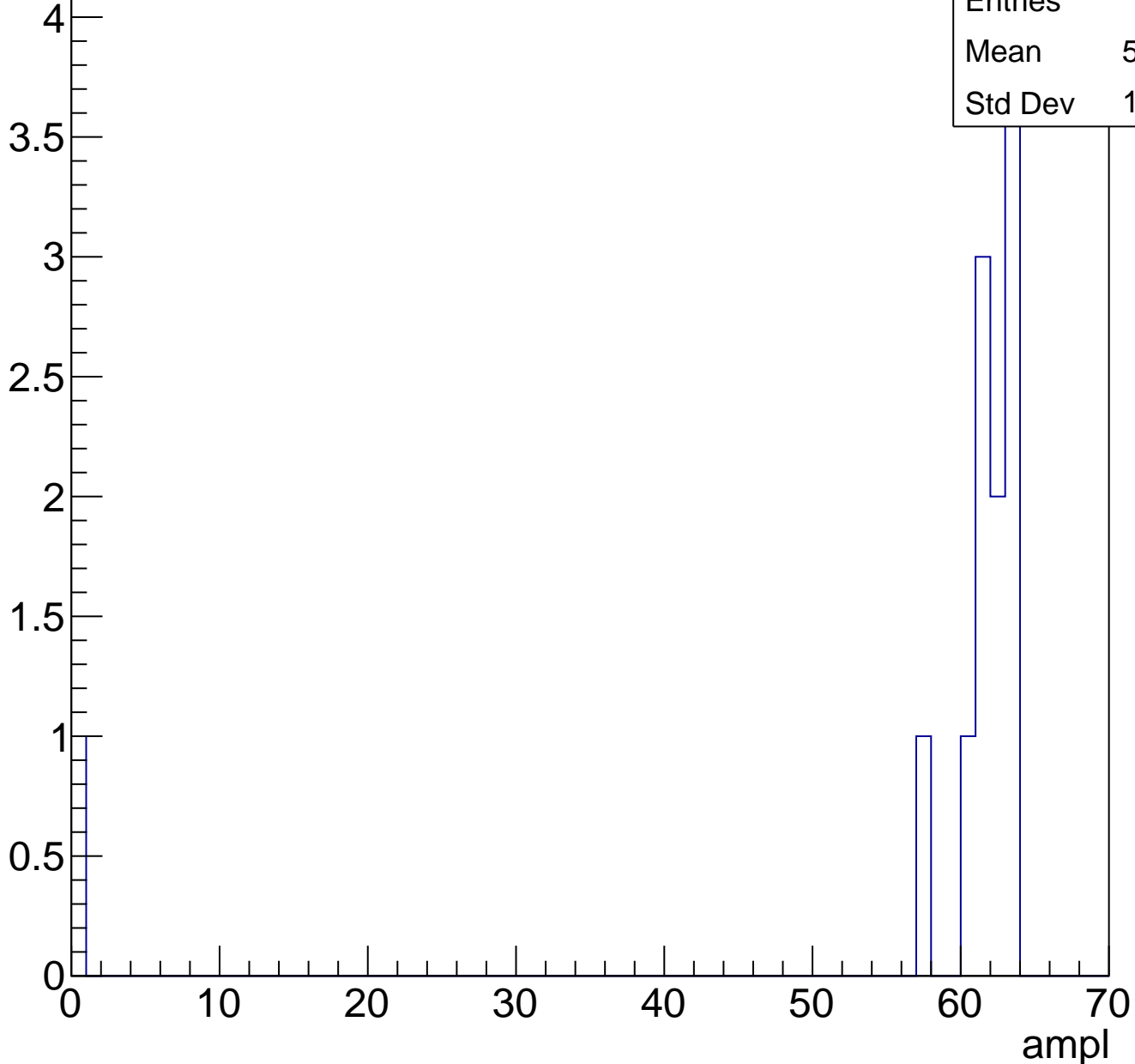
Entries	35
Mean	59.4
Std Dev	2.452



# B1L102S, U25-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

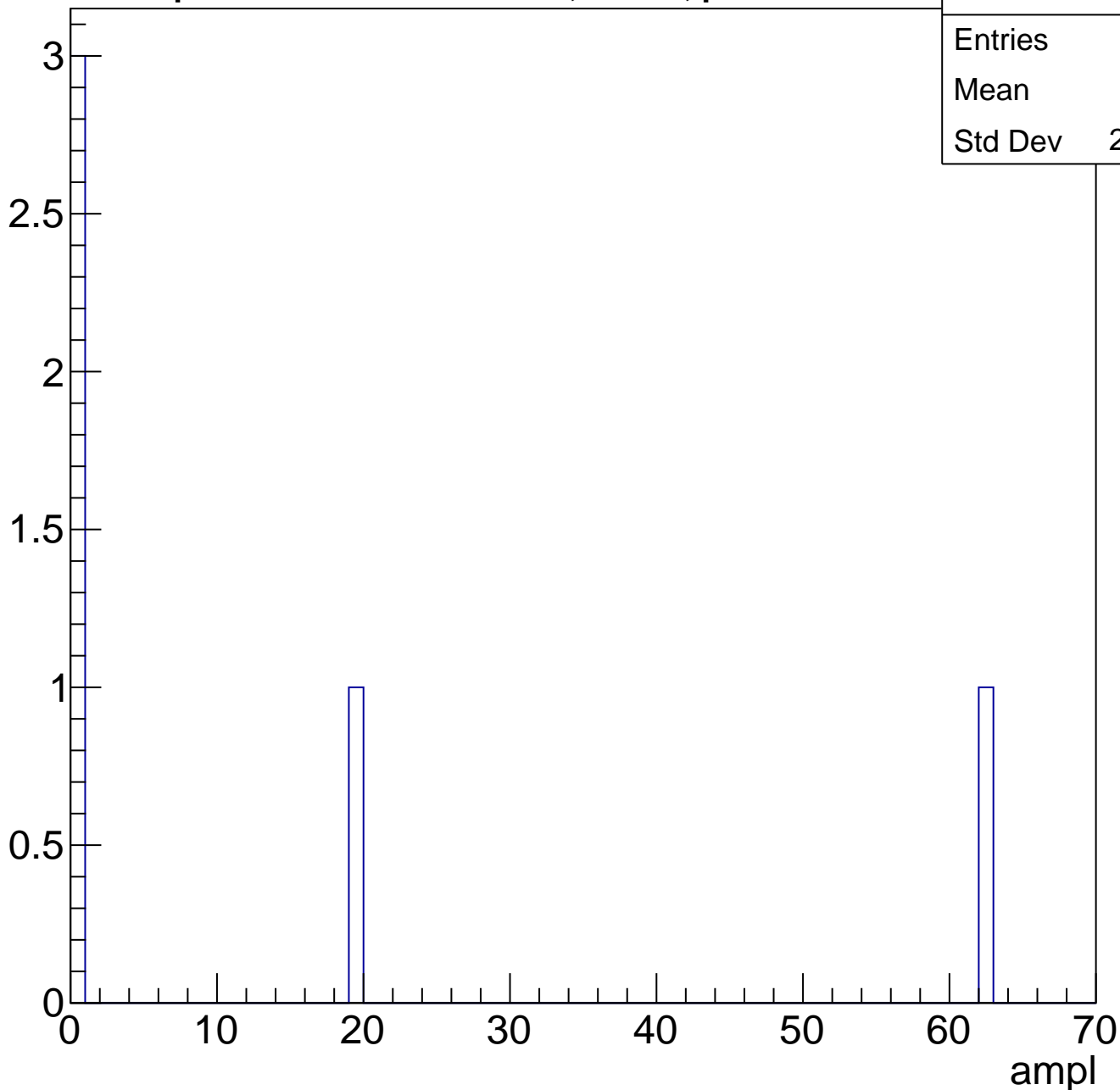




# B1L102S, U25-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	5
Mean	16.2
Std Dev	24.05

# B1L102S, U25-ch49, adc0

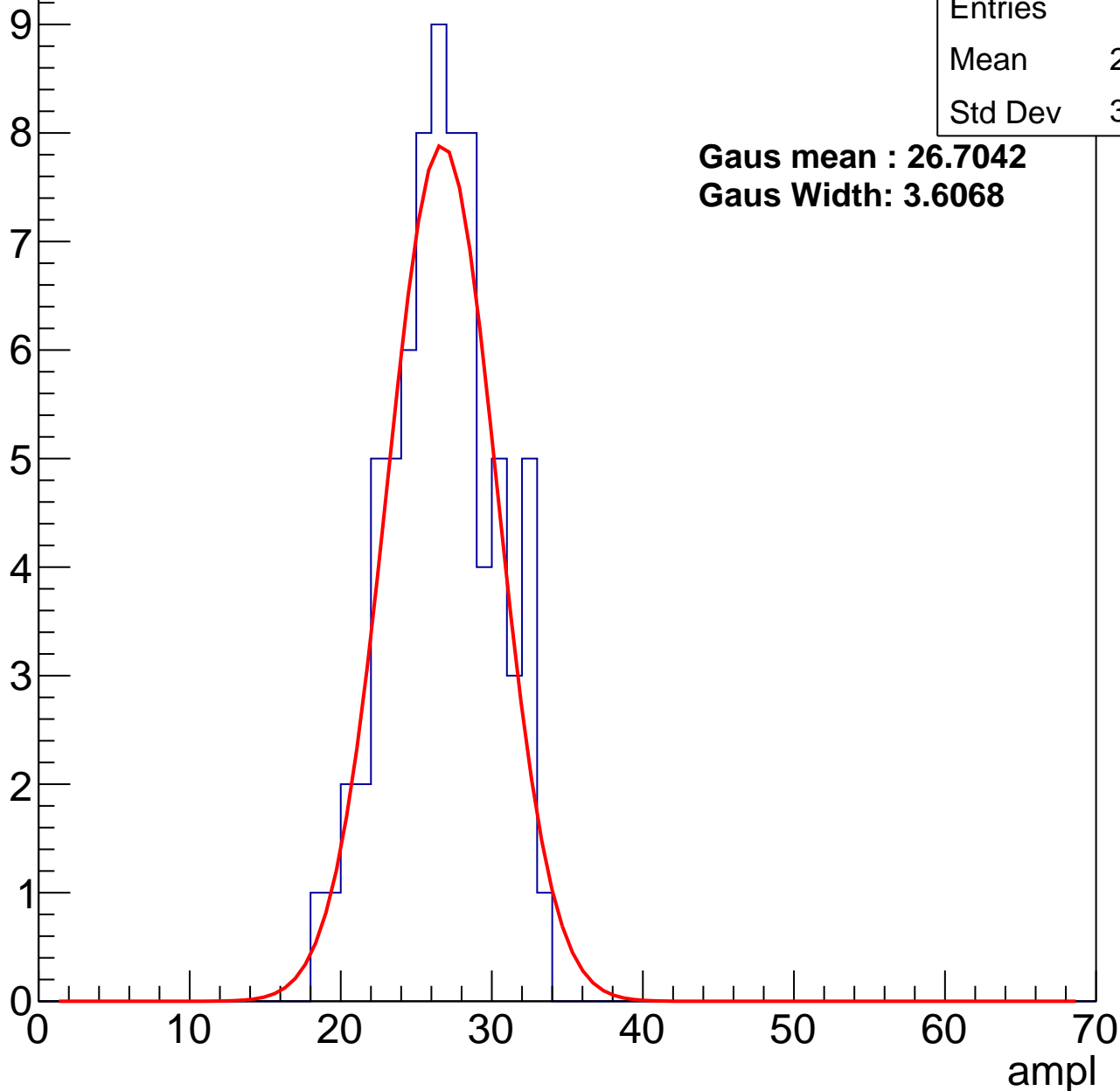
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	26.22
Std Dev	3.409

**Gaus mean : 26.7042**

**Gaus Width: 3.6068**



# B1L102S, U25-ch49, adc1

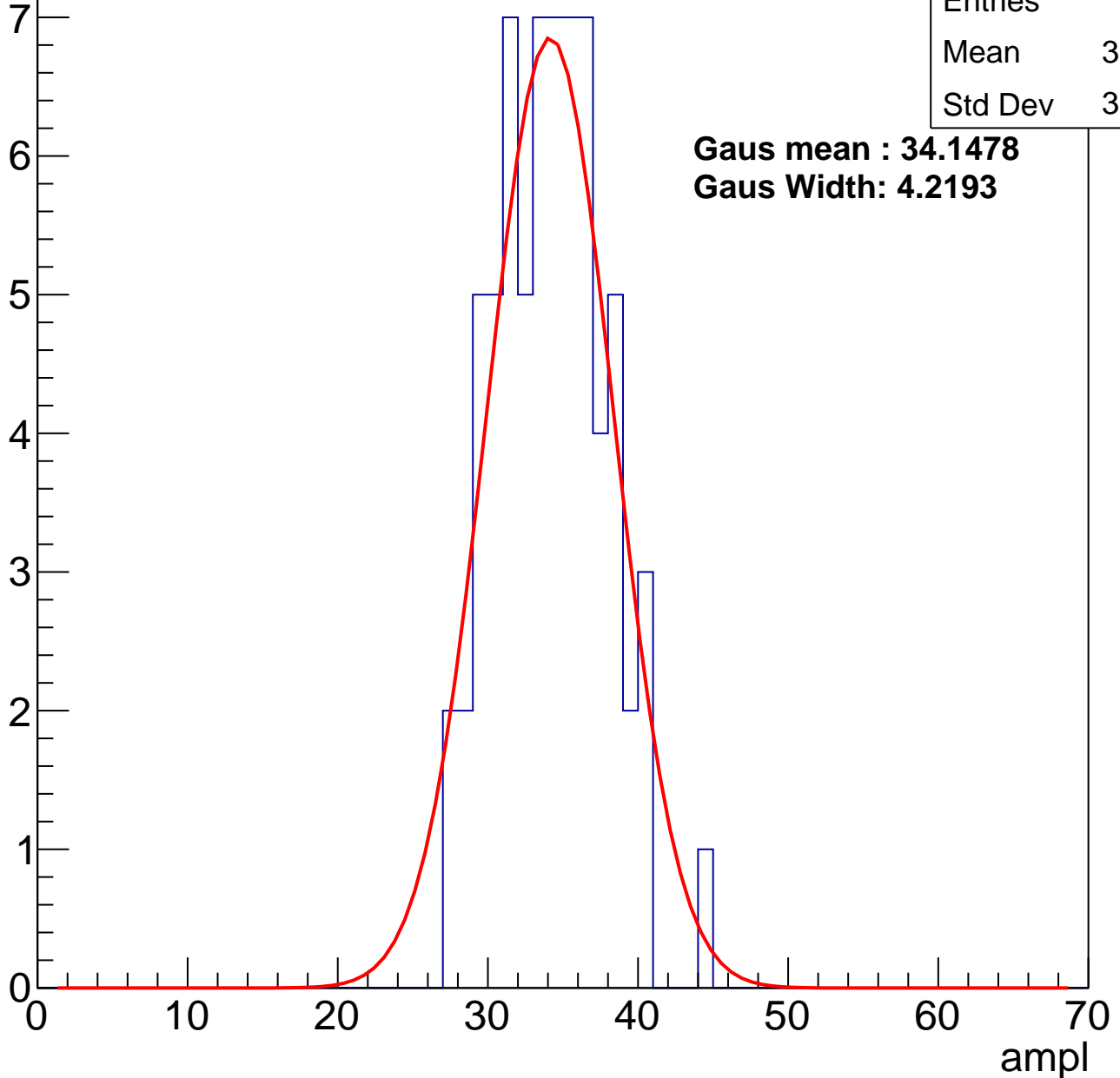
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	33.74
Std Dev	3.549

**Gaus mean : 34.1478**

**Gaus Width: 4.2193**



# B1L102S, U25-ch49, adc2

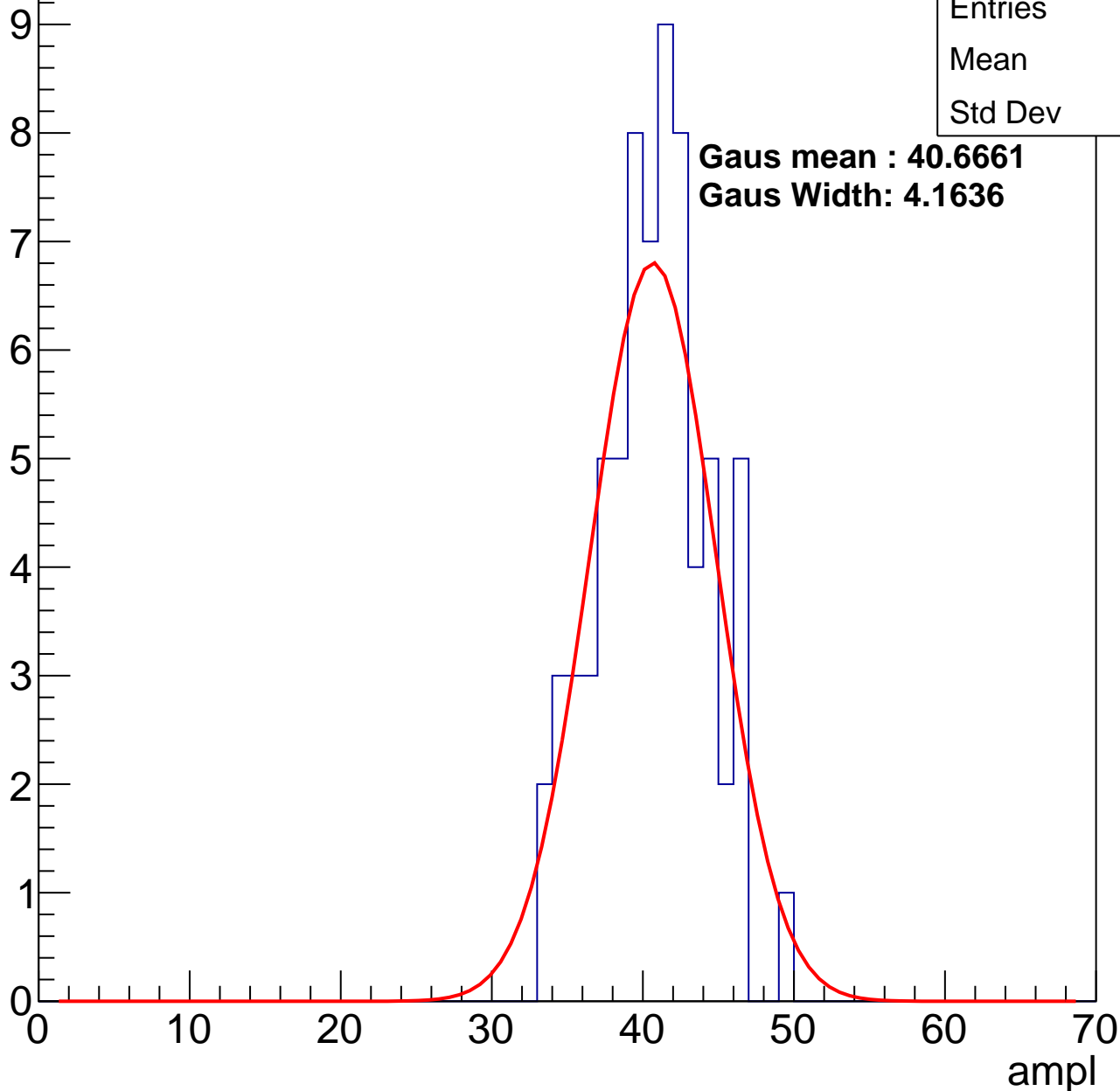
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.2
Std Dev	3.52

**Gaus mean : 40.6661**

**Gaus Width: 4.1636**

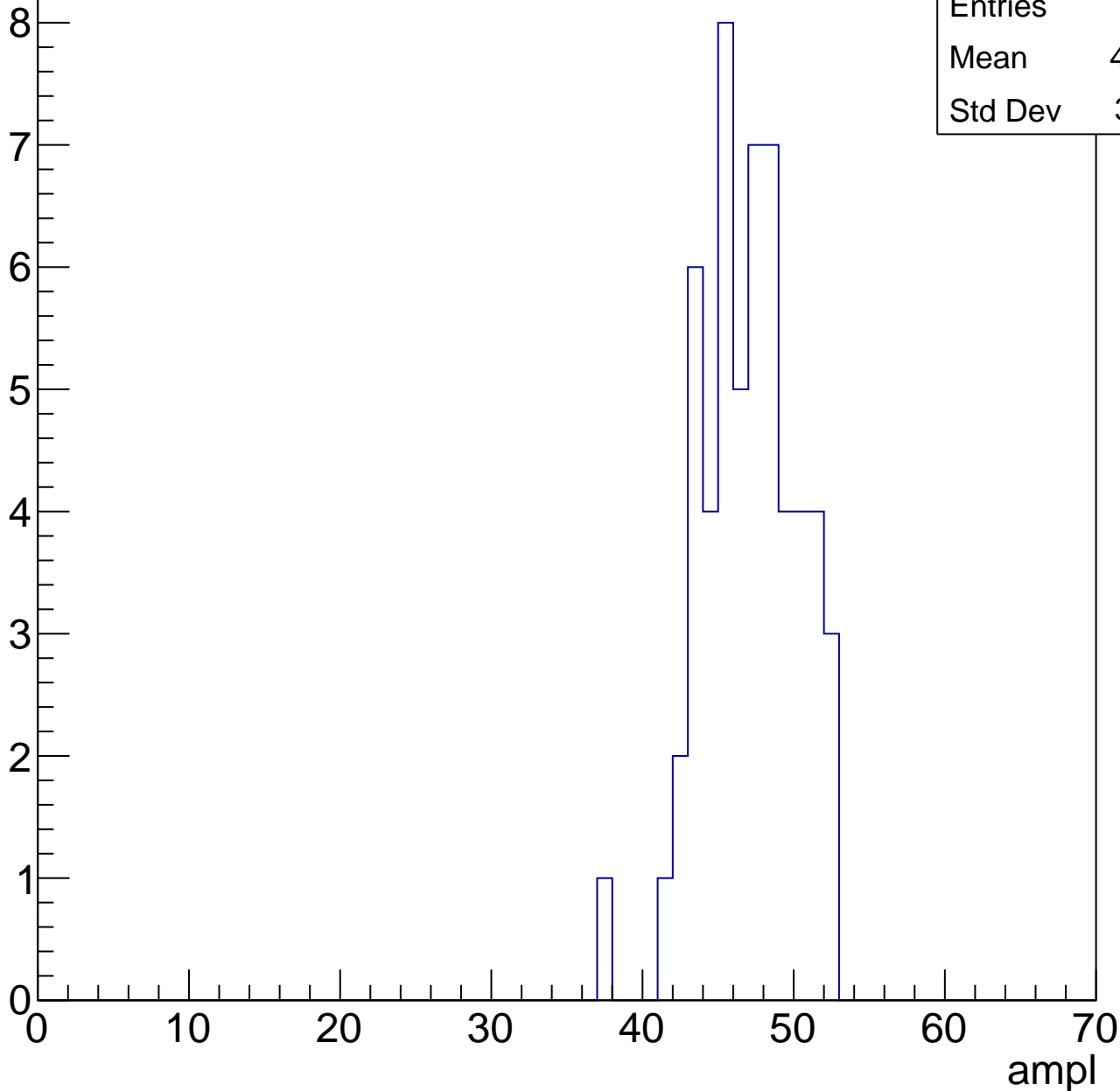


# B1L102S, U25-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

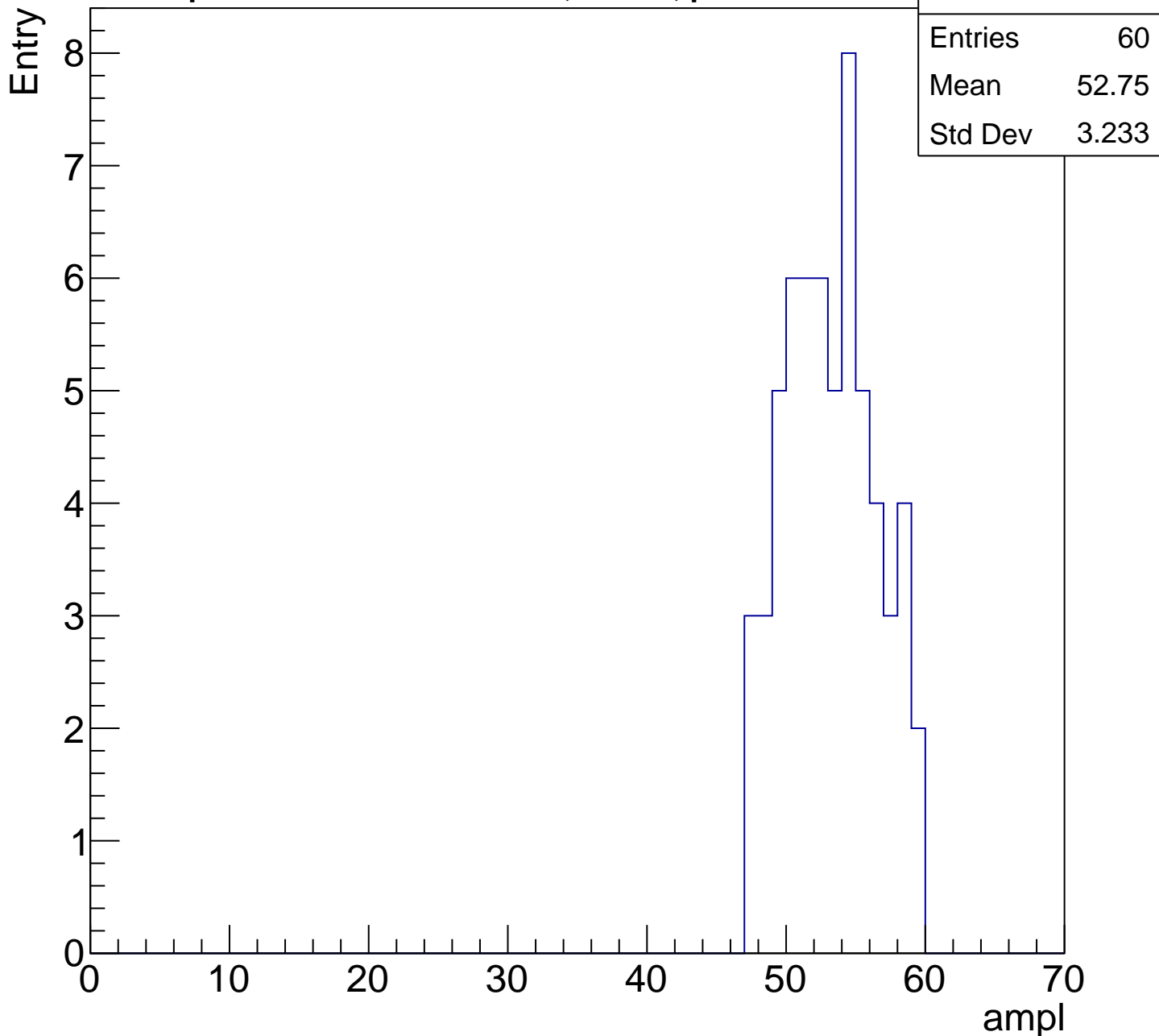
Entry

Entries	56
Mean	46.55
Std Dev	3.111



# B1L102S, U25-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

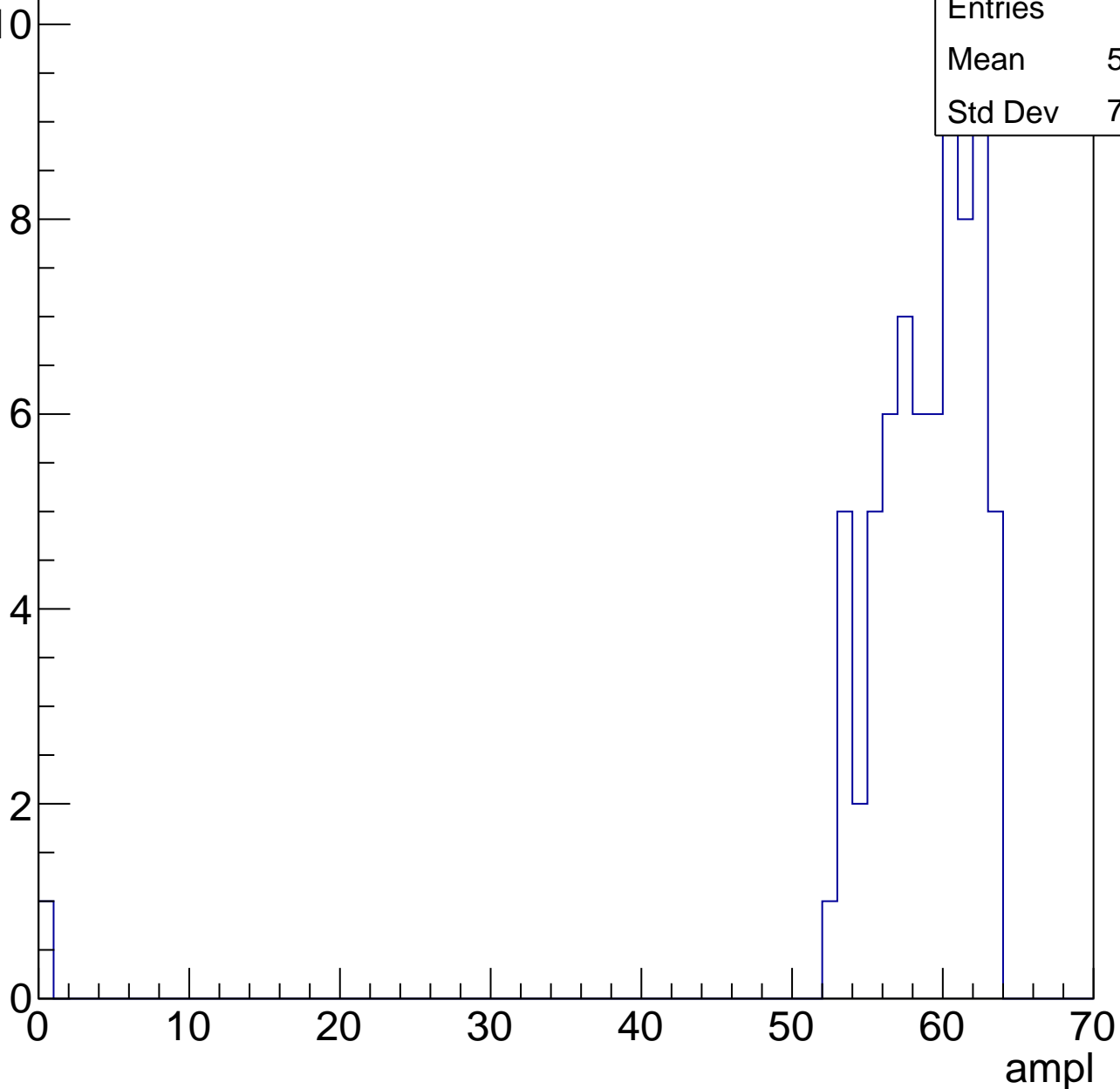


# B1L102S, U25-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	57.72
Std Dev	7.519

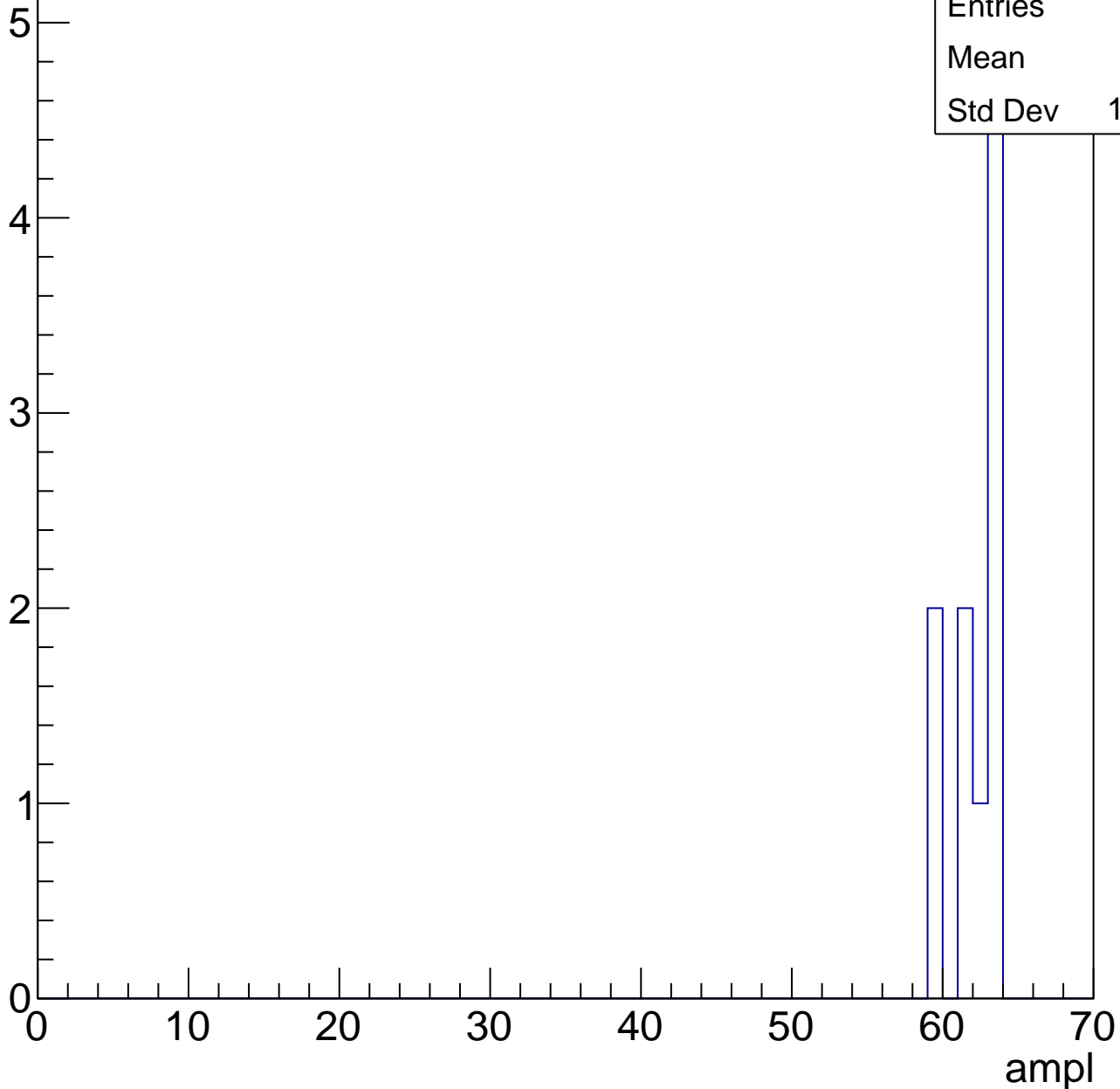


# B1L102S, U25-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.7
Std Dev	1.552

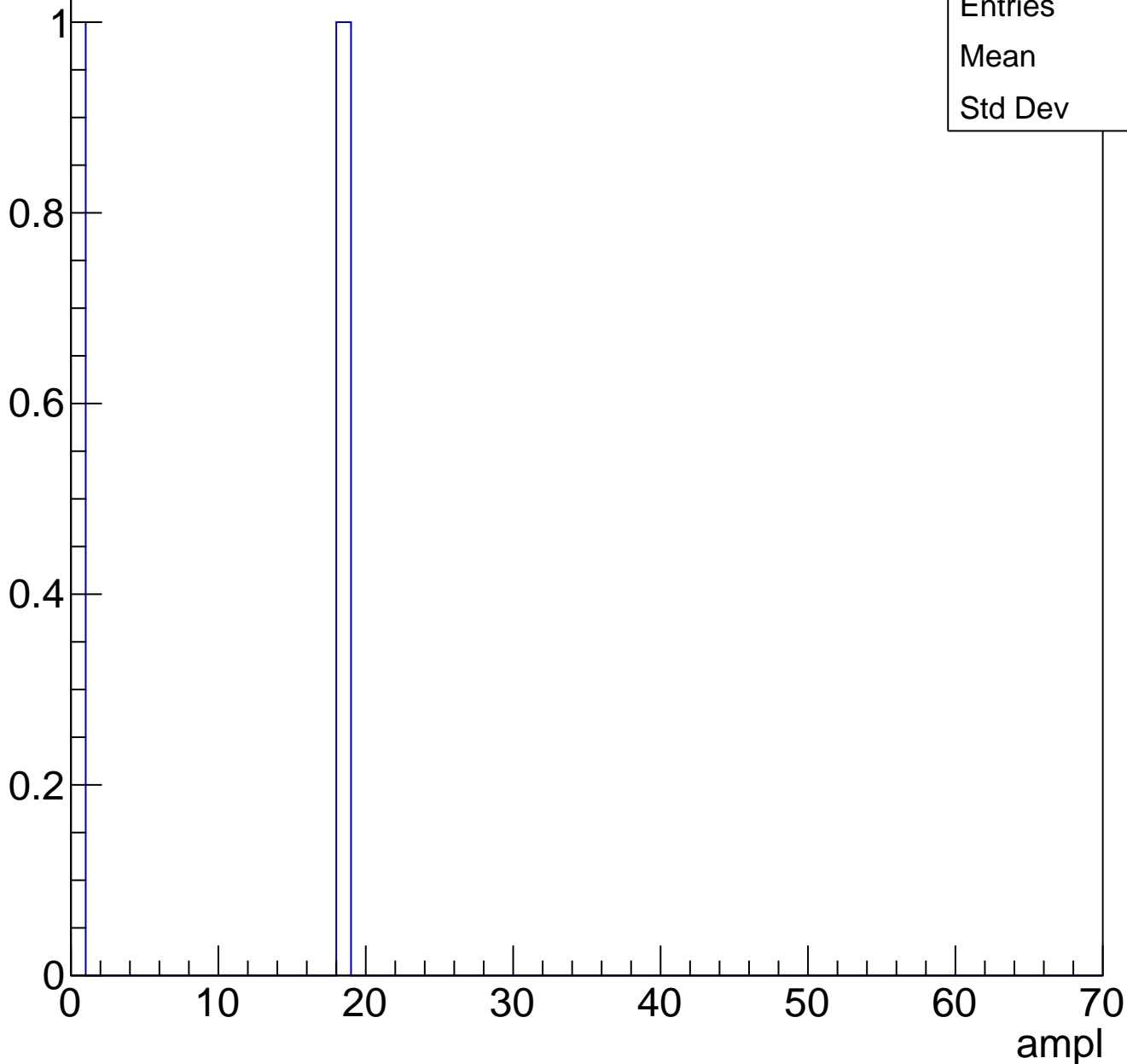




# B1L102S, U25-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9
Std Dev	9

# B1L102S, U25-ch50, adc0

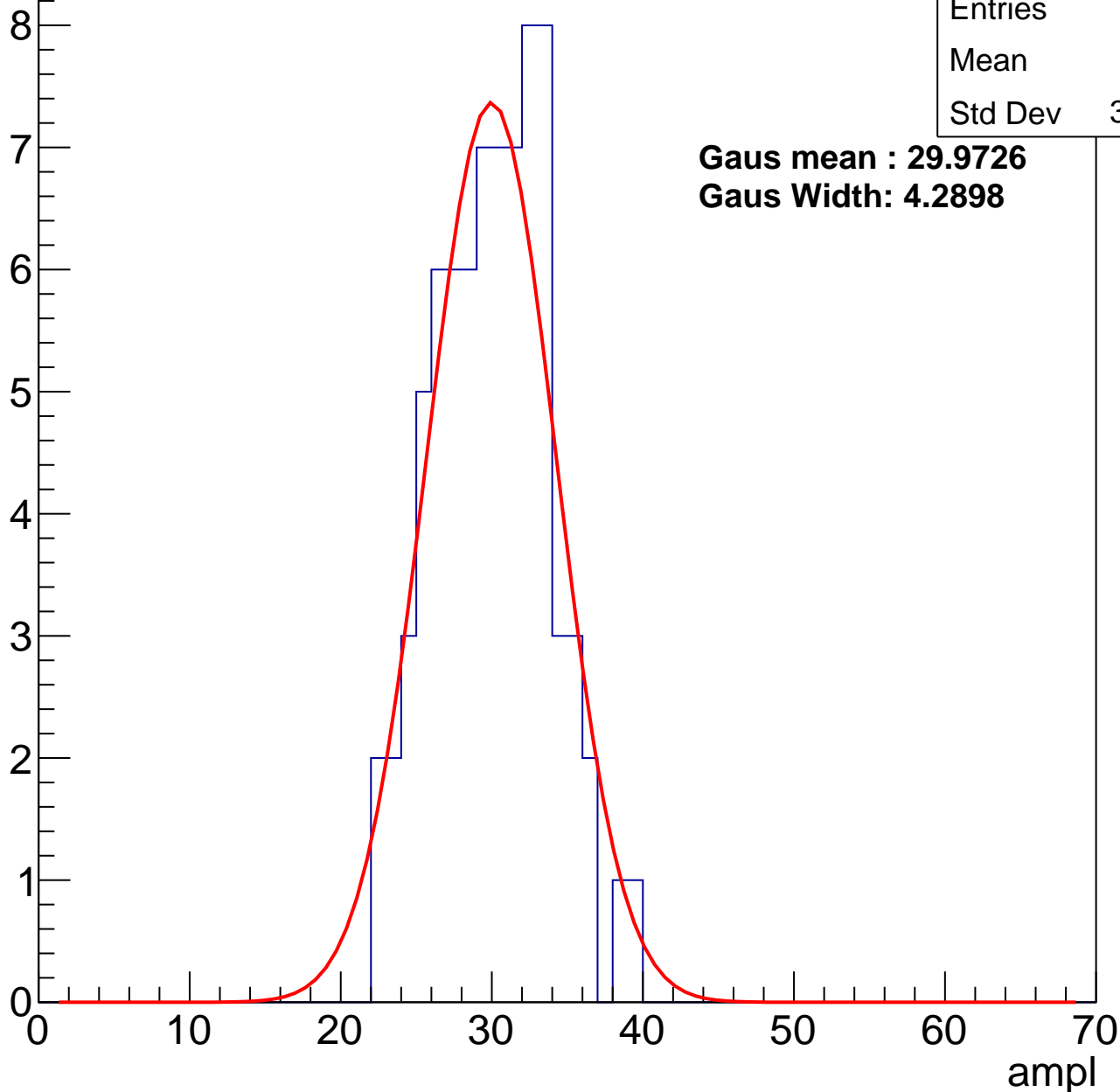
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.6
Std Dev	3.743

**Gaus mean : 29.9726**

**Gaus Width: 4.2898**



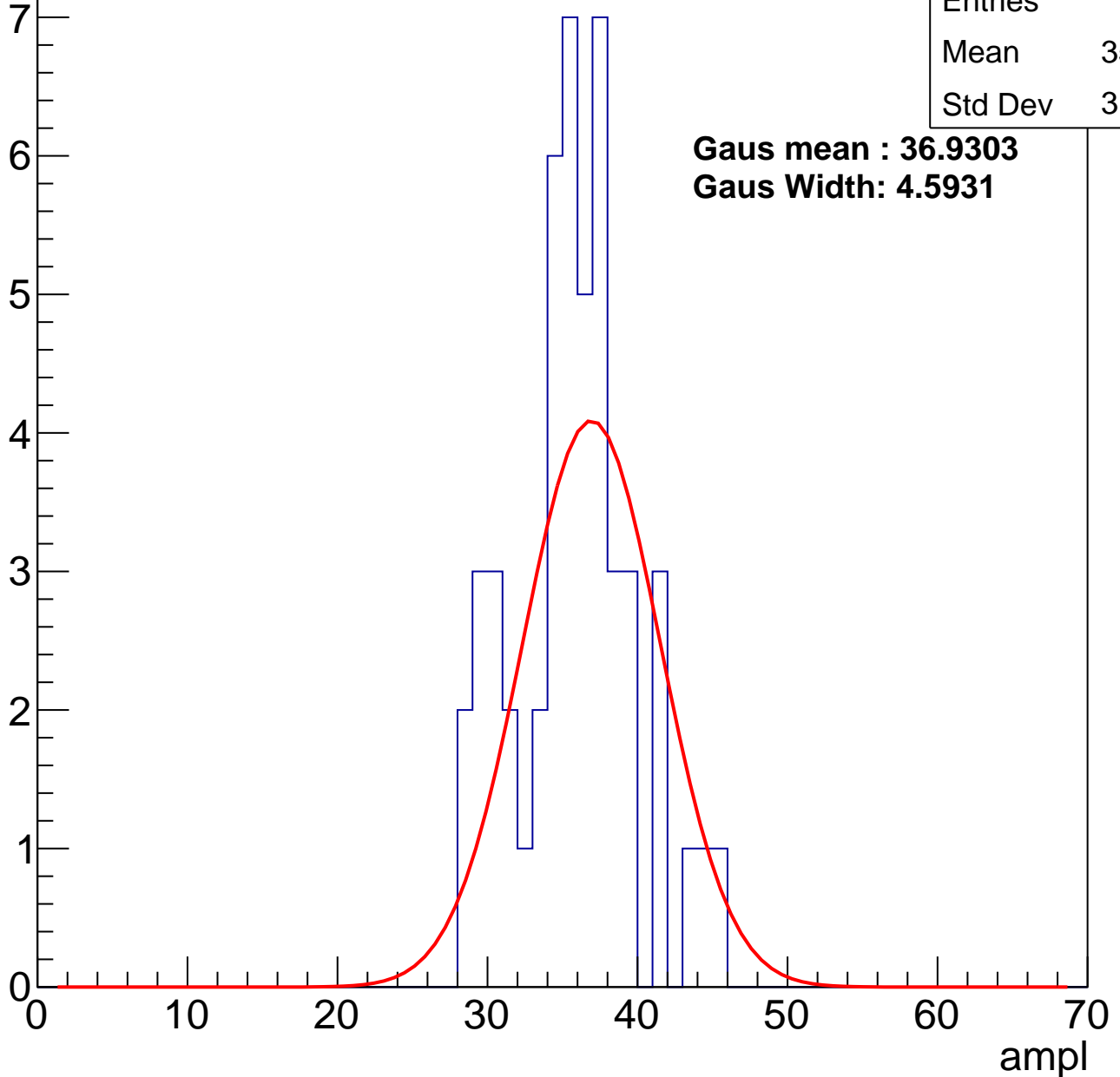
# B1L102S, U25-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	35.34
Std Dev	3.978

**Gaus mean : 36.9303**  
**Gaus Width: 4.5931**



# B1L102S, U25-ch50, adc2

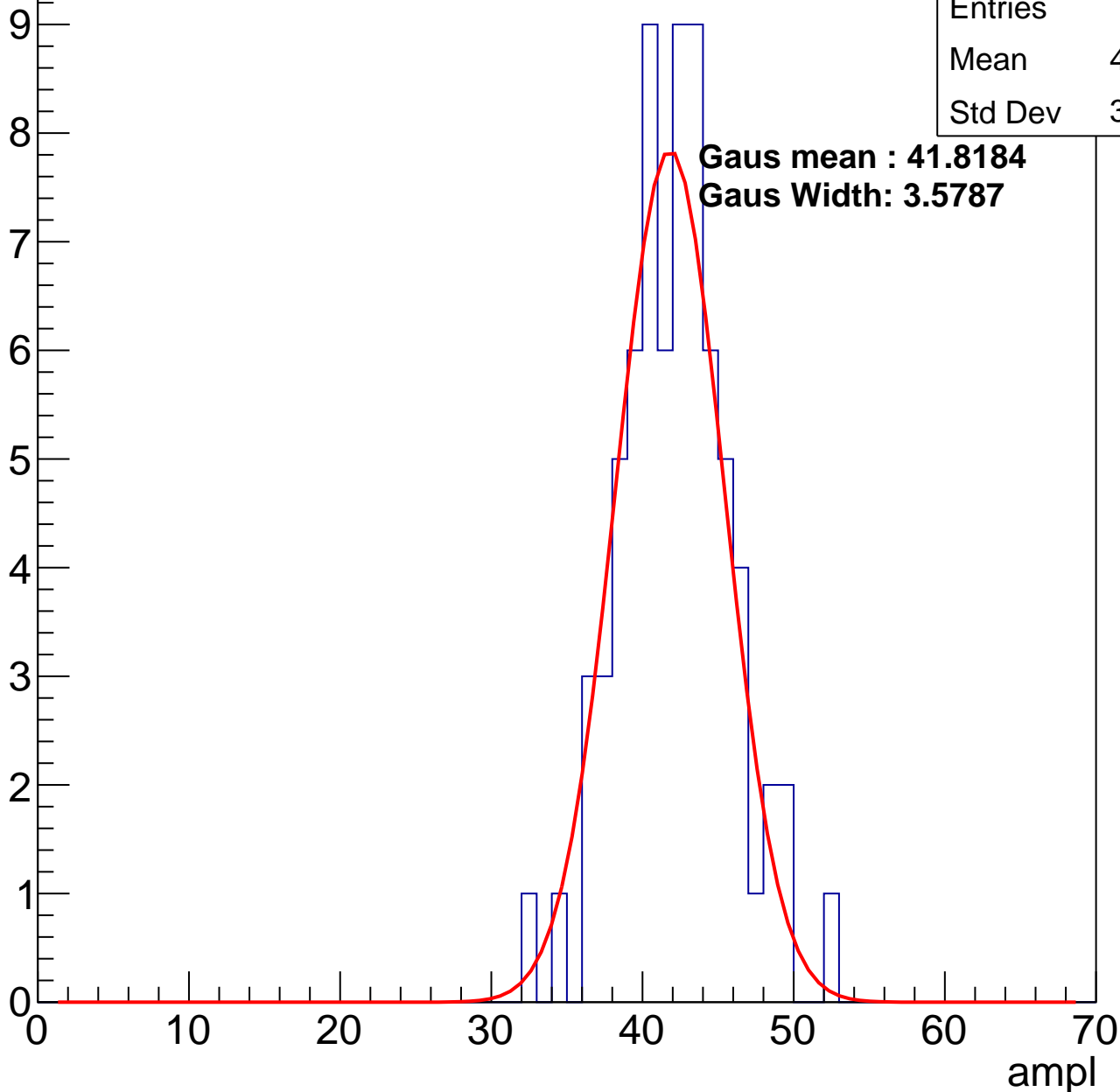
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.73
Std Dev	3.616

**Gaus mean : 41.8184**

**Gaus Width: 3.5787**

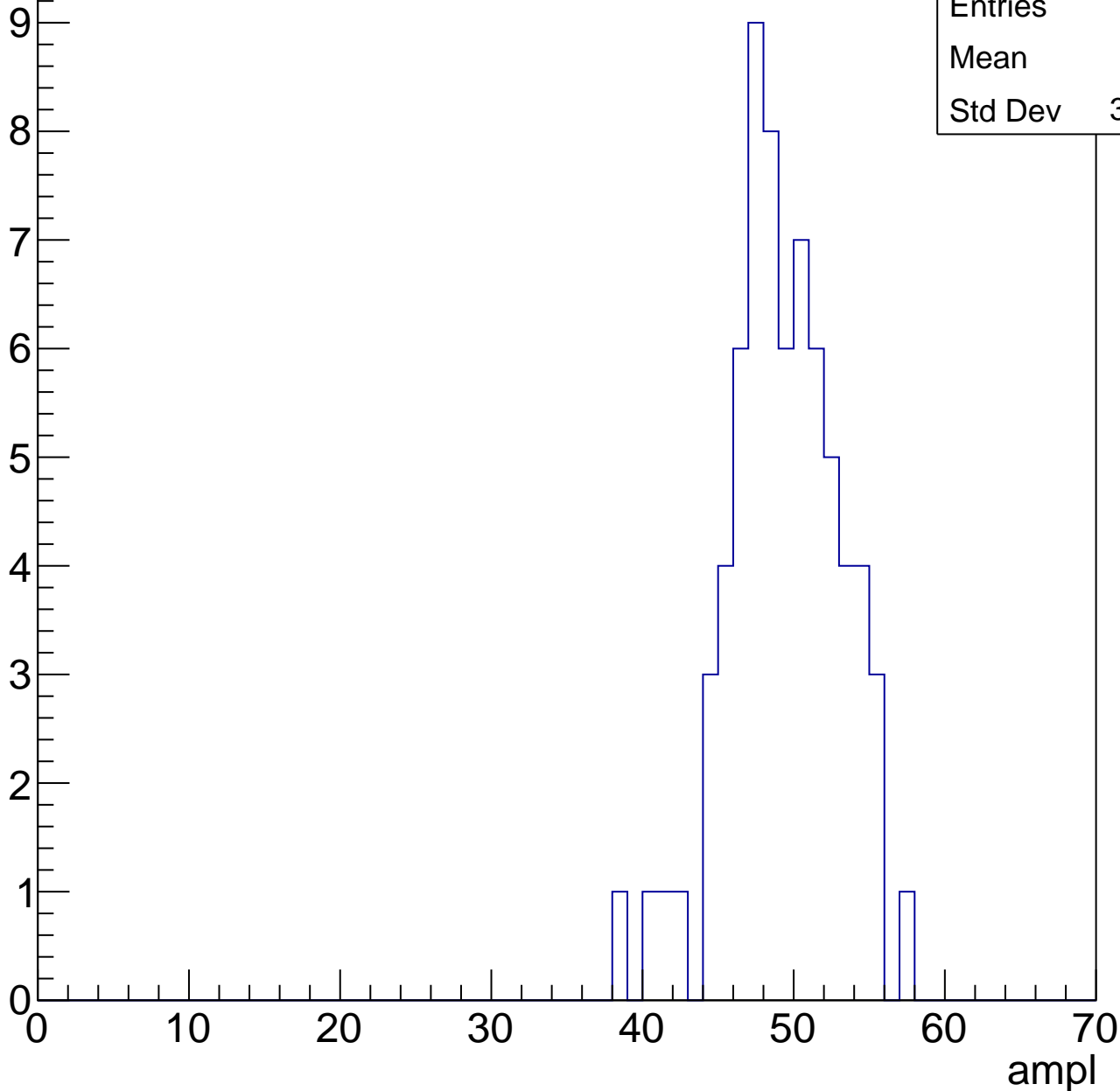


# B1L102S, U25-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.8
Std Dev	3.706



# B1L102S, U25-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

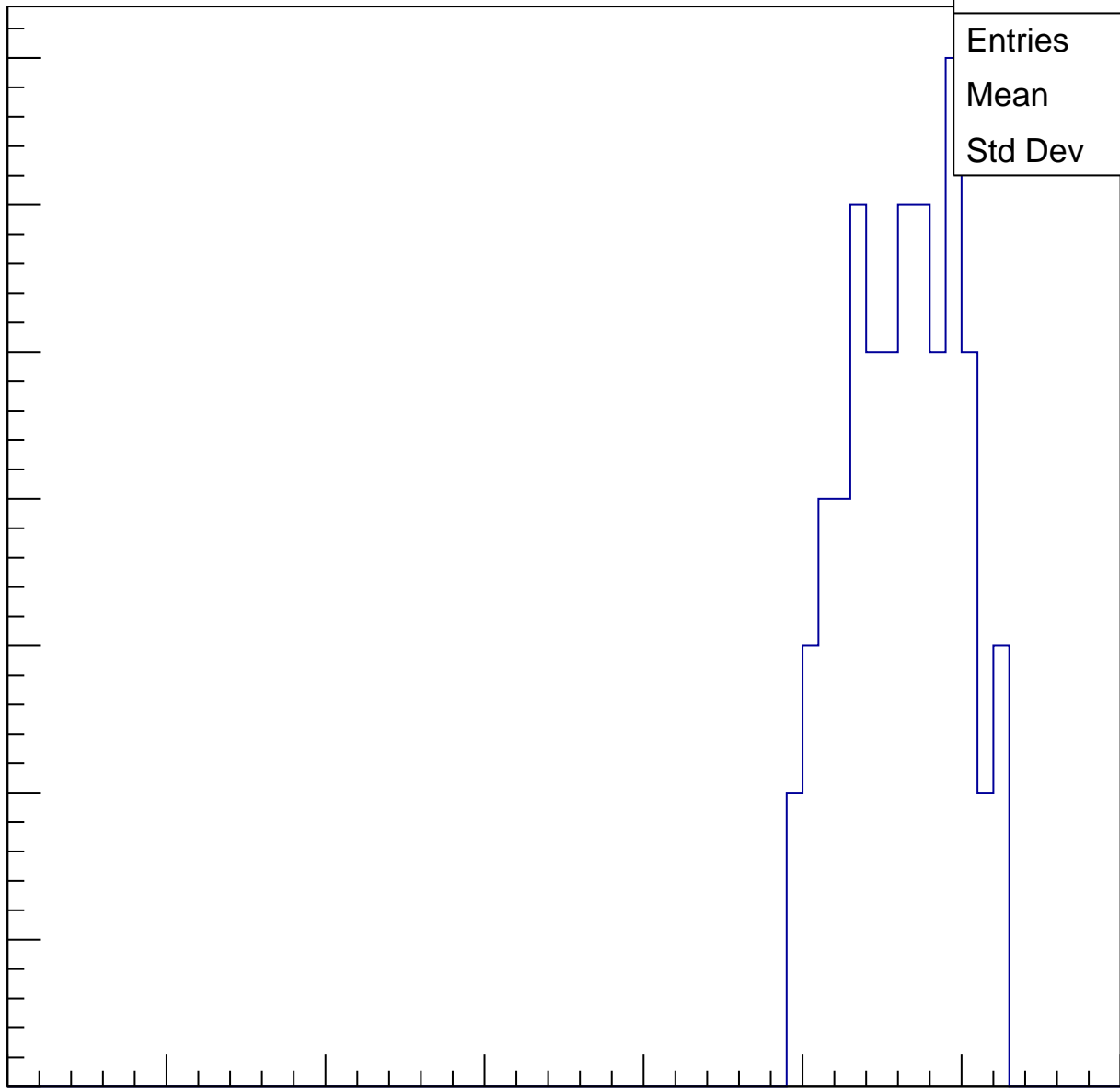
Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	55.75
Std Dev	3.5

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

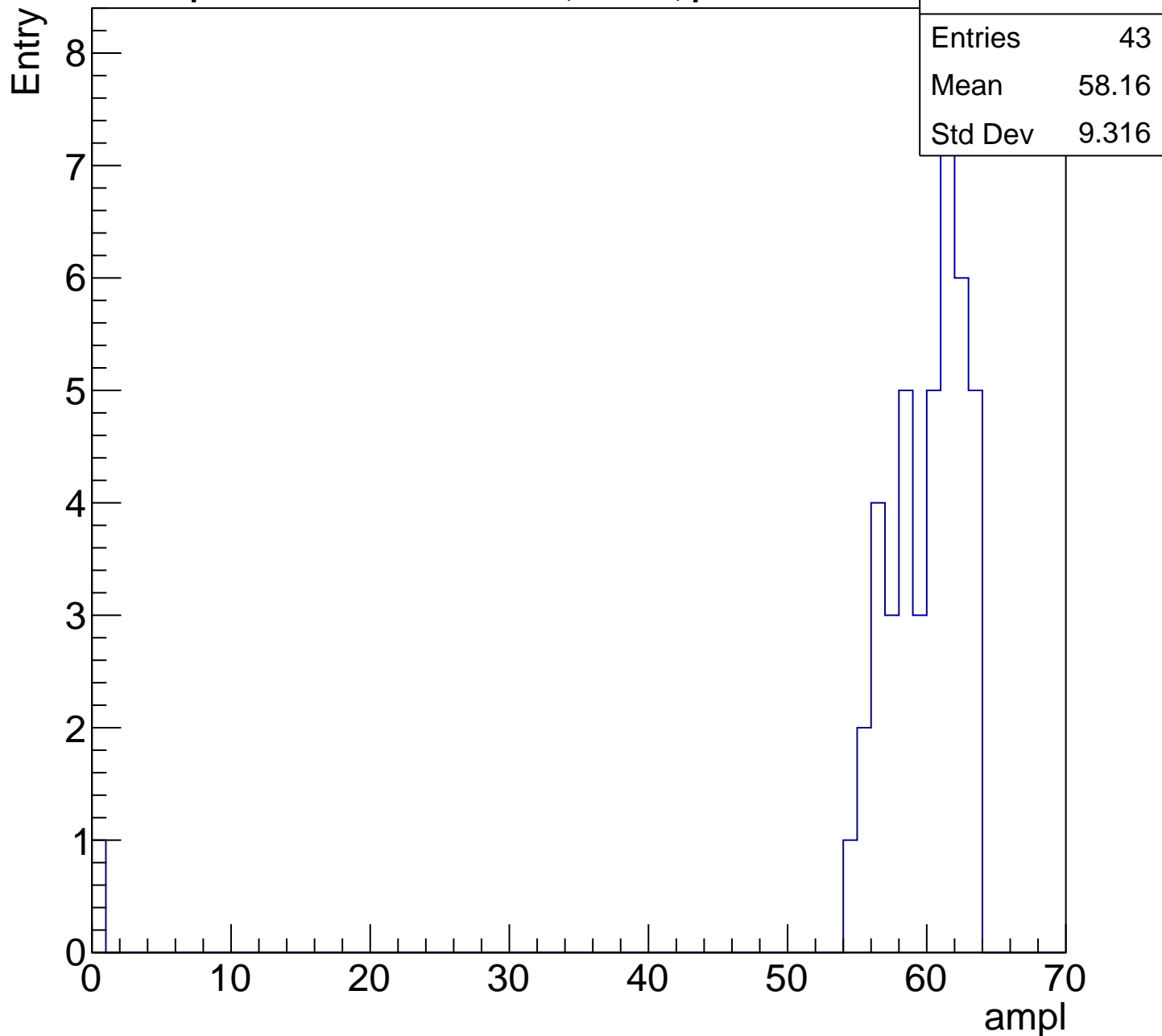
Entry

Entries	43
Mean	58.16
Std Dev	9.316

8  
7  
6  
5  
4  
3  
2  
1  
0

ampl

0 10 20 30 40 50 60 70

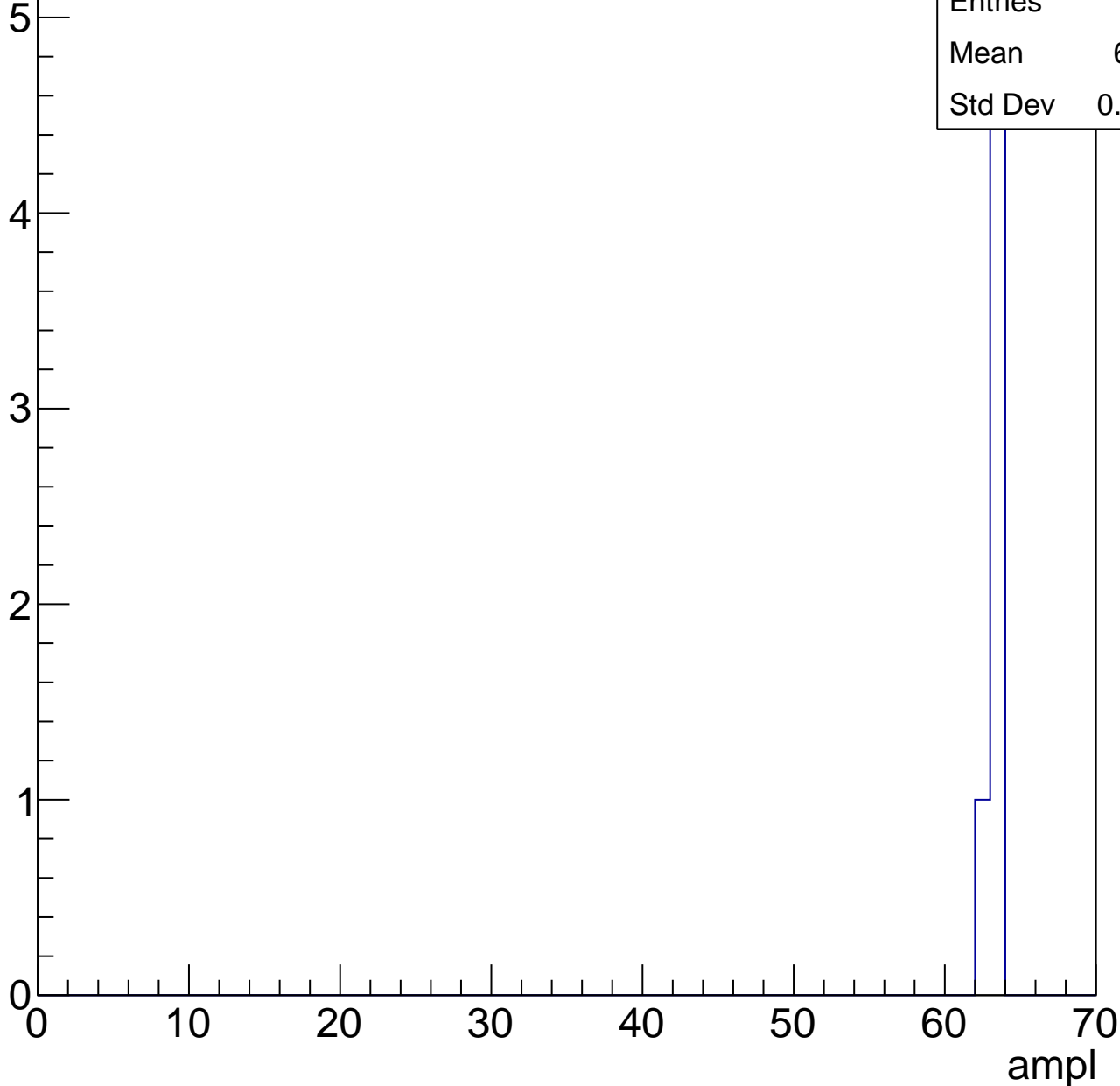


# B1L102S, U25-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727





# B1L102S, U25-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L102S, U25-ch51, adc0

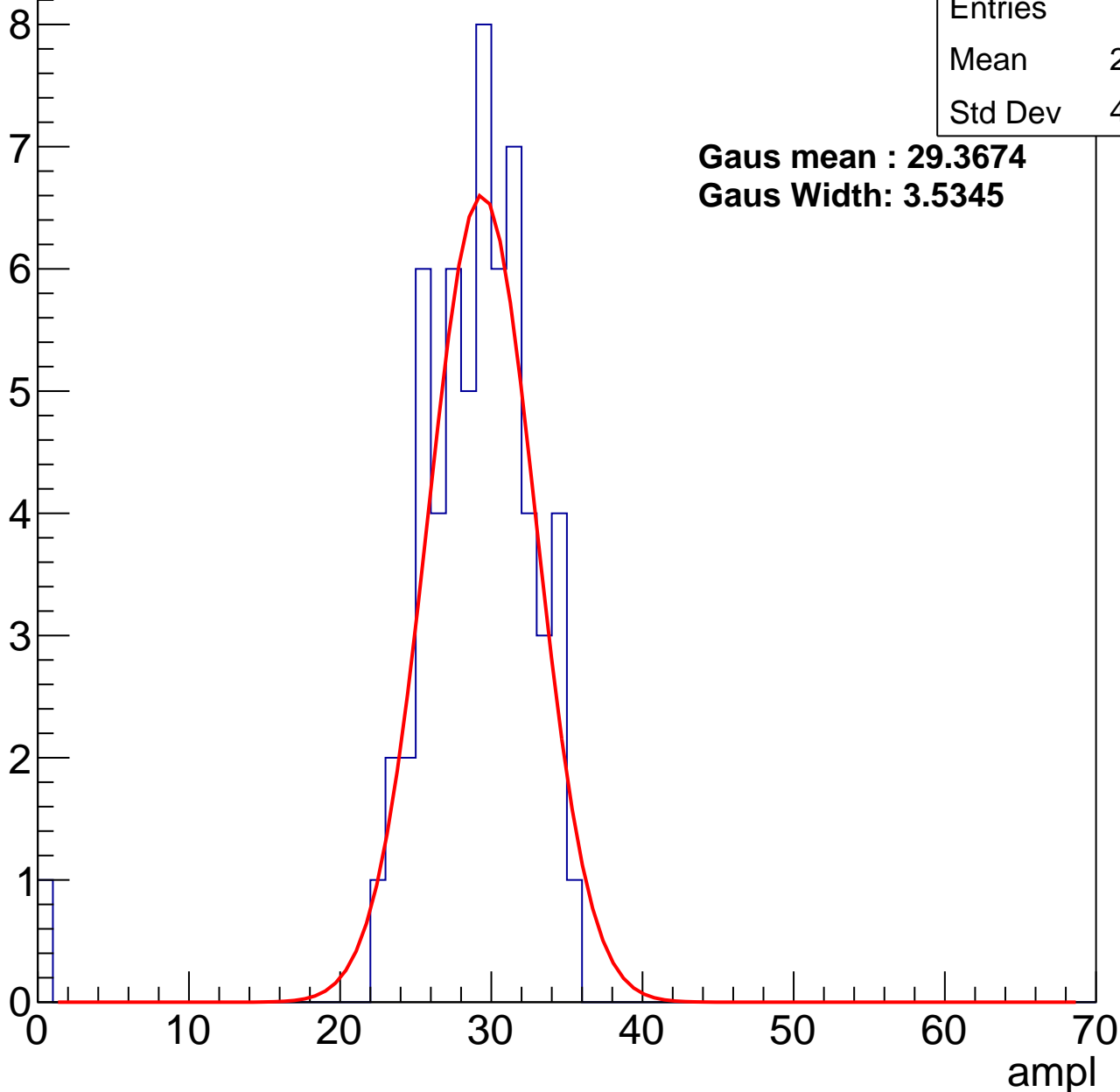
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.32
Std Dev	4.829

**Gaus mean : 29.3674**

**Gaus Width: 3.5345**

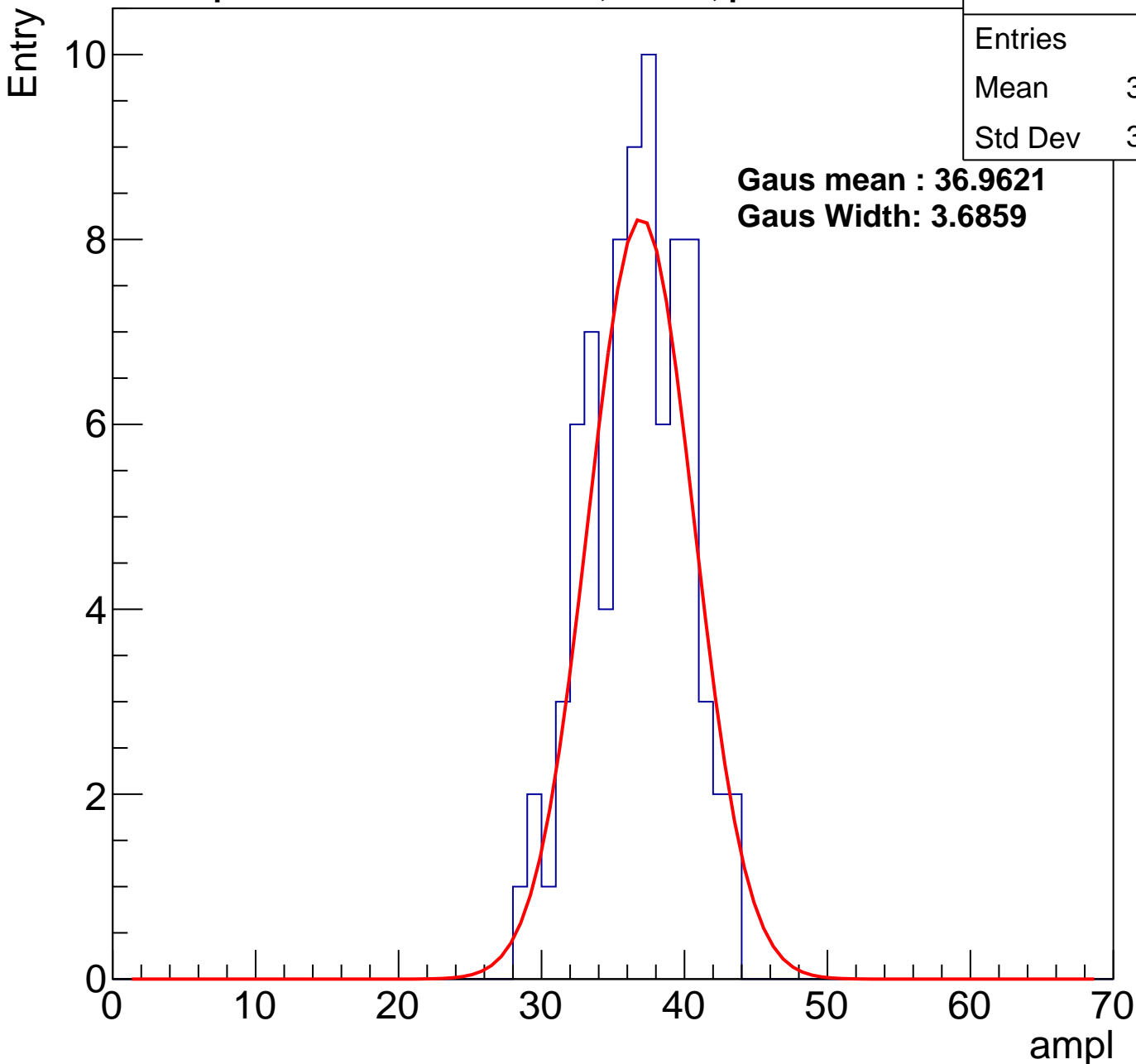


# B1L102S, U25-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	36.19
Std Dev	3.414

**Gaus mean : 36.9621**  
**Gaus Width: 3.6859**



# B1L102S, U25-ch51, adc2

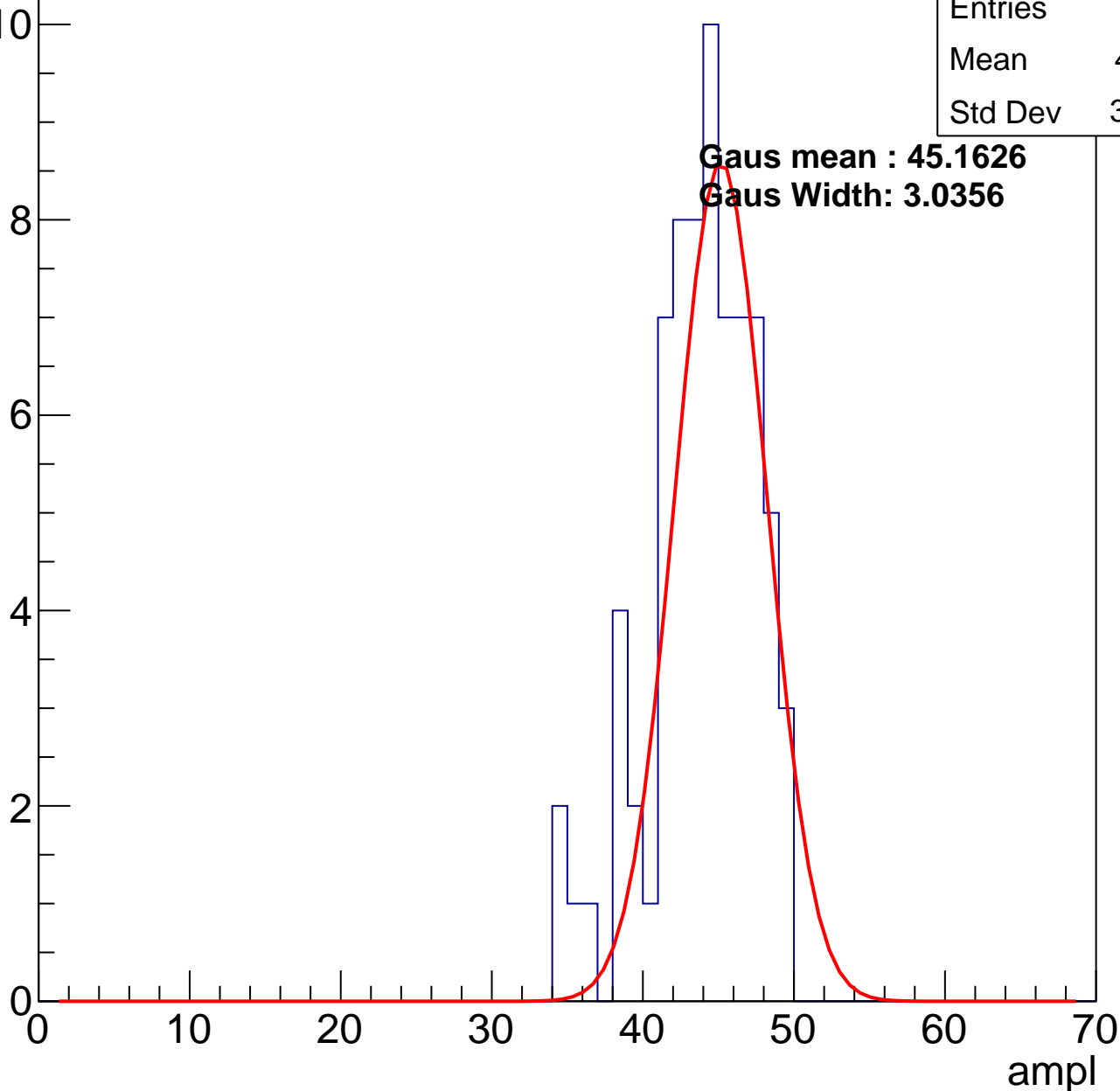
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.41
Std Dev	3.483

**Gaus mean : 45.1626**

**Gaus Width: 3.0356**

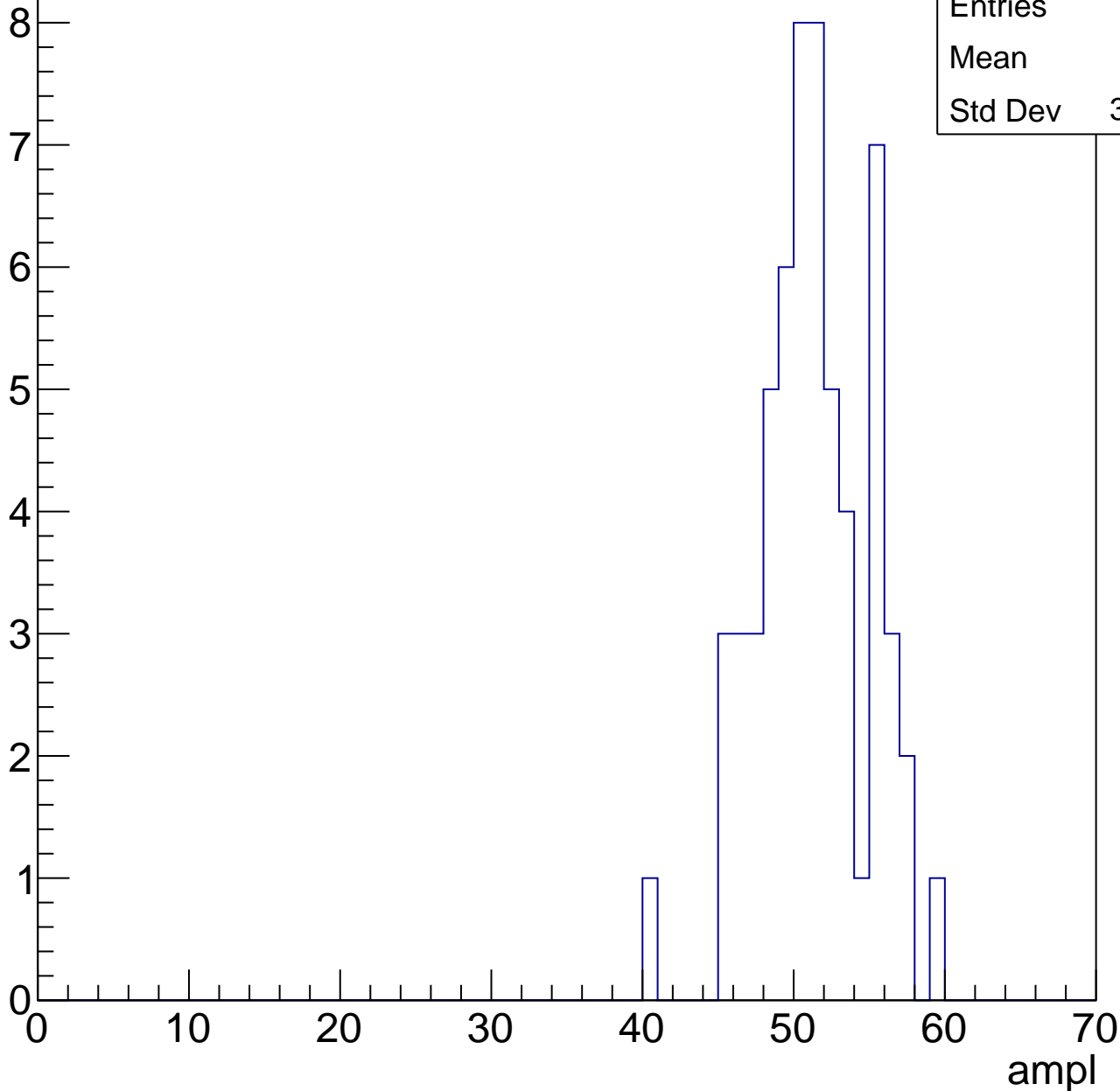


# B1L102S, U25-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.8
Std Dev	3.605

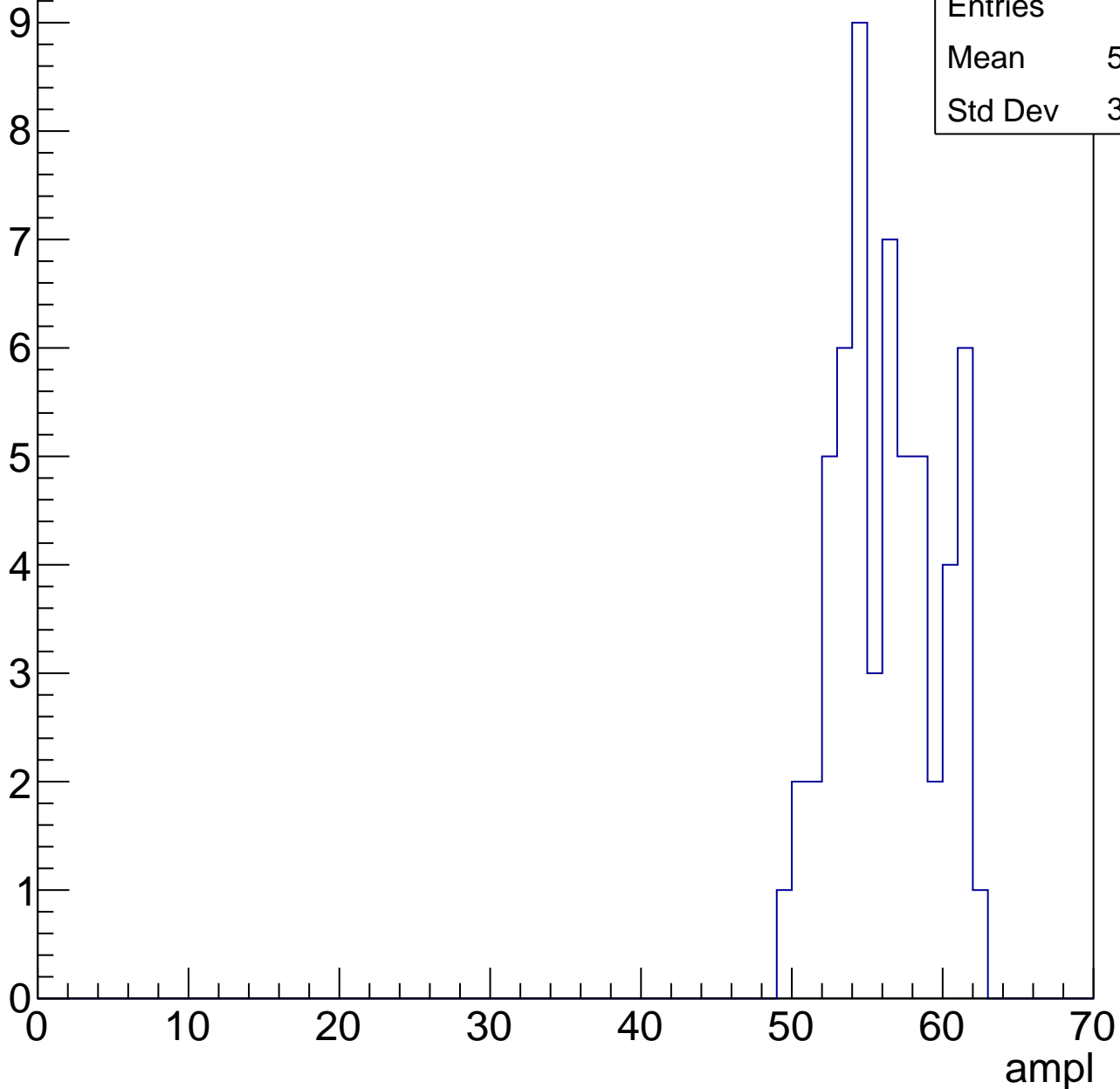


# B1L102S, U25-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.74
Std Dev	3.304

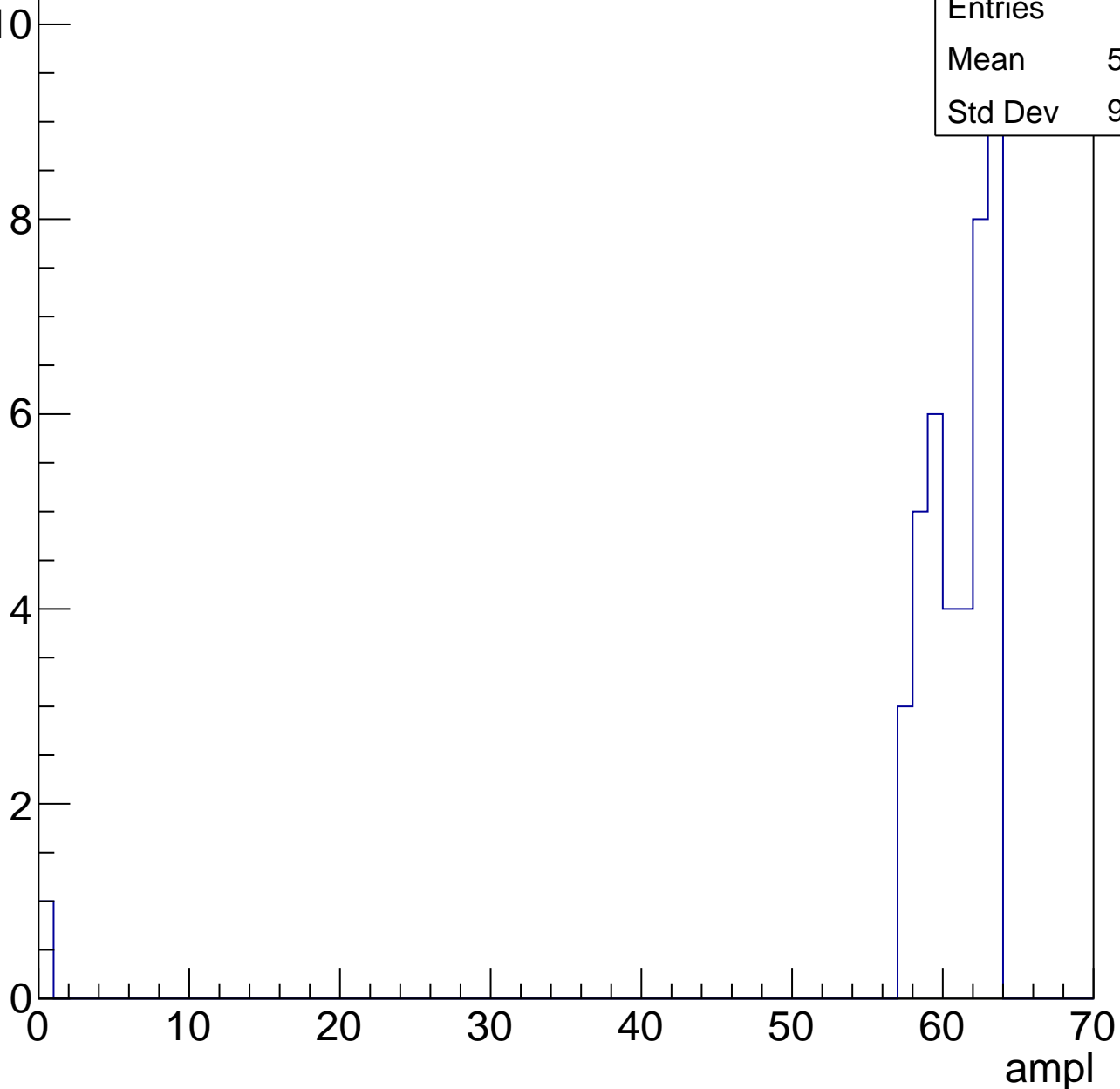


# B1L102S, U25-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

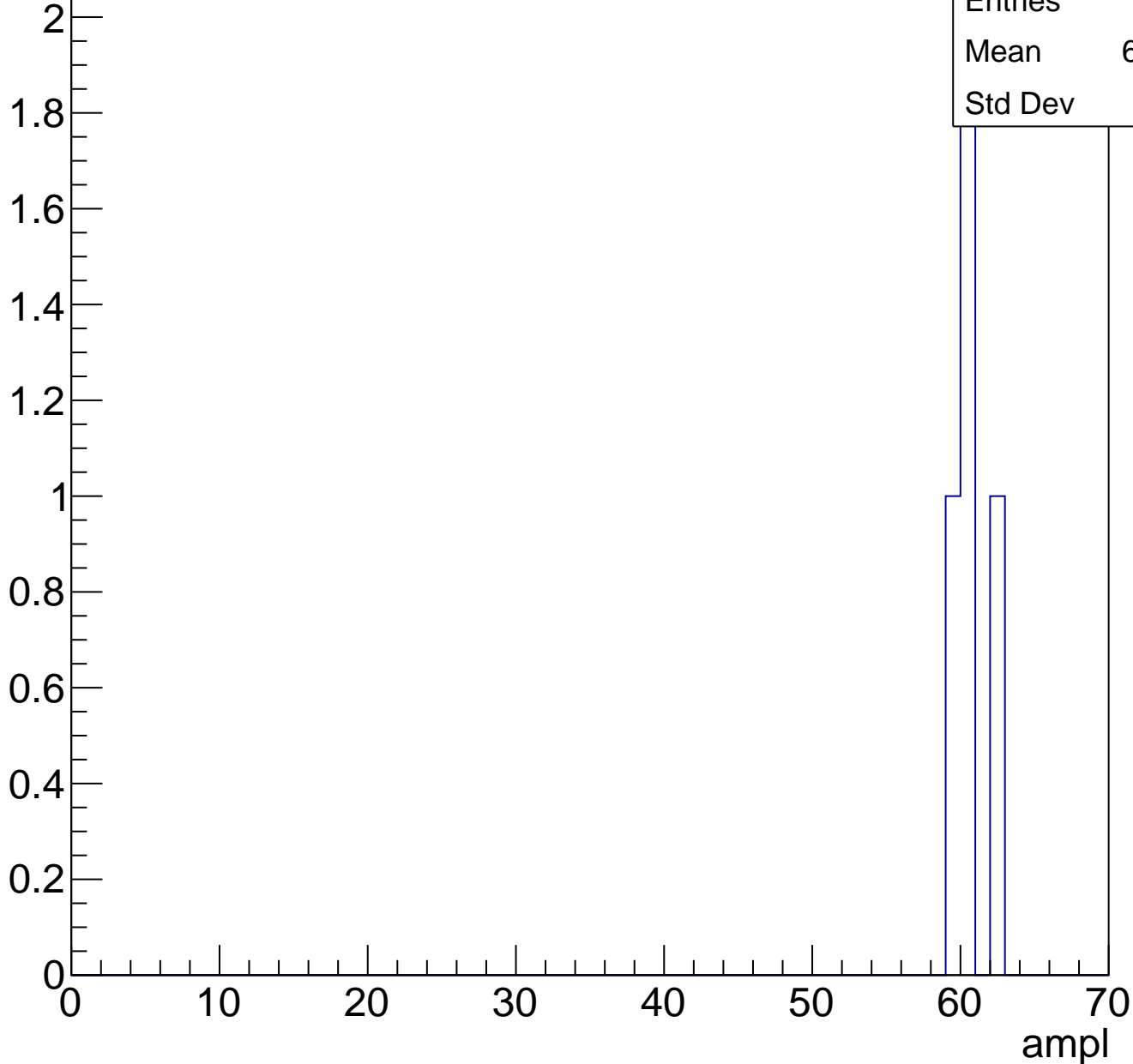
Entries	41
Mean	59.15
Std Dev	9.563



# B1L102S, U25-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch52, adc0

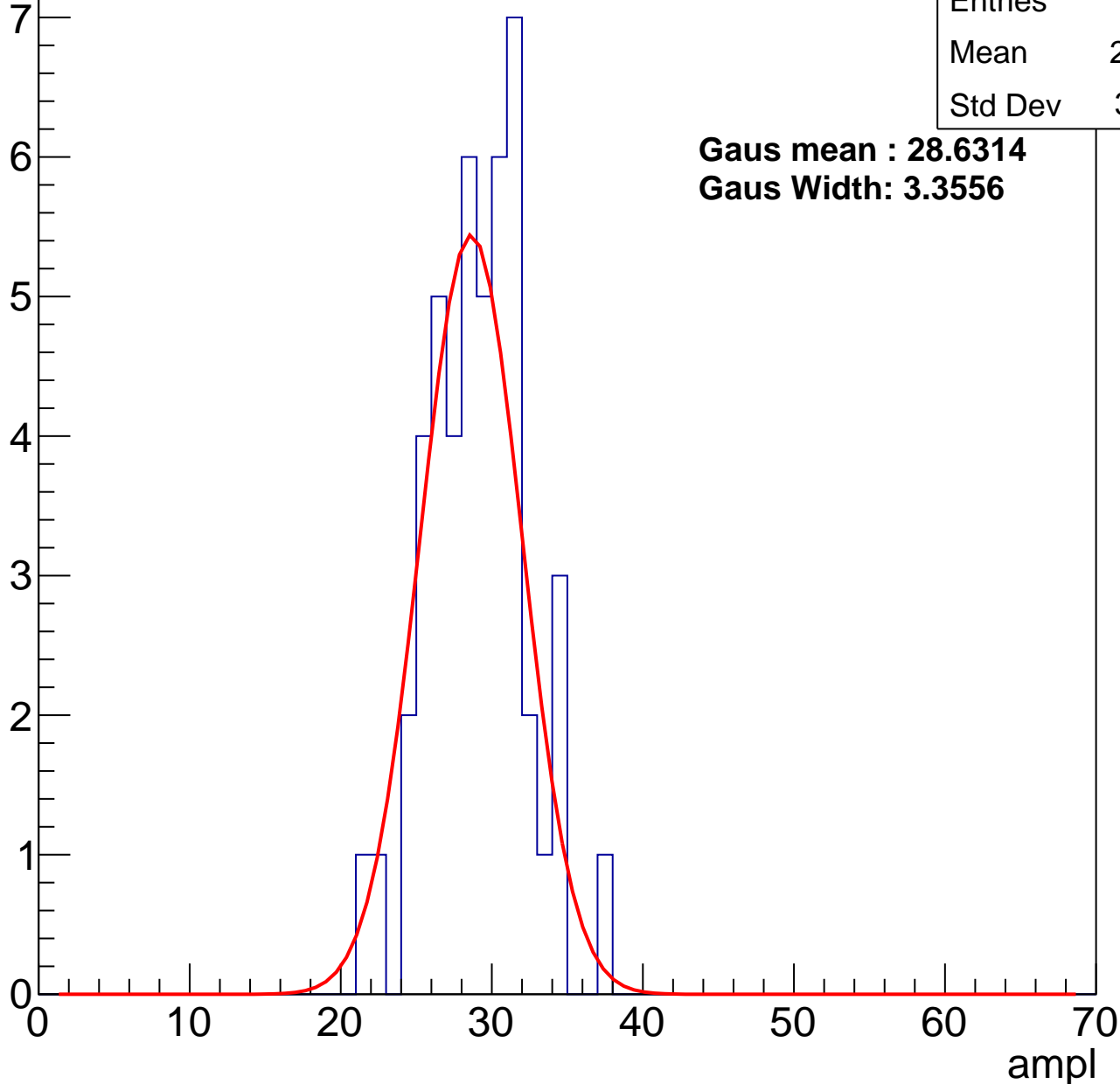
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	28.65
Std Dev	3.211

**Gaus mean : 28.6314**

**Gaus Width: 3.3556**



# B1L102S, U25-ch52, adc1

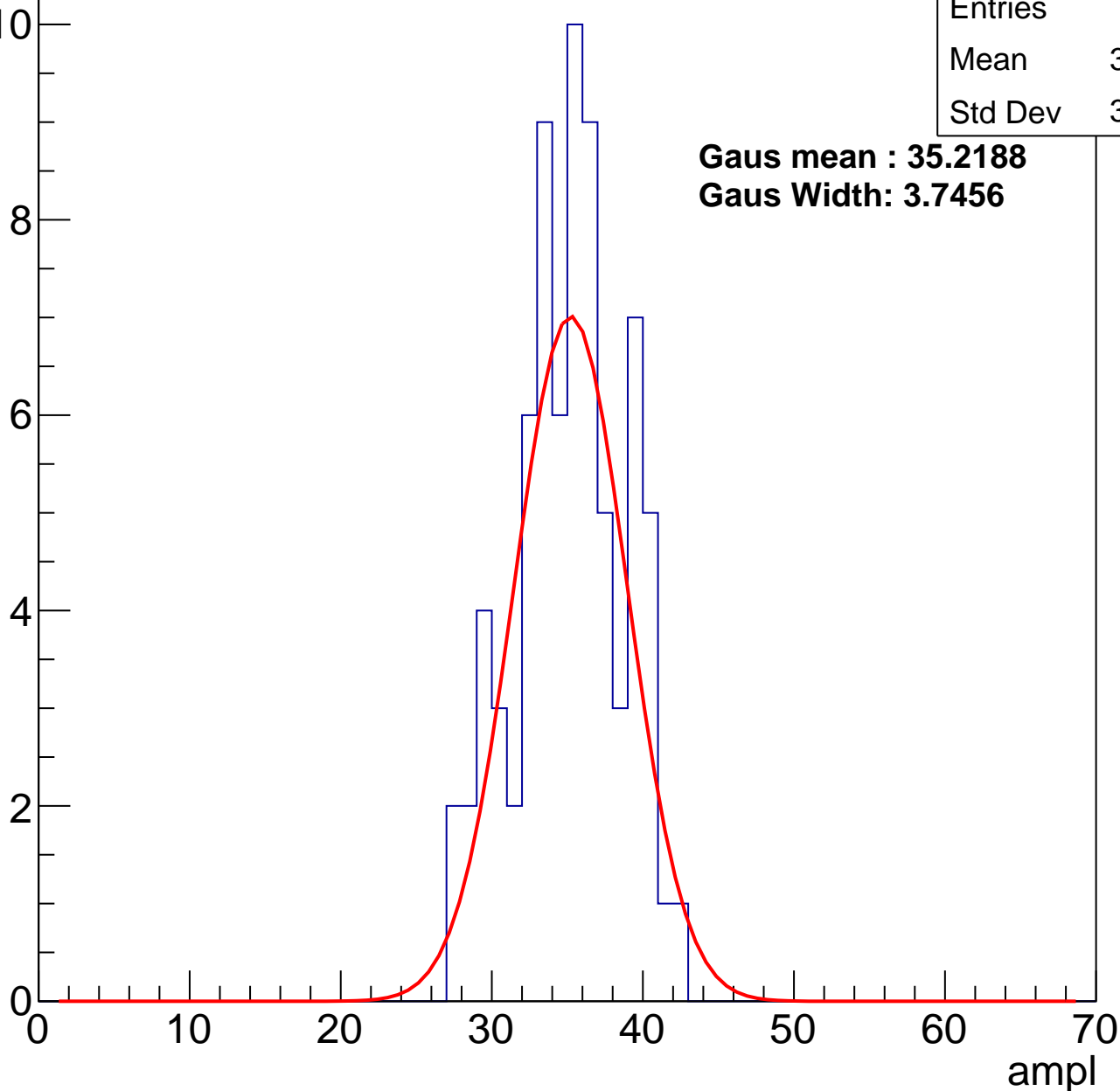
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.67
Std Dev	3.545

**Gaus mean : 35.2188**

**Gaus Width: 3.7456**



# B1L102S, U25-ch52, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.38
Std Dev	3.328

**Gaus mean : 43.1212**

**Gaus Width: 3.4189**

10

8

6

4

2

0

0

10

20

30

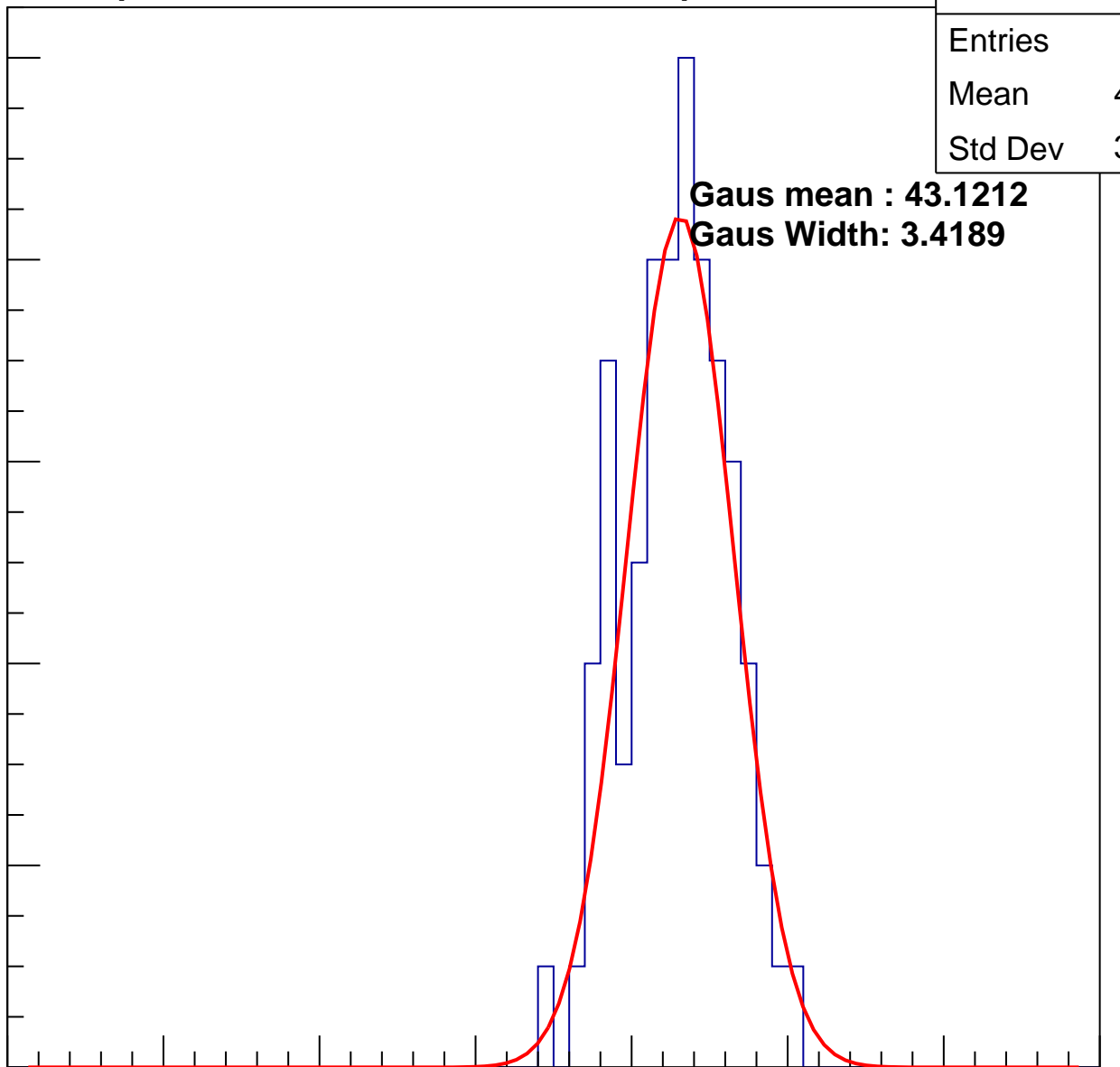
40

50

60

70

ampl

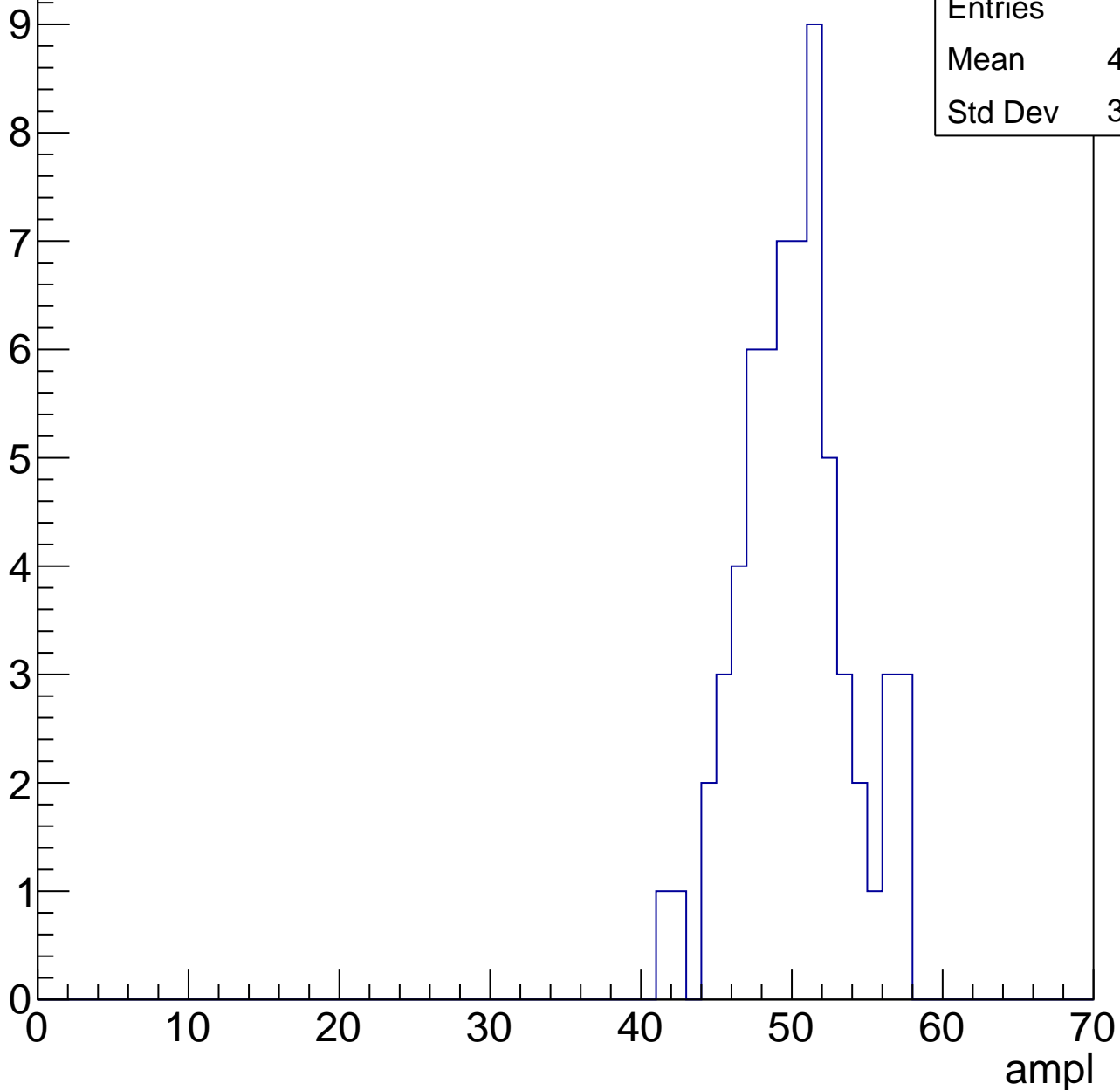


# B1L102S, U25-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

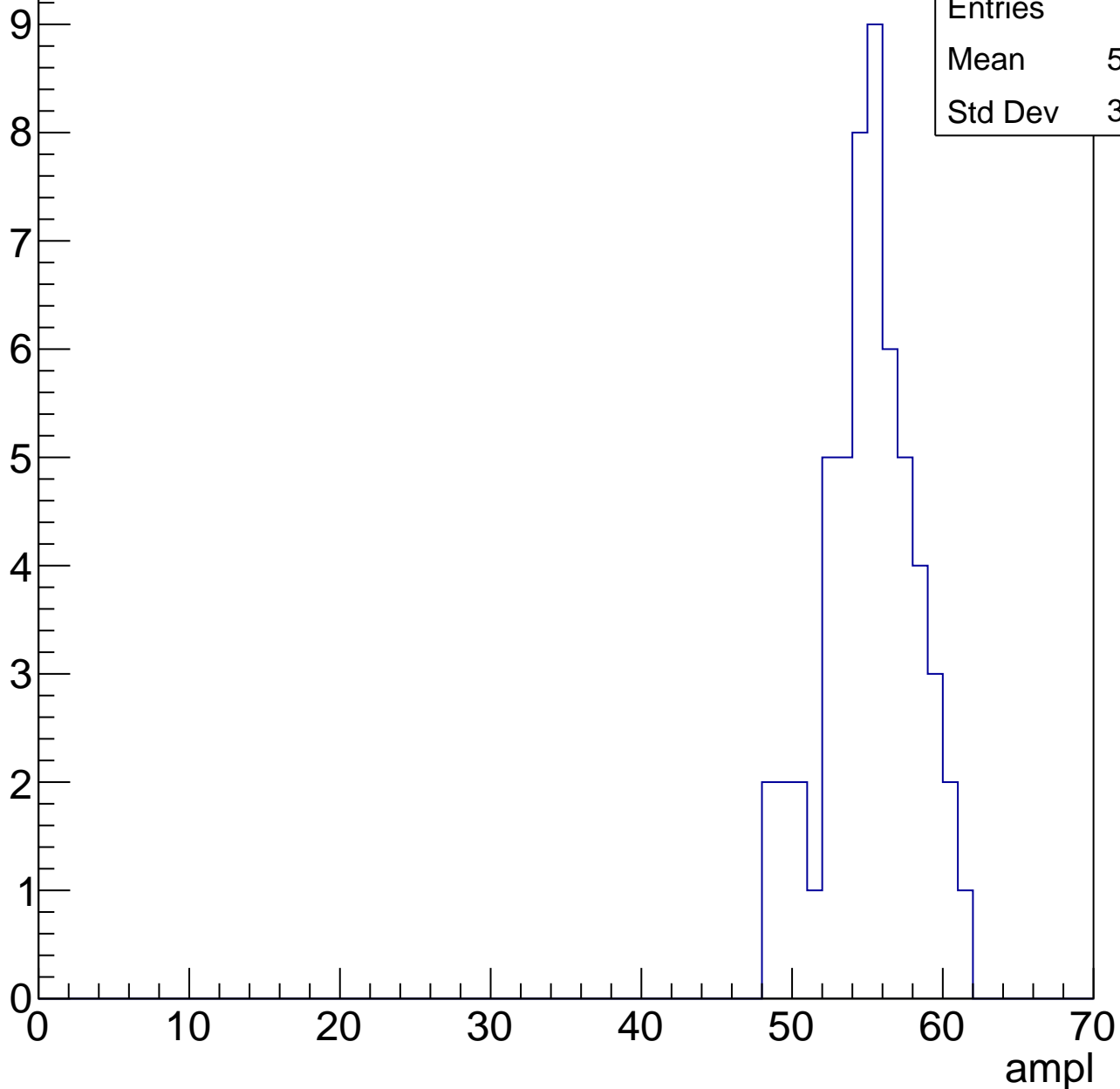
Entries	63
Mean	49.73
Std Dev	3.573



# B1L102S, U25-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	55
Mean	54.69
Std Dev	3.014

# B1L102S, U25-ch52, adc5

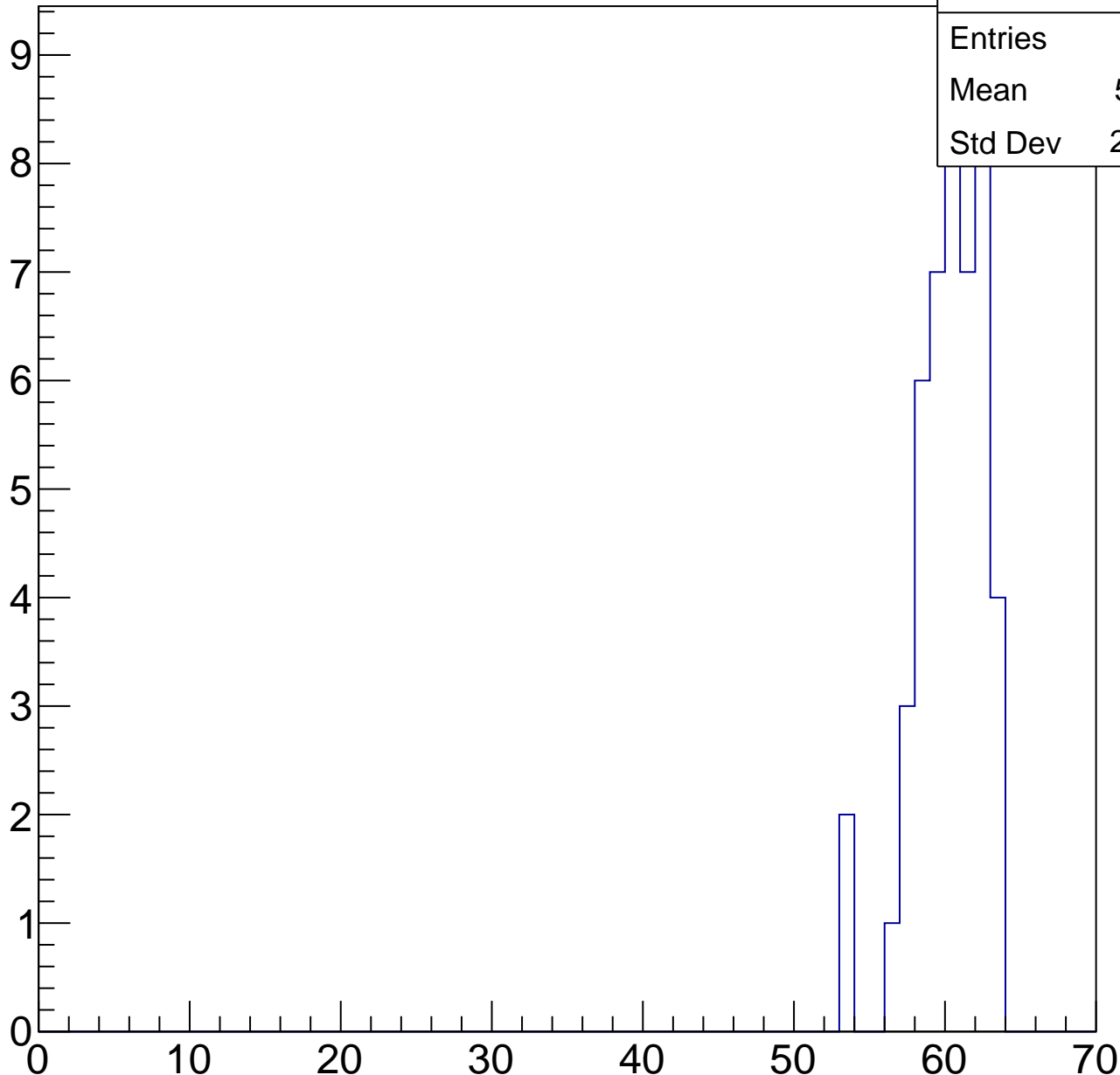
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	59.81
Std Dev	2.303

ampl

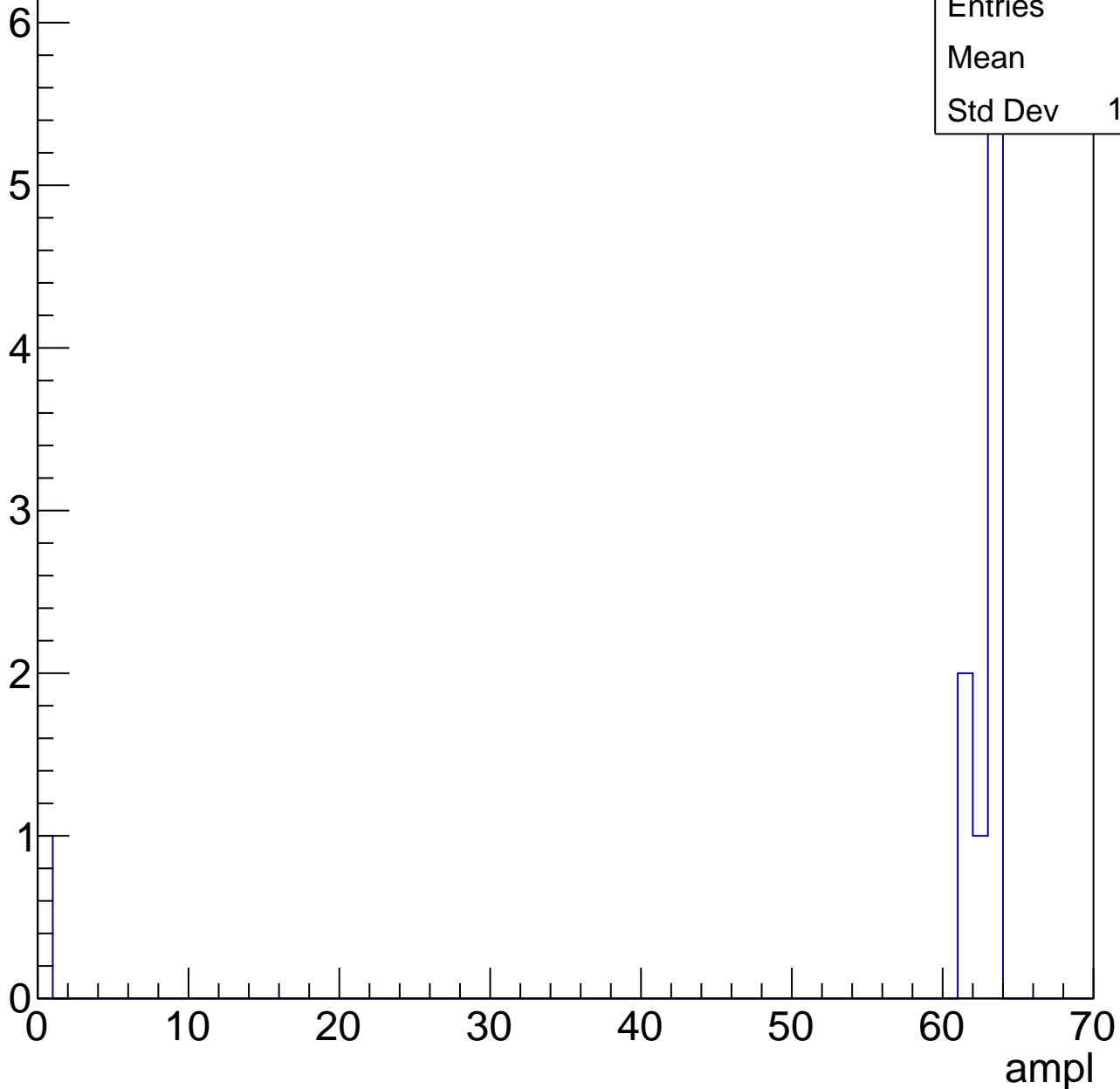


# B1L102S, U25-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	10
Mean	56.2
Std Dev	18.75





# B1L102S, U25-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch53, adc0

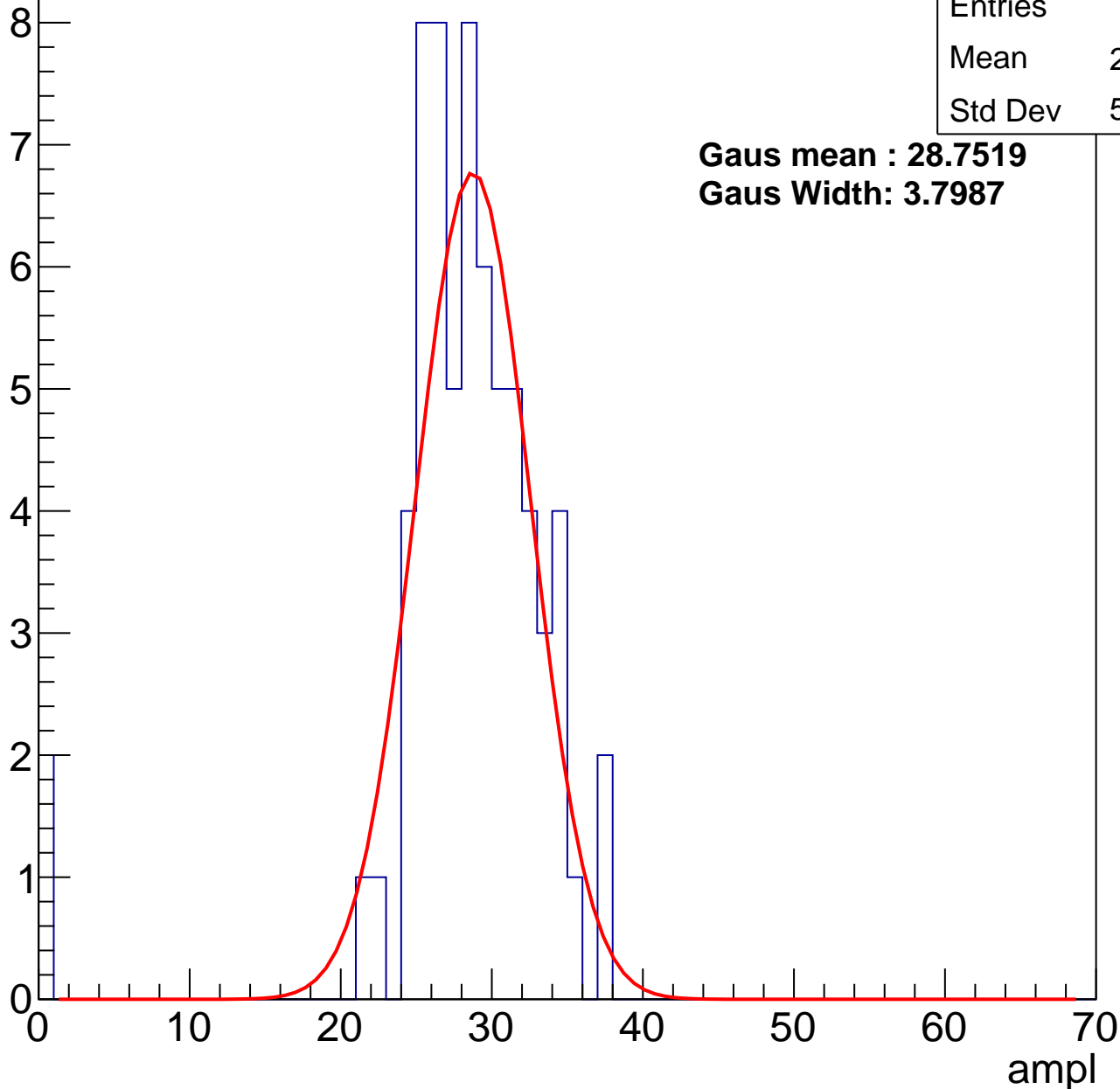
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.72
Std Dev	5.967

**Gaus mean : 28.7519**

**Gaus Width: 3.7987**



# B1L102S, U25-ch53, adc1

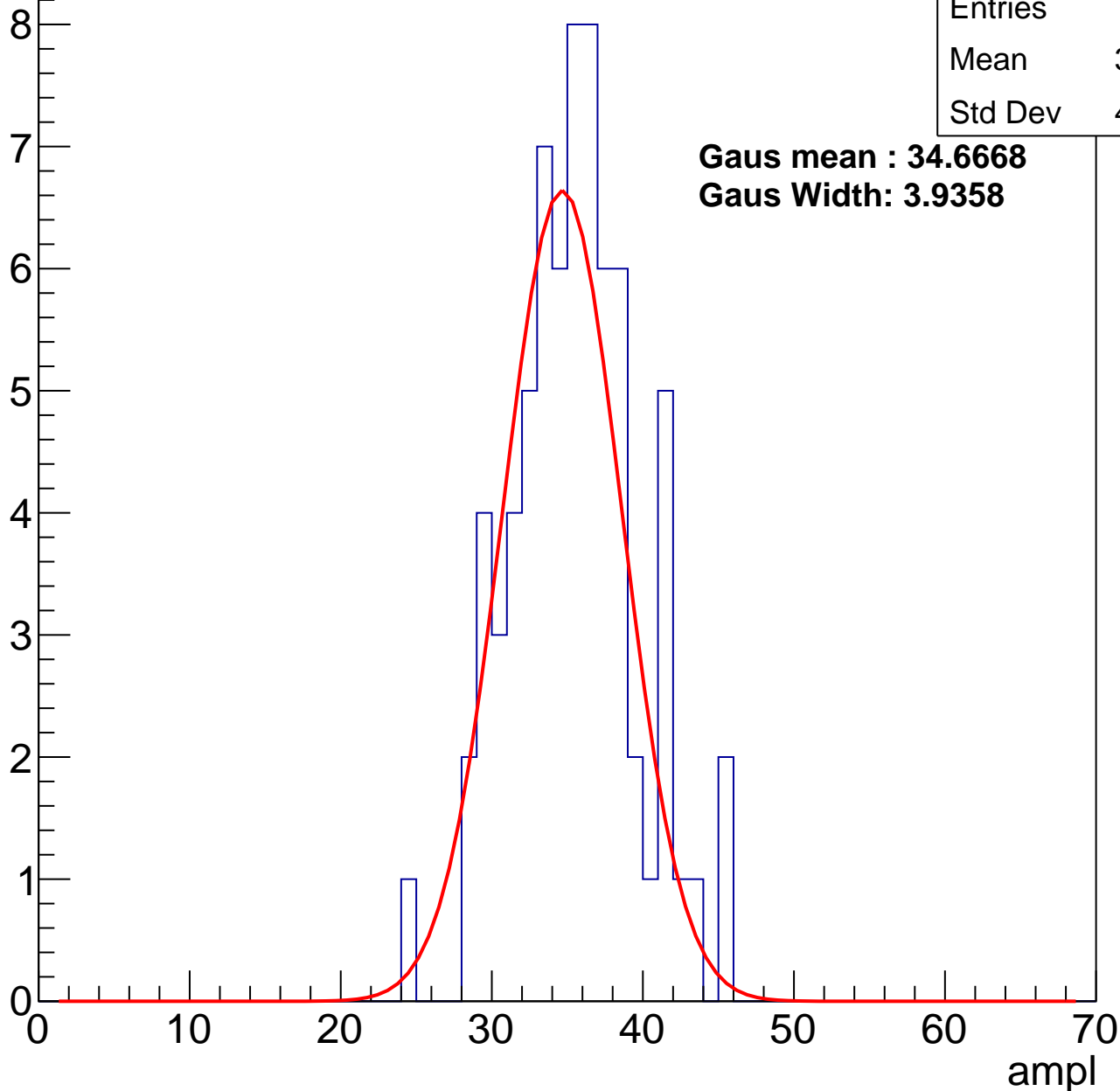
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.01
Std Dev	4.111

**Gaus mean : 34.6668**

**Gaus Width: 3.9358**



# B1L102S, U25-ch53, adc2

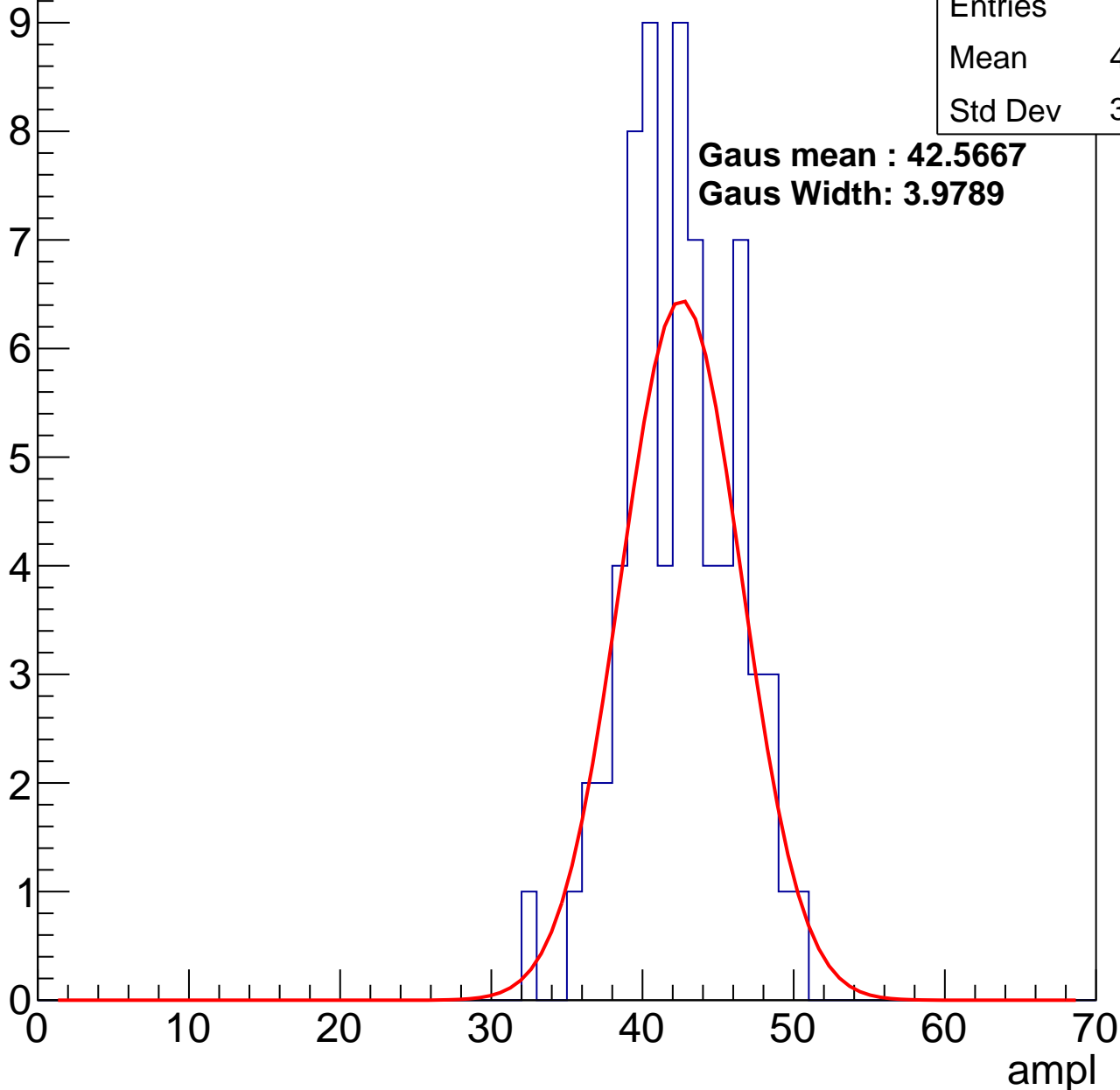
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.03
Std Dev	3.613

**Gaus mean : 42.5667**

**Gaus Width: 3.9789**

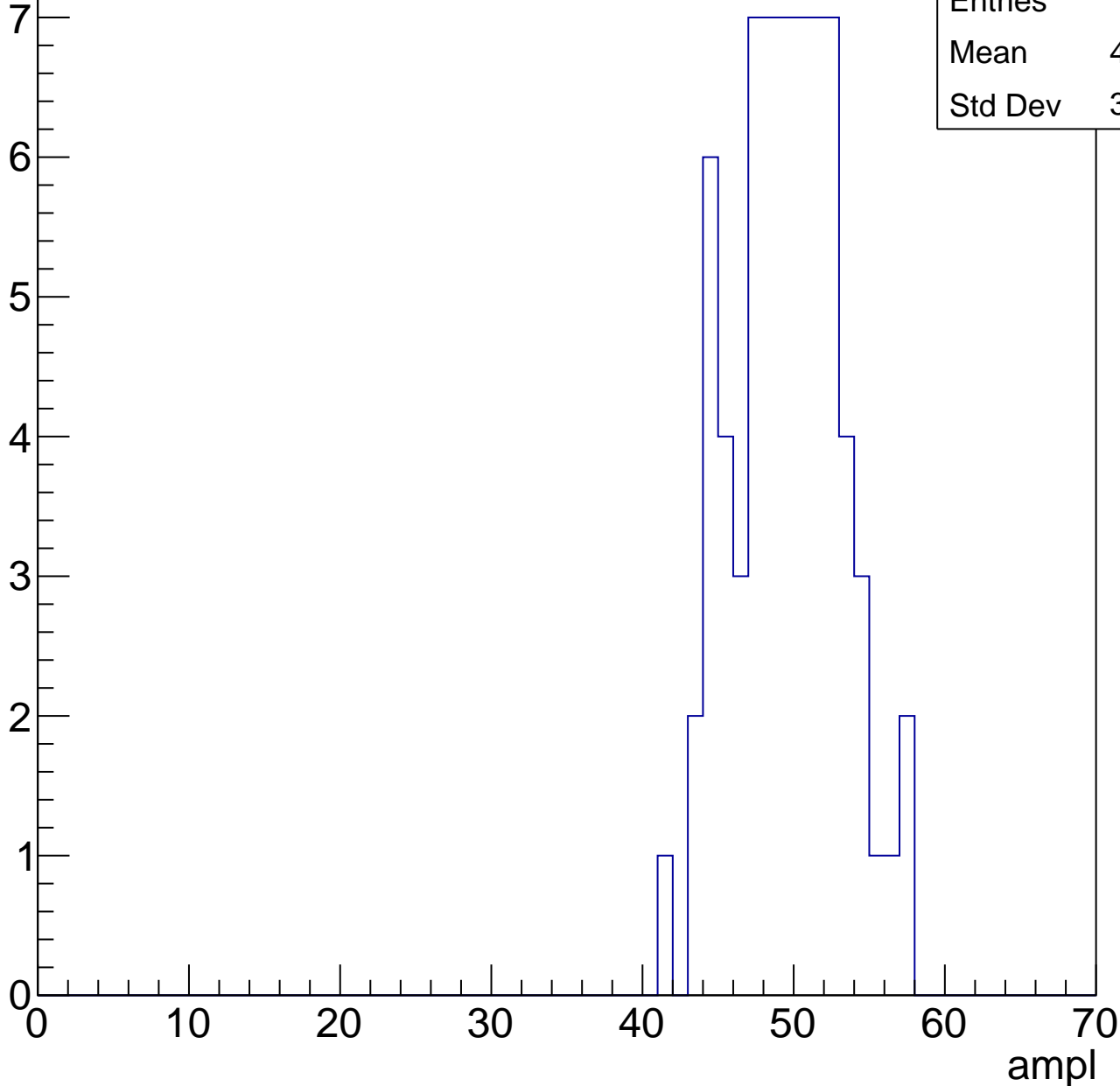


# B1L102S, U25-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

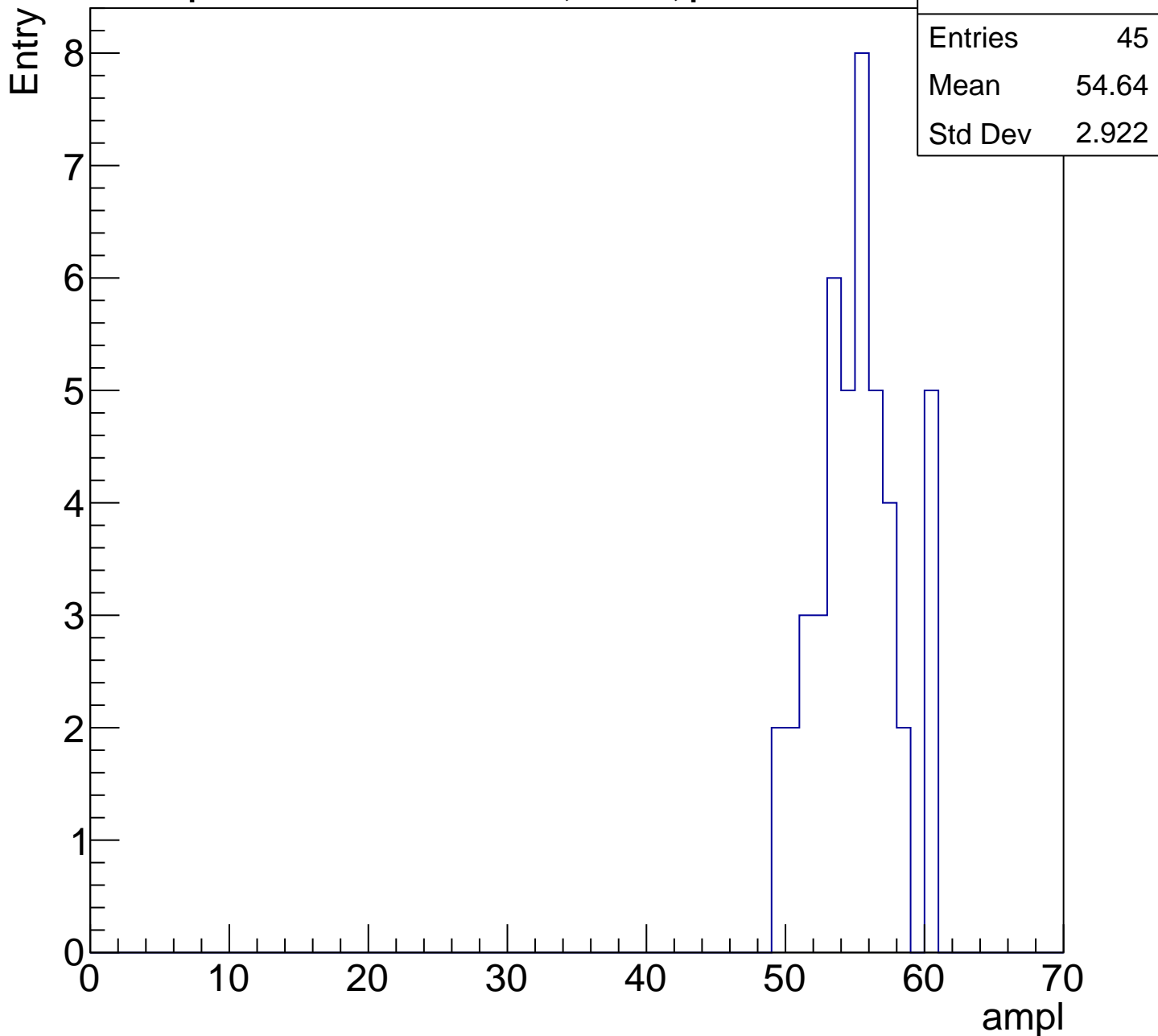
Entry

Entries	69
Mean	49.09
Std Dev	3.533



# B1L102S, U25-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

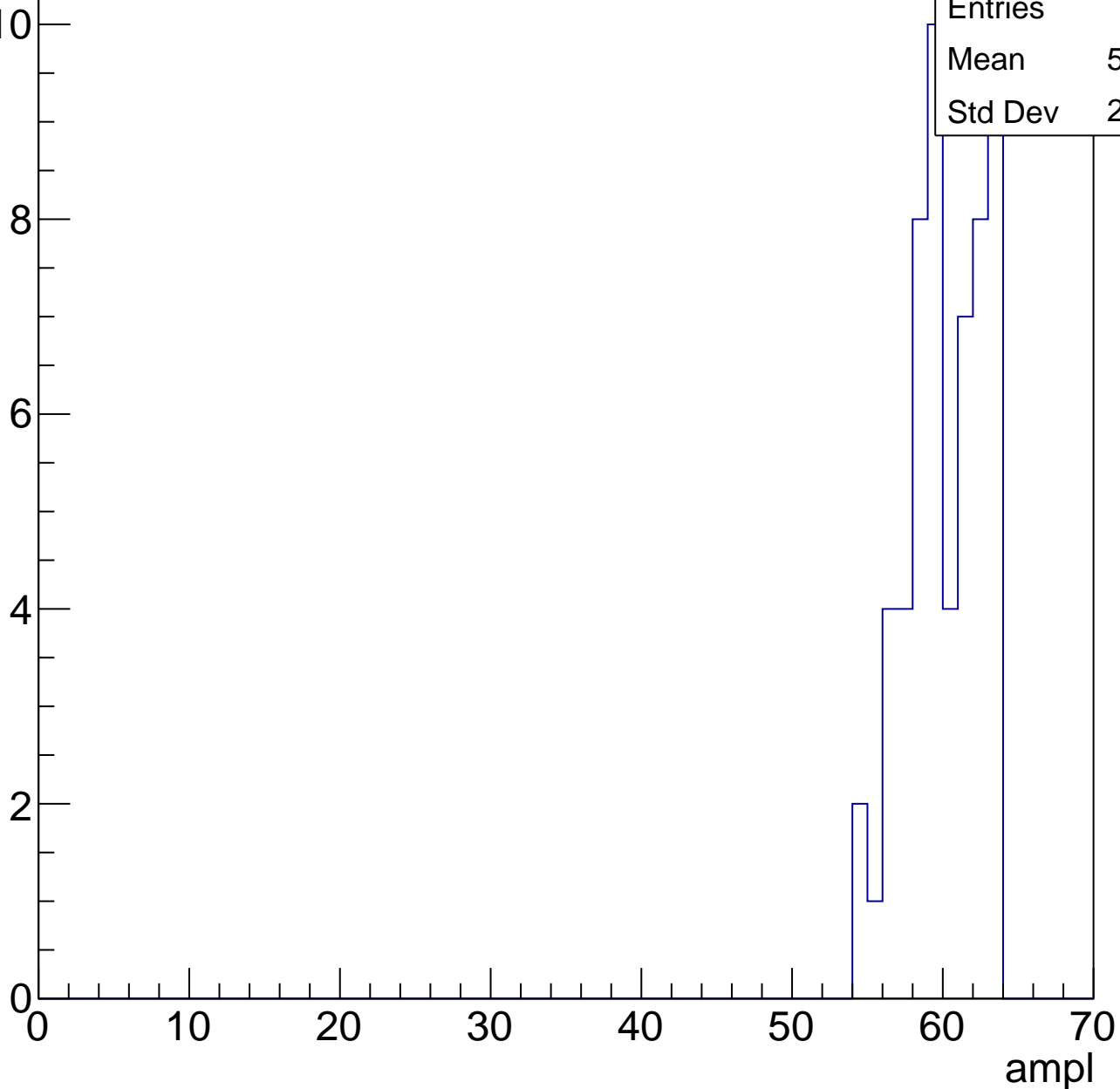


# B1L102S, U25-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

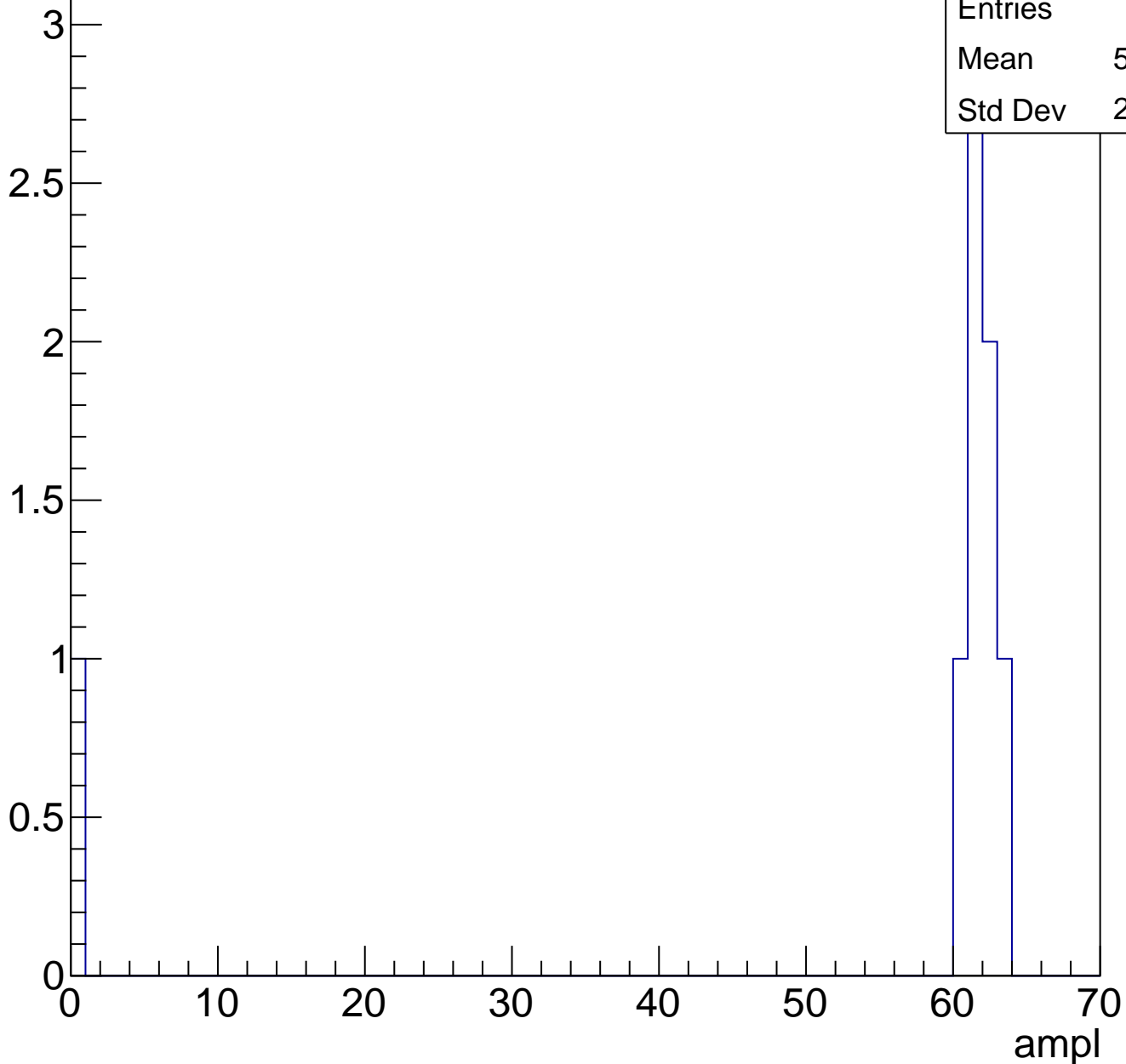
Entries	57
Mean	59.63
Std Dev	2.482



# B1L102S, U25-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch54, adc0

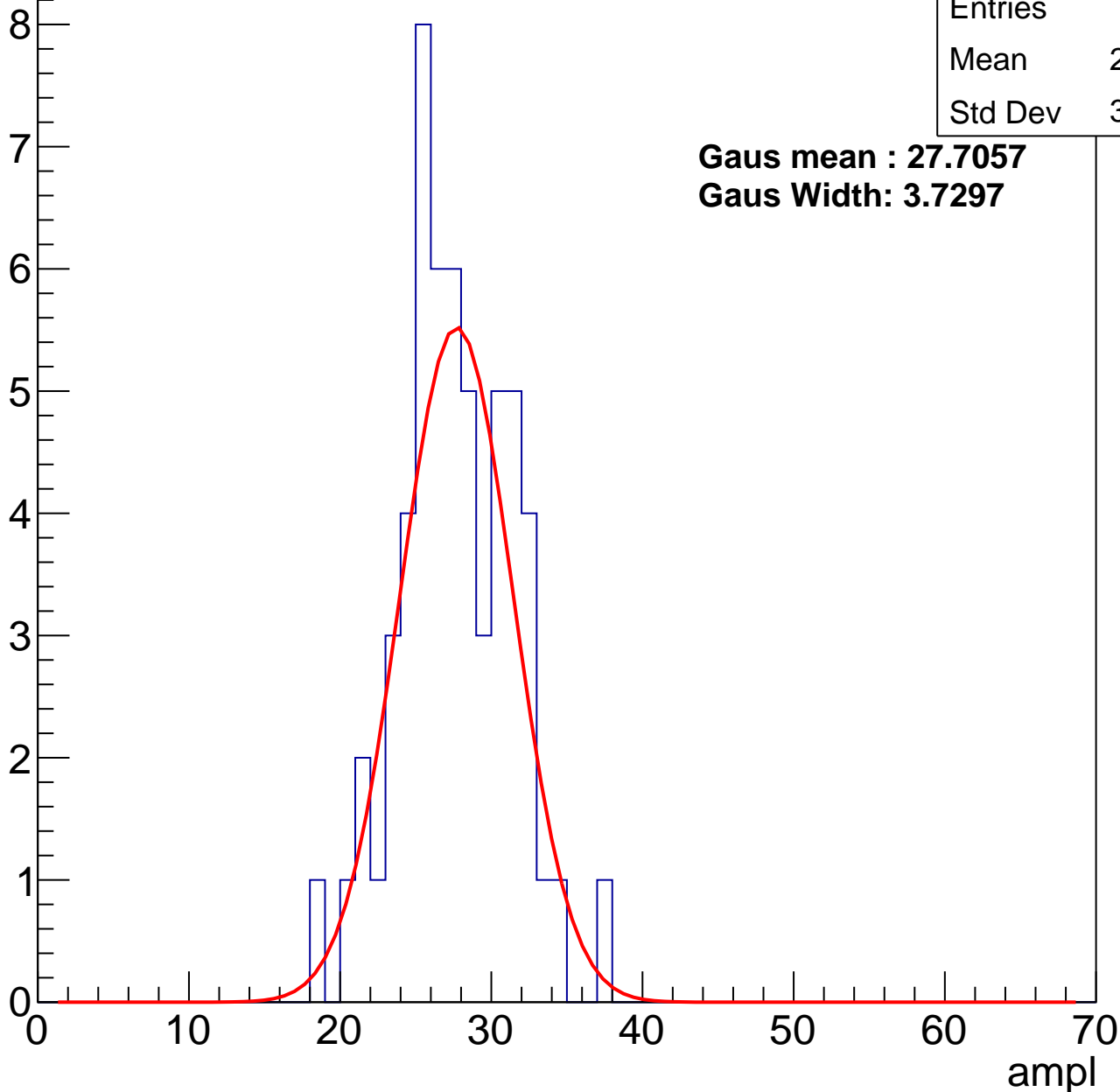
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.18
Std Dev	3.686

**Gaus mean : 27.7057**

**Gaus Width: 3.7297**



# B1L102S, U25-ch54, adc1

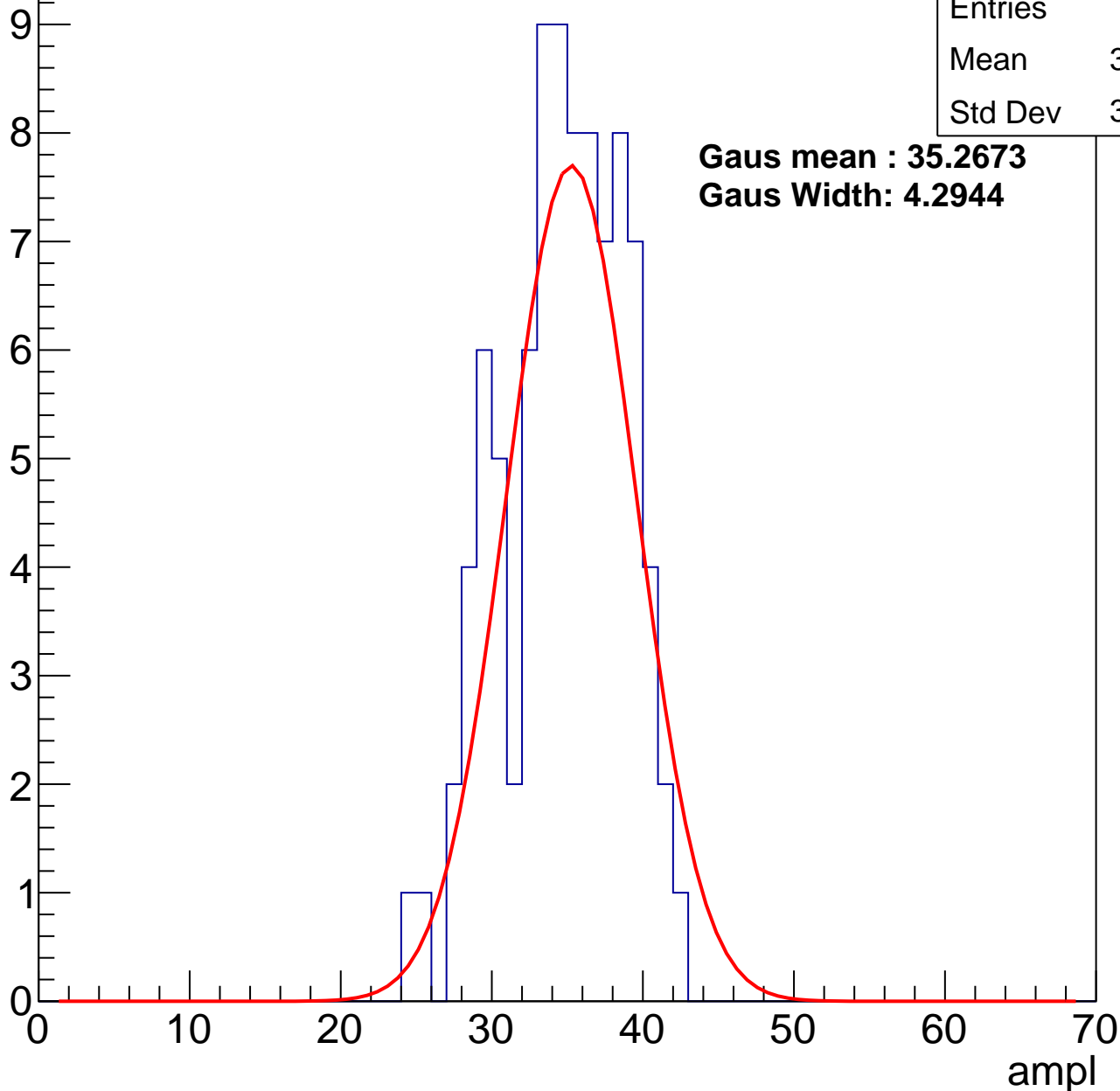
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	90
Mean	34.27
Std Dev	3.958

**Gaus mean : 35.2673**

**Gaus Width: 4.2944**



# B1L102S, U25-ch54, adc2

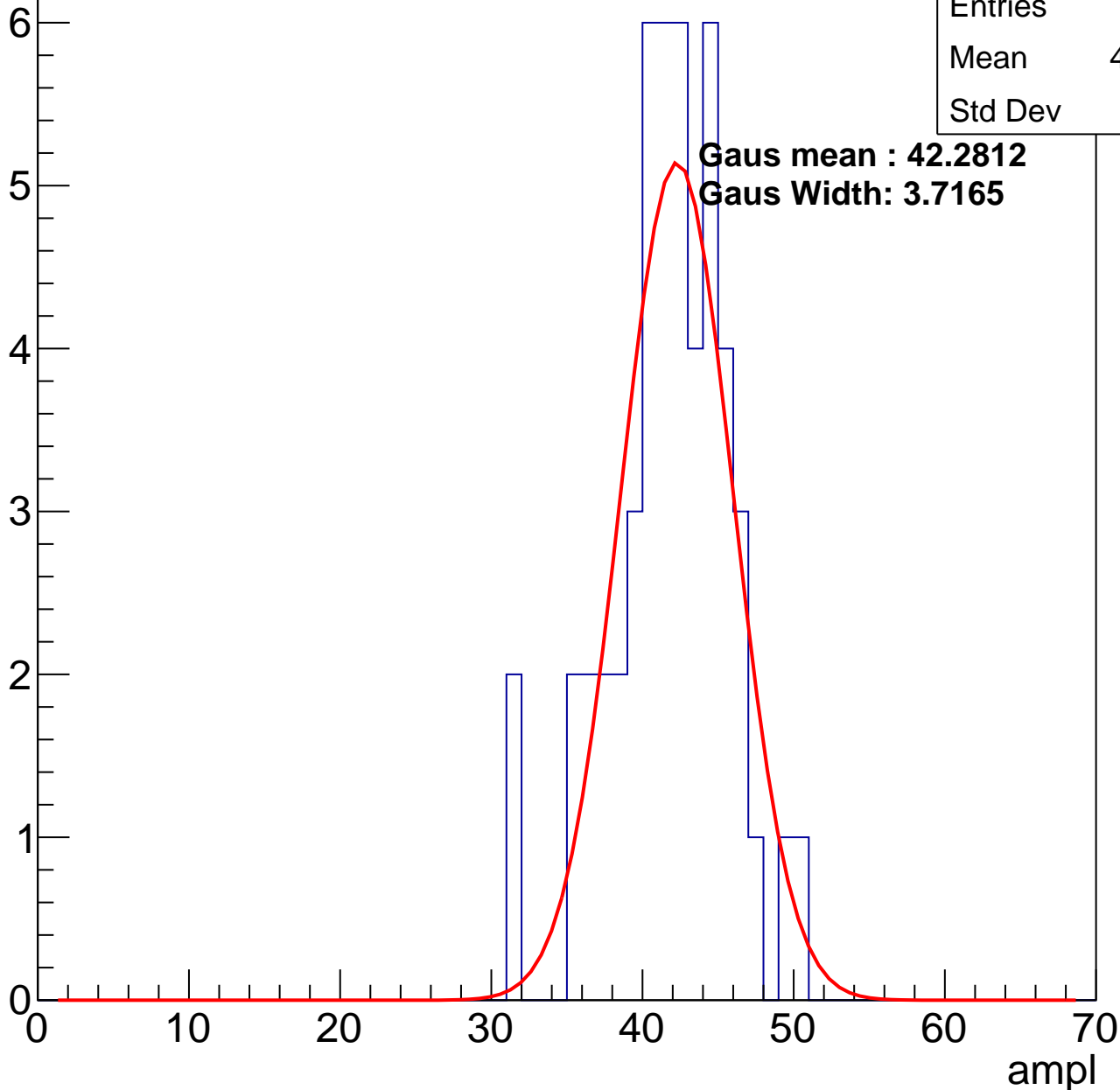
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.35
Std Dev	3.91

**Gaus mean : 42.2812**

**Gaus Width: 3.7165**

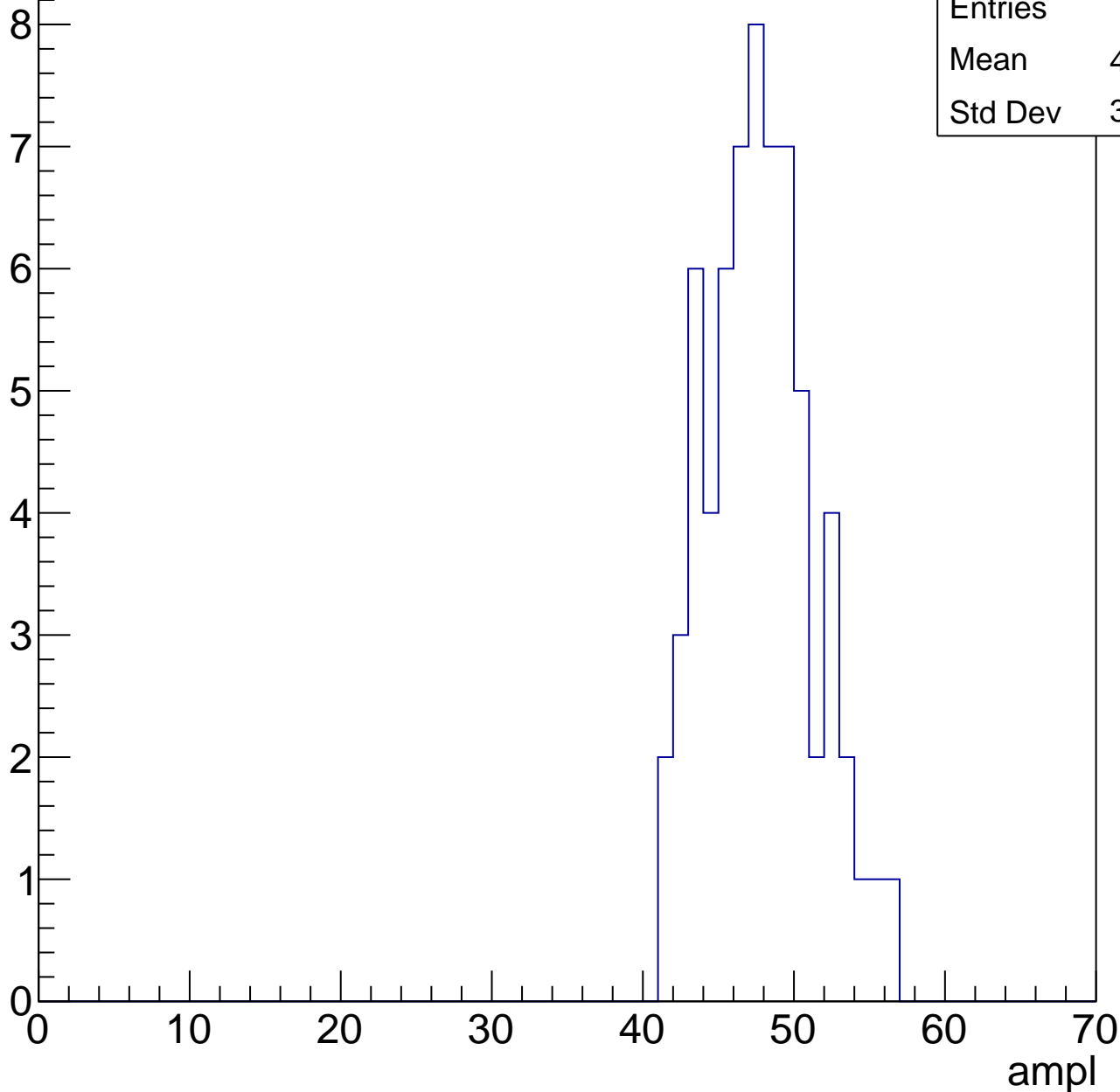


# B1L102S, U25-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.27
Std Dev	3.445

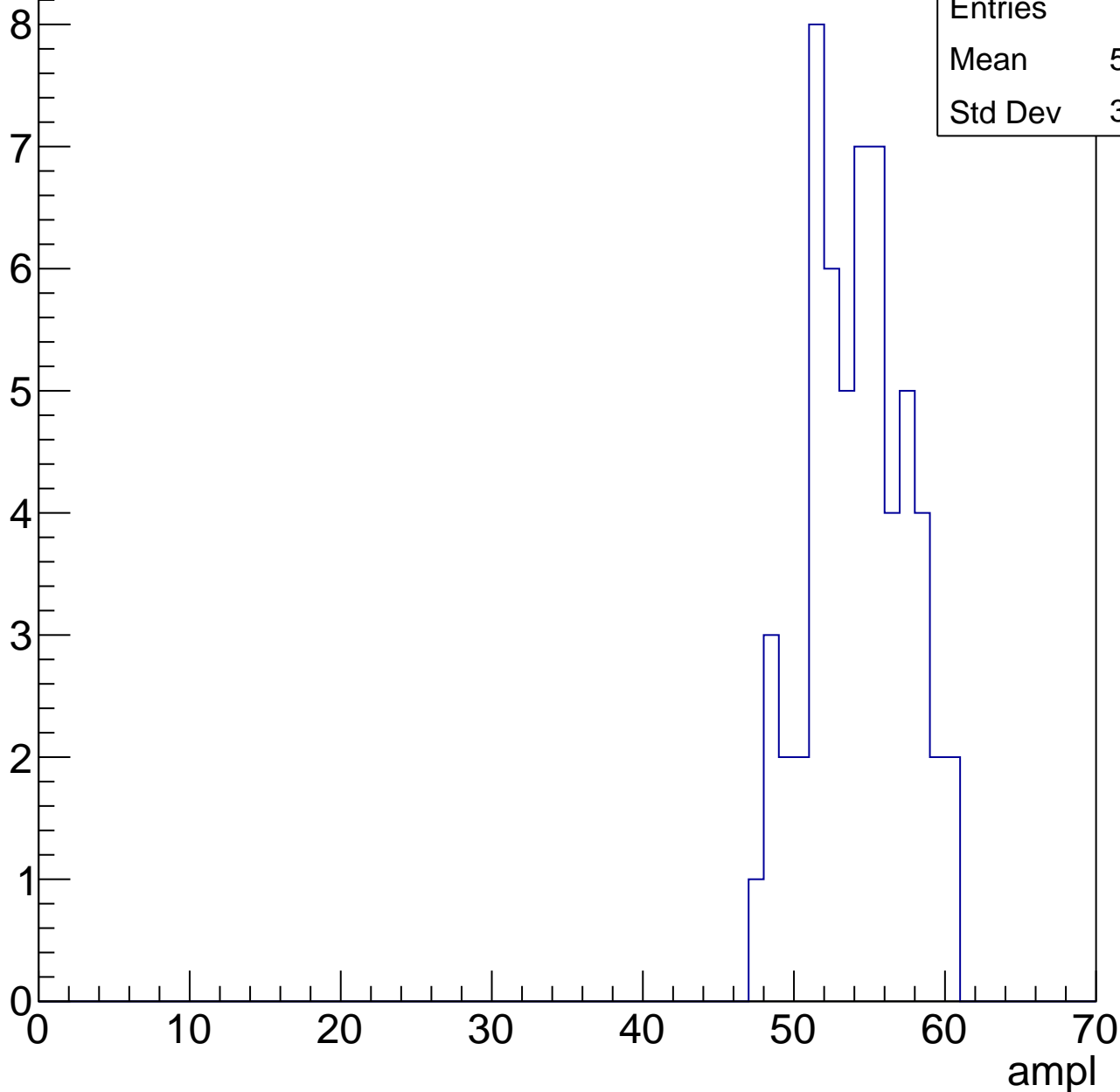


# B1L102S, U25-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.72
Std Dev	3.188

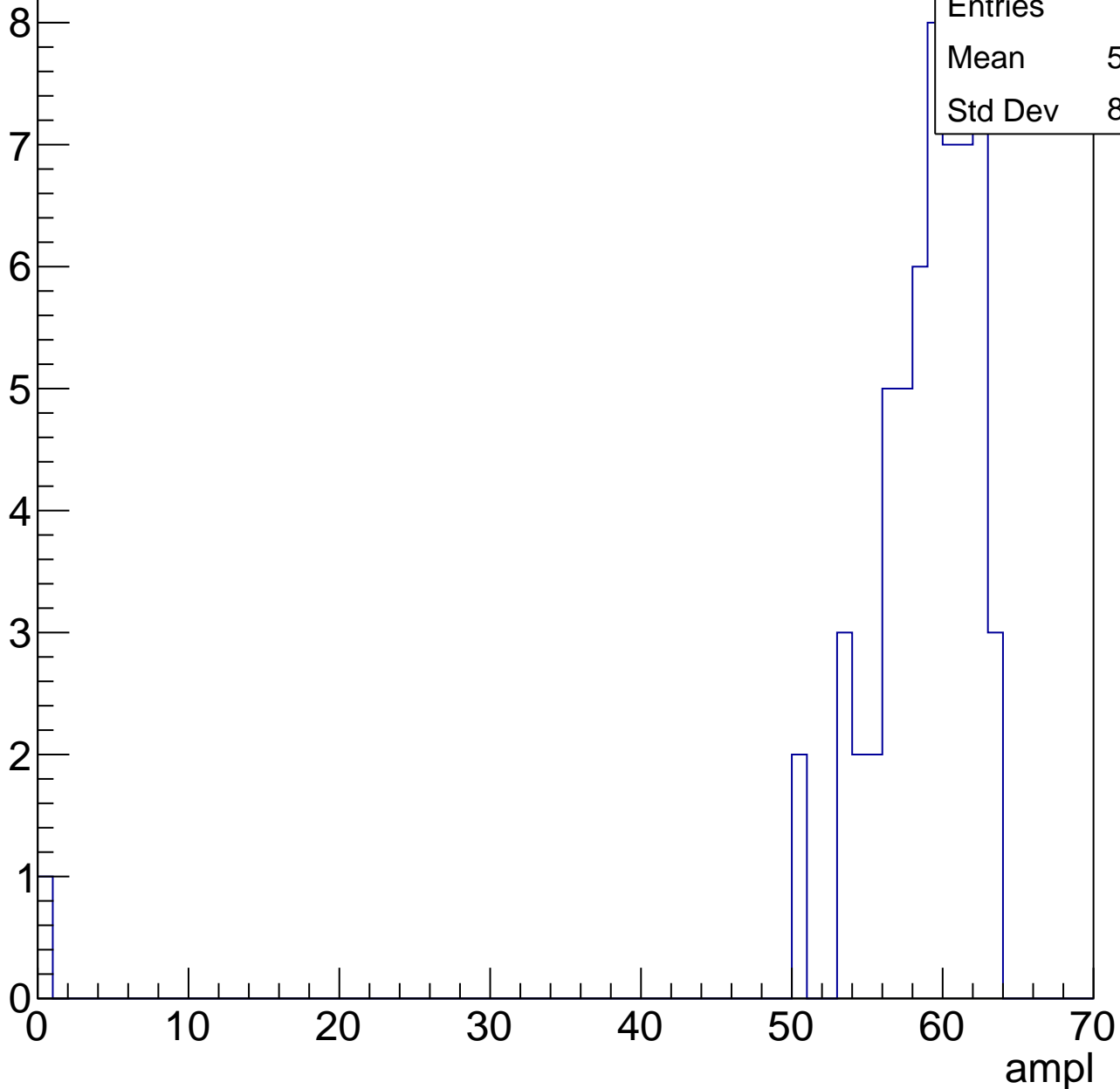


# B1L102S, U25-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.53
Std Dev	8.162

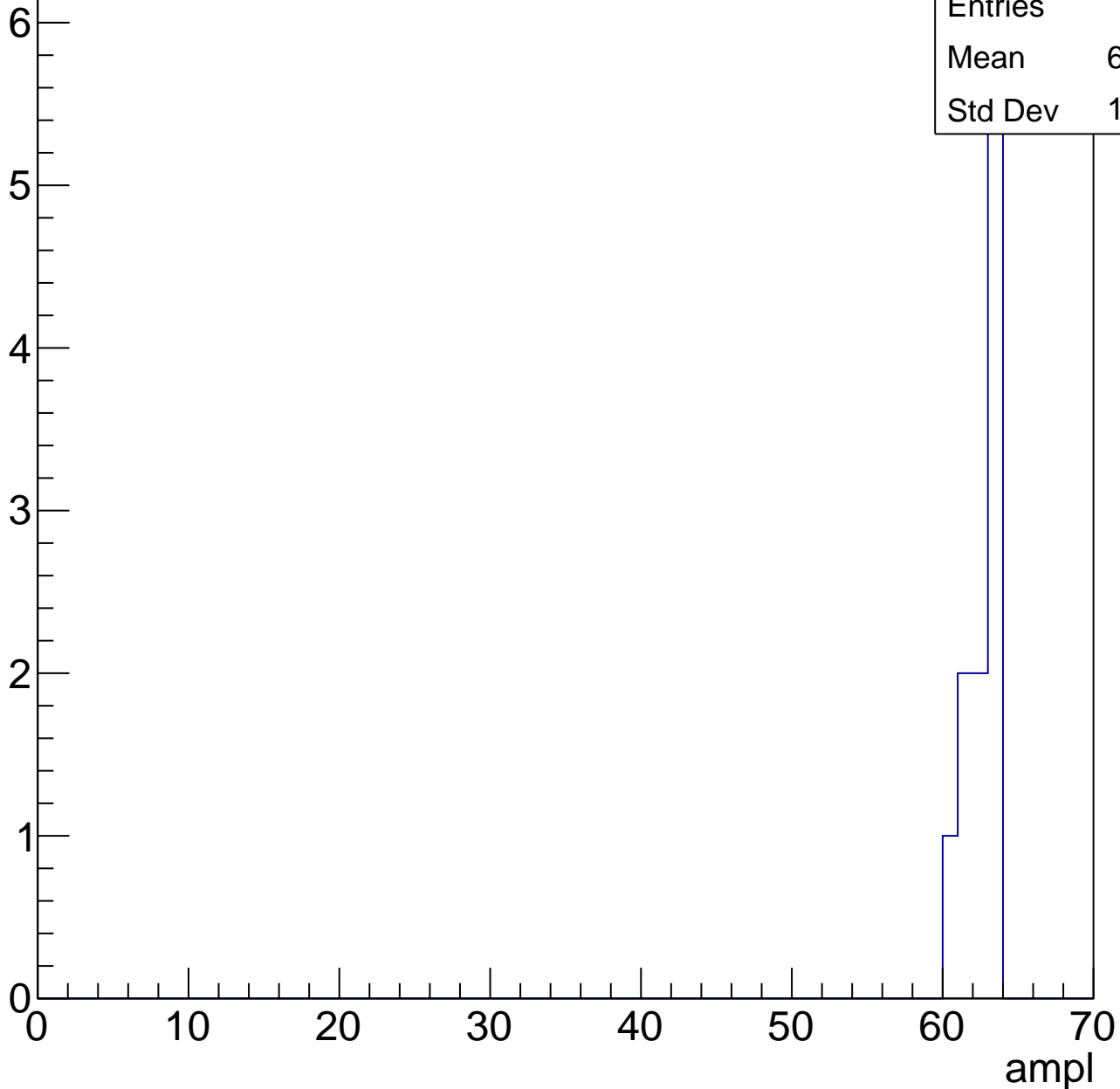


# B1L102S, U25-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62.18
Std Dev	1.029

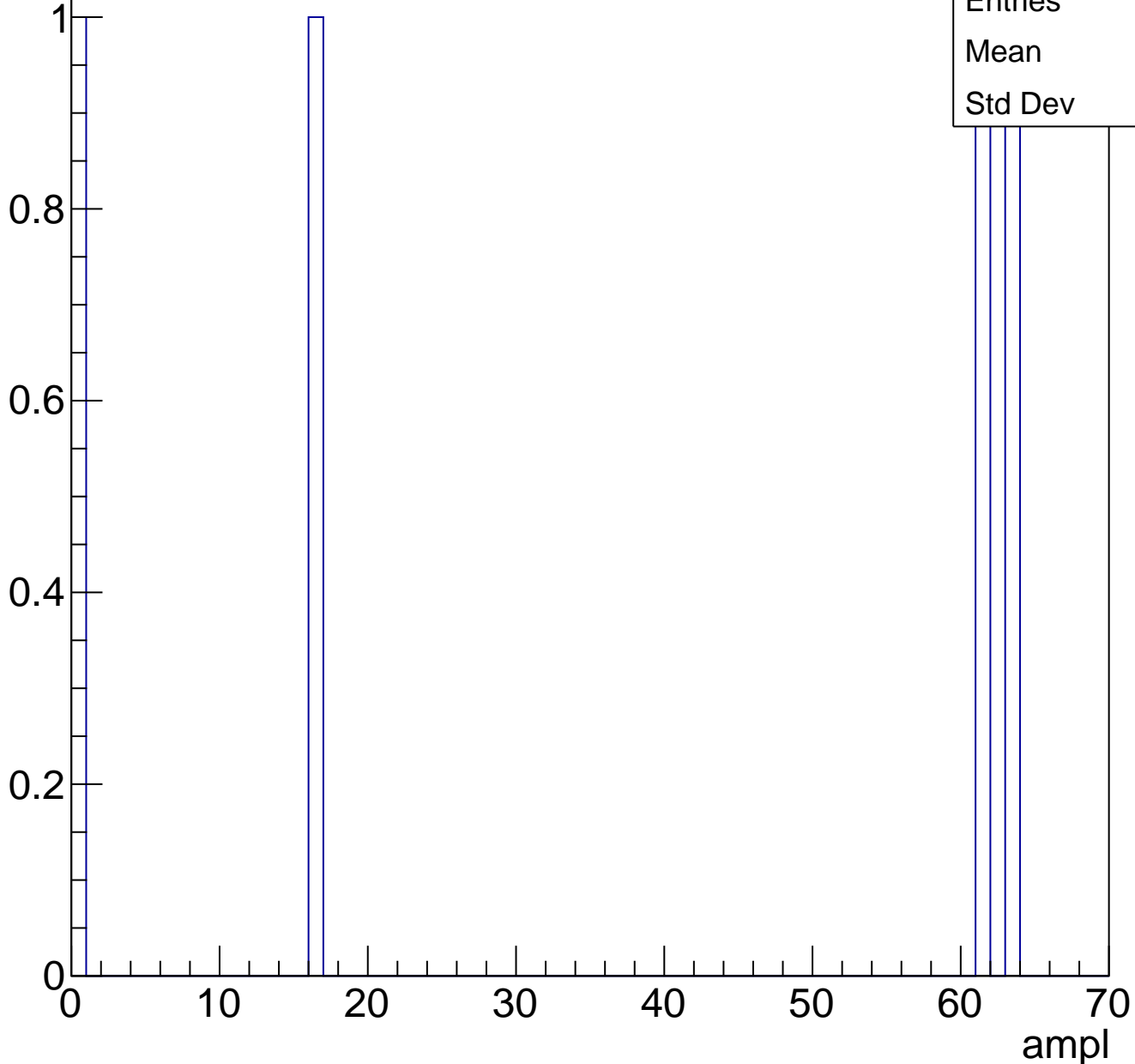




# B1L102S, U25-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	4
Mean	35
Std Dev	27.6

# B1L102S, U25-ch55, adc0

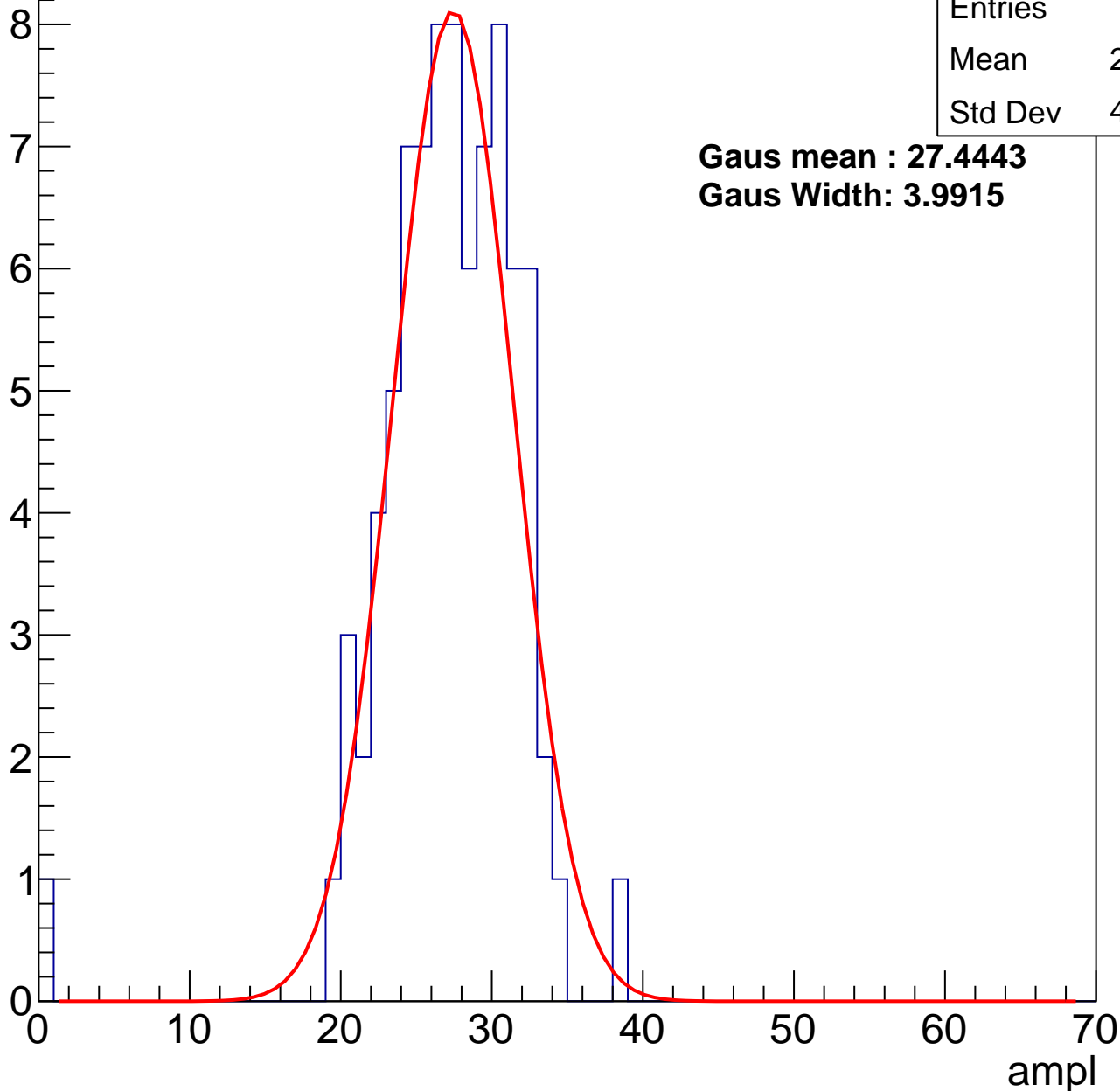
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	26.72
Std Dev	4.758

**Gaus mean : 27.4443**

**Gaus Width: 3.9915**



# B1L102S, U25-ch55, adc1

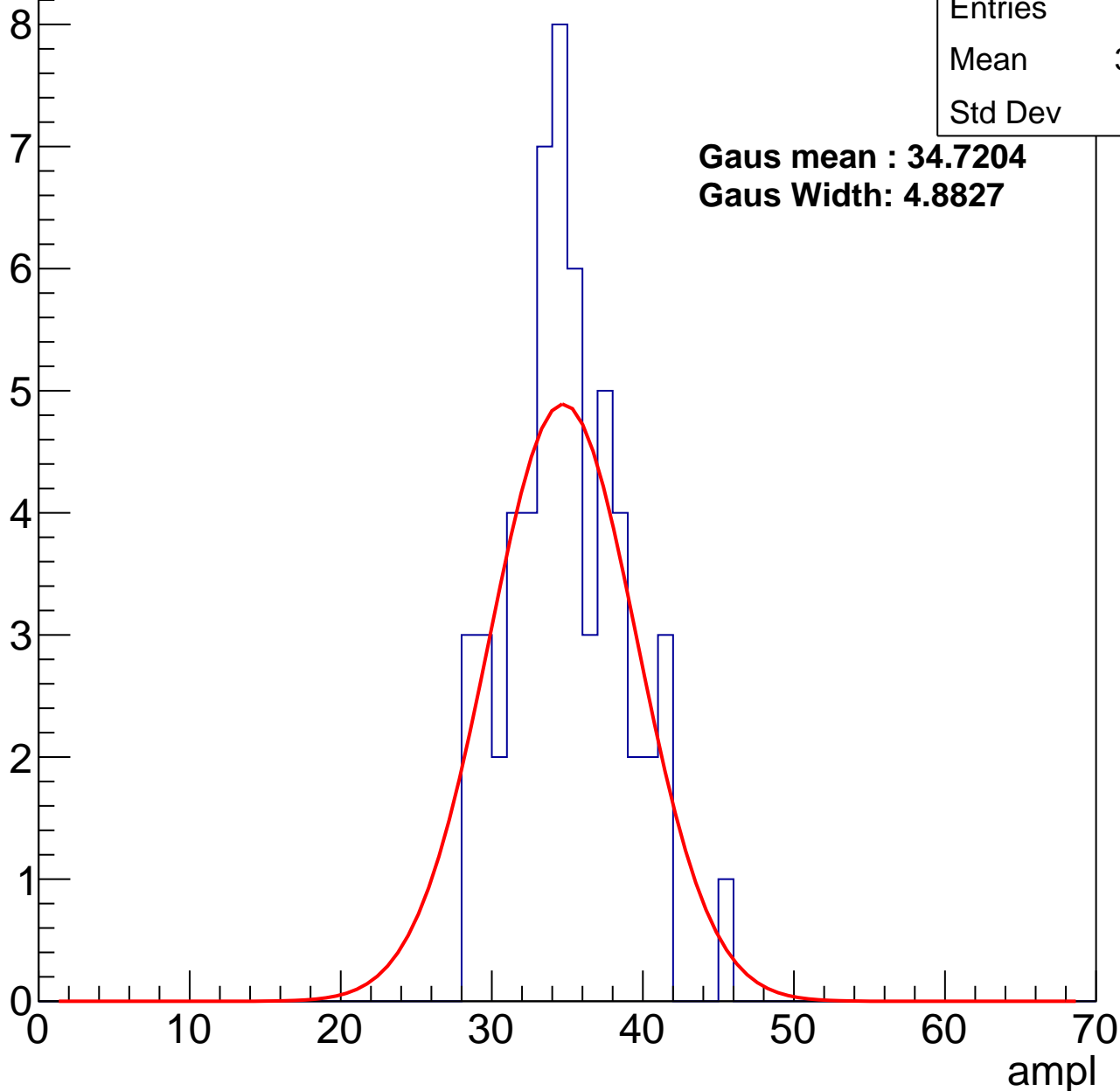
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.51
Std Dev	3.69

**Gaus mean : 34.7204**

**Gaus Width: 4.8827**



# B1L102S, U25-ch55, adc2

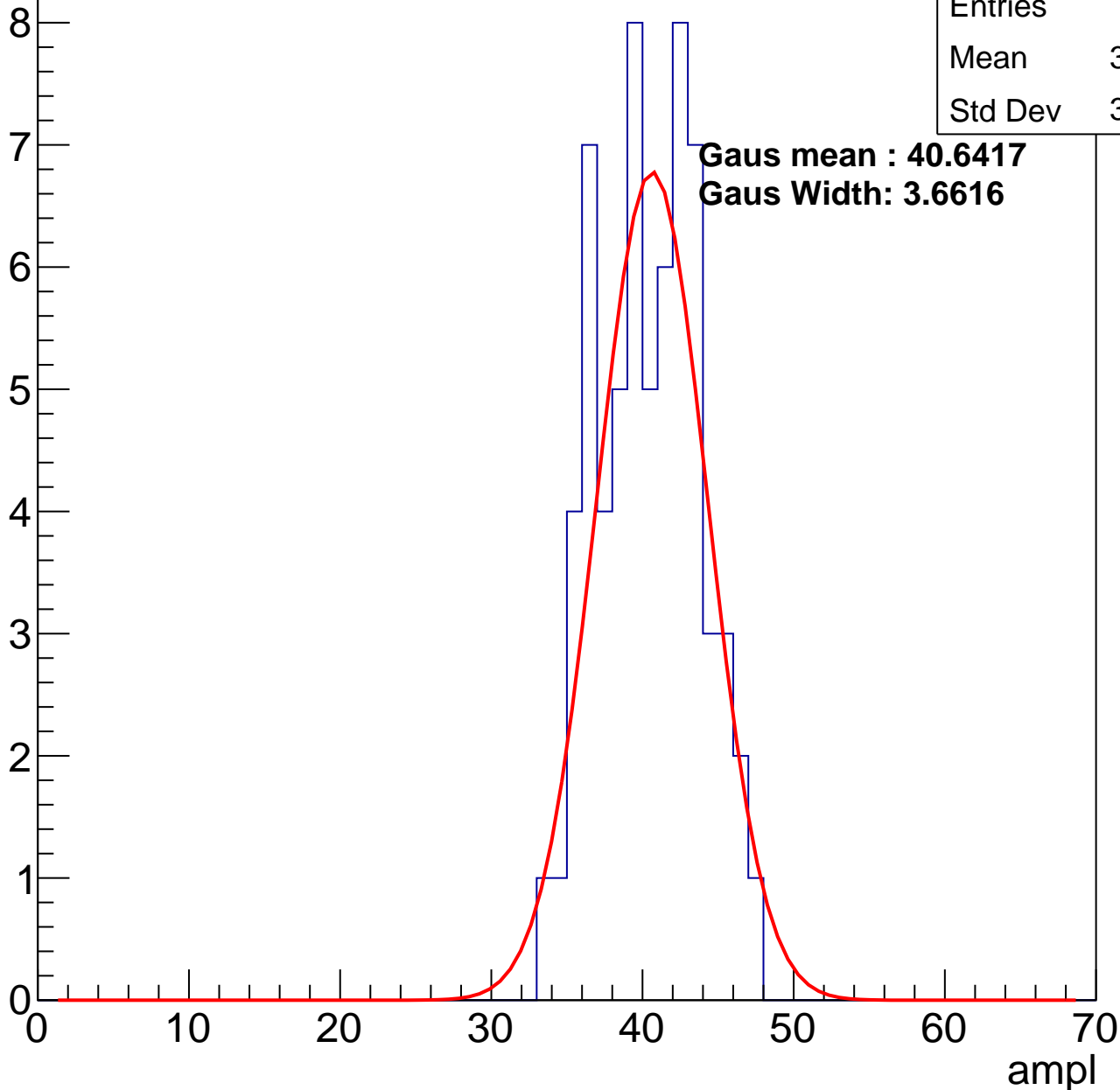
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	39.97
Std Dev	3.296

**Gaus mean : 40.6417**

**Gaus Width: 3.6616**

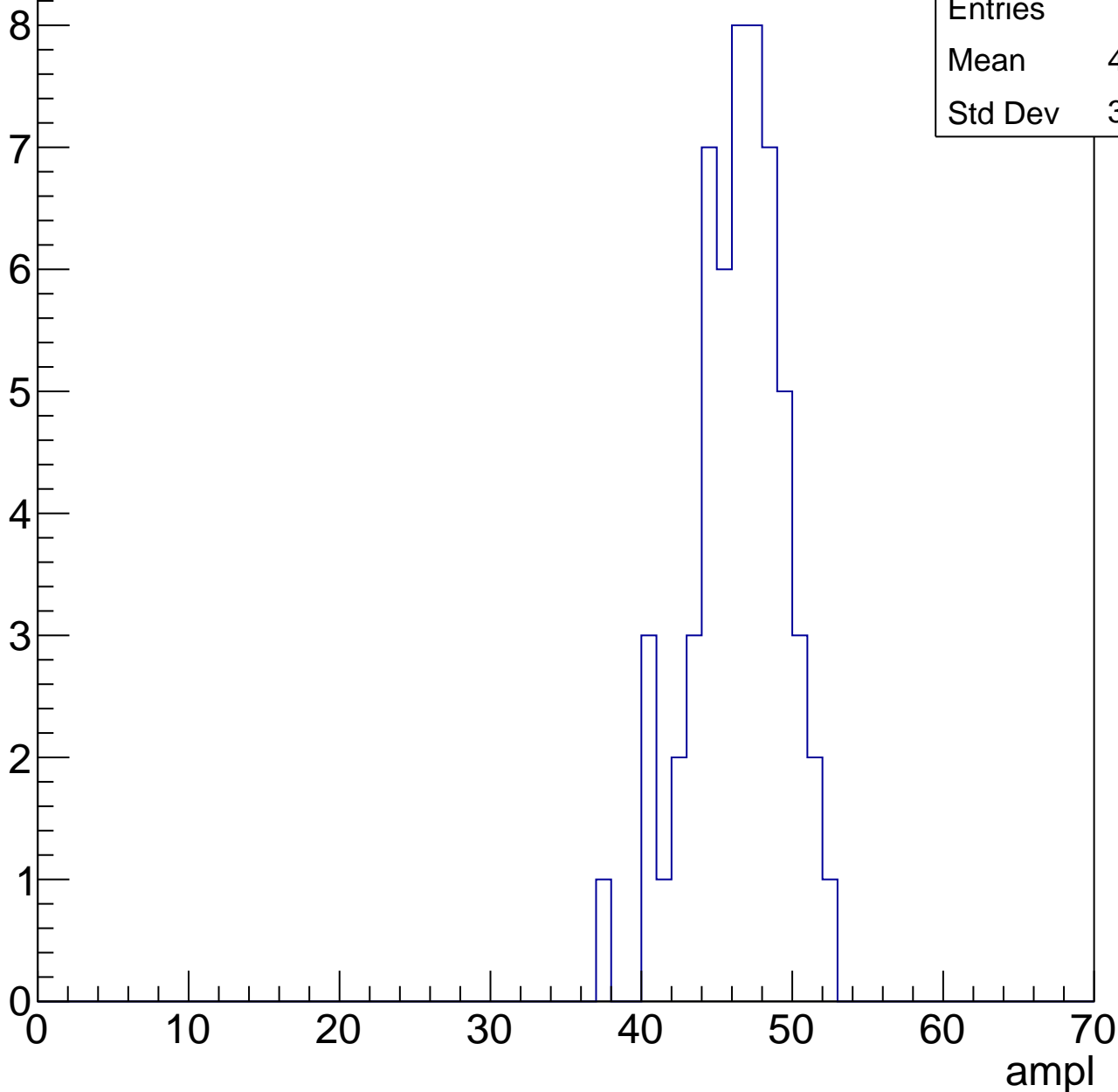


# B1L102S, U25-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	45.93
Std Dev	3.025



# B1L102S, U25-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	79
Mean	53.43
Std Dev	3.279

Entry

10

8

6

4

2

0

0

10

20

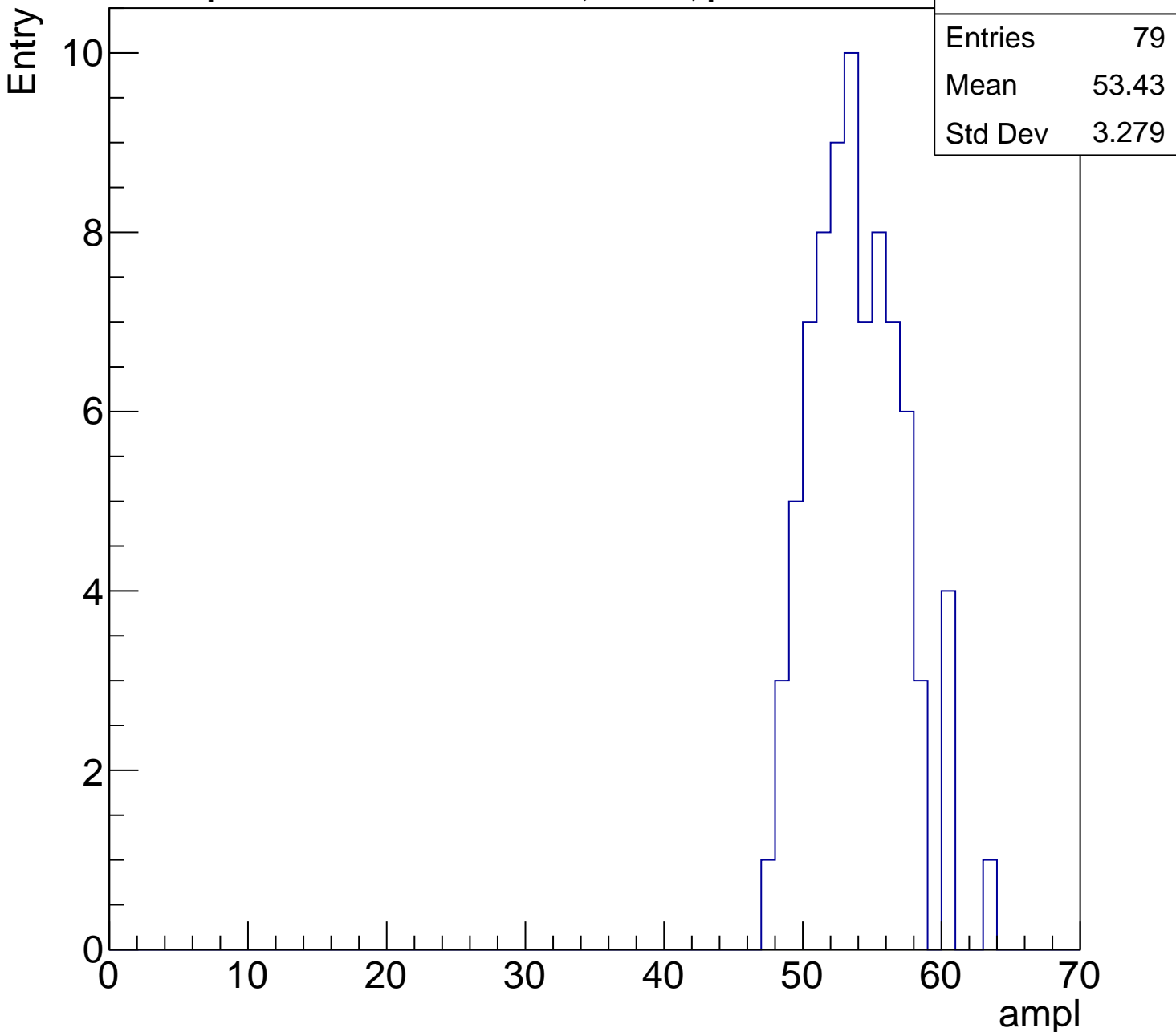
30

40

50

60

ampl

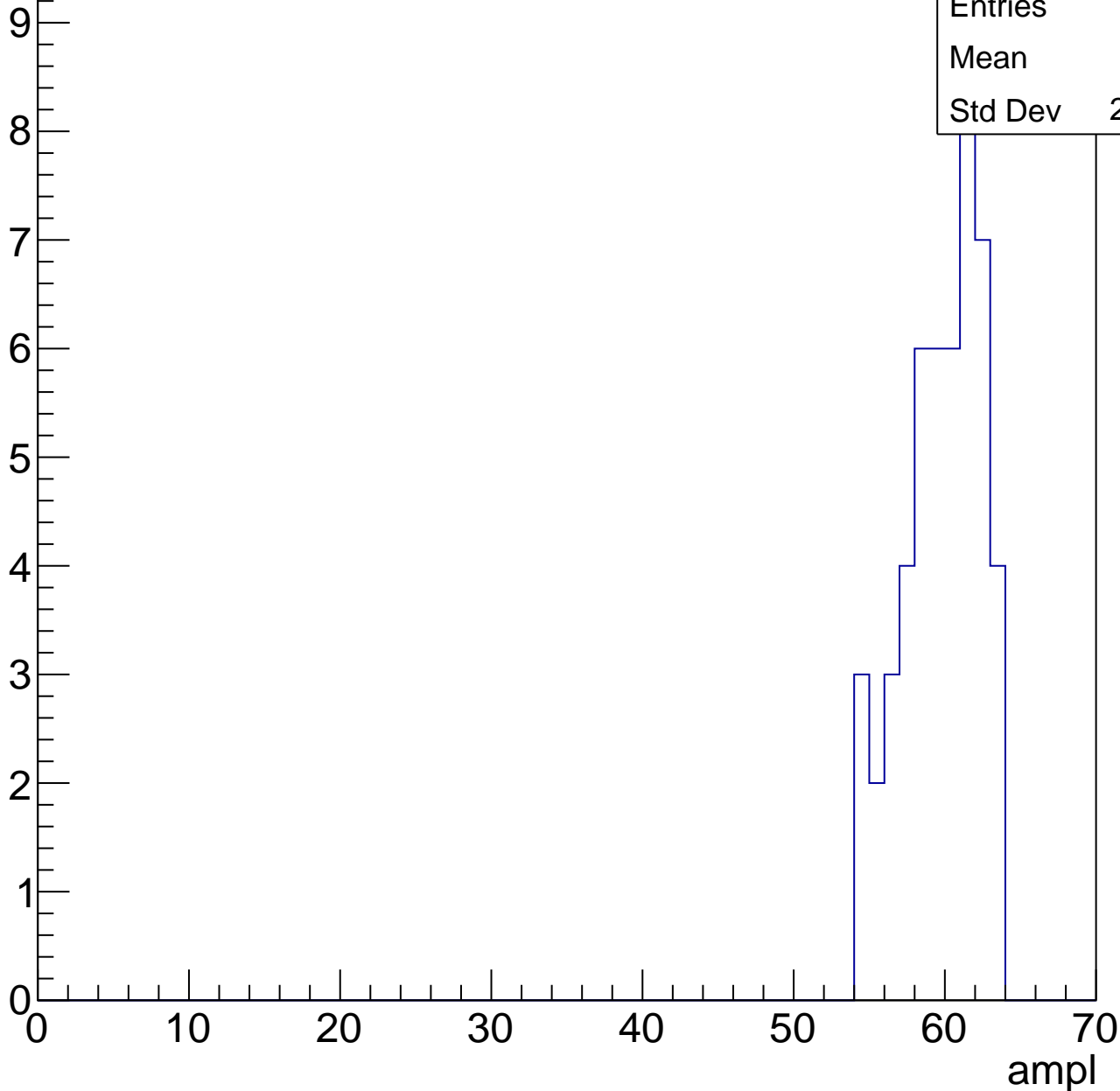


# B1L102S, U25-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

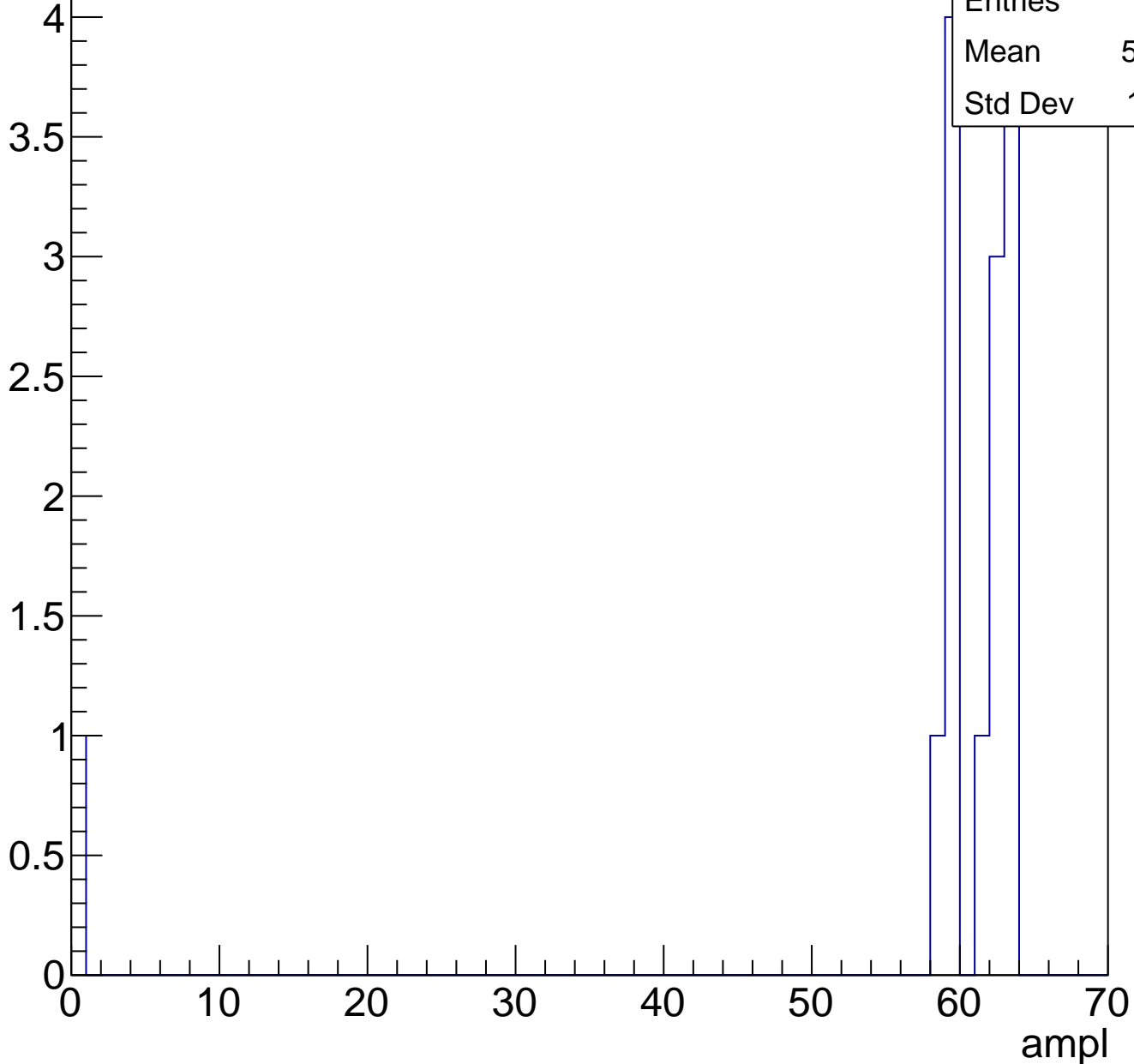
Entries	50
Mean	59.3
Std Dev	2.532



# B1L102S, U25-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

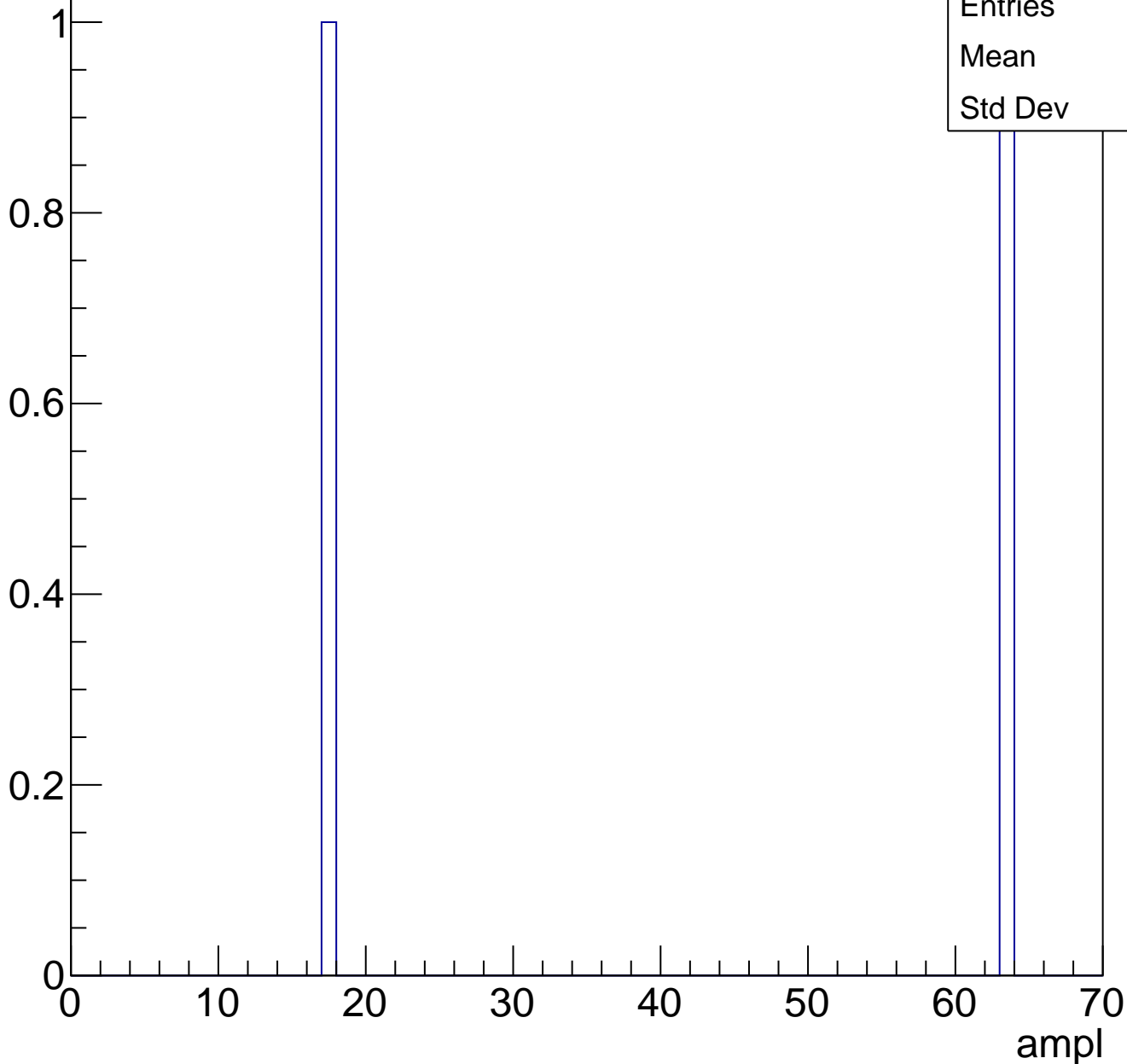




# B1L102S, U25-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch56, adc0

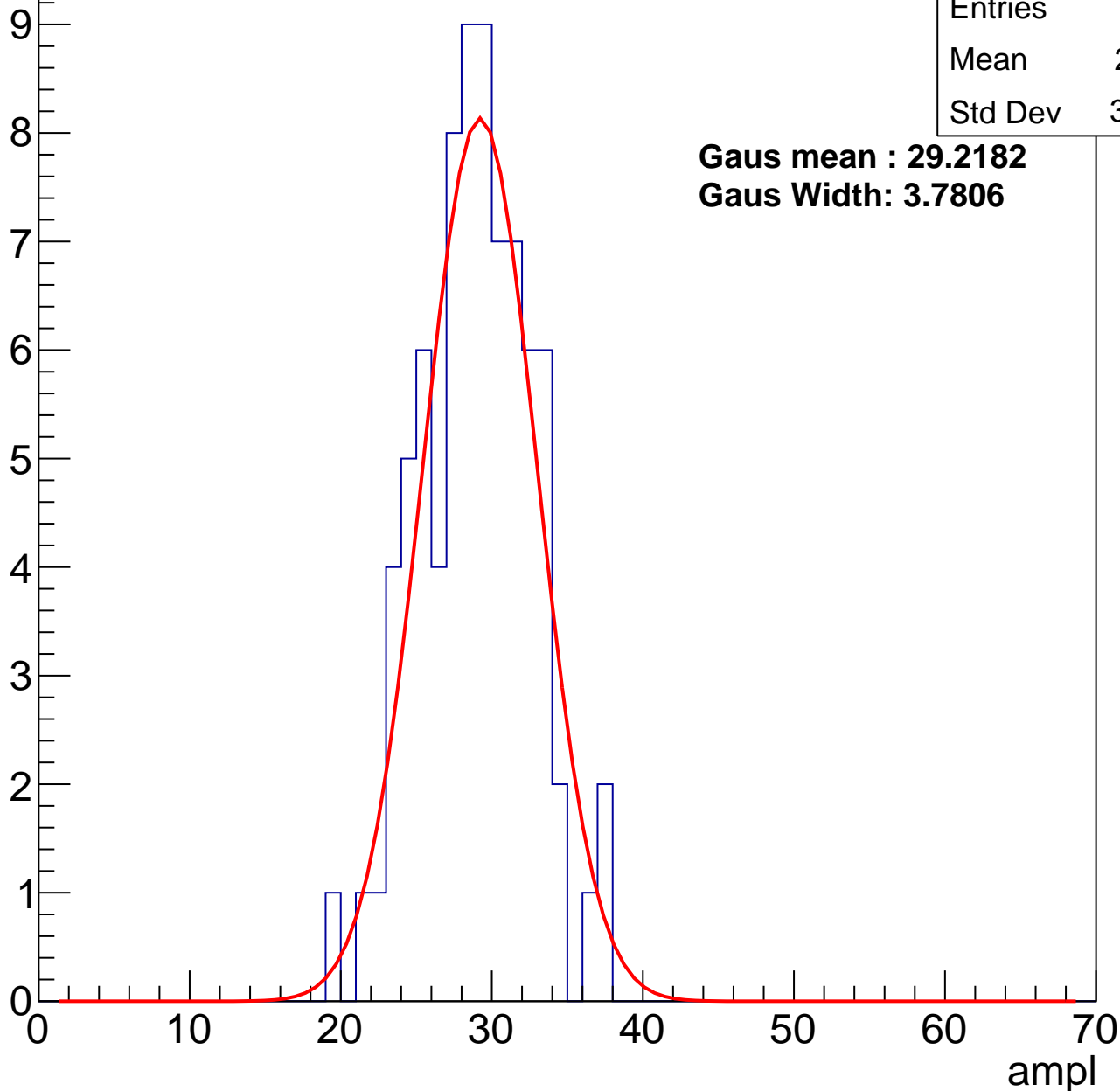
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.51
Std Dev	3.642

**Gaus mean : 29.2182**

**Gaus Width: 3.7806**



# B1L102S, U25-ch56, adc1

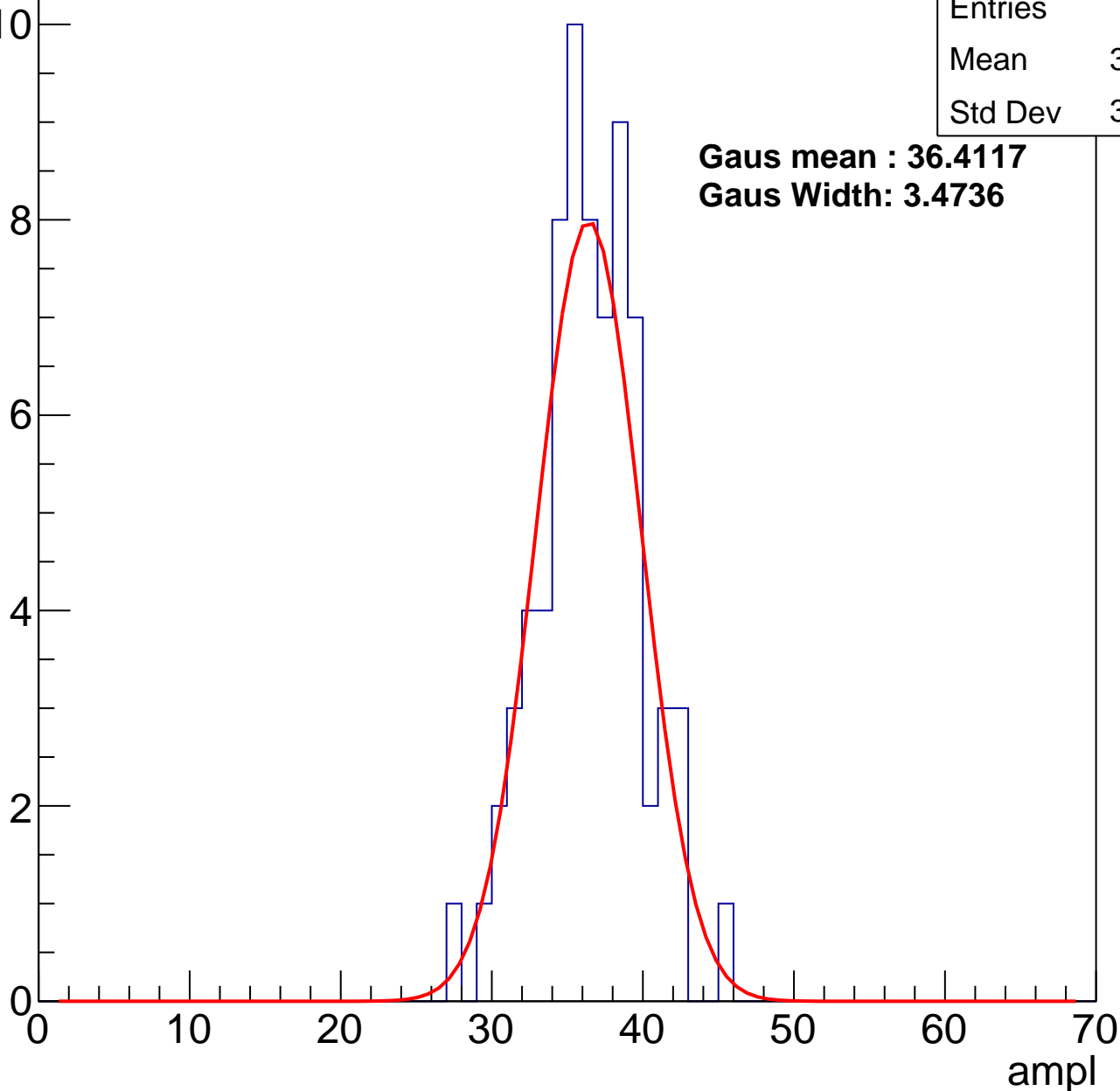
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.99
Std Dev	3.354

**Gaus mean : 36.4117**

**Gaus Width: 3.4736**



# B1L102S, U25-ch56, adc2

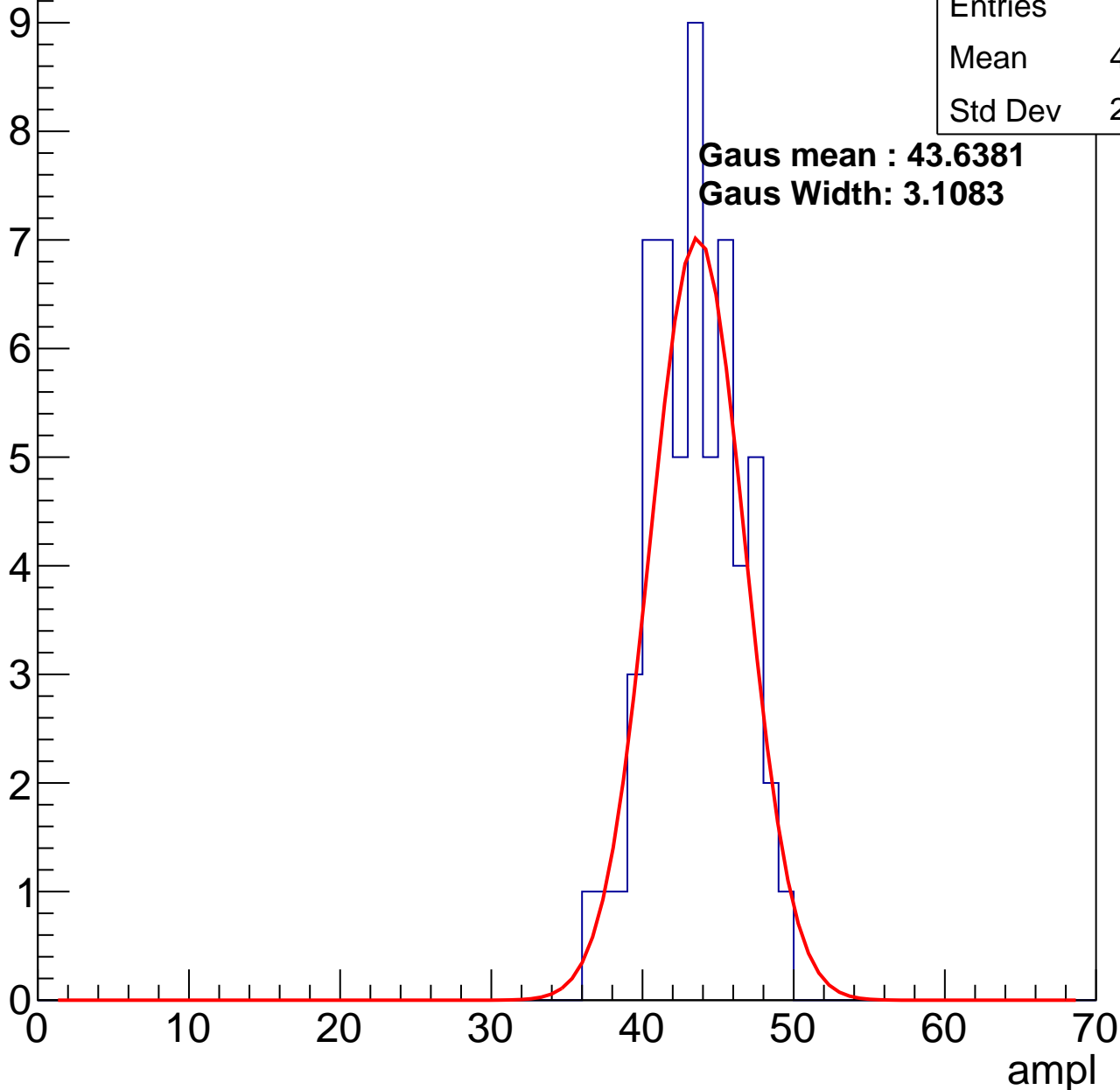
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.95
Std Dev	2.903

**Gaus mean : 43.6381**

**Gaus Width: 3.1083**

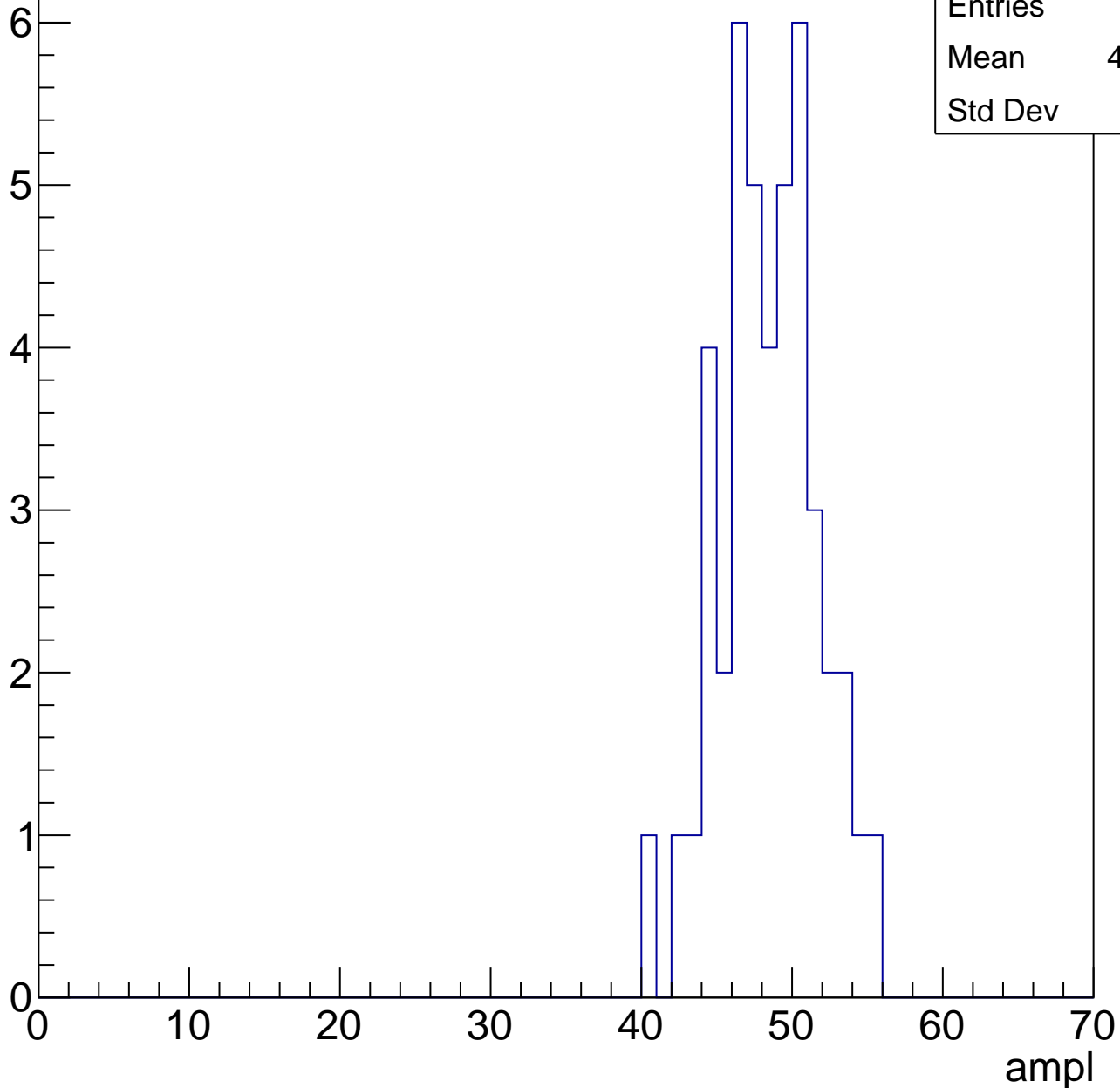


# B1L102S, U25-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	47.98
Std Dev	3.23

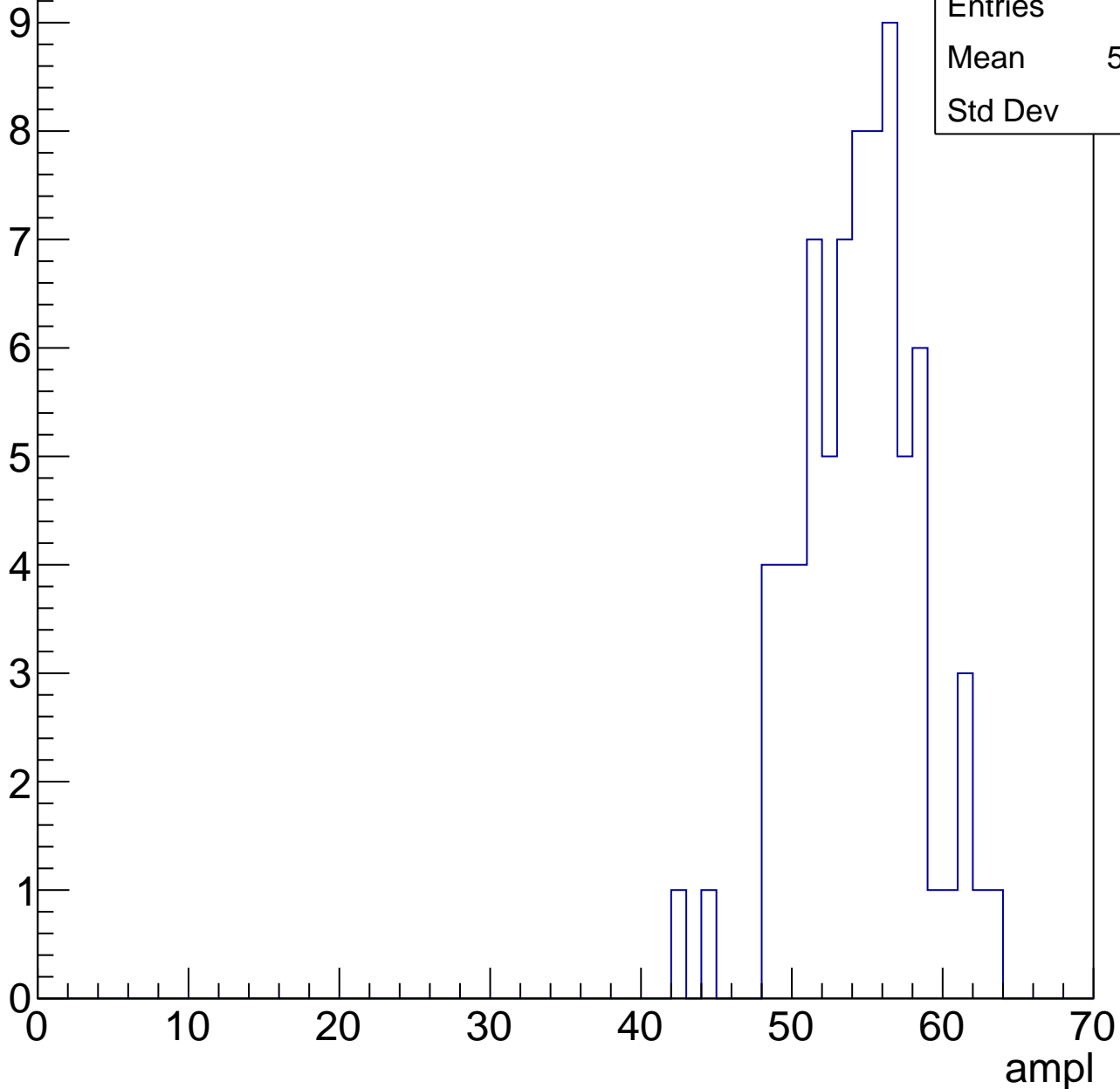


# B1L102S, U25-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

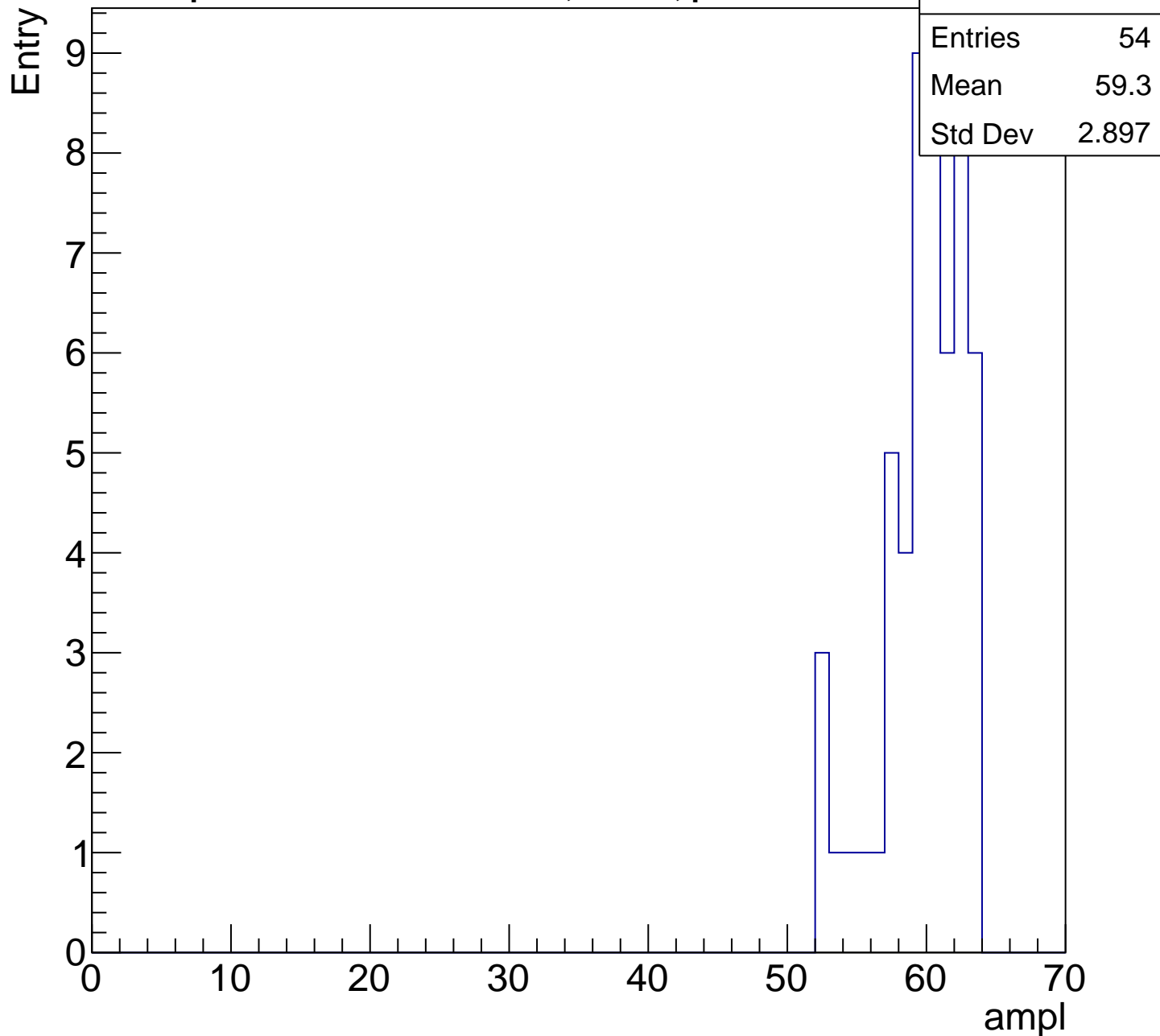
Entry

Entries	76
Mean	53.92
Std Dev	3.94



# B1L102S, U25-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2



# B1L102S, U25-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch57, adc0

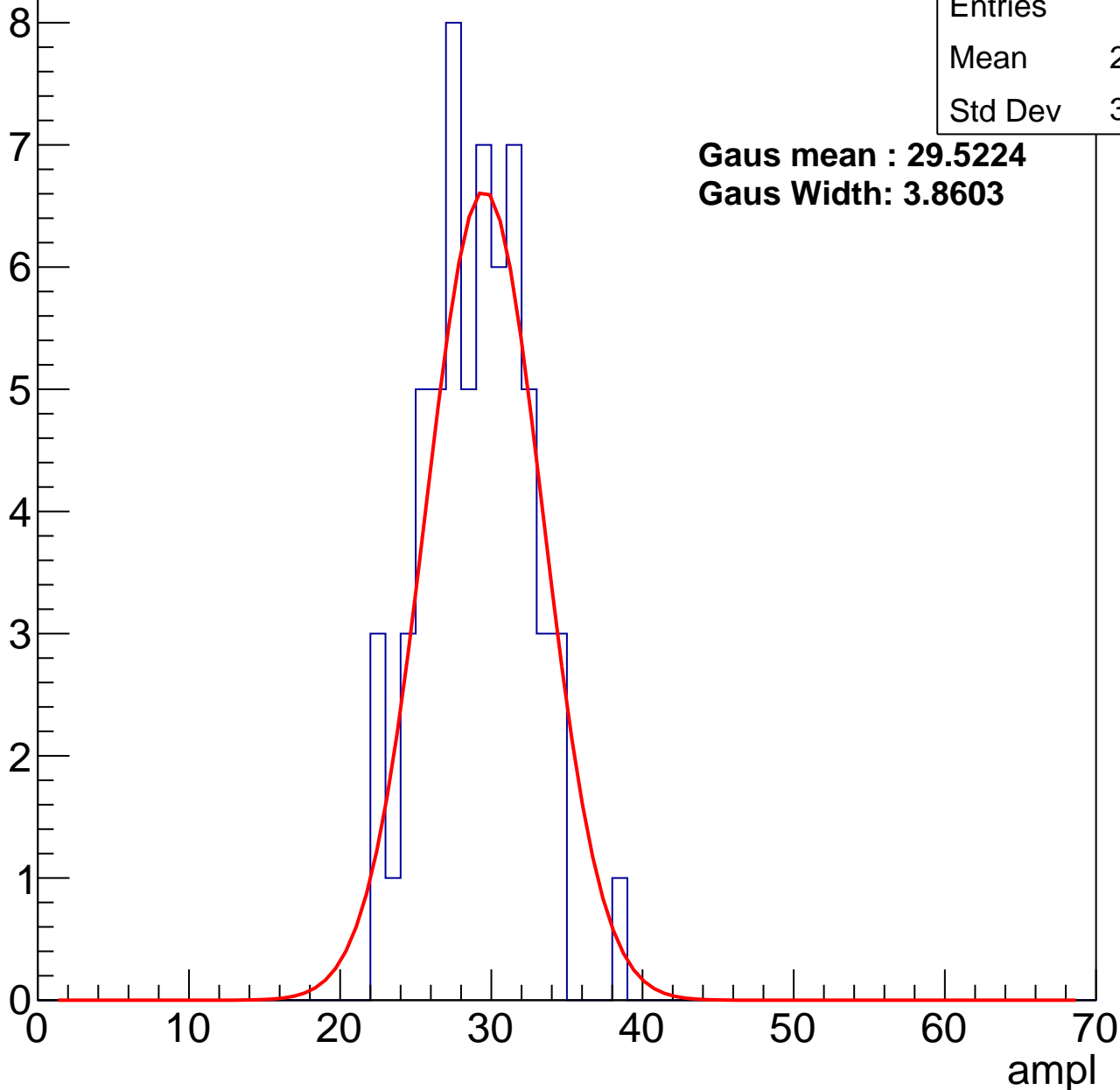
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.56
Std Dev	3.339

**Gaus mean : 29.5224**

**Gaus Width: 3.8603**



# B1L102S, U25-ch57, adc1

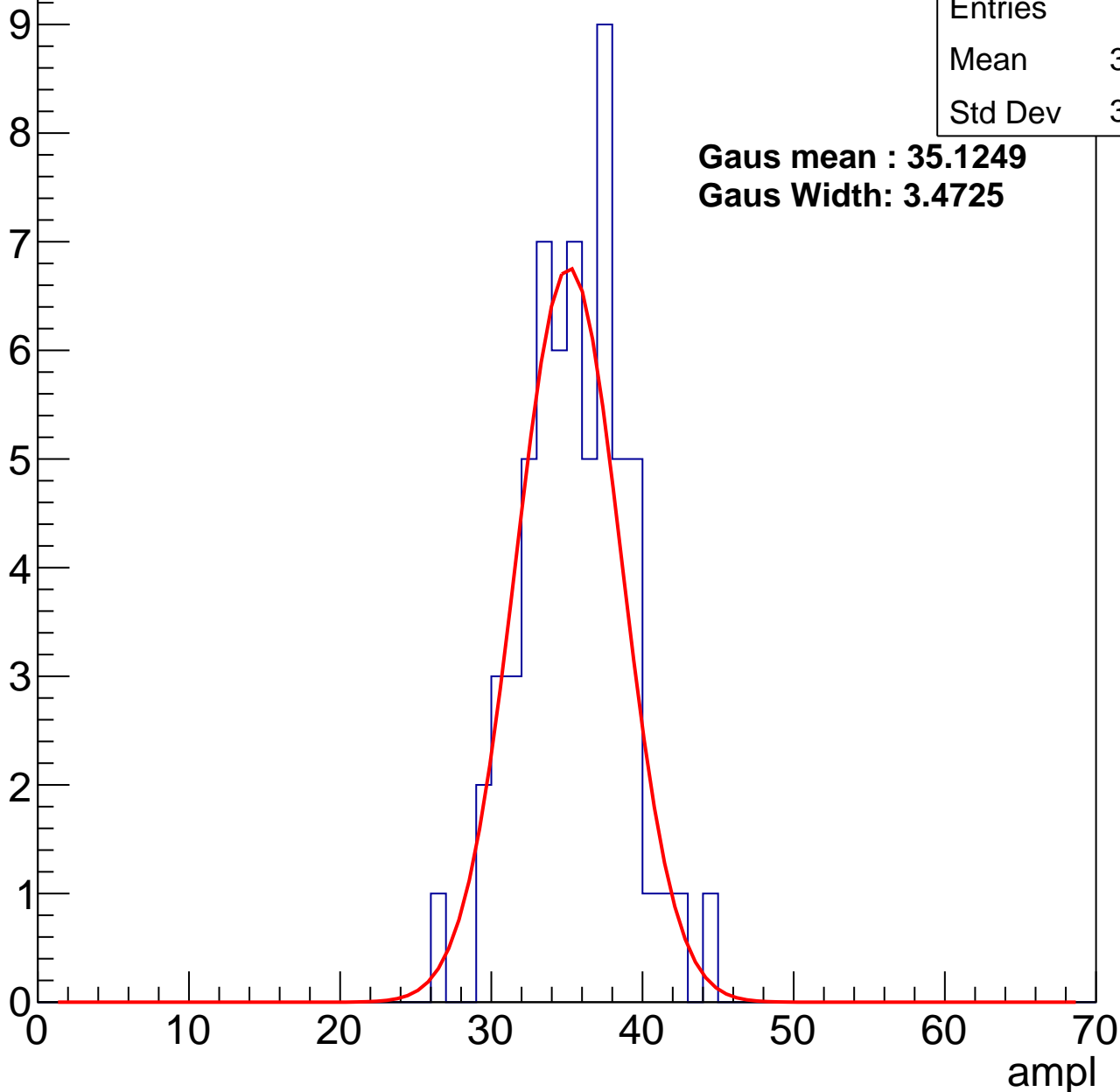
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.03
Std Dev	3.389

**Gaus mean : 35.1249**

**Gaus Width: 3.4725**



# B1L102S, U25-ch57, adc2

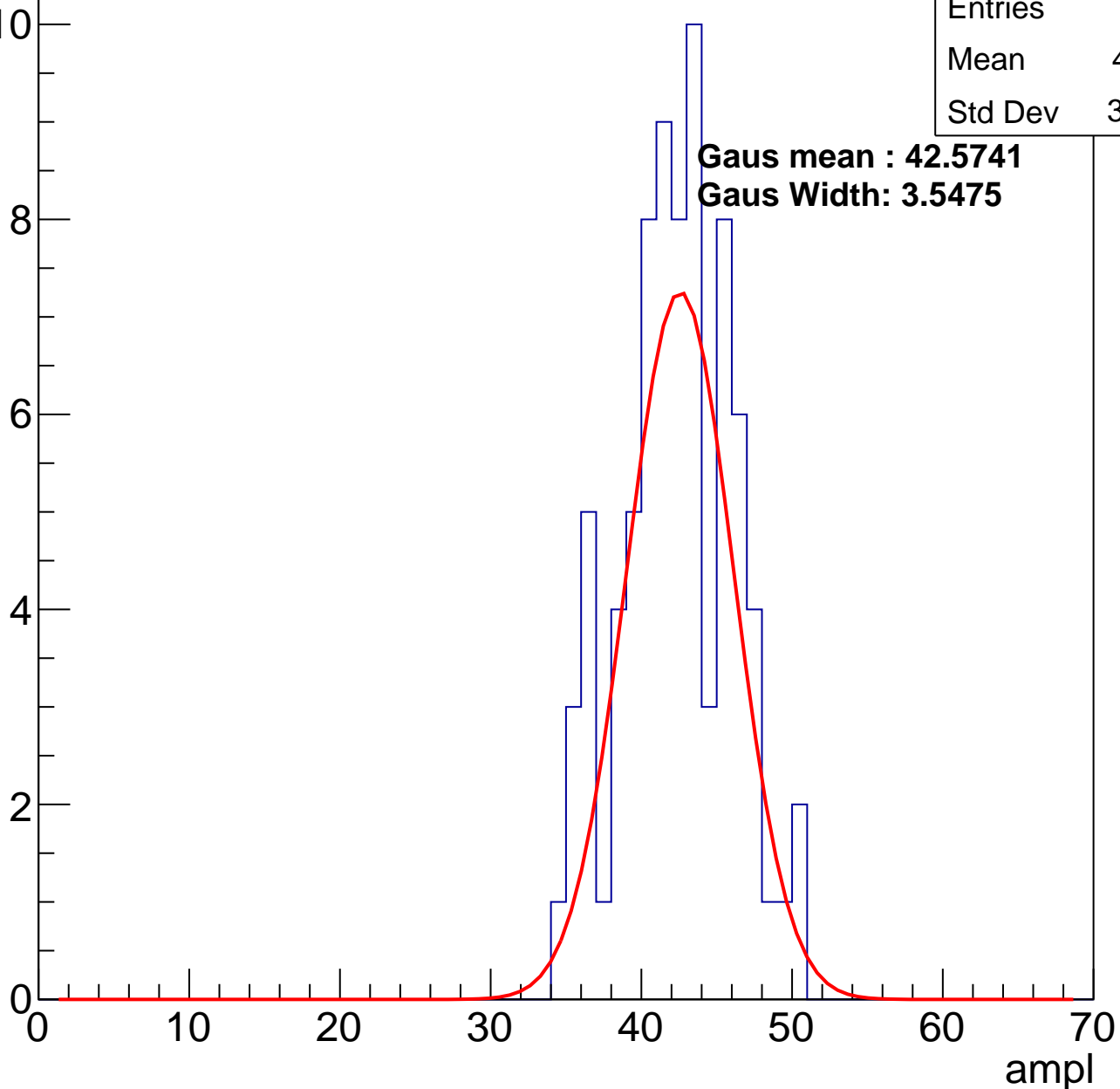
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	41.91
Std Dev	3.677

**Gaus mean : 42.5741**

**Gaus Width: 3.5475**

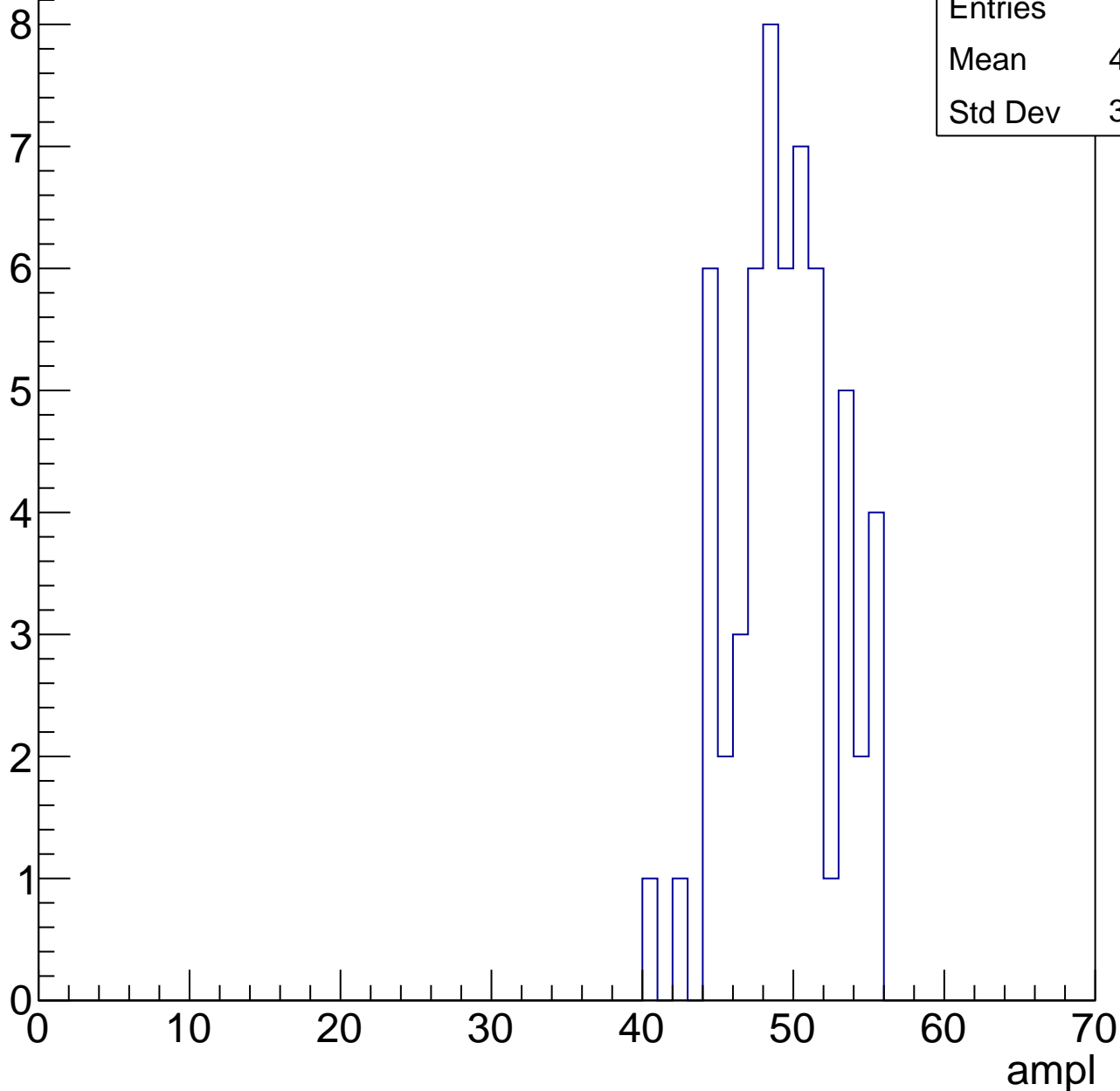


# B1L102S, U25-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

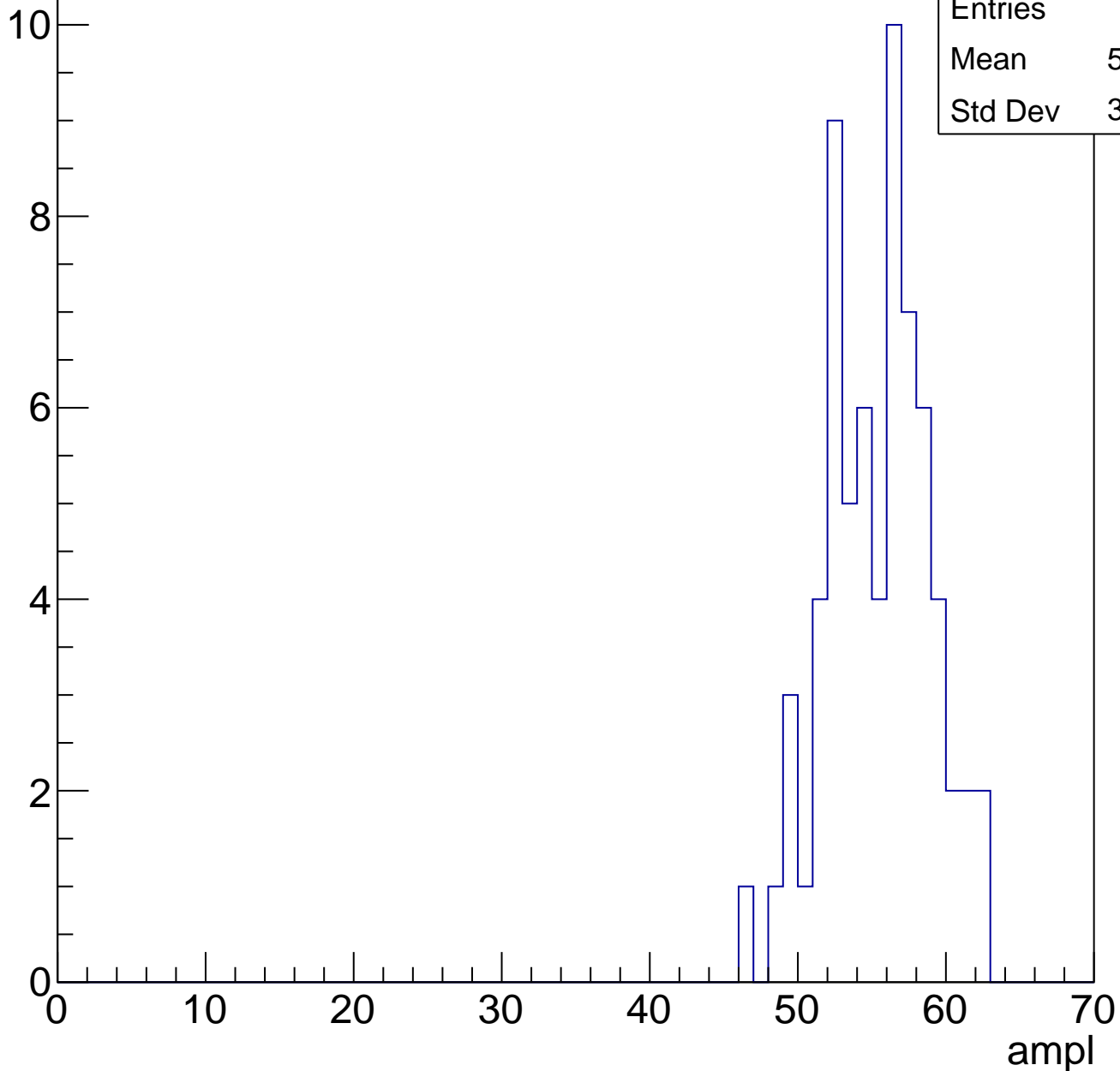
Entries	58
Mean	48.88
Std Dev	3.434



# B1L102S, U25-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

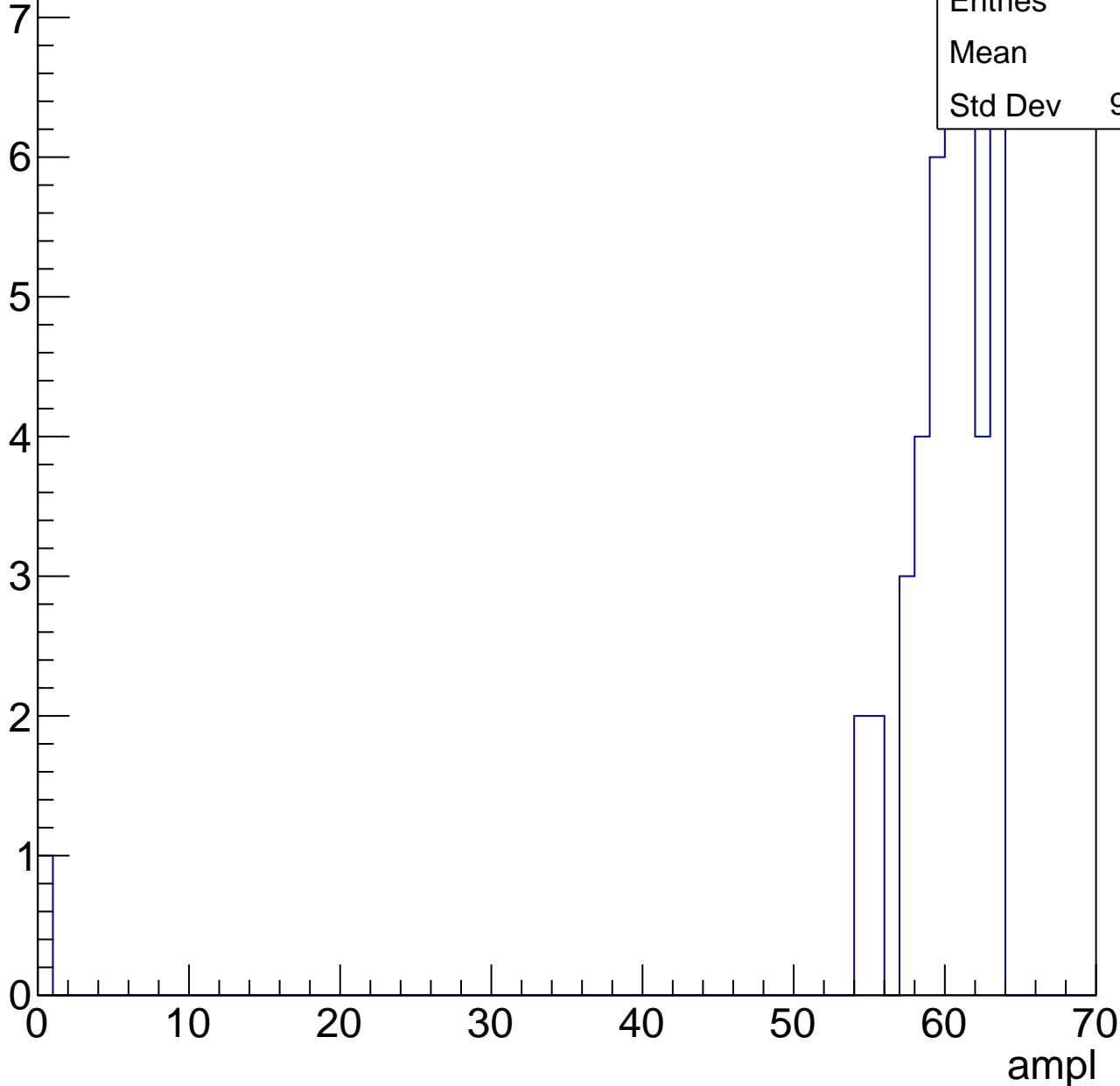


# B1L102S, U25-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

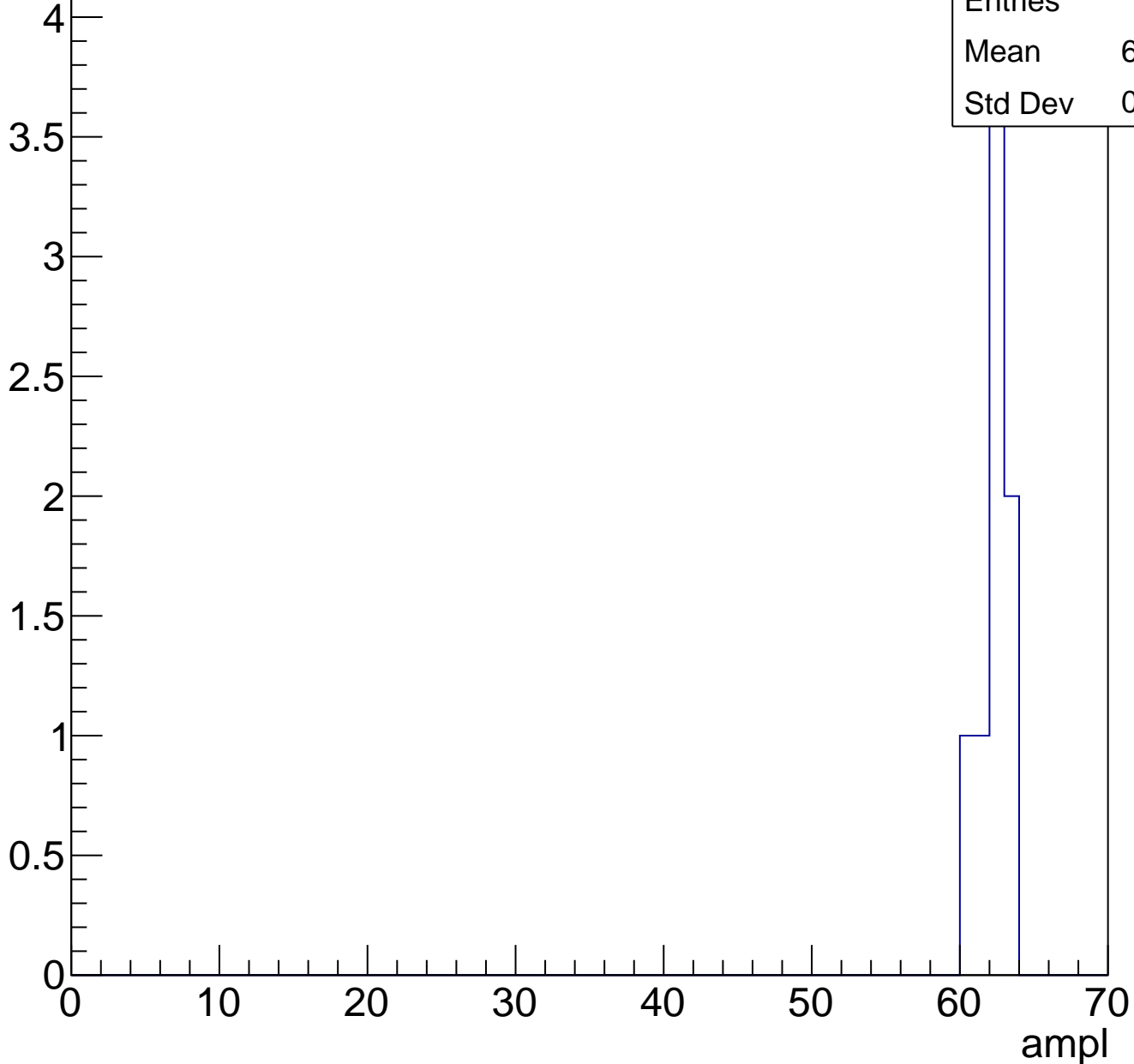
Entries	43
Mean	58.4
Std Dev	9.334



# B1L102S, U25-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L102S, U25-ch58, adc0

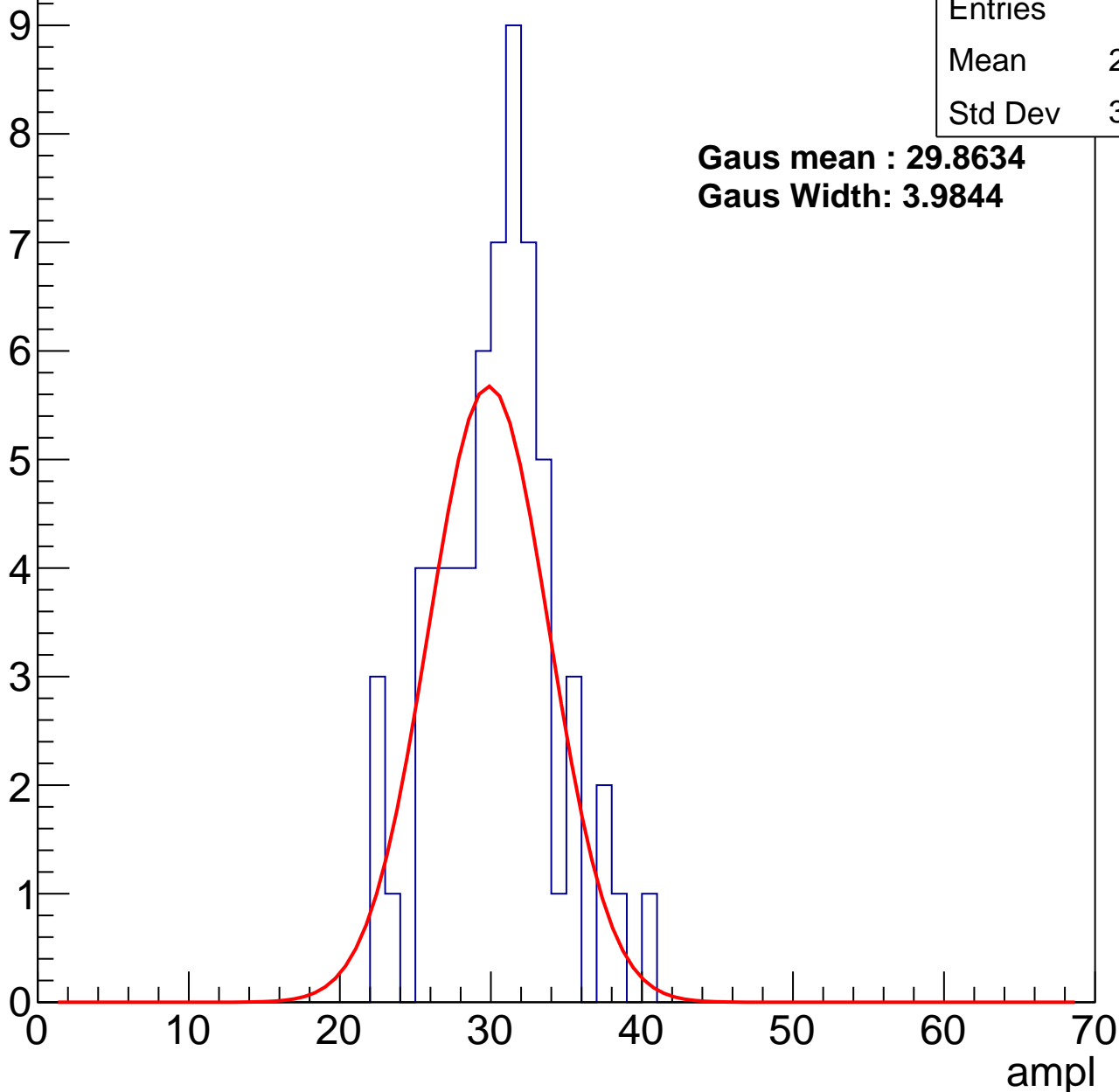
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.94
Std Dev	3.805

**Gaus mean : 29.8634**

**Gaus Width: 3.9844**



# B1L102S, U25-ch58, adc1

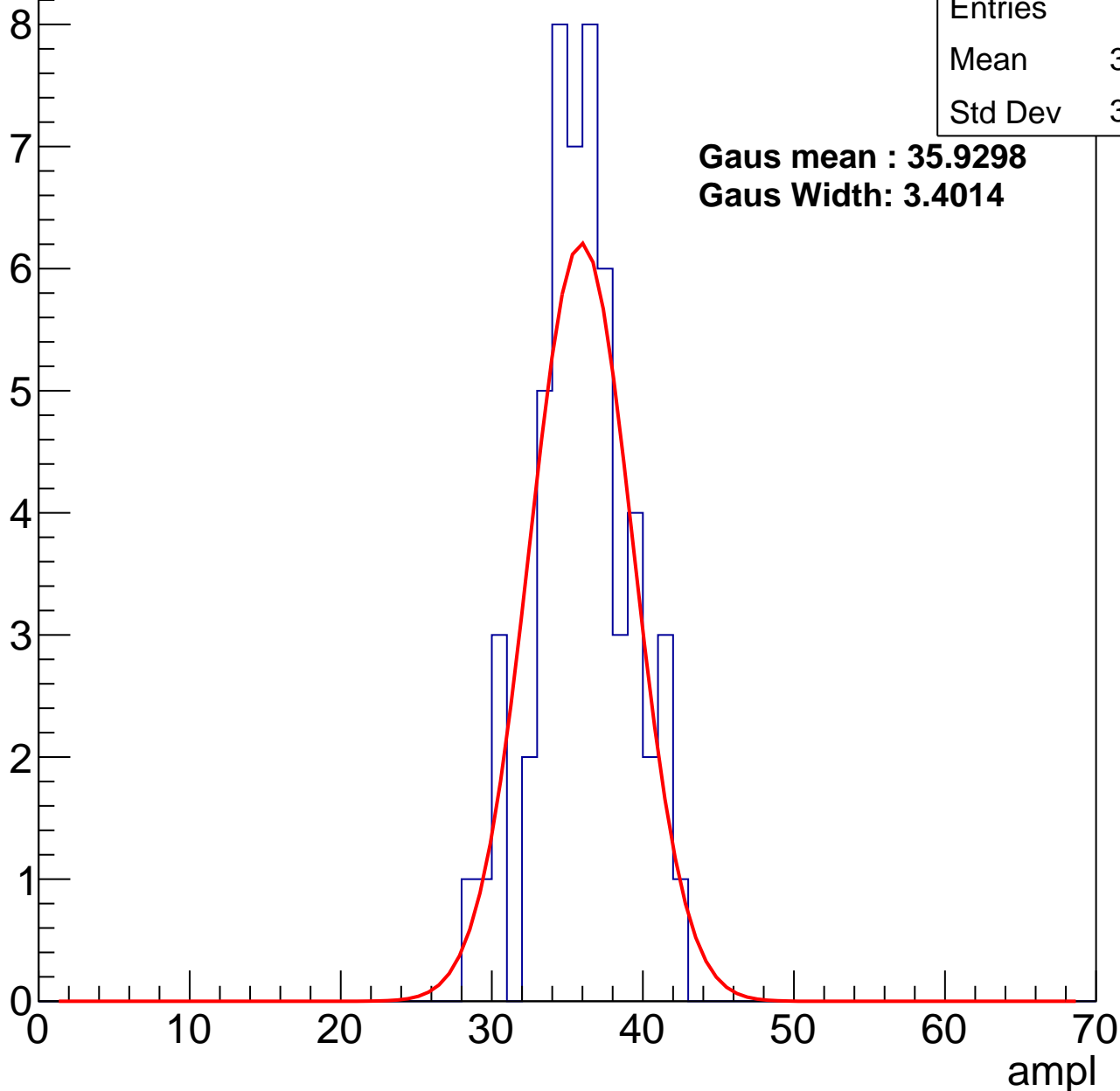
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.52
Std Dev	3.108

**Gaus mean : 35.9298**

**Gaus Width: 3.4014**



# B1L102S, U25-ch58, adc2

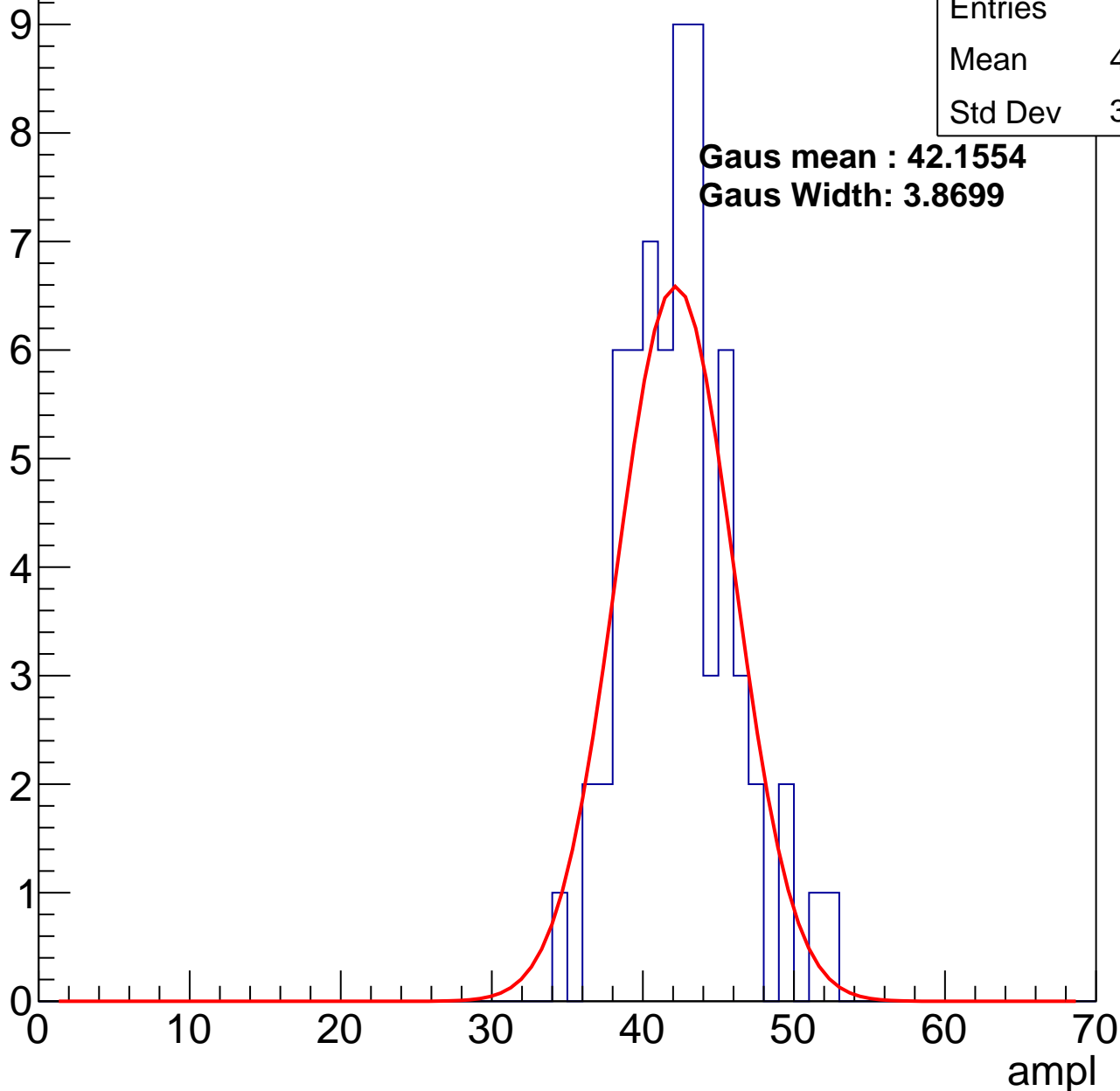
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.94
Std Dev	3.529

**Gaus mean : 42.1554**

**Gaus Width: 3.8699**

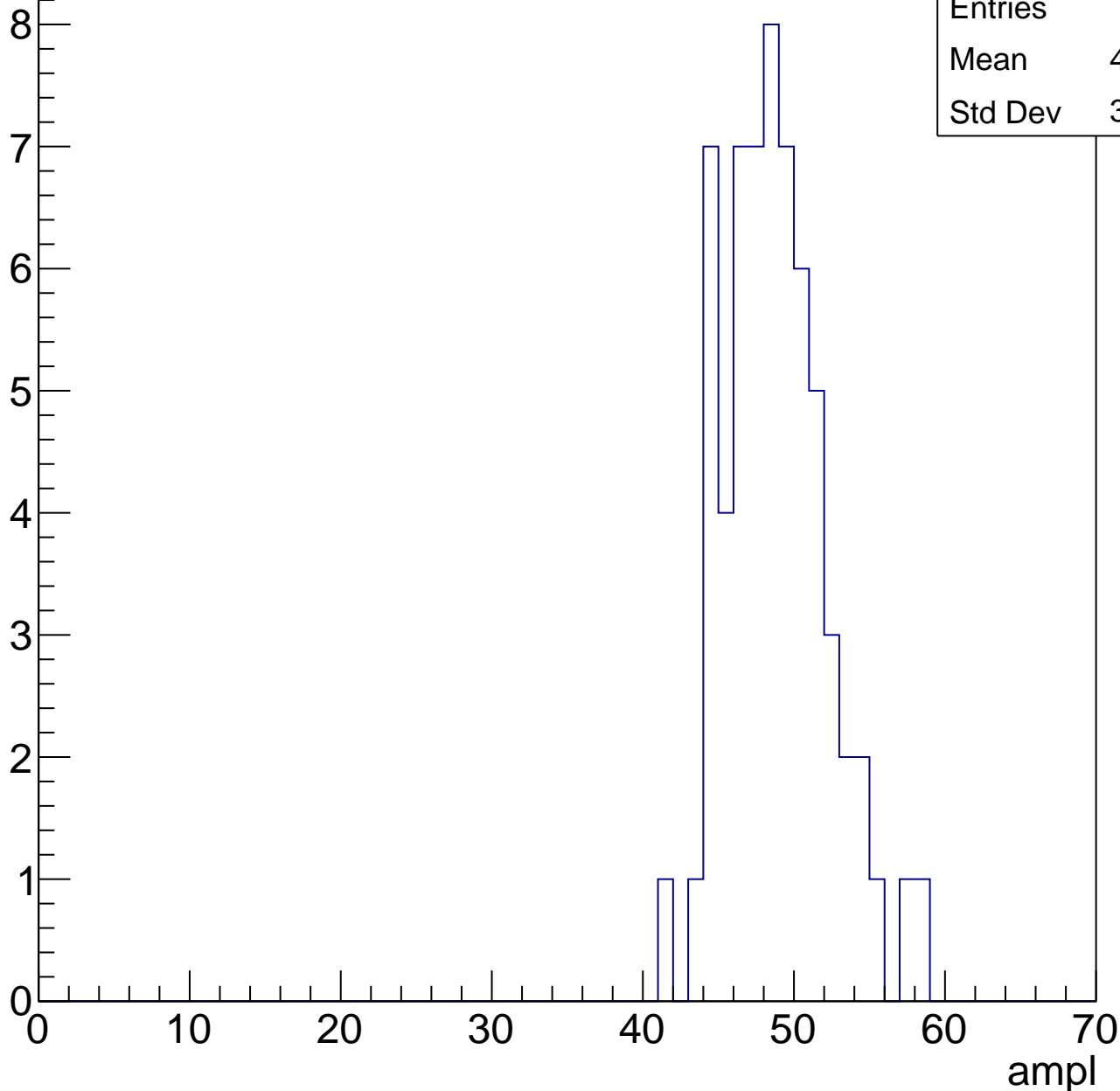


# B1L102S, U25-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.33
Std Dev	3.395

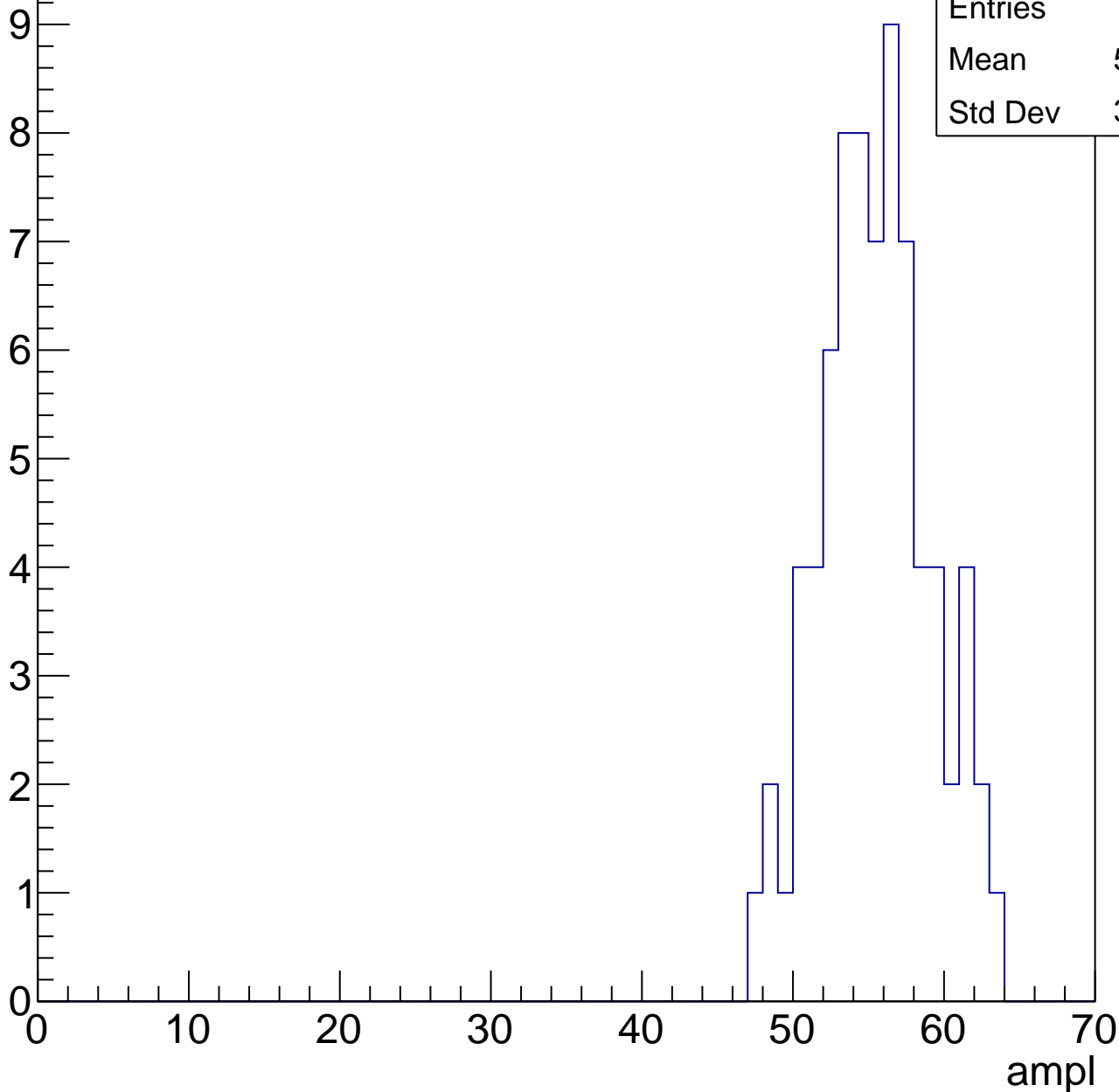


# B1L102S, U25-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

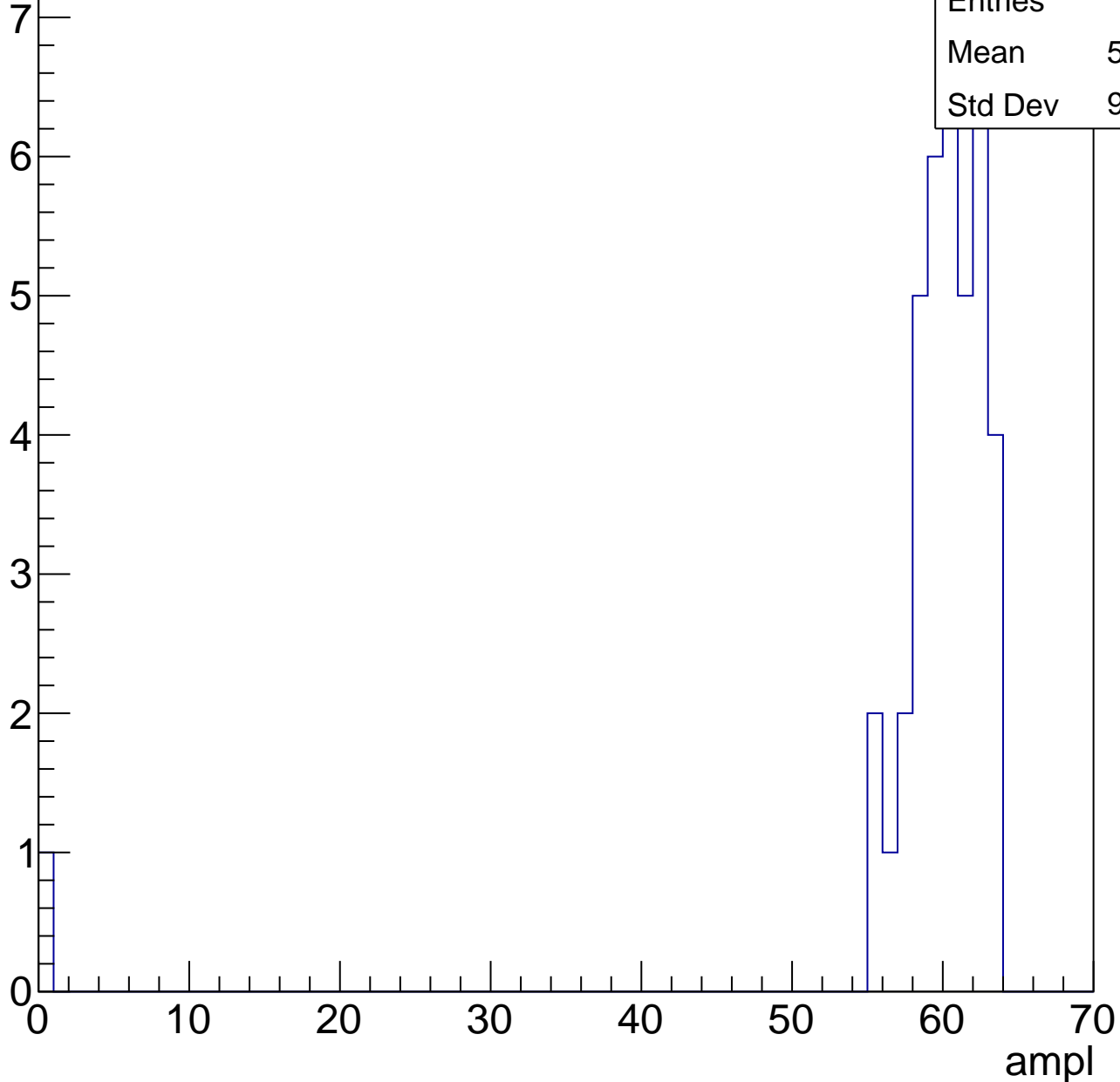
Entries	74
Mean	55.01
Std Dev	3.581



# B1L102S, U25-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

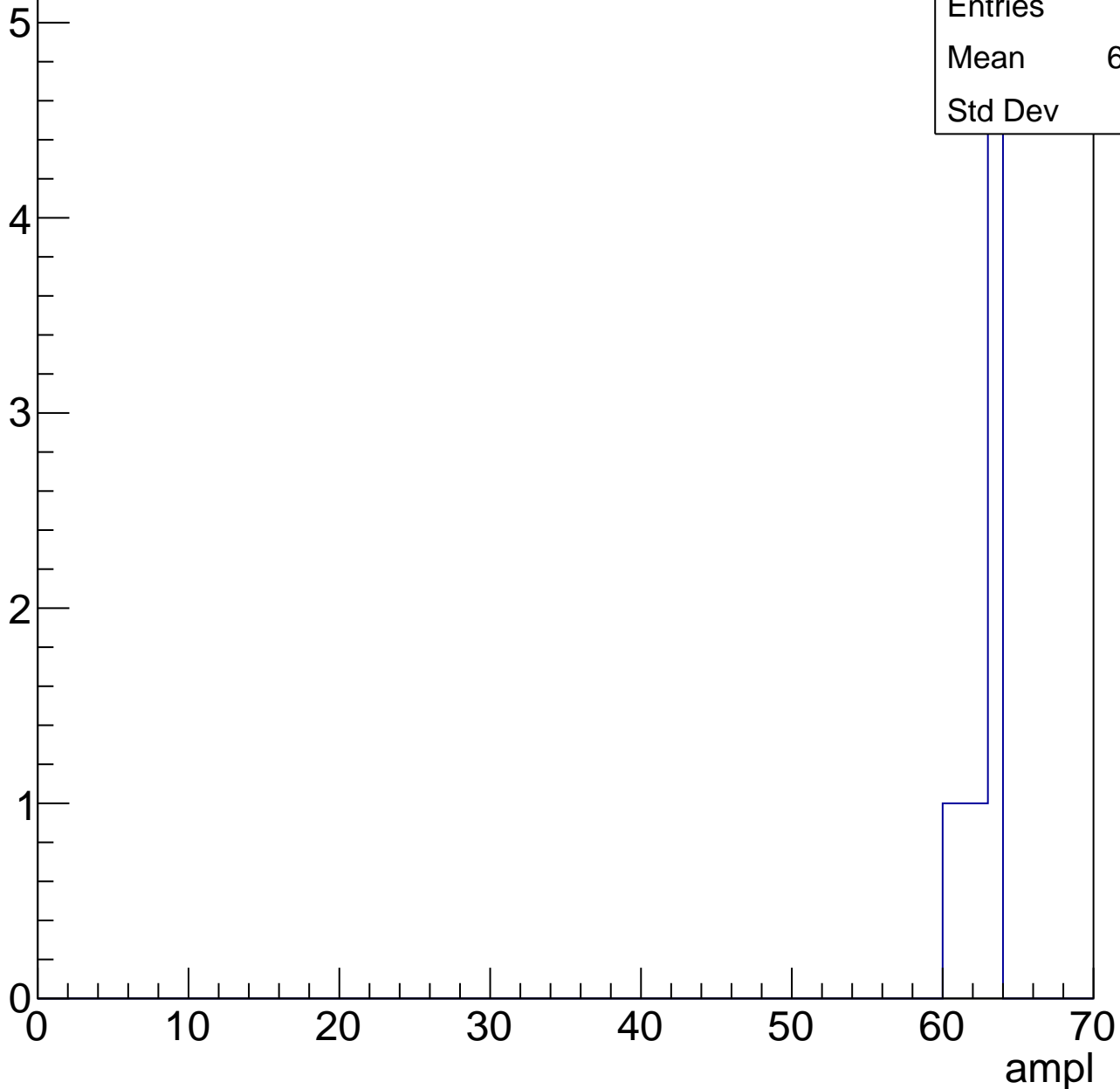


# B1L102S, U25-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	8
Mean	62.25
Std Dev	1.09





# B1L102S, U25-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U25-ch59, adc0

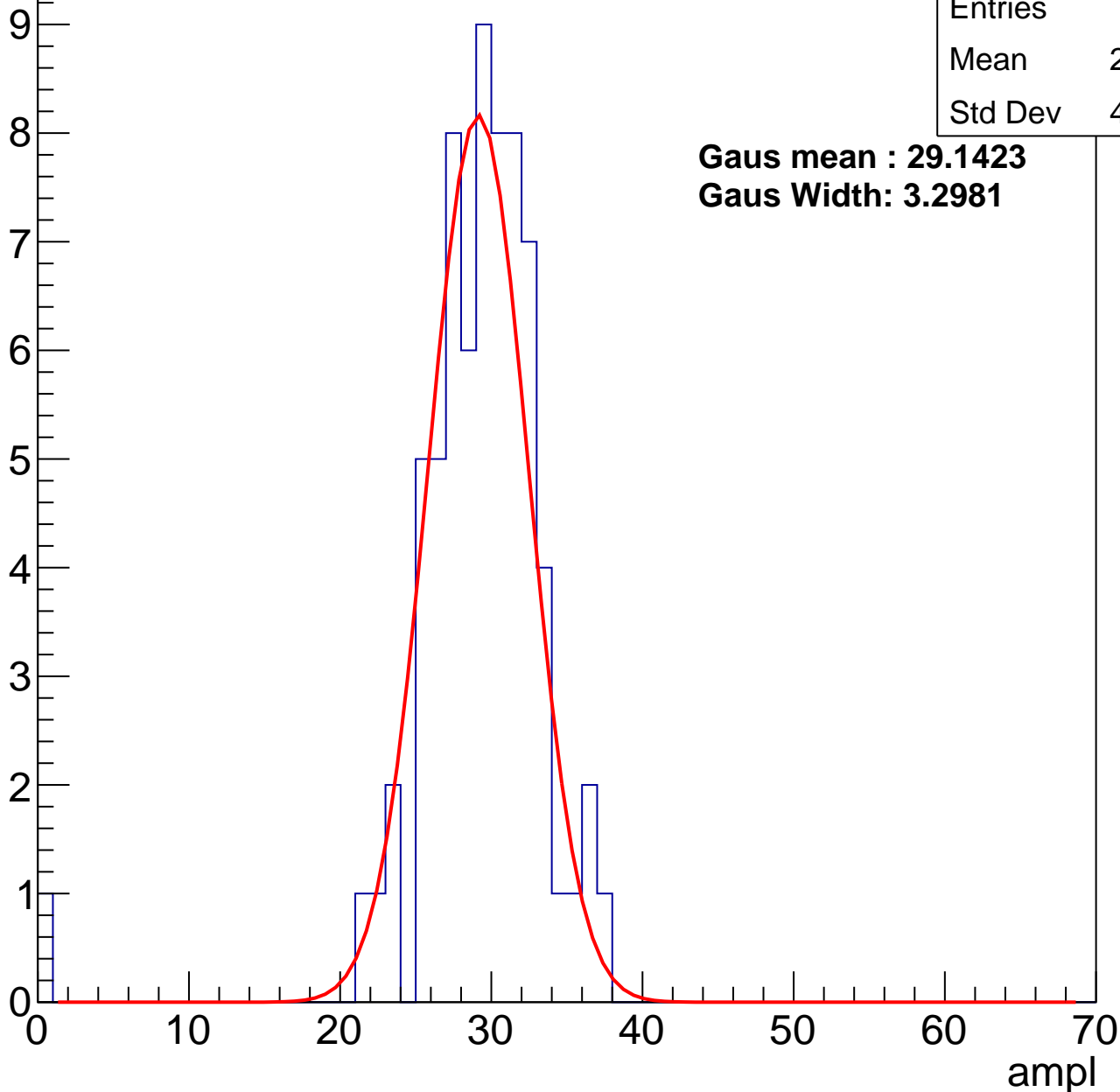
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.73
Std Dev	4.742

**Gaus mean : 29.1423**

**Gaus Width: 3.2981**



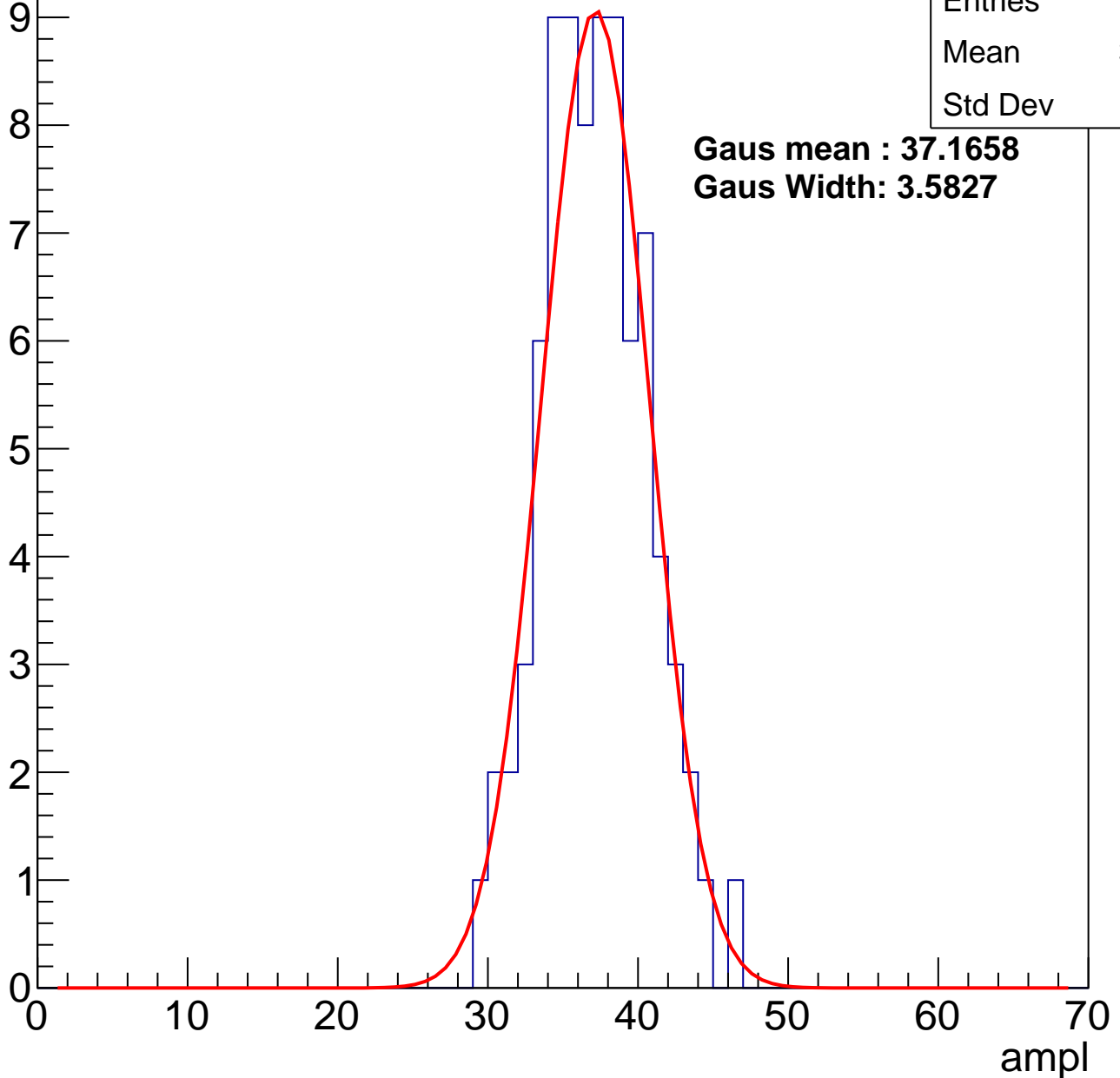
# B1L102S, U25-ch59, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	82
Mean	36.7
Std Dev	3.42

**Gaus mean : 37.1658**  
**Gaus Width: 3.5827**



# B1L102S, U25-ch59, adc2

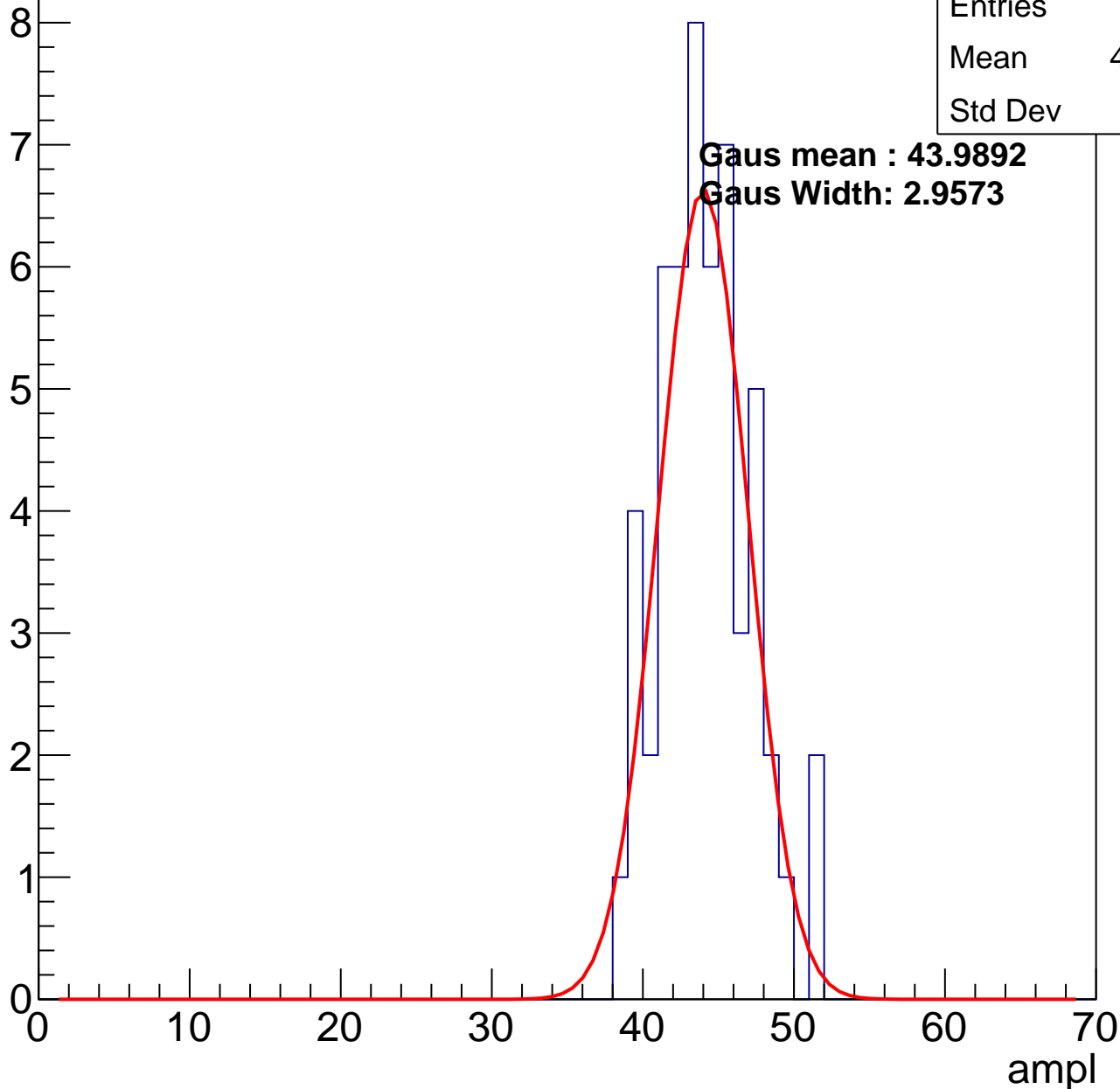
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.68
Std Dev	2.97

**Gaus mean : 43.9892**

**Gaus Width: 2.9573**

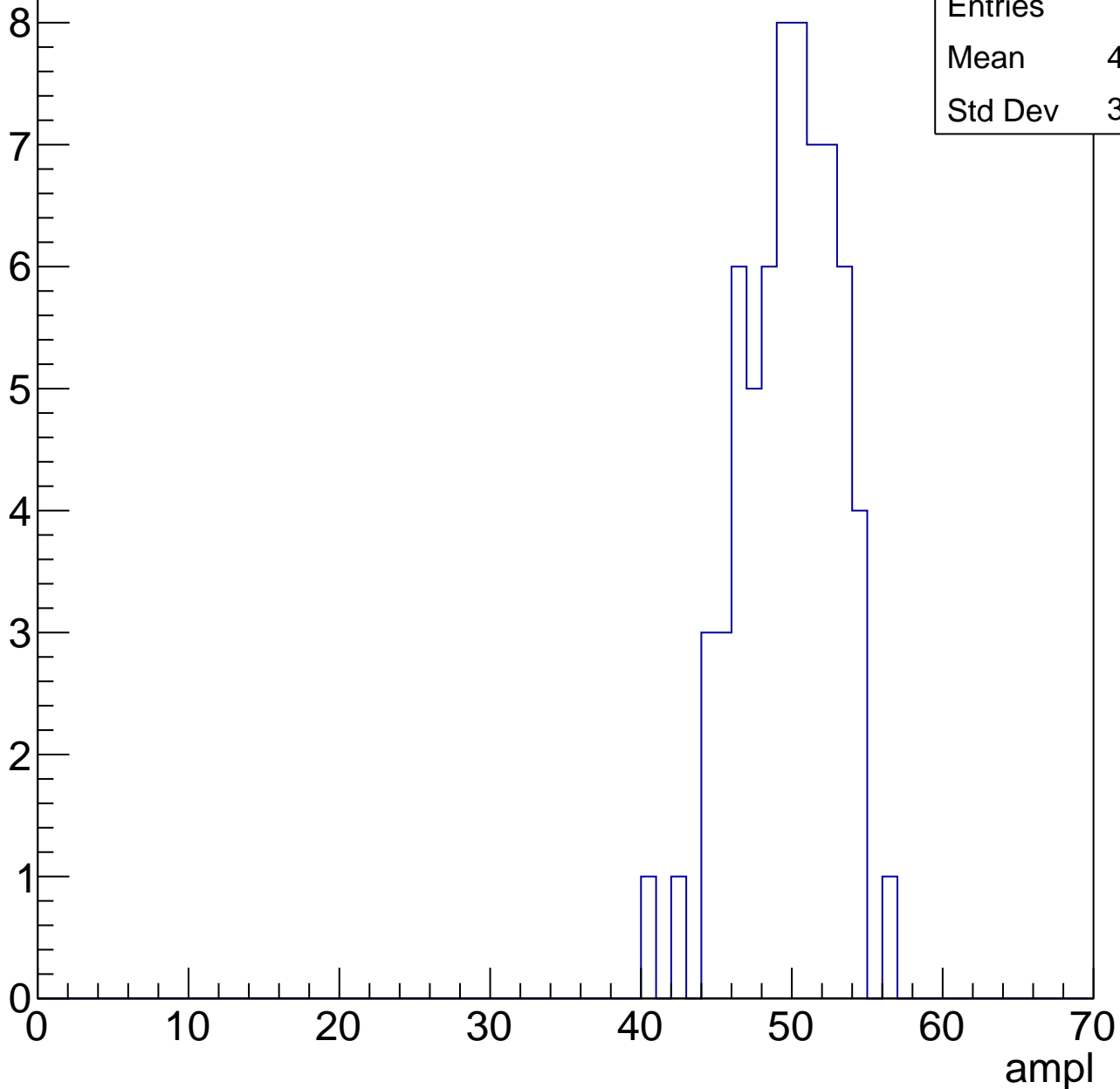


# B1L102S, U25-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.26
Std Dev	3.192

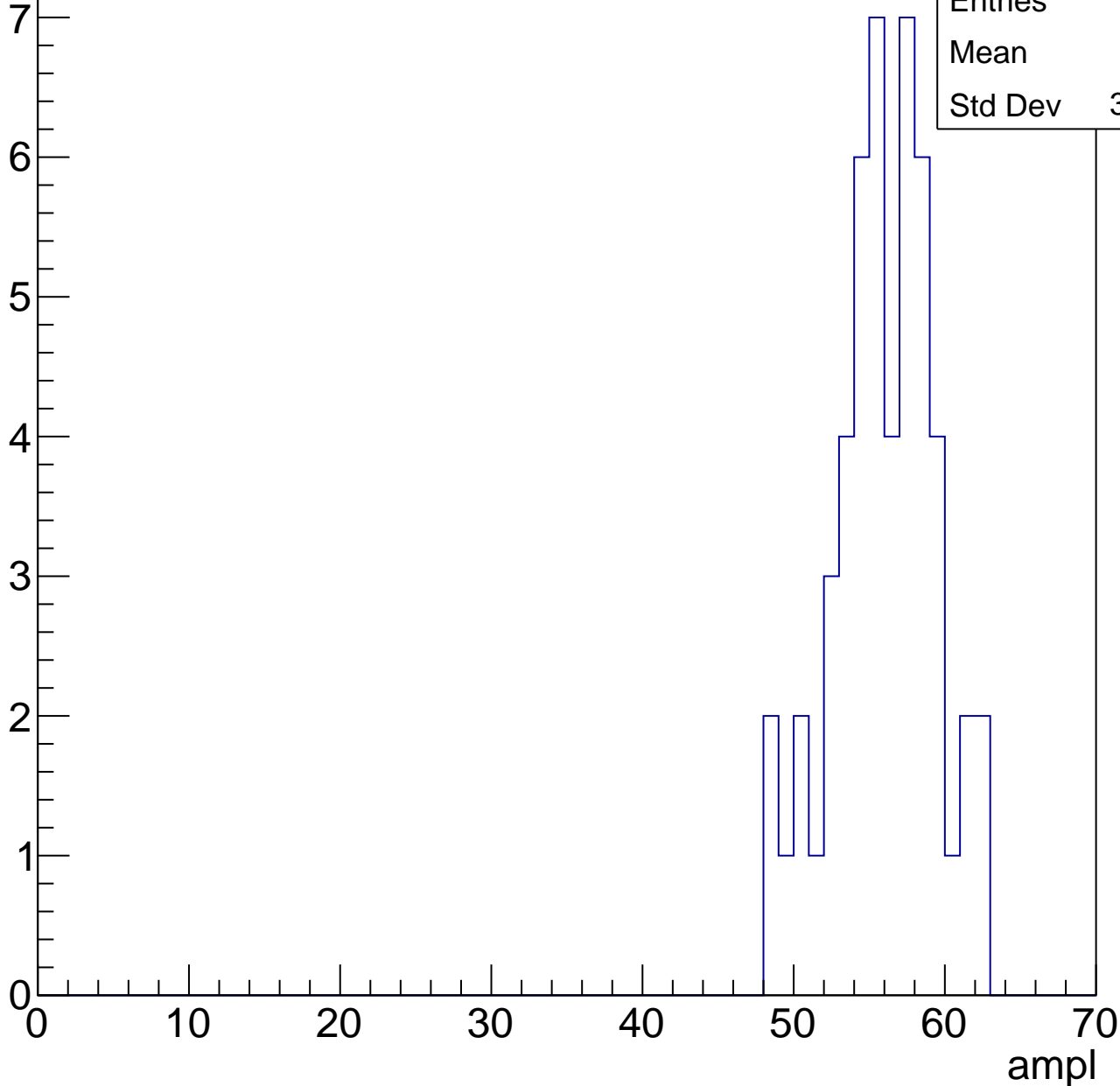


# B1L102S, U25-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.5
Std Dev	3.343

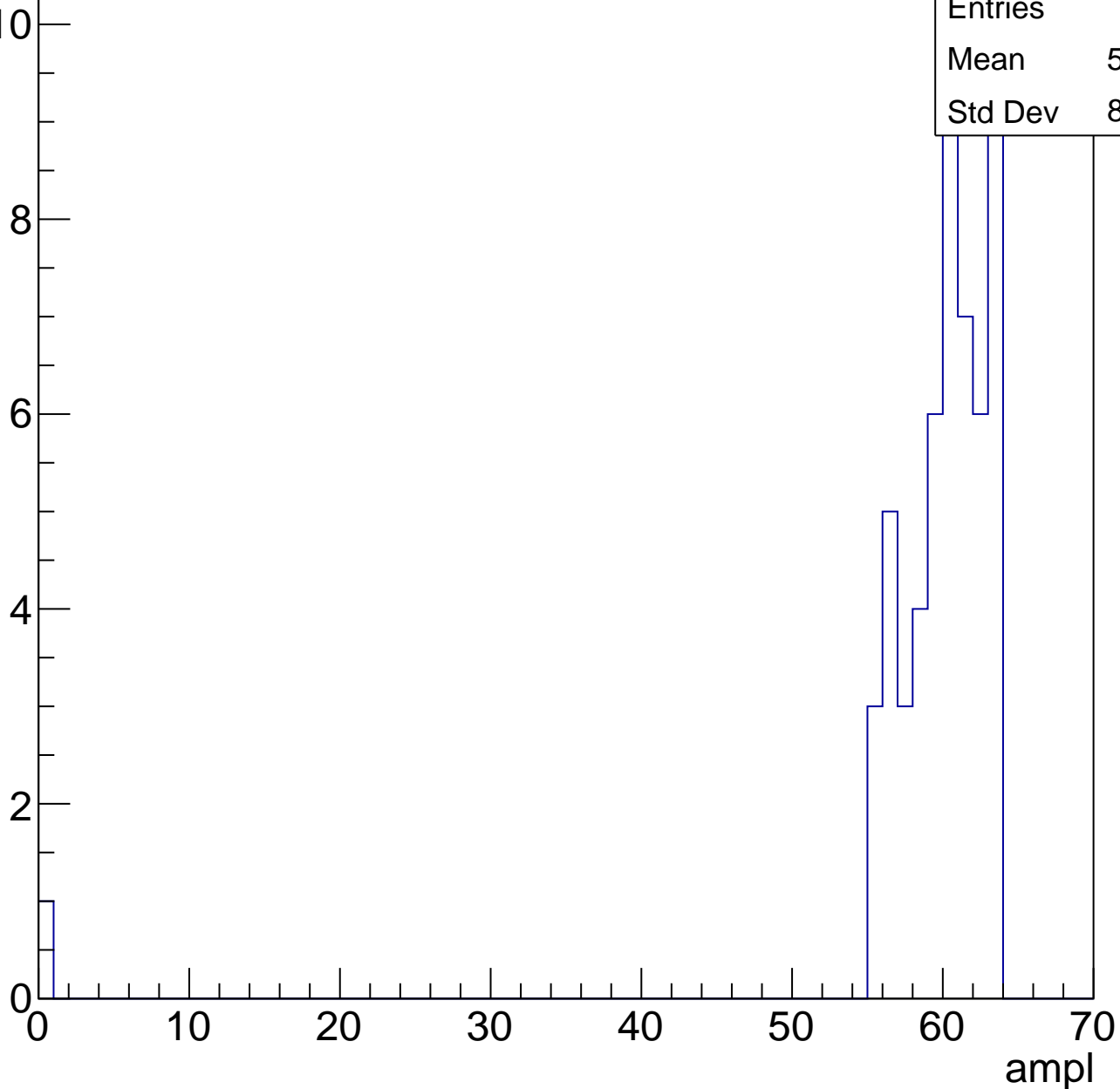


# B1L102S, U25-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.72
Std Dev	8.429



# B1L102S, U25-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

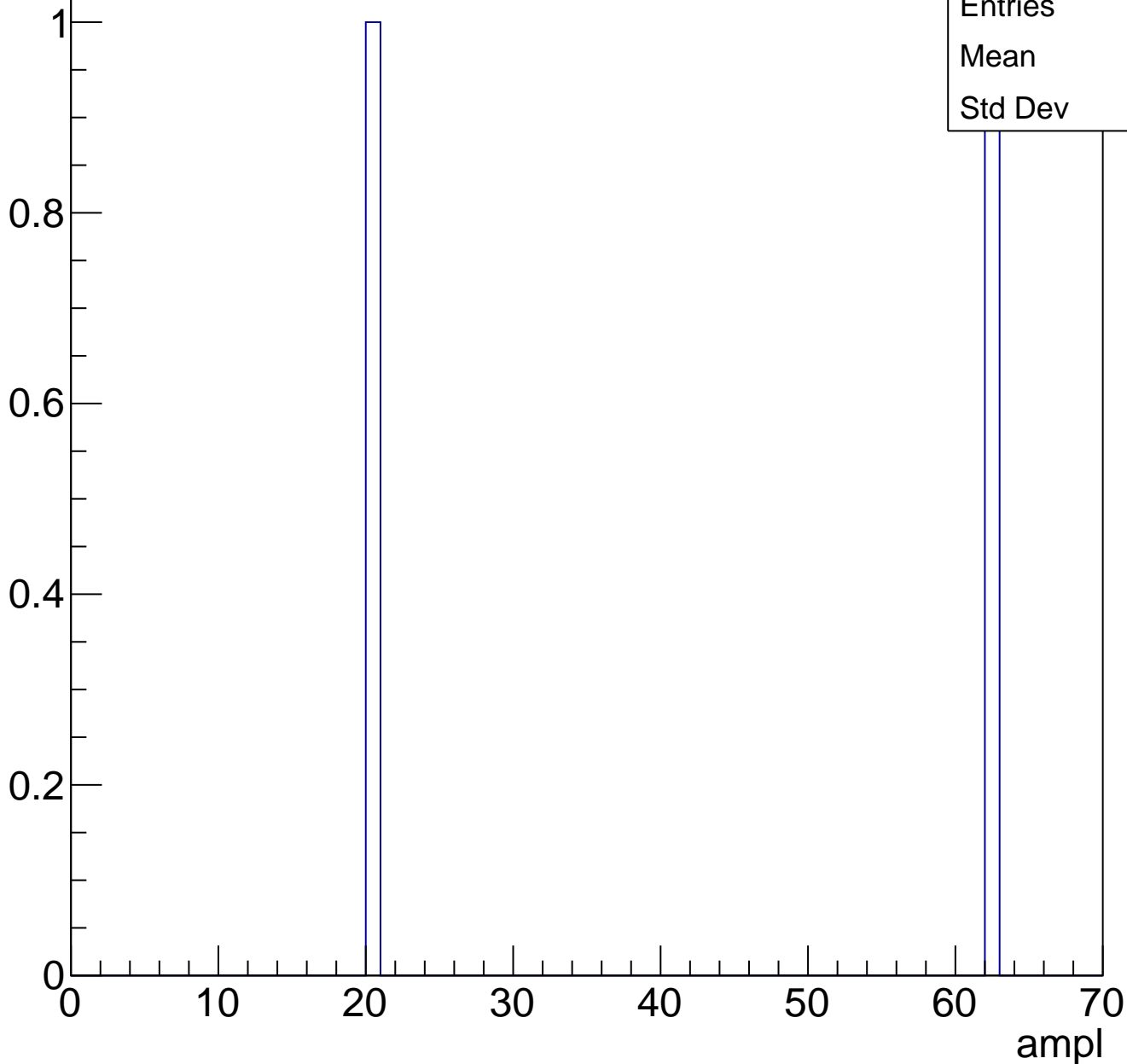




# B1L102S, U25-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	41
Std Dev	21

# B1L102S, U25-ch60, adc0

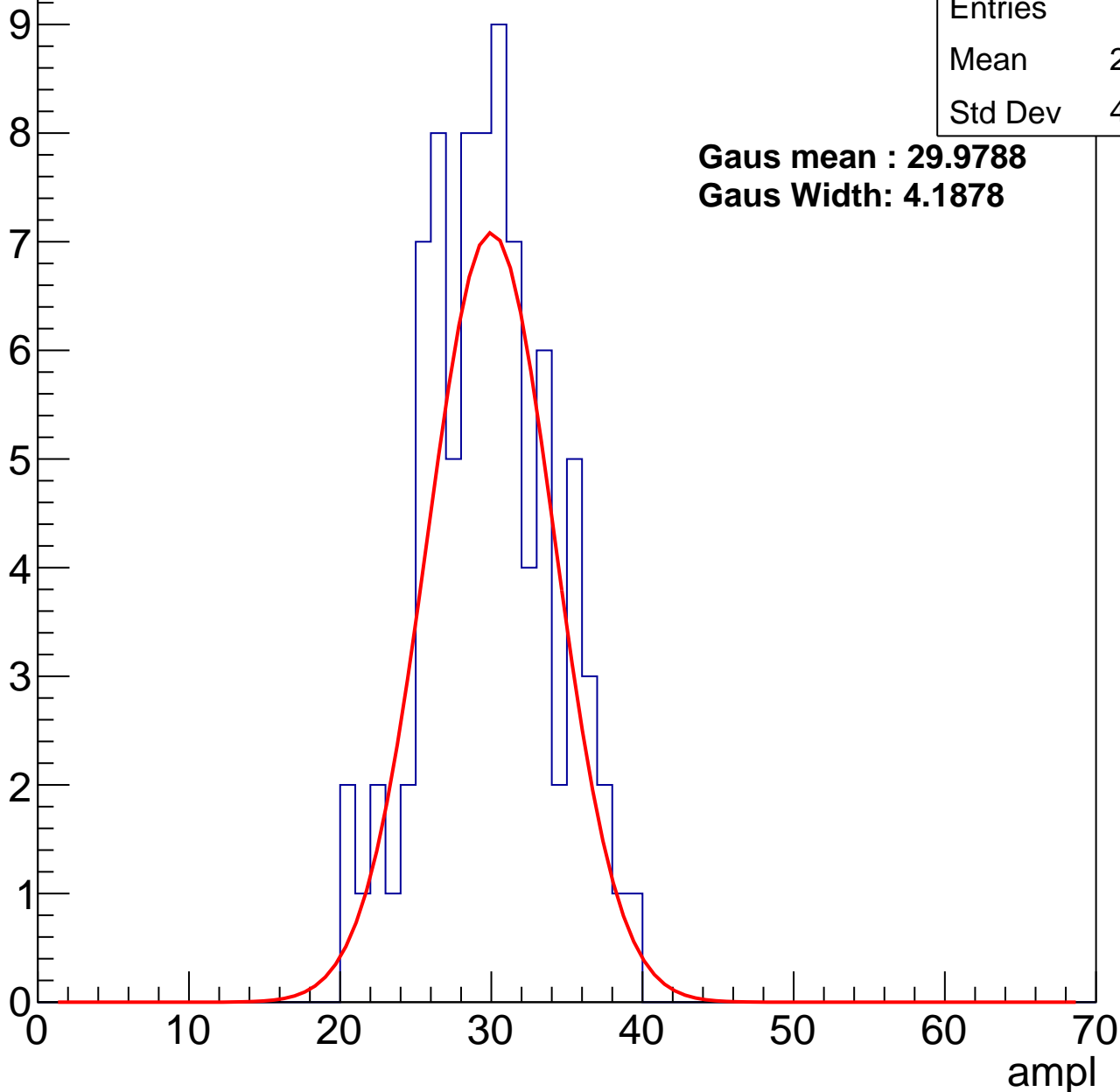
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	29.35
Std Dev	4.179

**Gaus mean : 29.9788**

**Gaus Width: 4.1878**



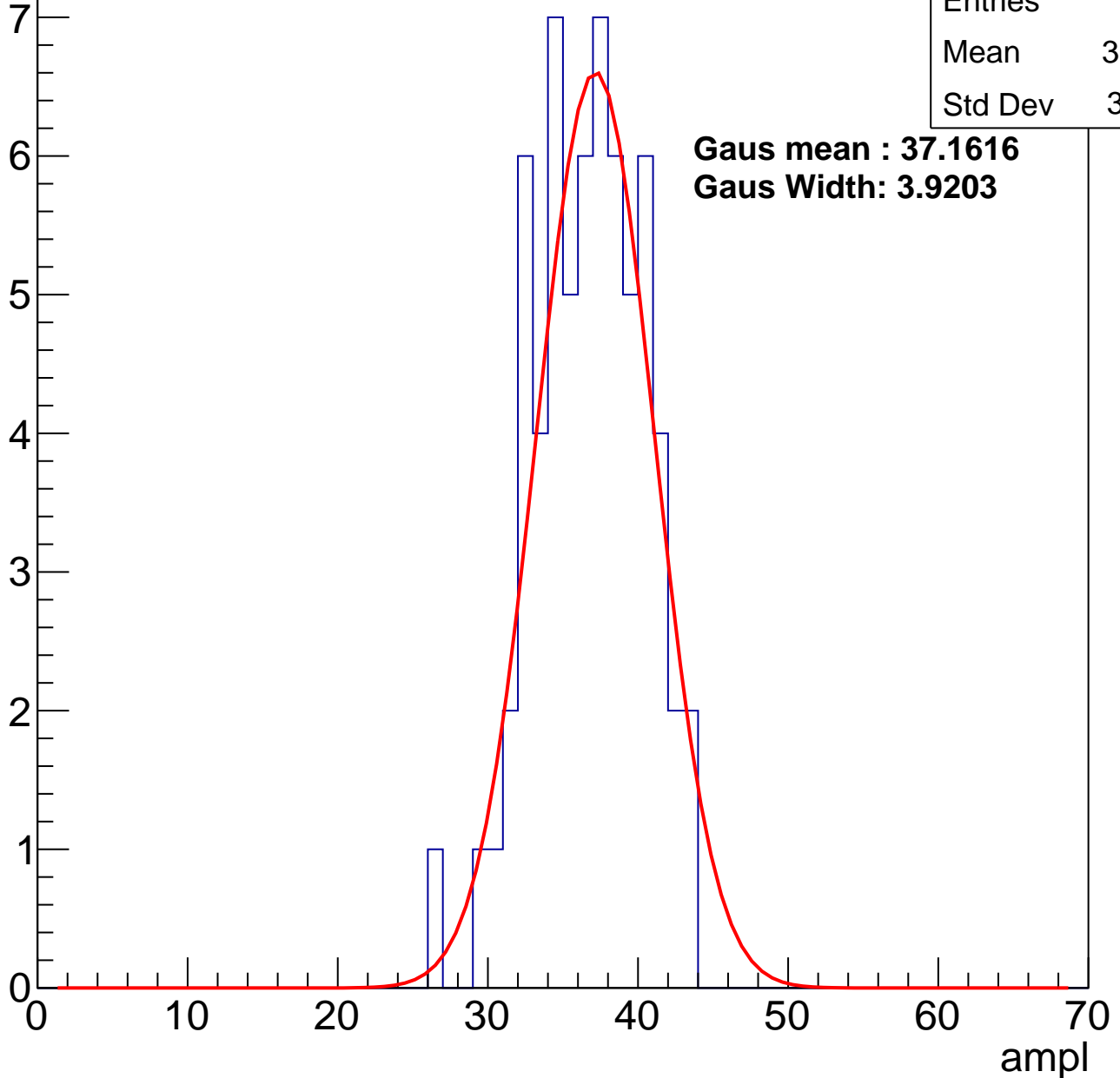
# B1L102S, U25-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.25
Std Dev	3.591

**Gaus mean : 37.1616**  
**Gaus Width: 3.9203**

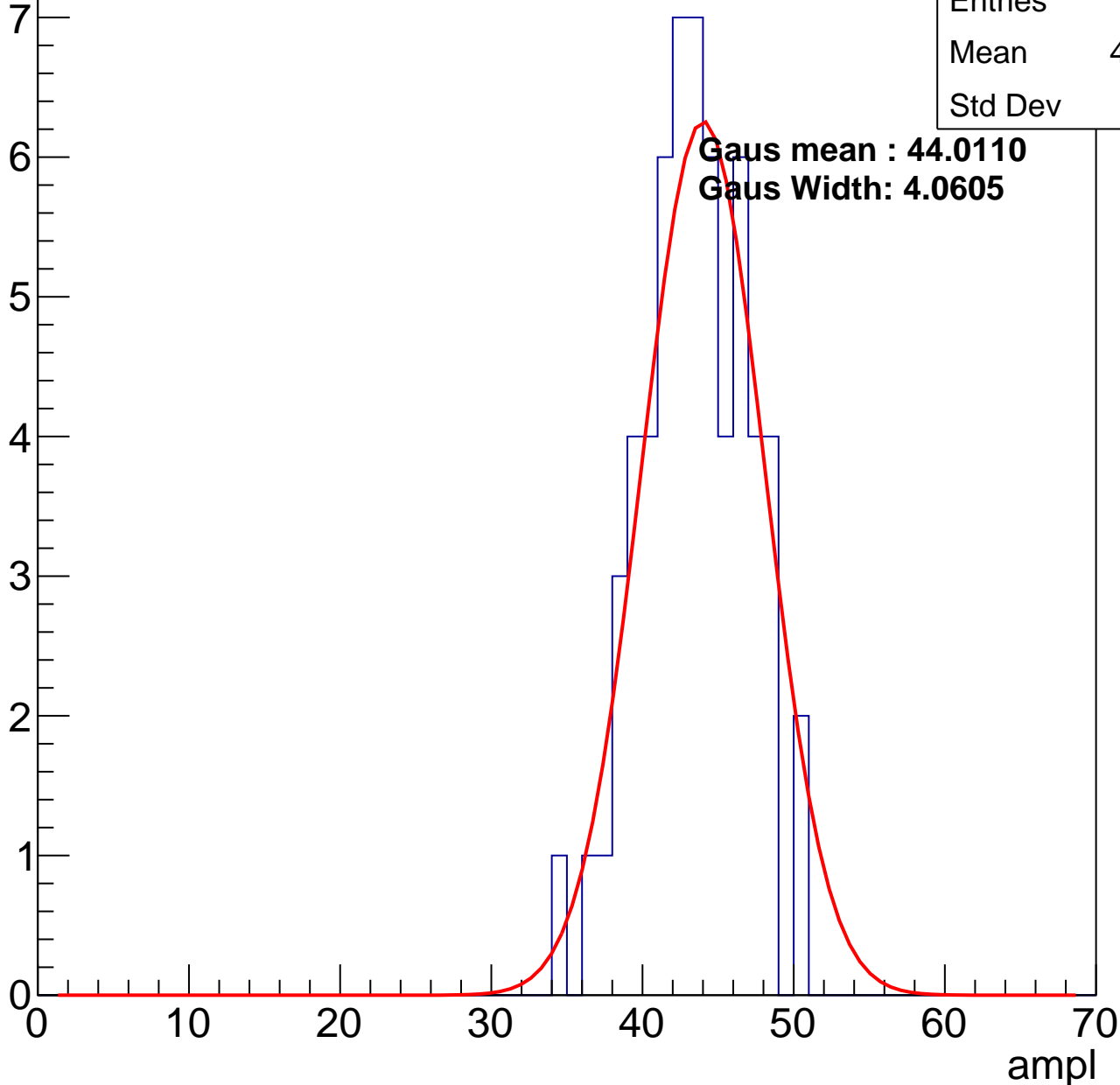


# B1L102S, U25-ch60, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.97
Std Dev	3.44

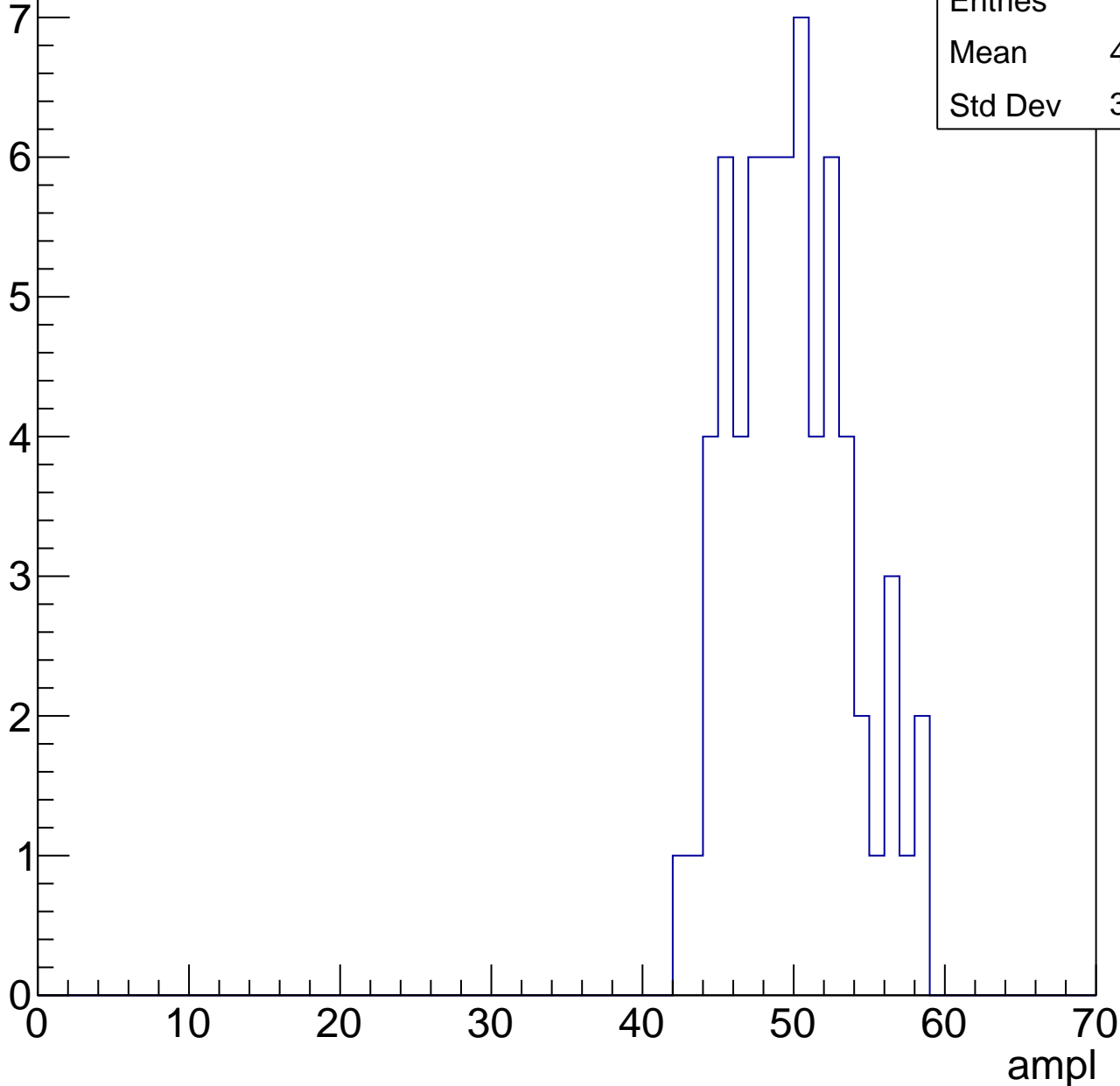


# B1L102S, U25-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

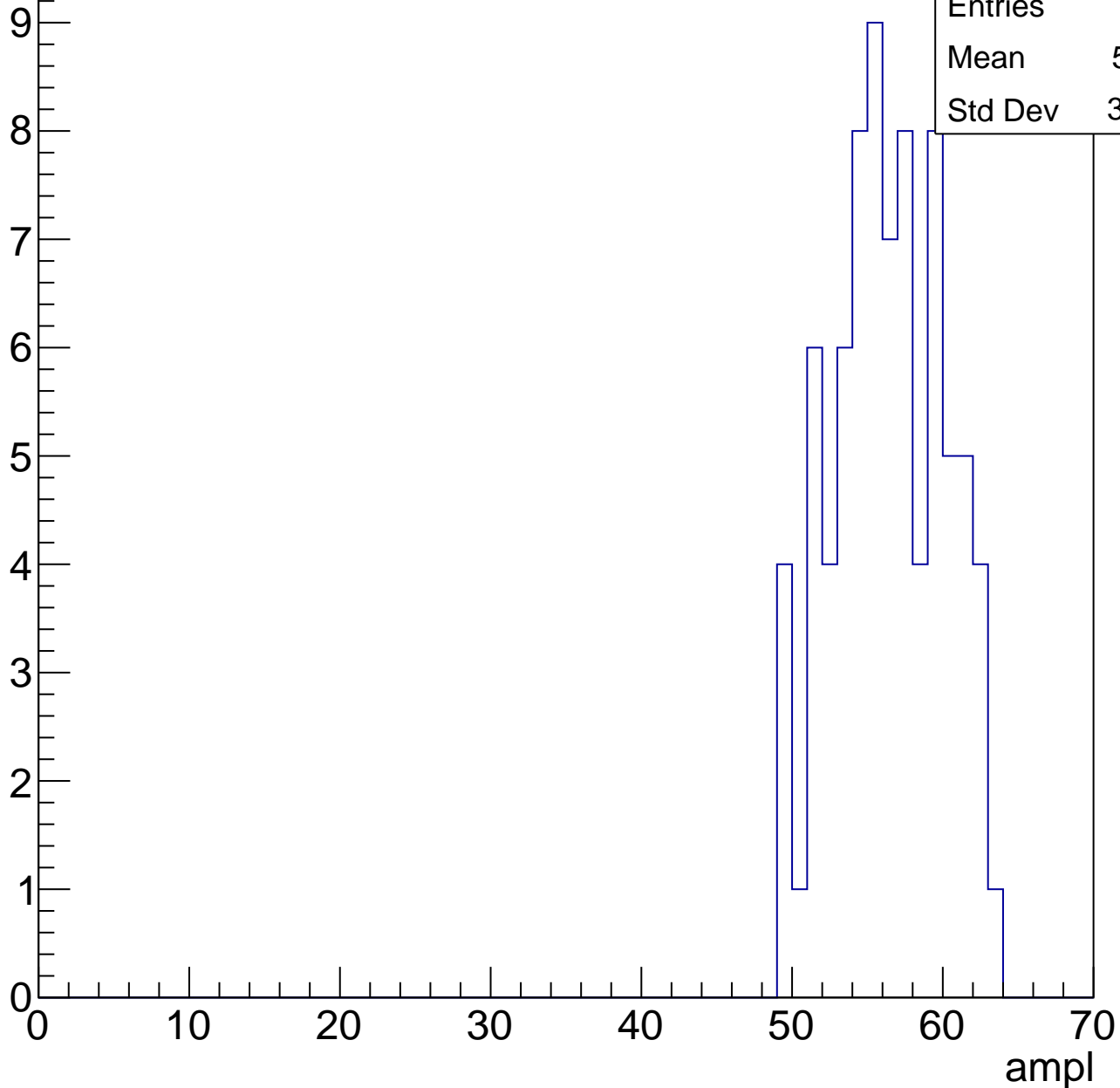
Entries	64
Mean	49.39
Std Dev	3.827



# B1L102S, U25-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



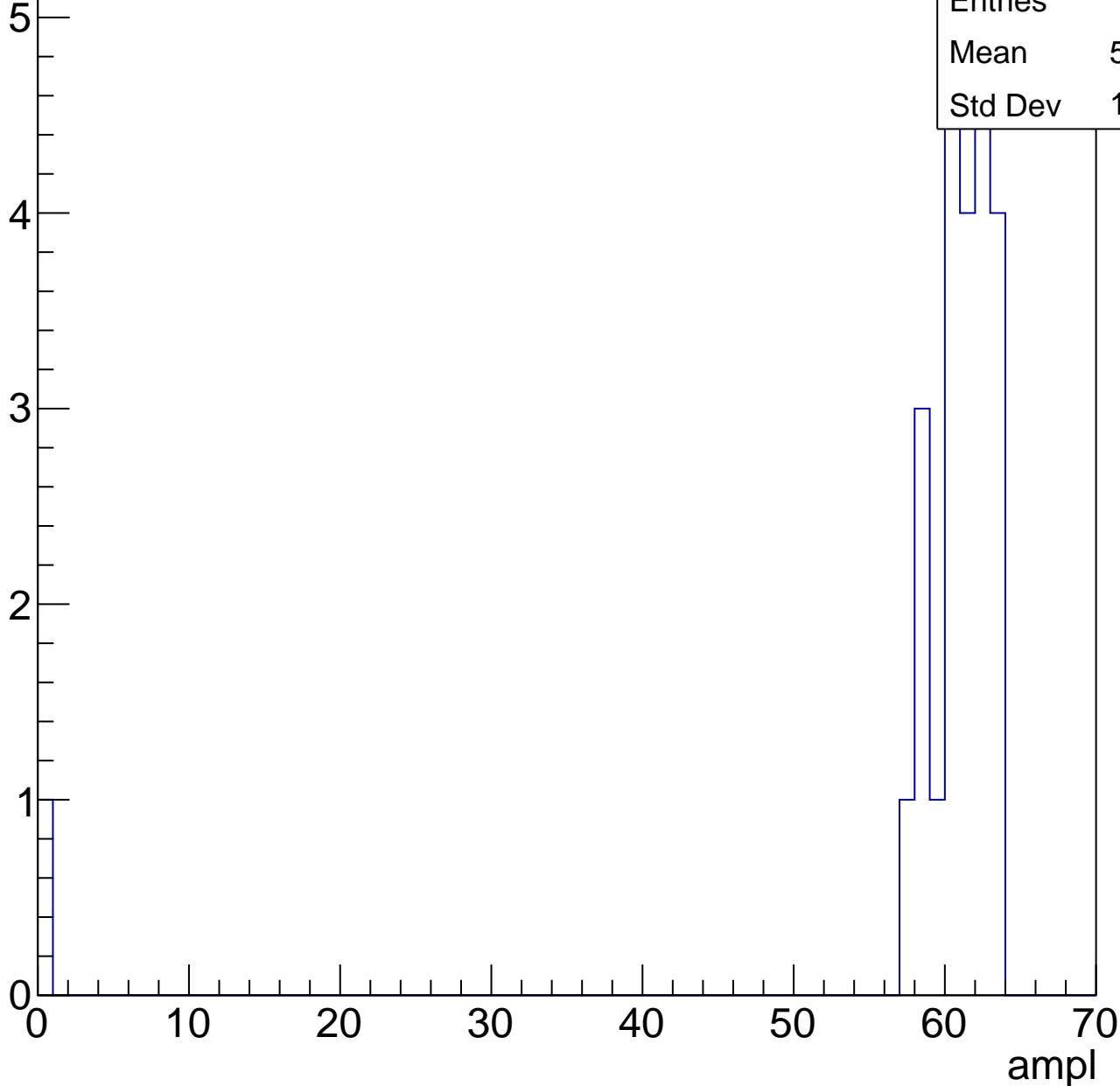
Entries	80
Mean	55.91
Std Dev	3.596

# B1L102S, U25-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	24
Mean	58.17
Std Dev	12.25

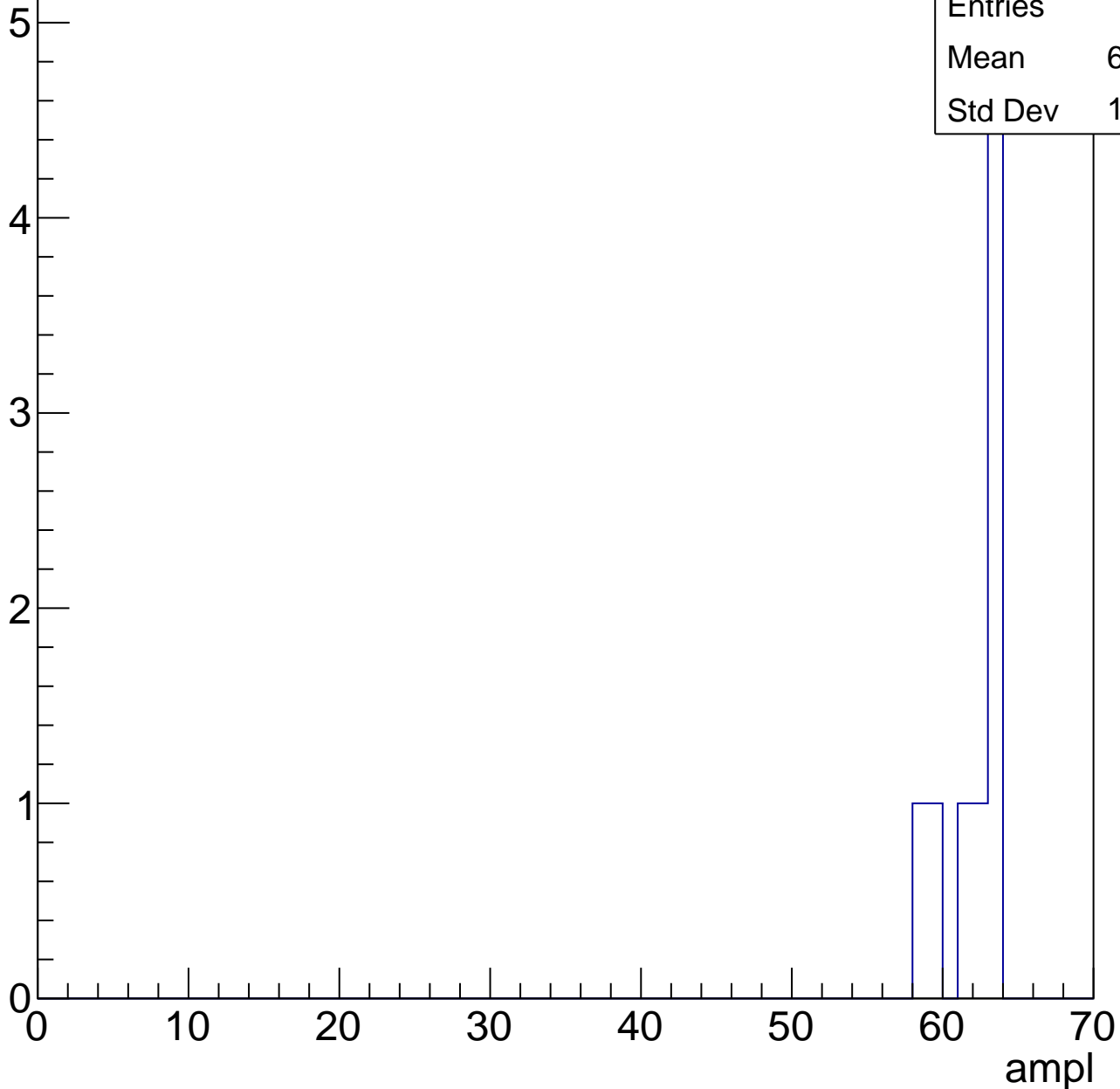


# B1L102S, U25-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.67
Std Dev	1.826





# B1L102S, U25-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch61, adc0

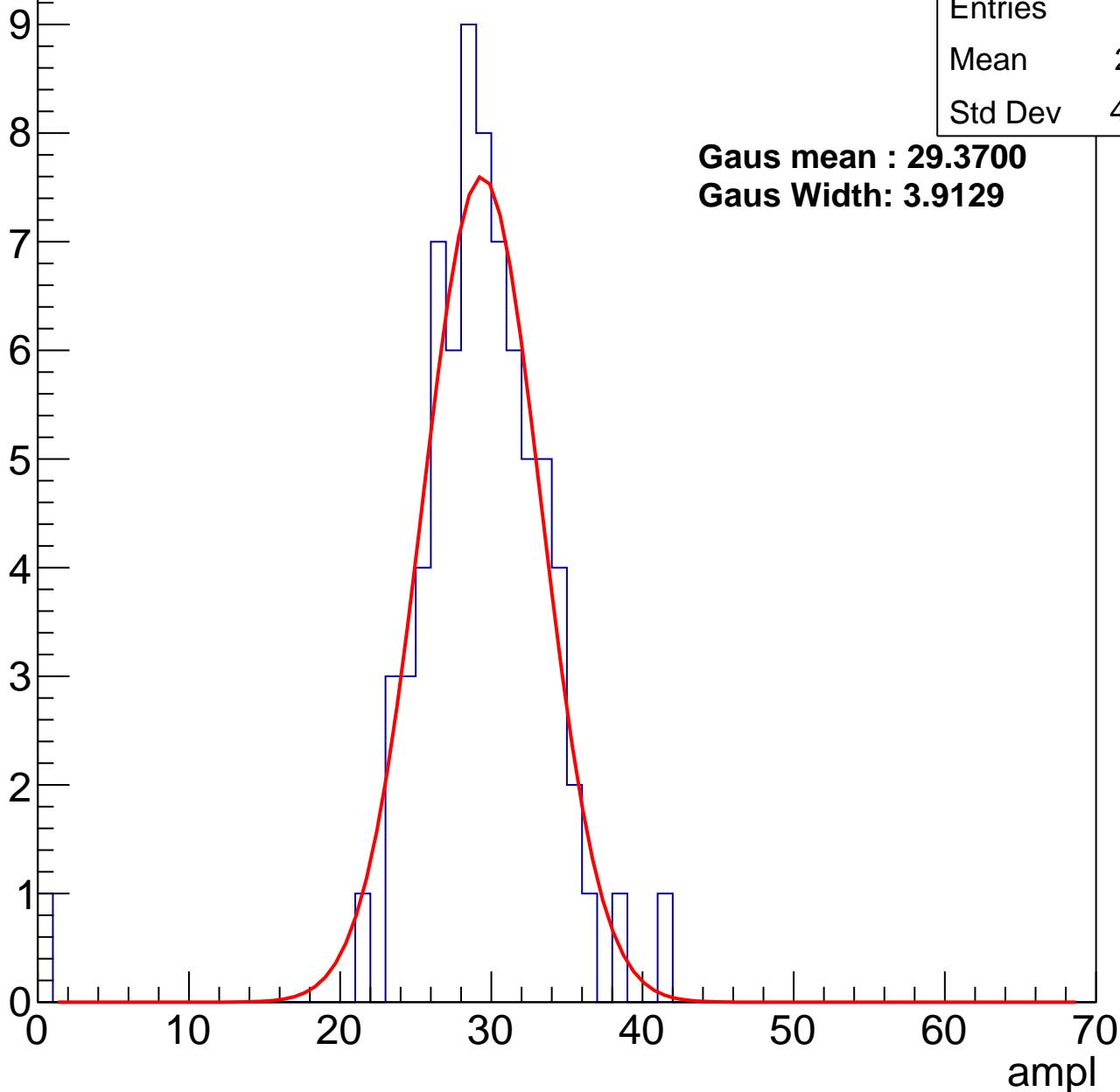
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.81
Std Dev	4.996

**Gaus mean : 29.3700**

**Gaus Width: 3.9129**



# B1L102S, U25-ch61, adc1

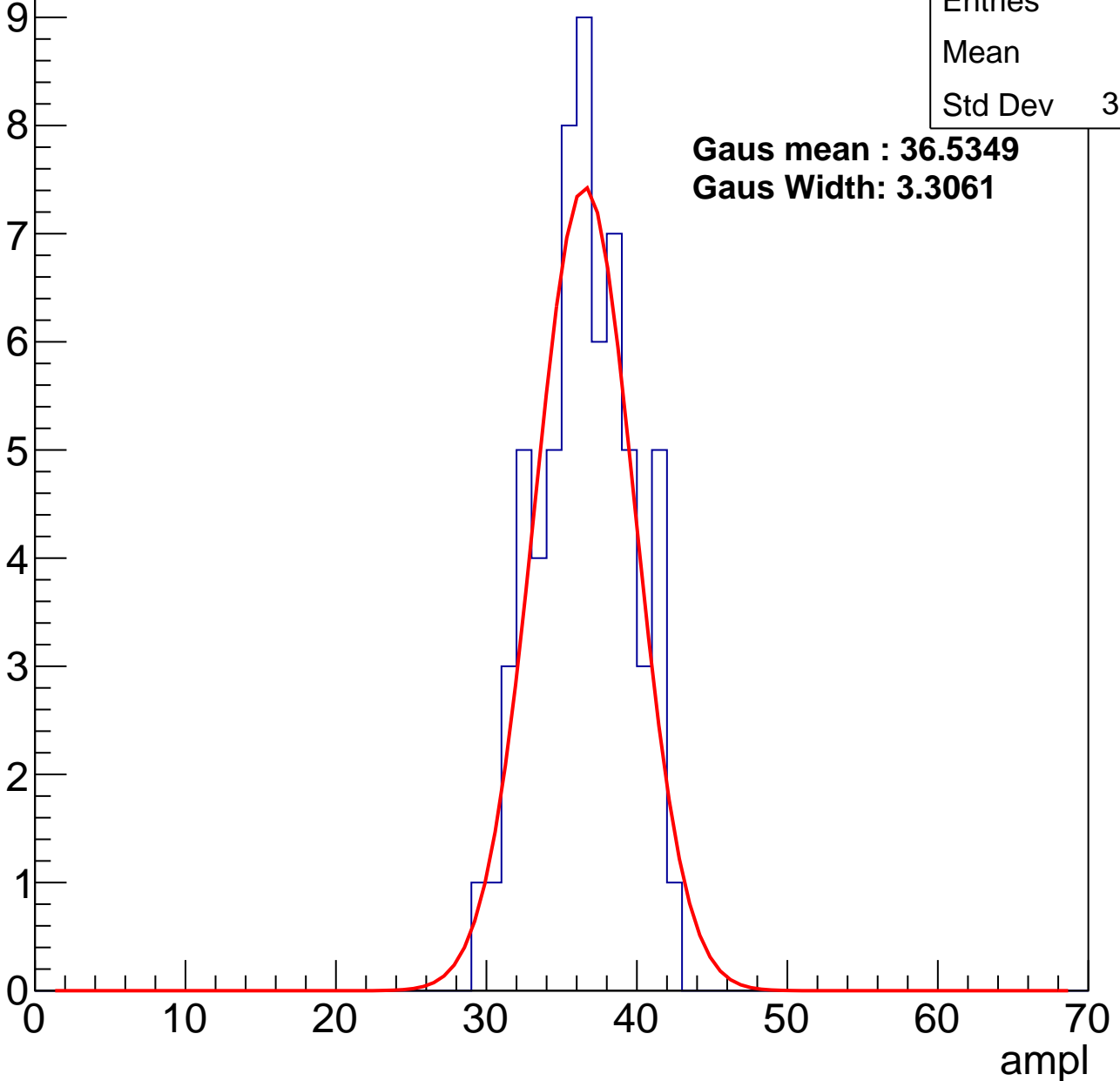
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36
Std Dev	3.065

**Gaus mean : 36.5349**

**Gaus Width: 3.3061**



# B1L102S, U25-ch61, adc2

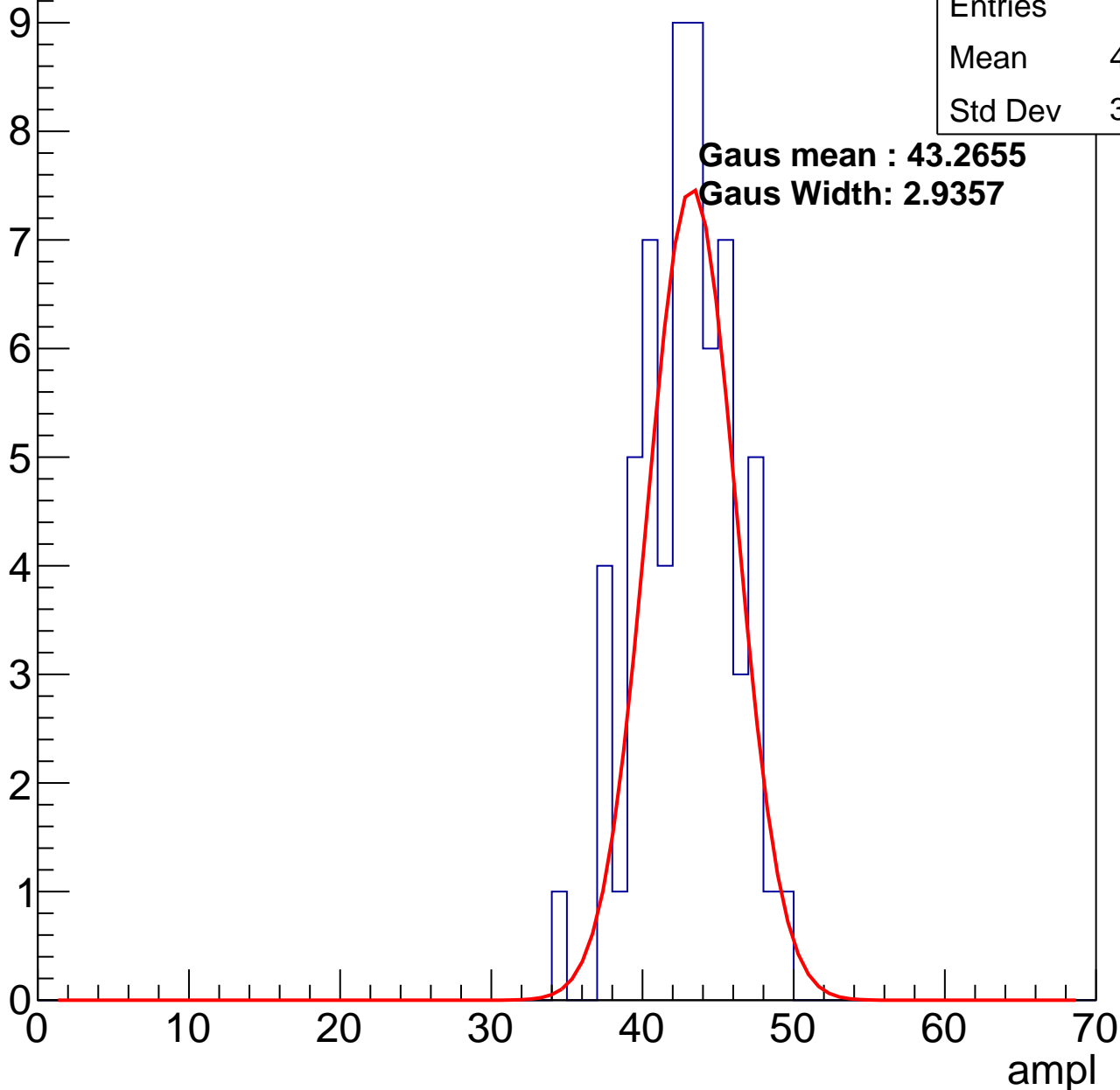
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.43
Std Dev	3.085

**Gaus mean : 43.2655**

**Gaus Width: 2.9357**

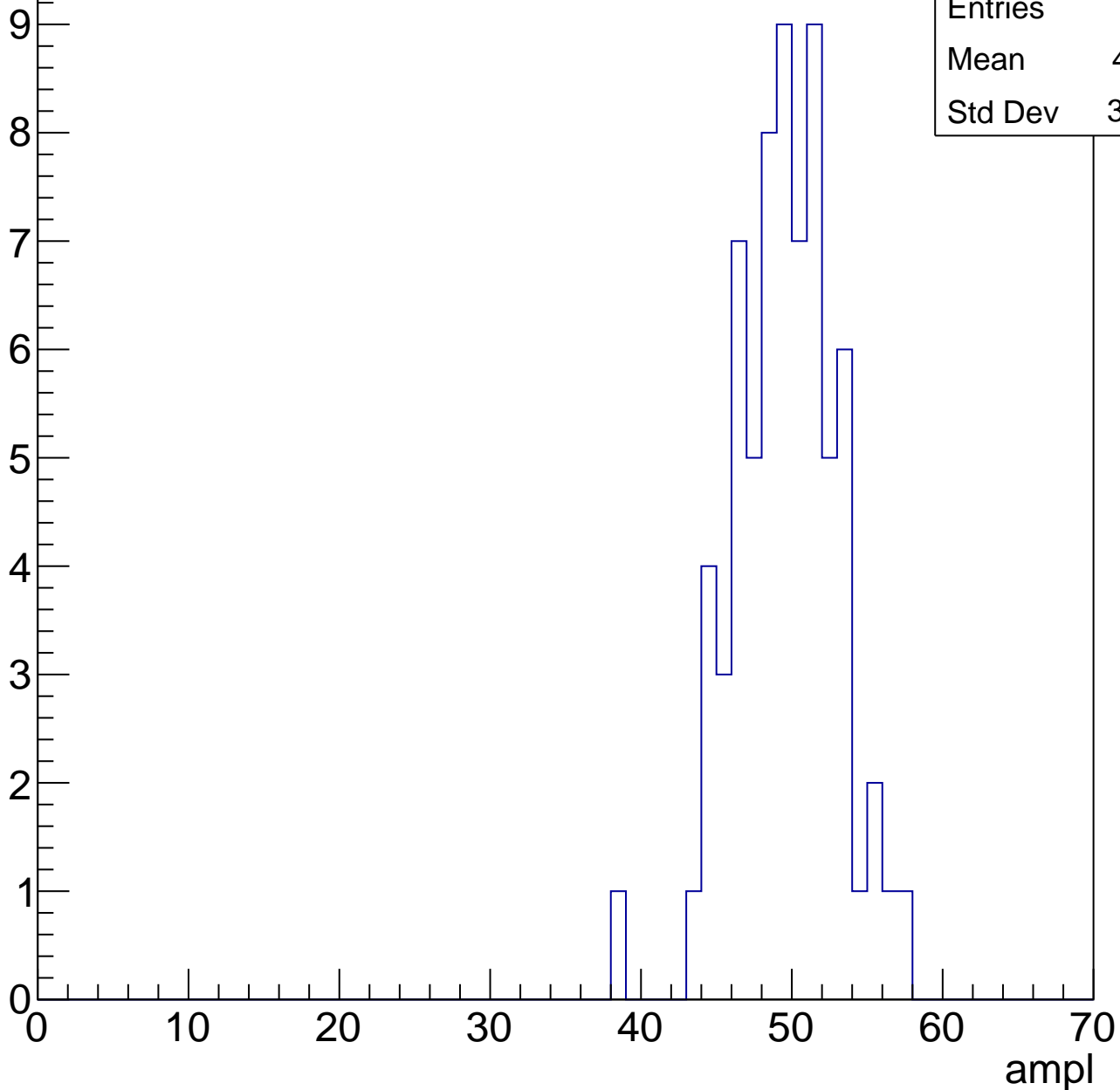


# B1L102S, U25-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.11
Std Dev	3.357

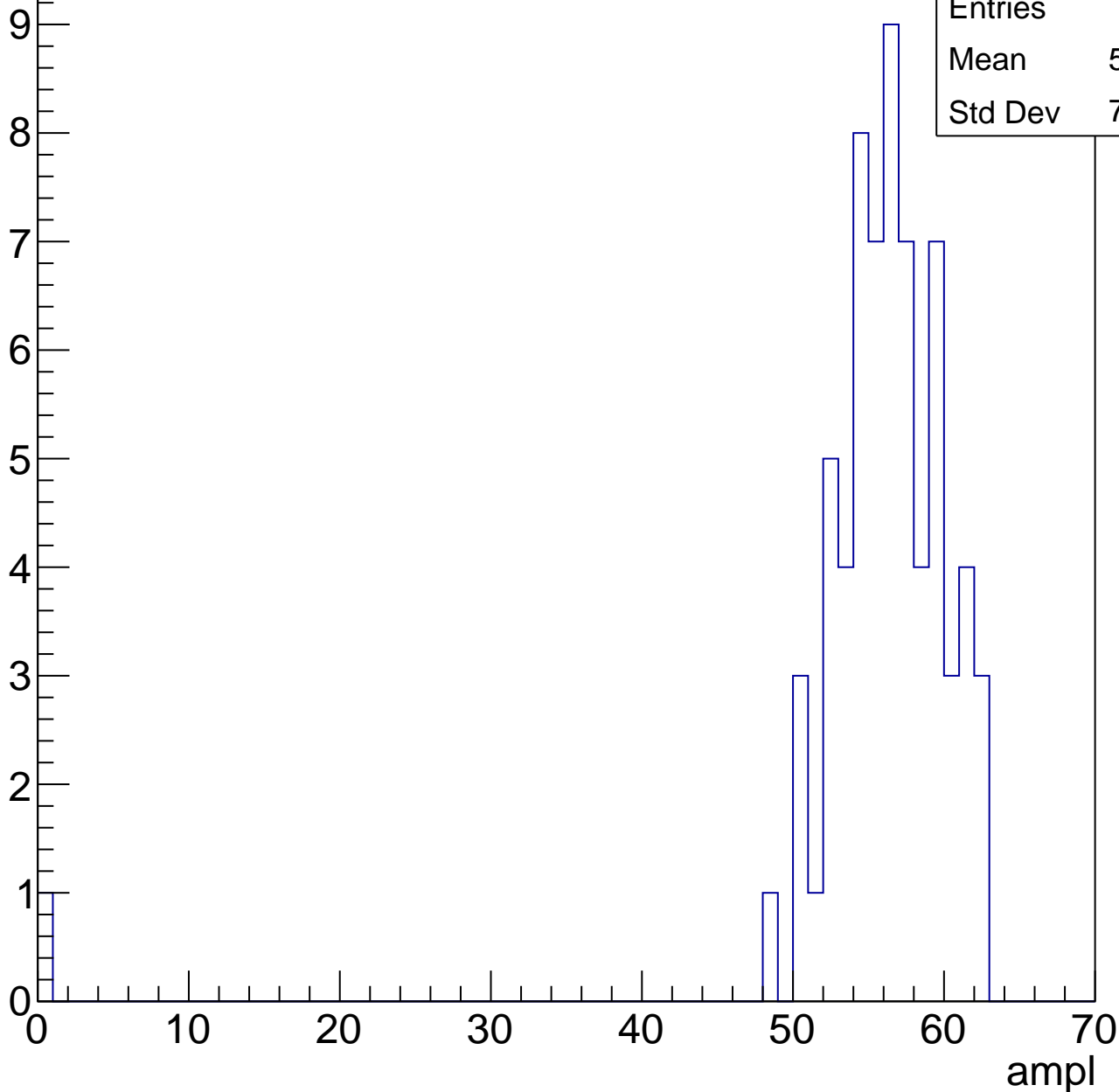


# B1L102S, U25-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

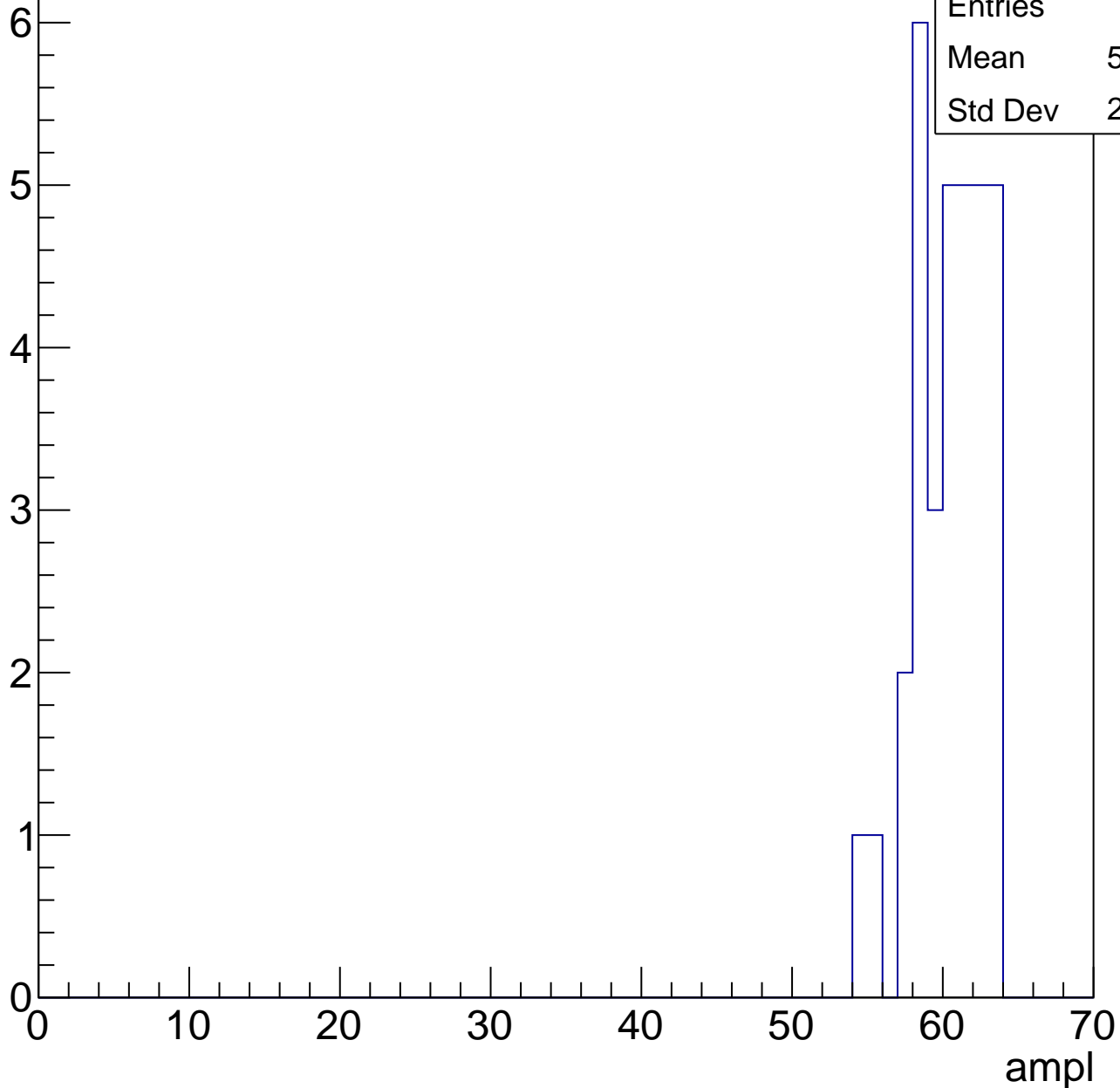
Entries	67
Mean	55.16
Std Dev	7.514



# B1L102S, U25-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

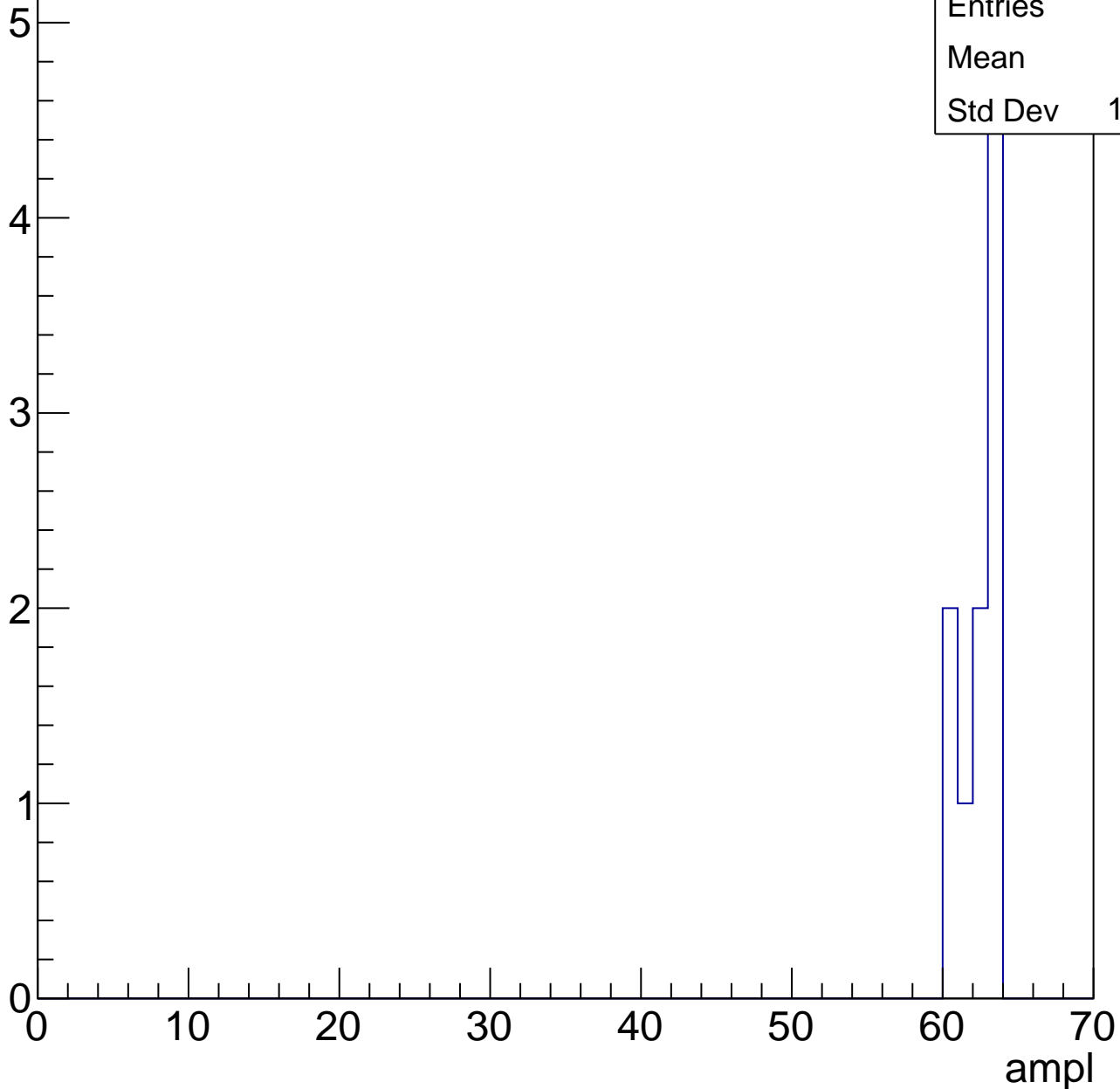


# B1L102S, U25-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	10
Mean	62
Std Dev	1.183





# B1L102S, U25-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch62, adc0

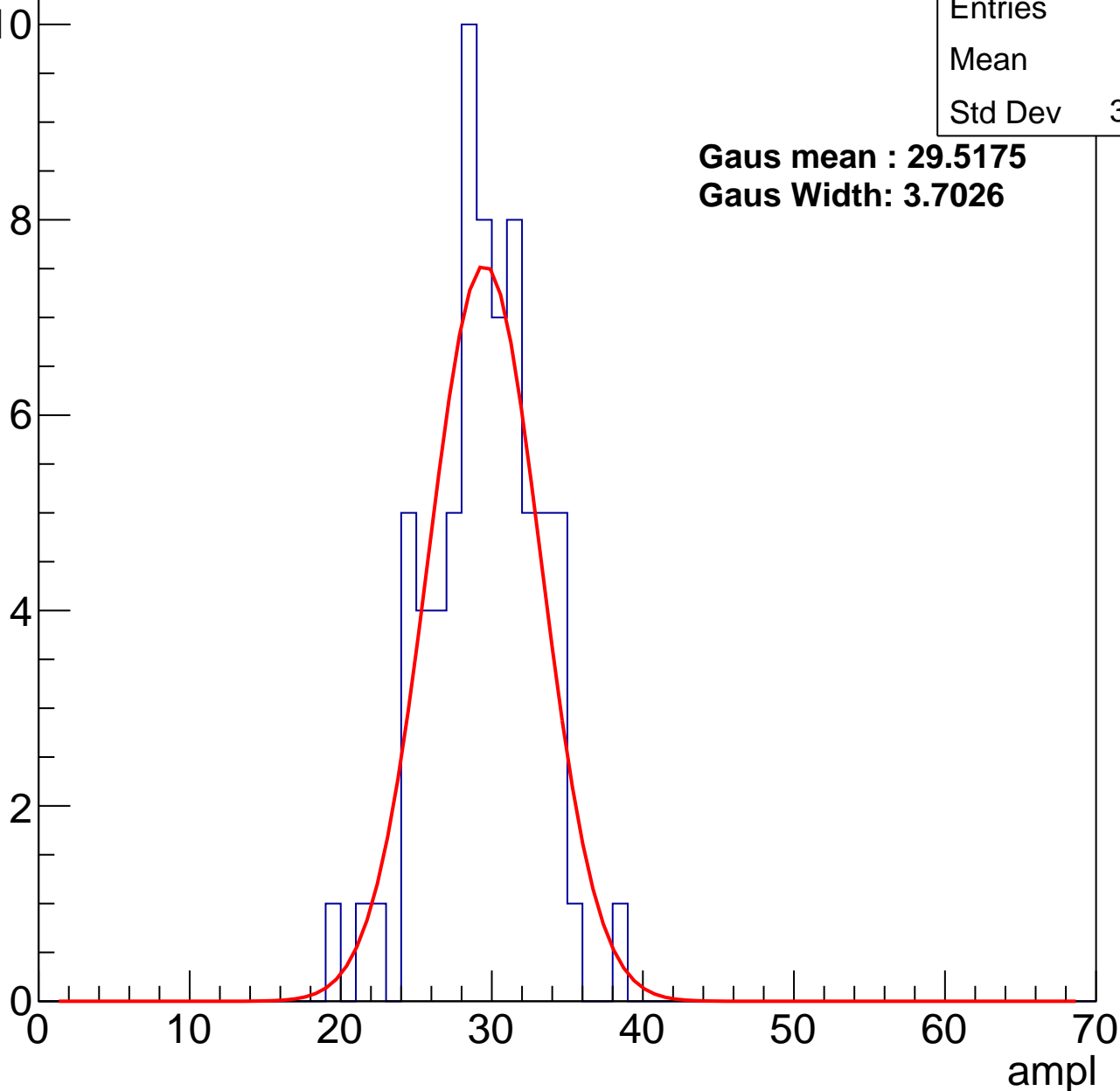
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29
Std Dev	3.509

**Gaus mean : 29.5175**

**Gaus Width: 3.7026**



# B1L102S, U25-ch62, adc1

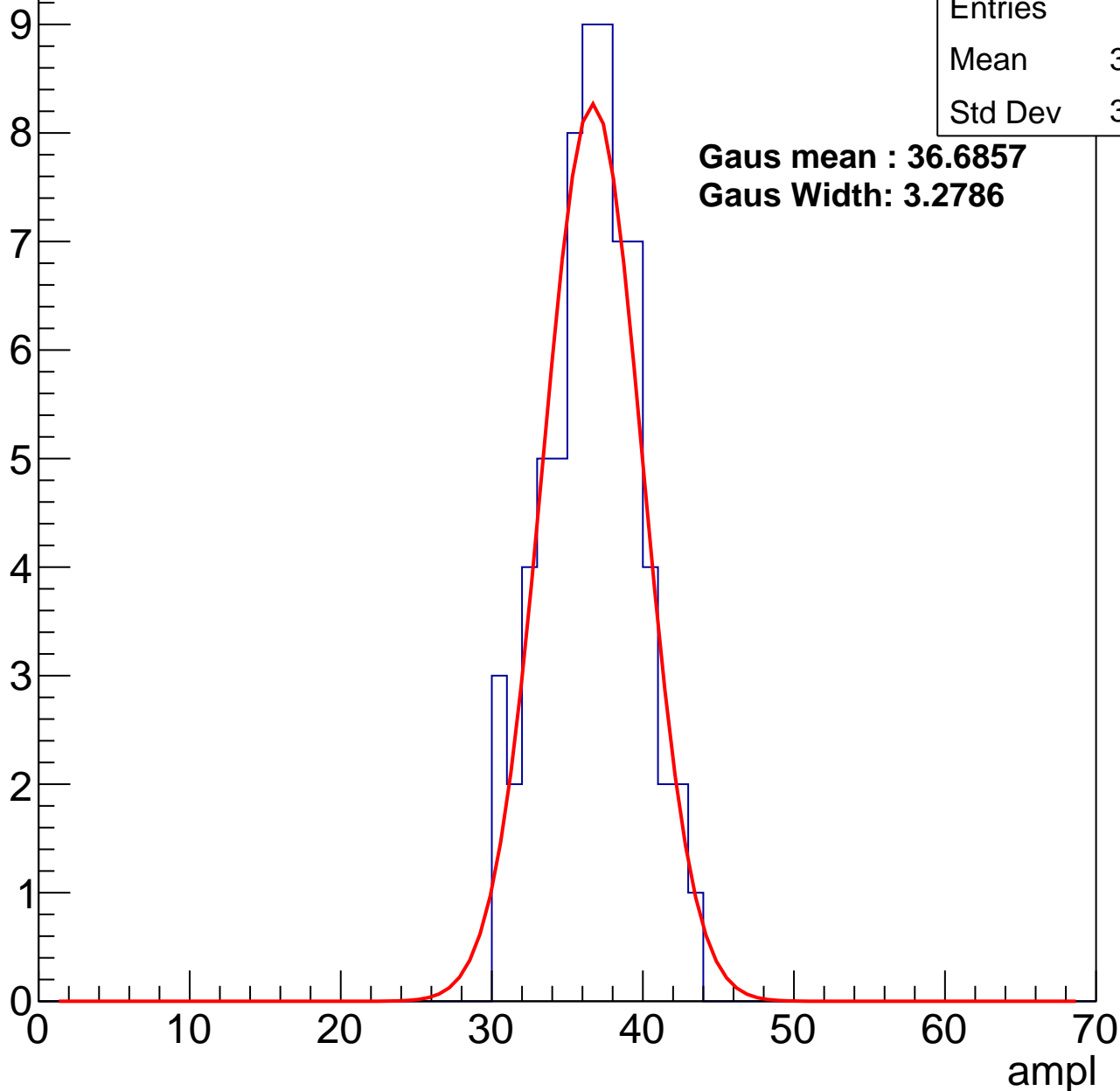
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.18
Std Dev	3.039

**Gaus mean : 36.6857**

**Gaus Width: 3.2786**



# B1L102S, U25-ch62, adc2

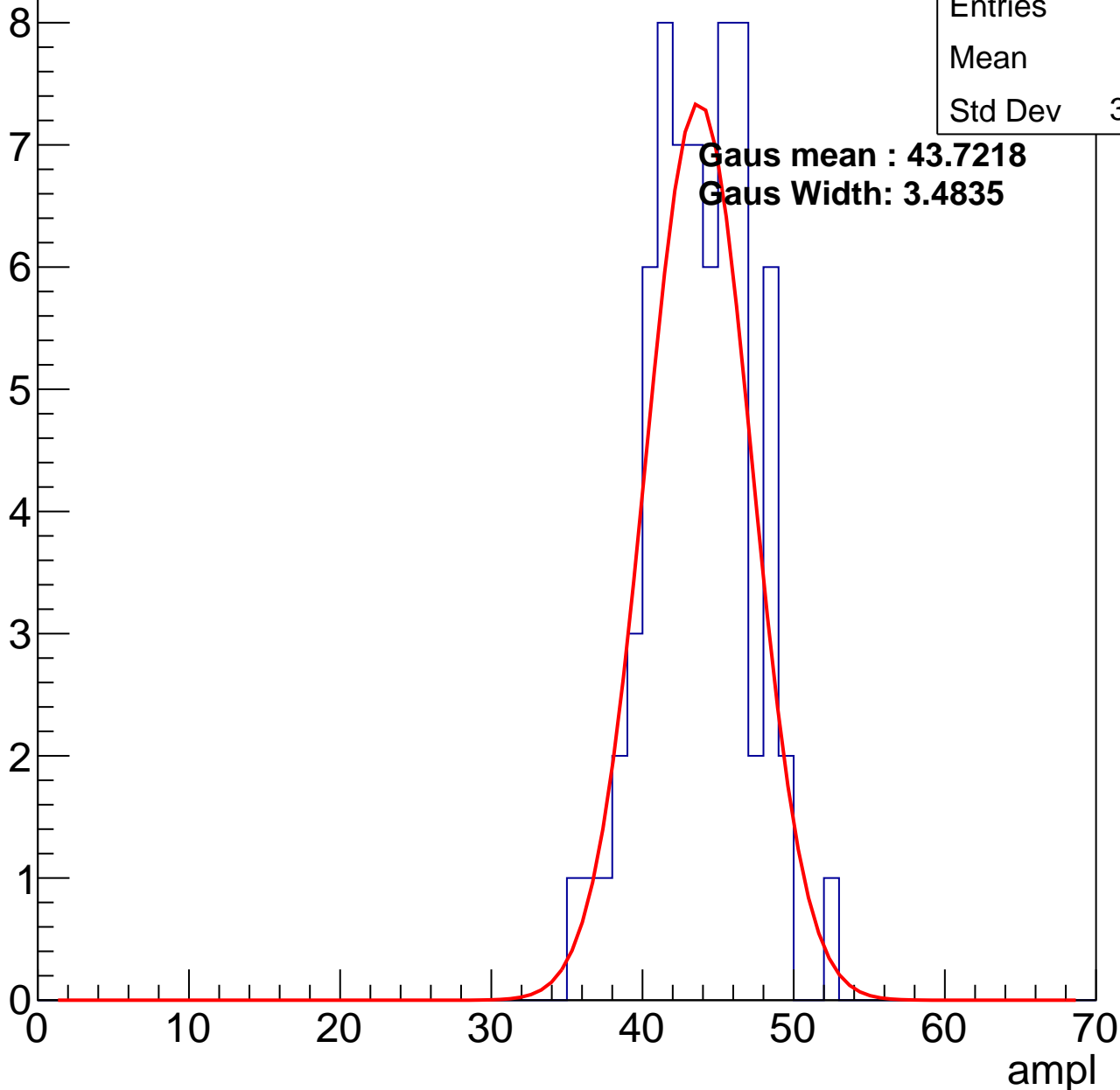
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.3
Std Dev	3.359

**Gaus mean : 43.7218**

**Gaus Width: 3.4835**

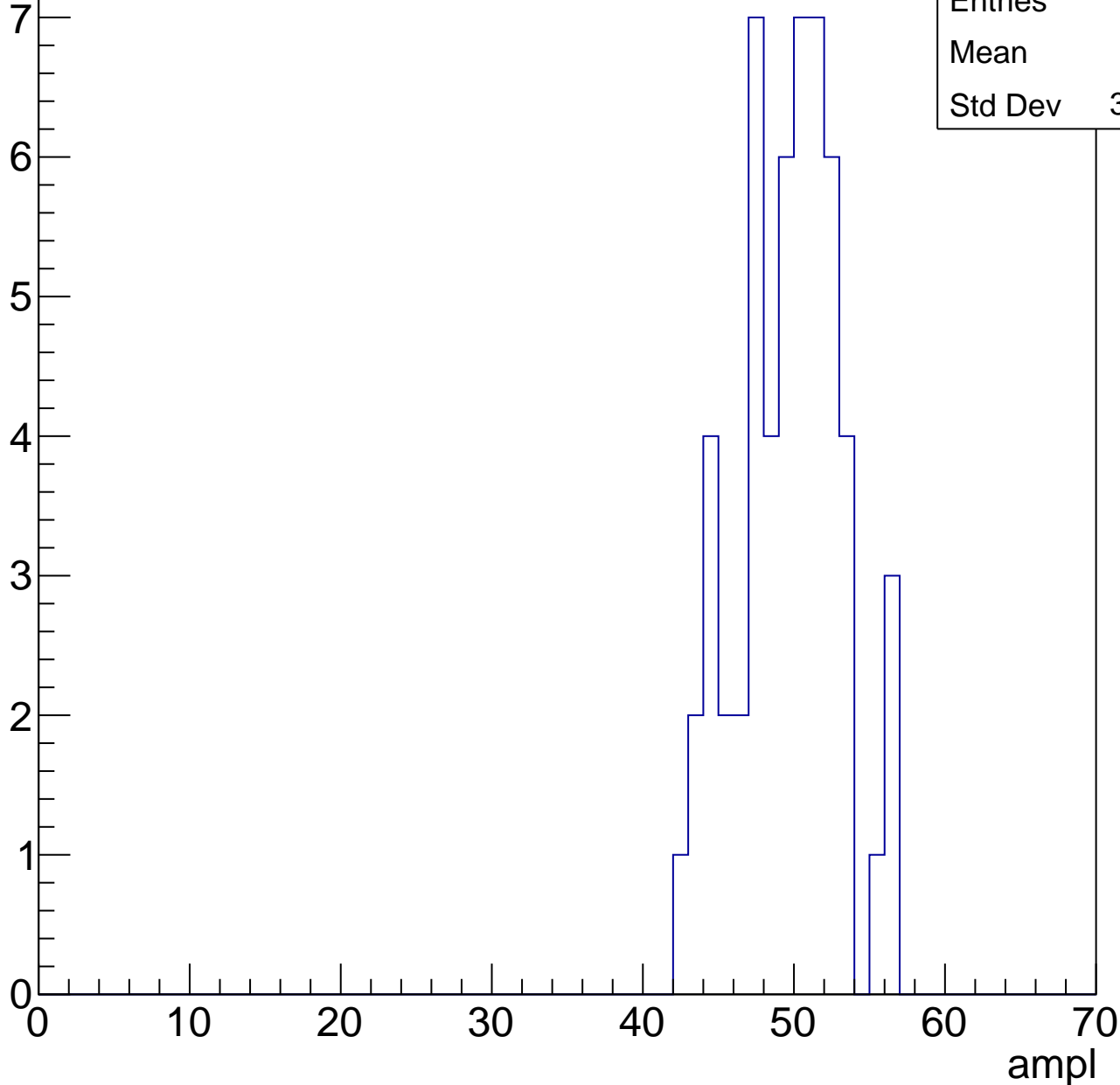


# B1L102S, U25-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

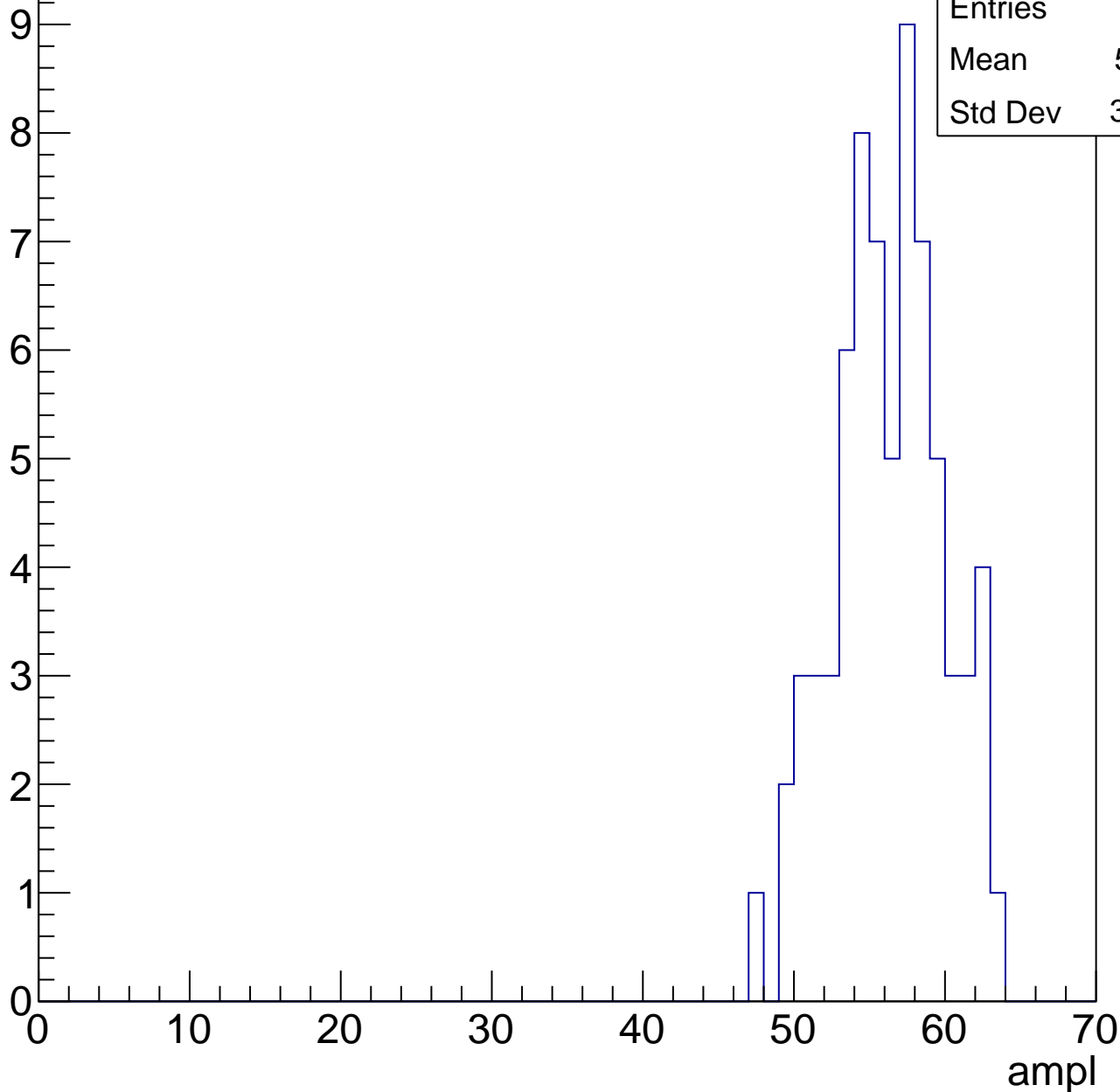
Entries	56
Mean	49.2
Std Dev	3.372



# B1L102S, U25-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



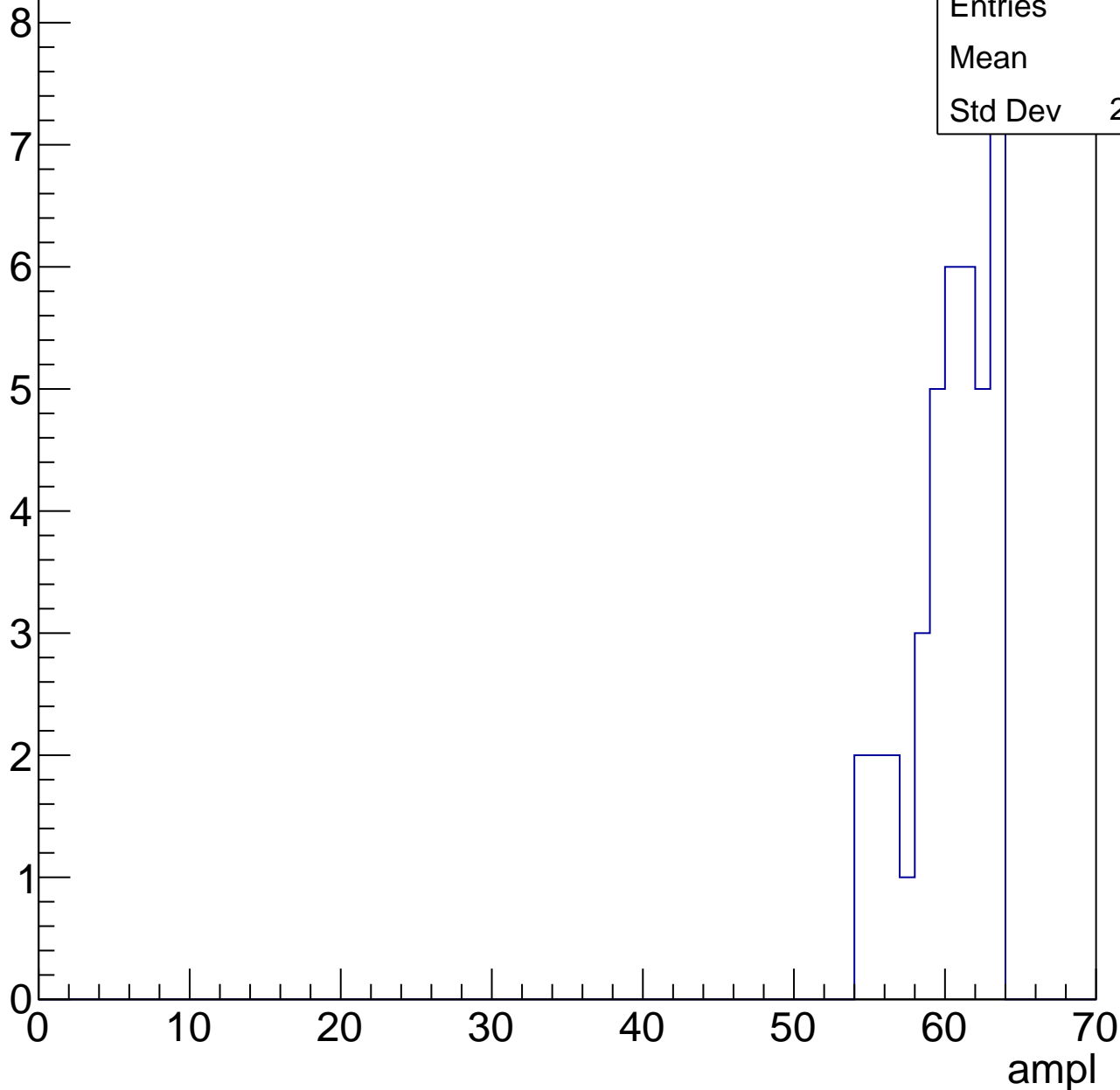
Entries	70
Mean	55.81
Std Dev	3.583

# B1L102S, U25-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

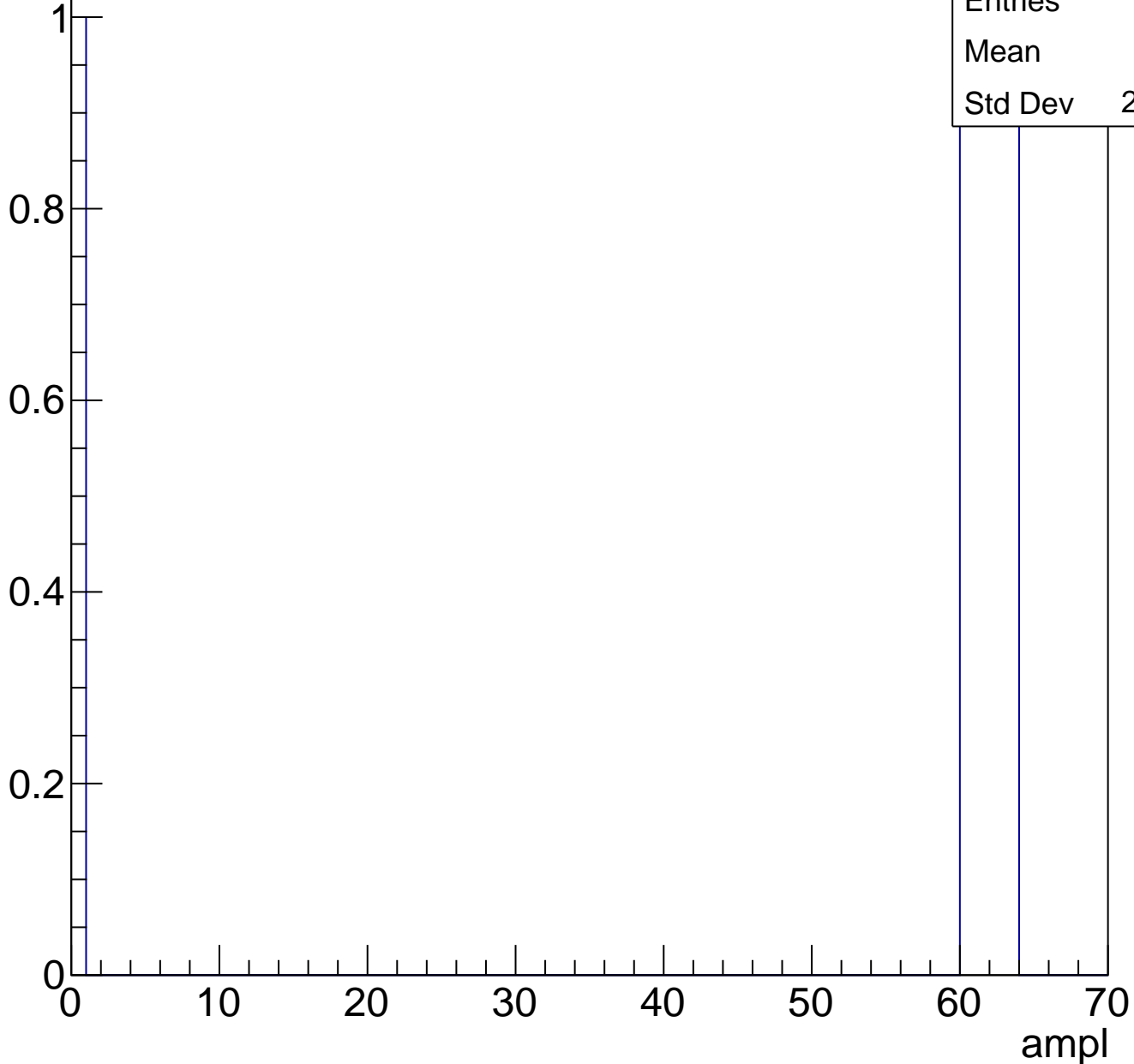
Entries	40
Mean	59.9
Std Dev	2.634



# B1L102S, U25-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch63, adc0

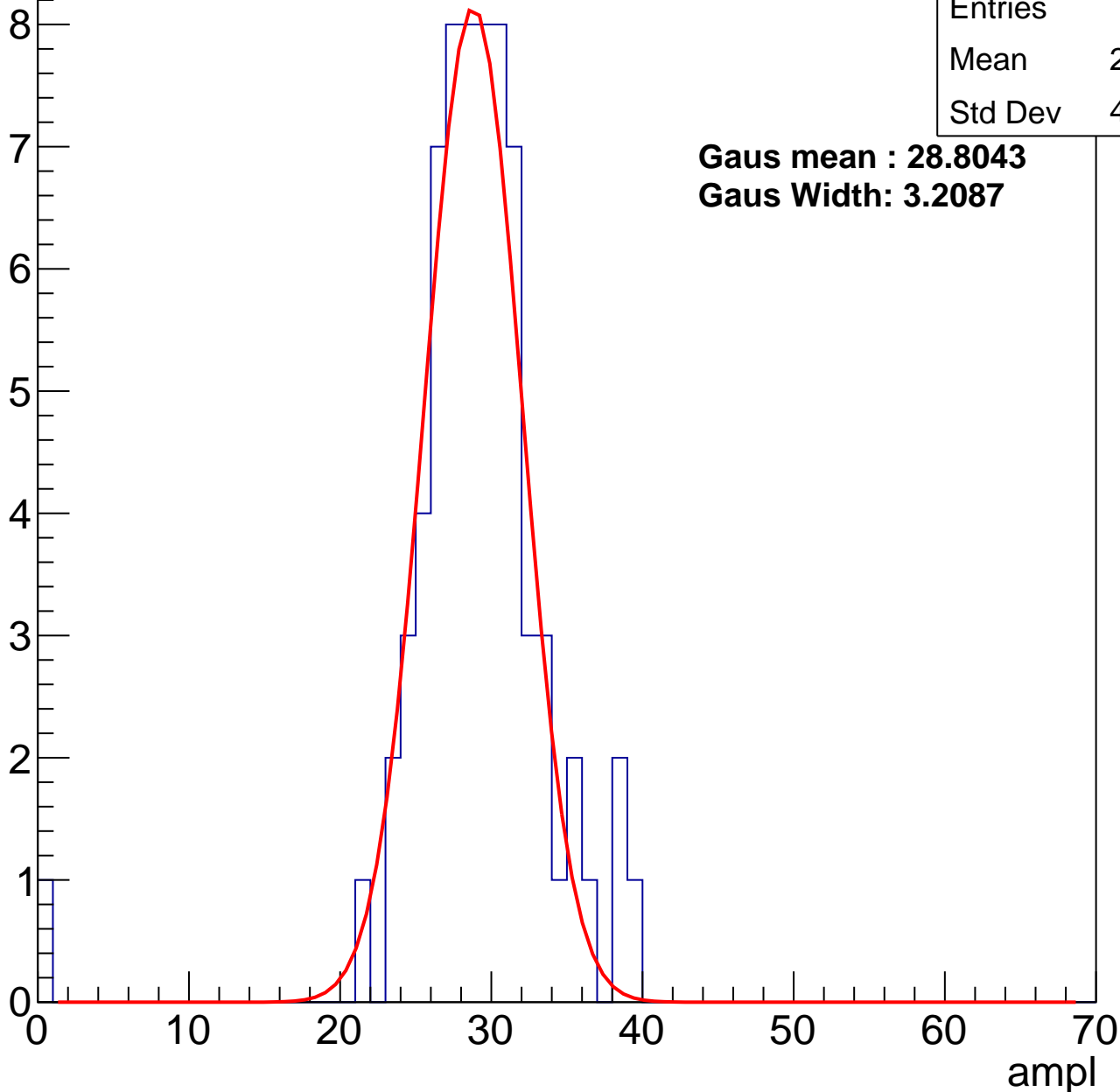
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.57
Std Dev	4.964

**Gaus mean : 28.8043**

**Gaus Width: 3.2087**



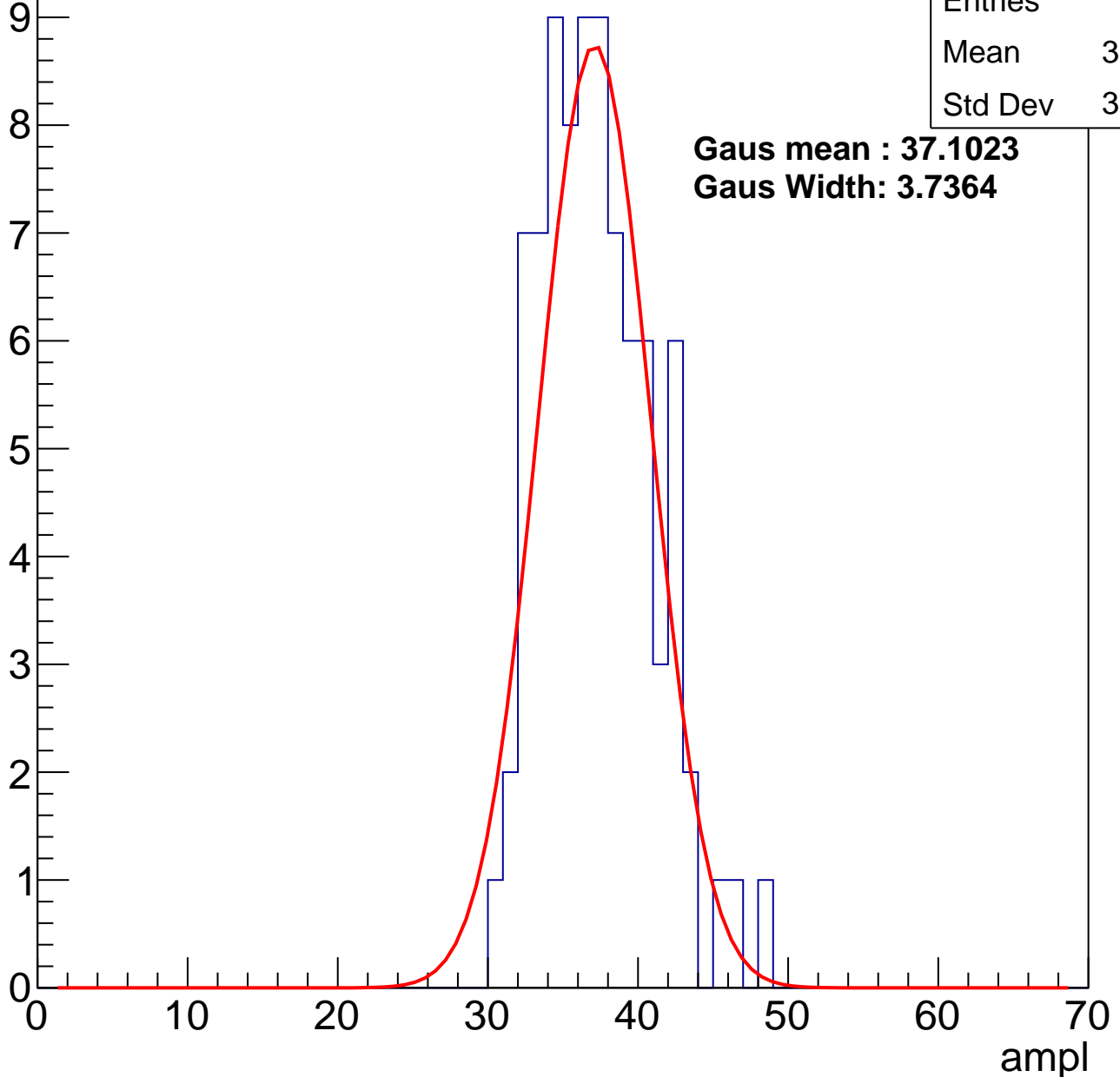
# B1L102S, U25-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	36.82
Std Dev	3.676

**Gaus mean : 37.1023**  
**Gaus Width: 3.7364**



# B1L102S, U25-ch63, adc2

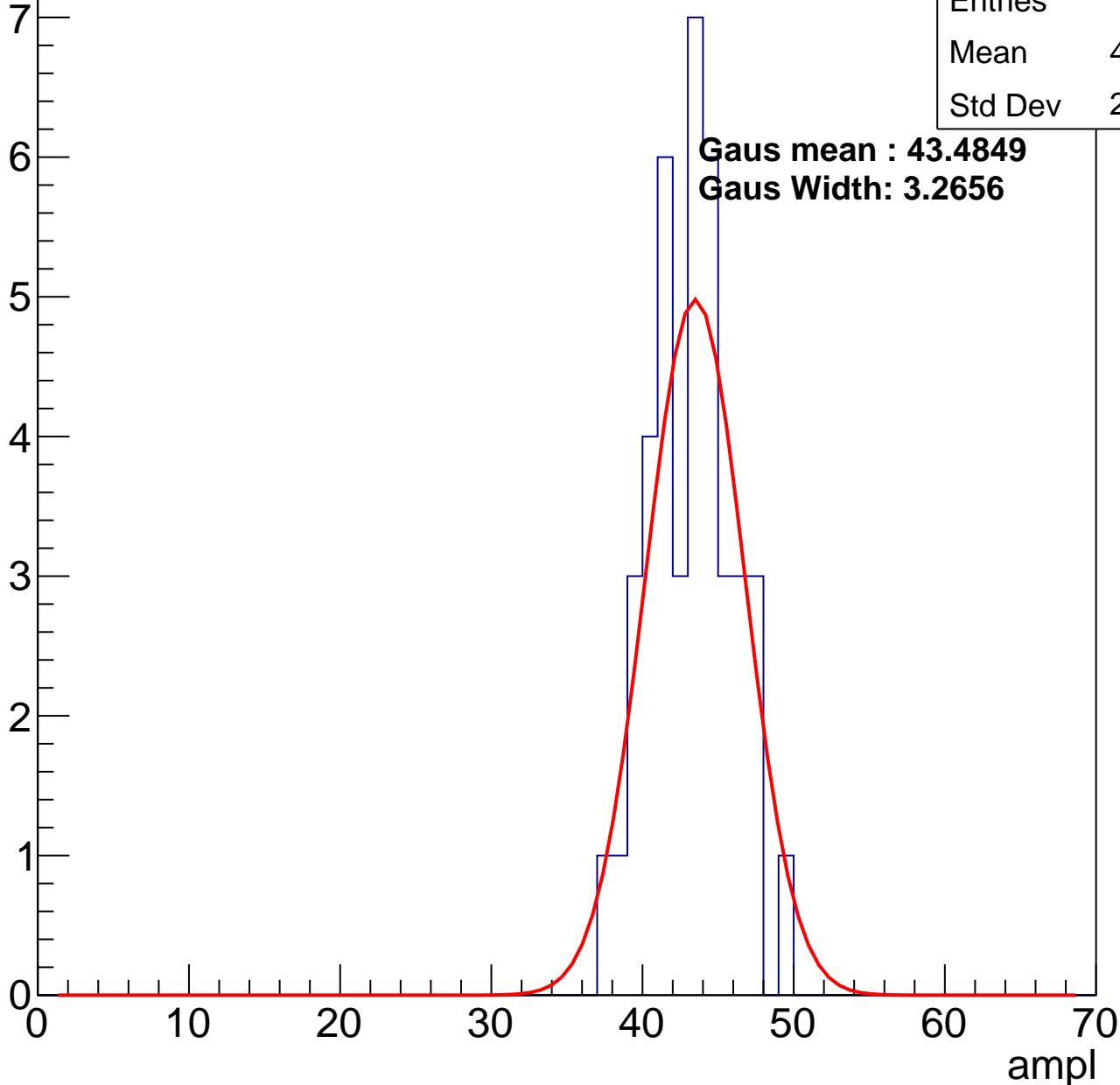
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	41
Mean	42.73
Std Dev	2.696

**Gaus mean : 43.4849**

**Gaus Width: 3.2656**



# B1L102S, U25-ch63, adc3

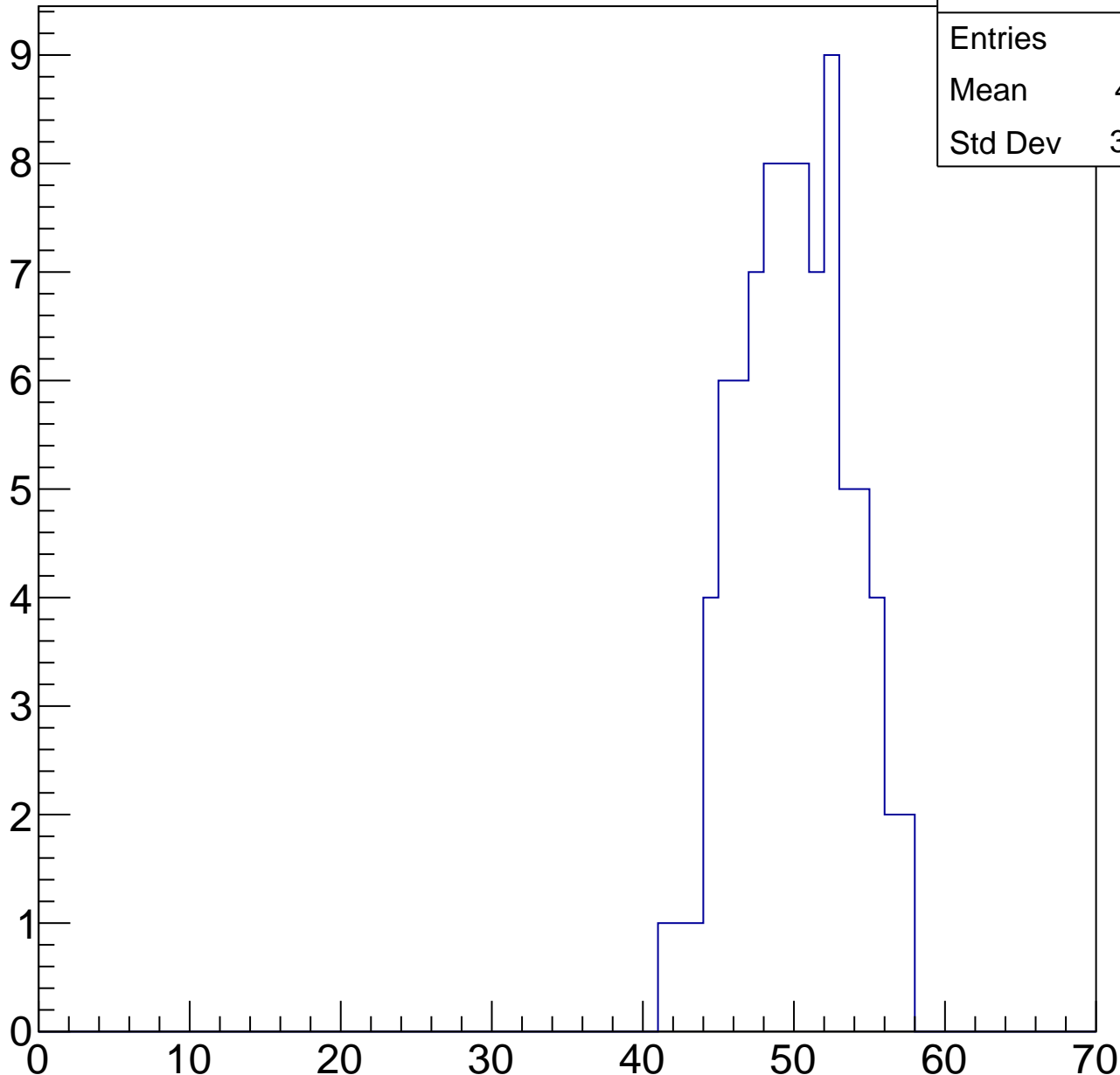
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	84
Mean	49.51
Std Dev	3.627

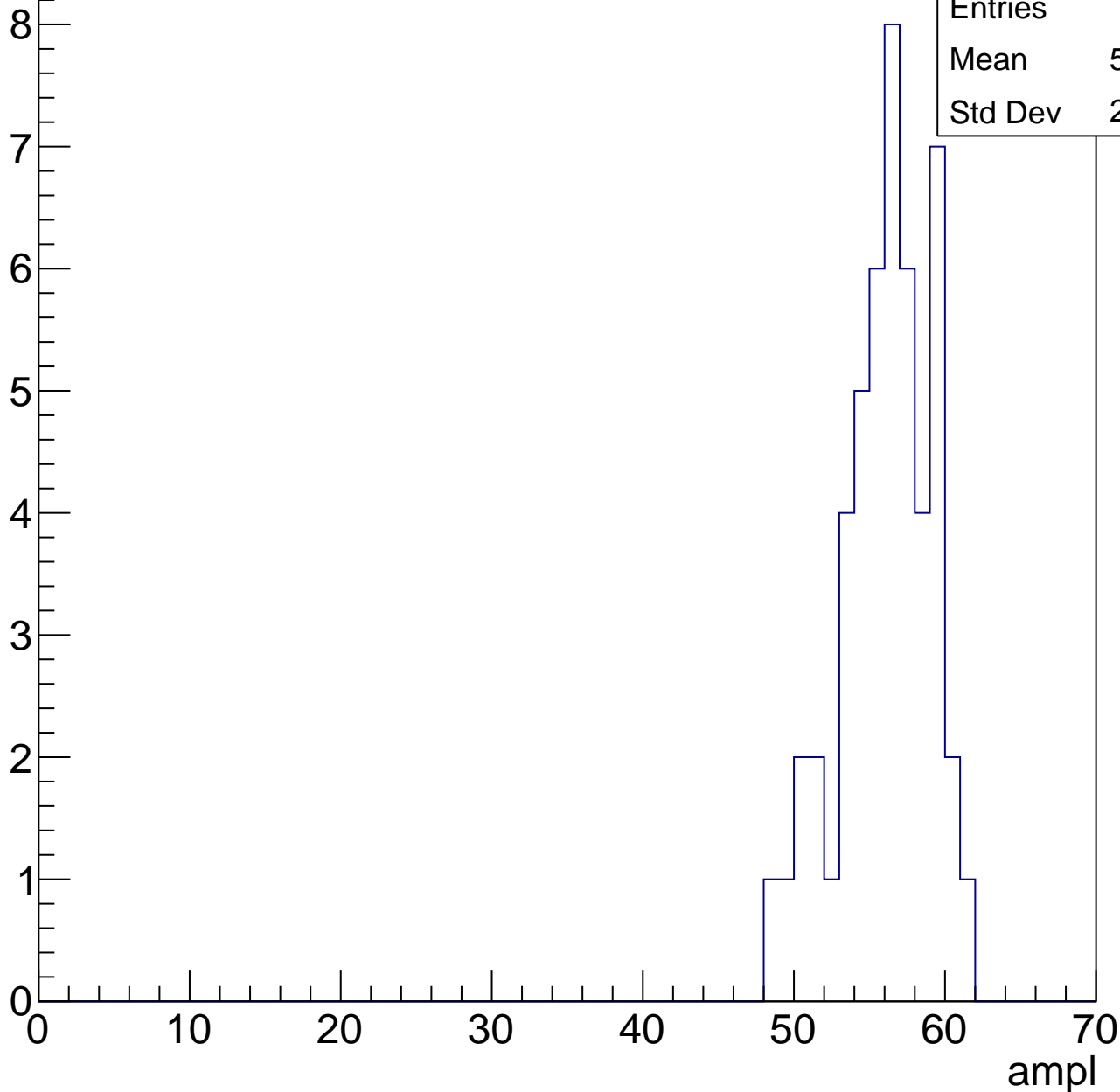
ampl



# B1L102S, U25-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

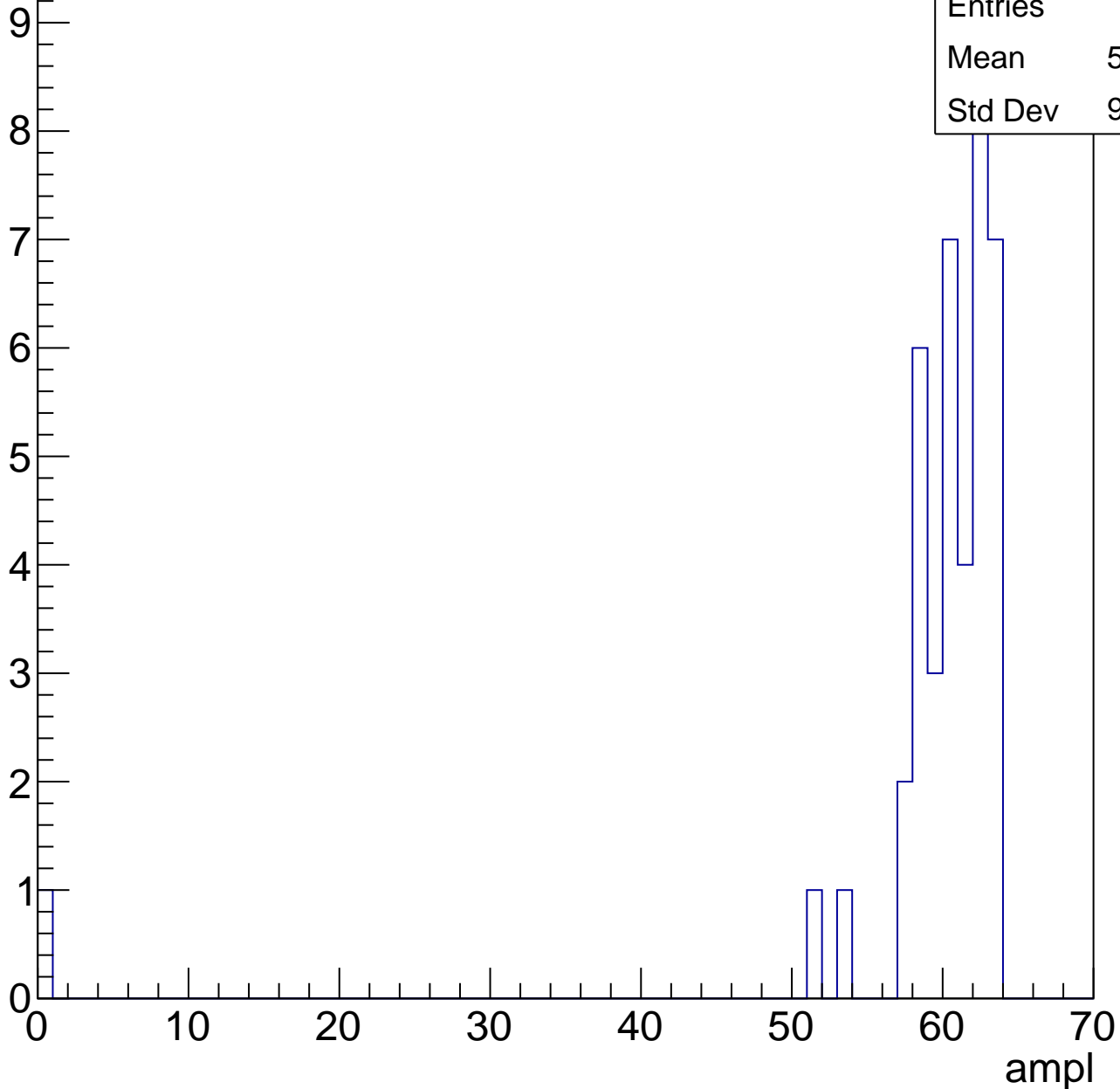


# B1L102S, U25-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.68
Std Dev	9.636

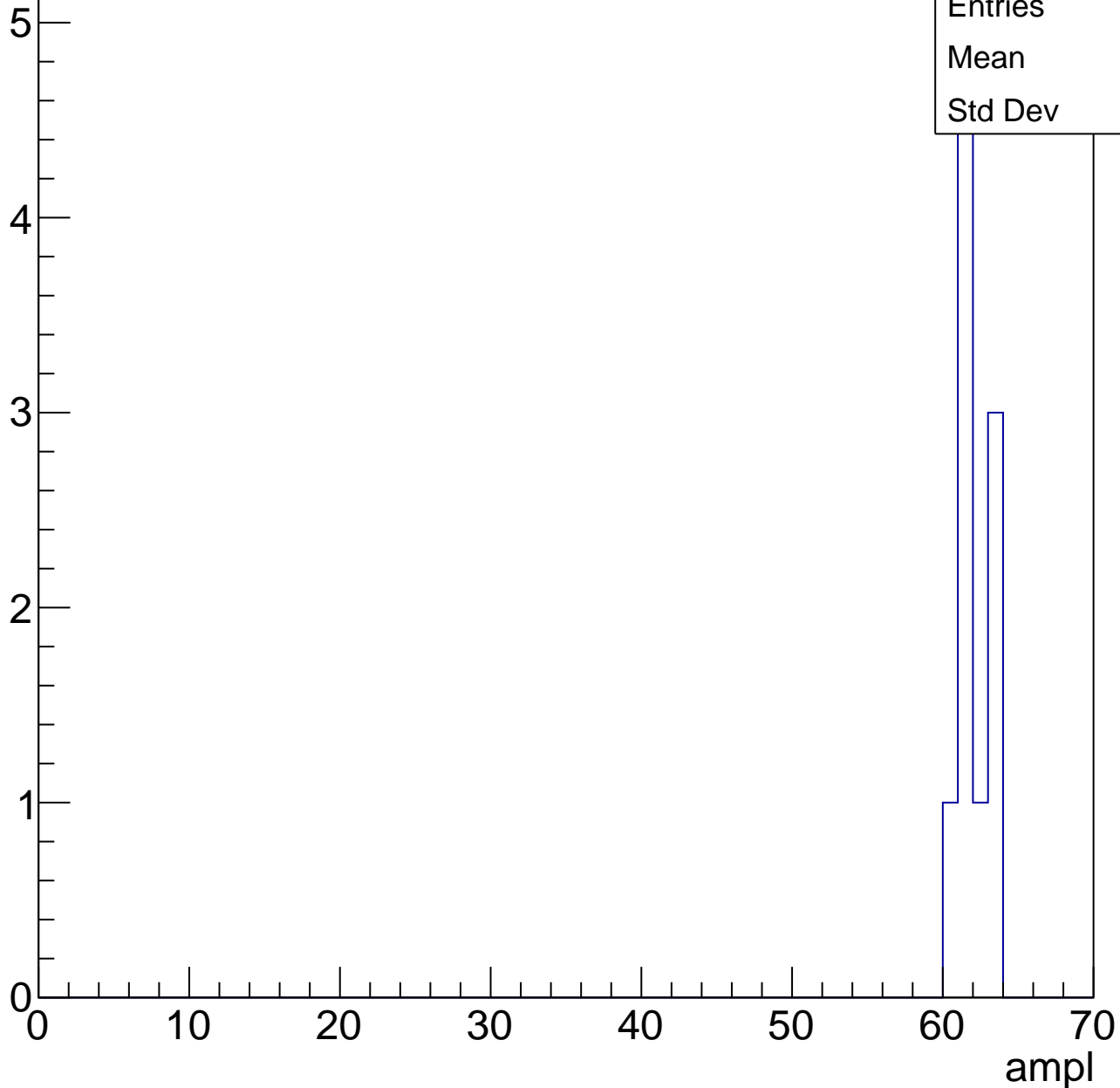


# B1L102S, U25-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.6
Std Dev	1.02





# B1L102S, U25-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch64, adc0

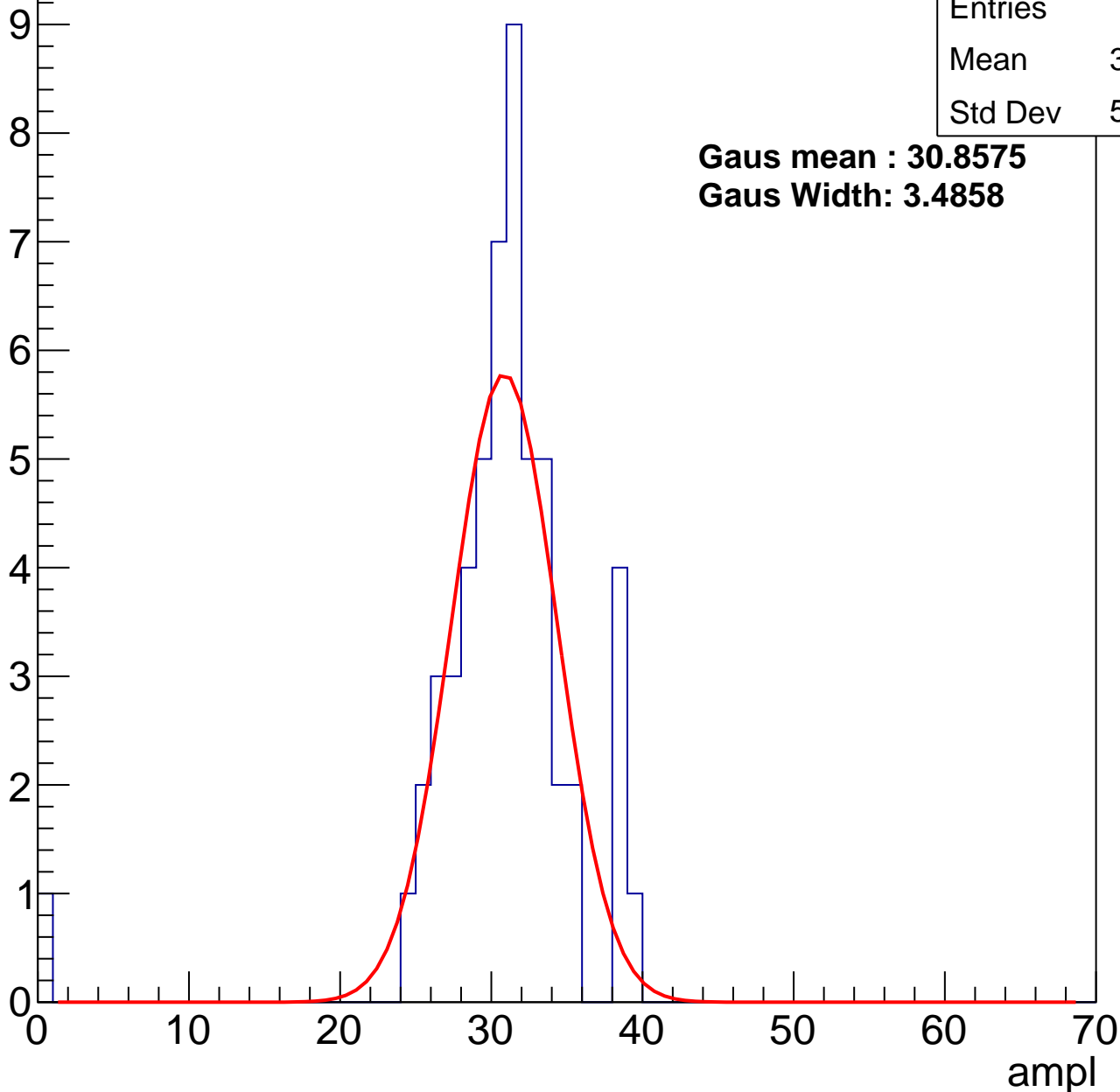
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	30.24
Std Dev	5.395

**Gaus mean : 30.8575**

**Gaus Width: 3.4858**



# B1L102S, U25-ch64, adc1

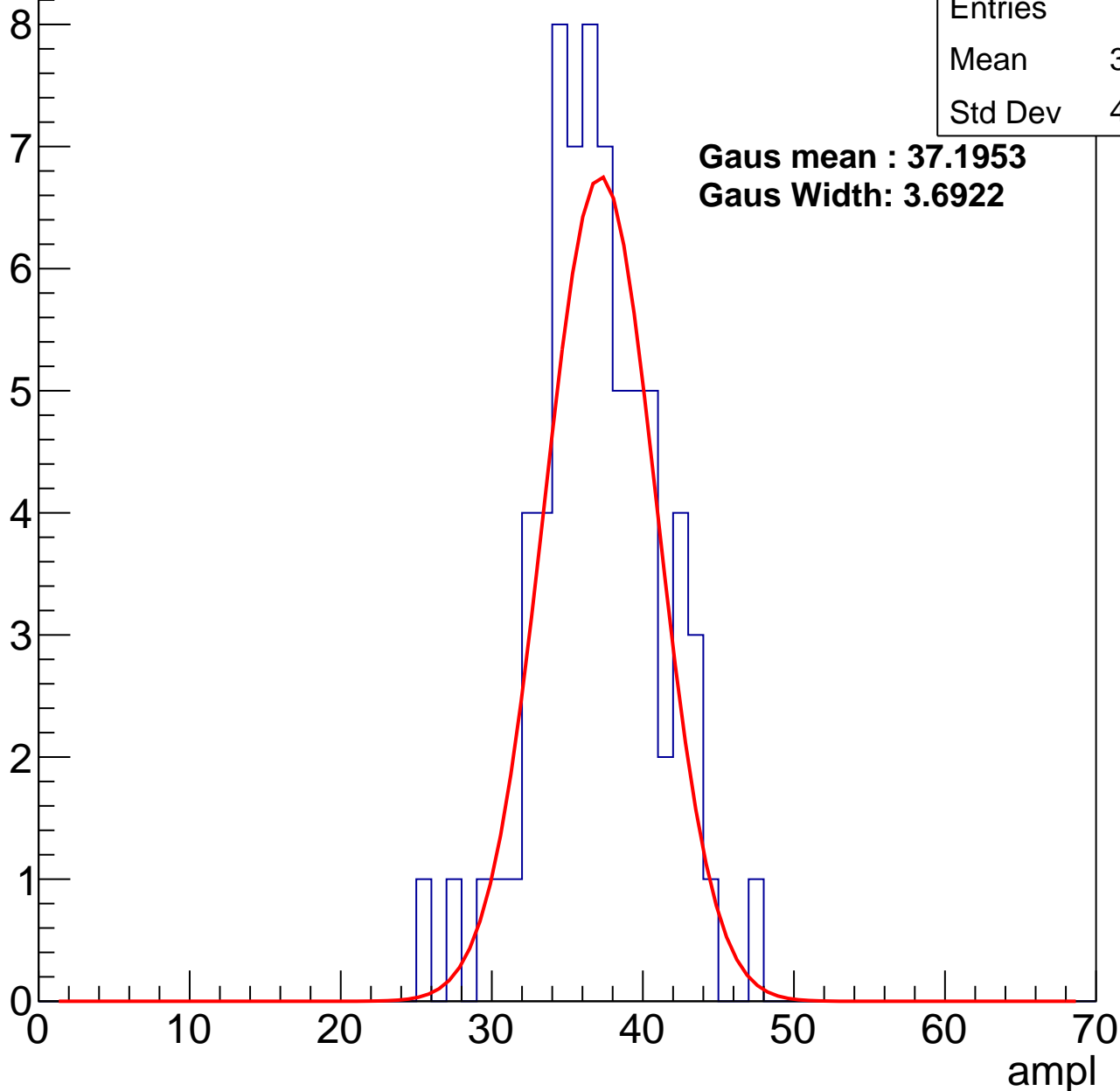
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.54
Std Dev	4.009

**Gaus mean : 37.1953**

**Gaus Width: 3.6922**



# B1L102S, U25-ch64, adc2

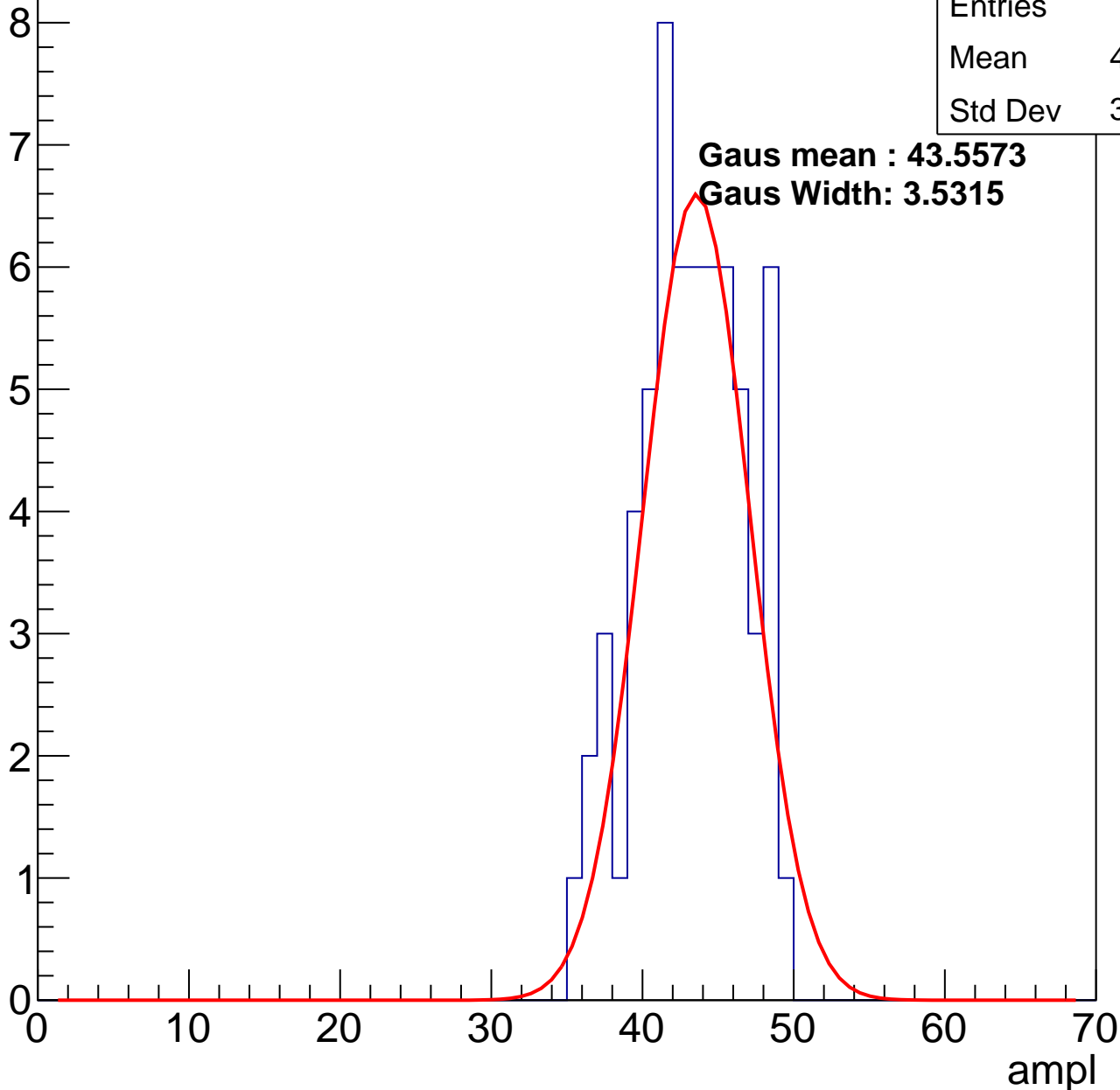
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.73
Std Dev	3.442

**Gaus mean : 43.5573**

**Gaus Width: 3.5315**



# B1L102S, U25-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

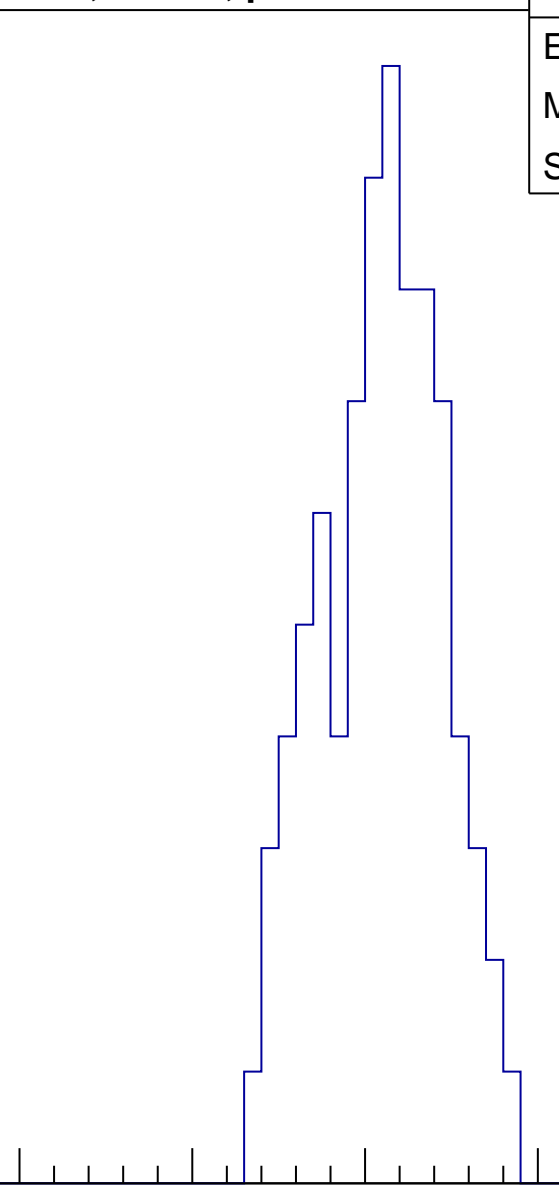
Entries	82
Mean	50.49
Std Dev	3.476

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

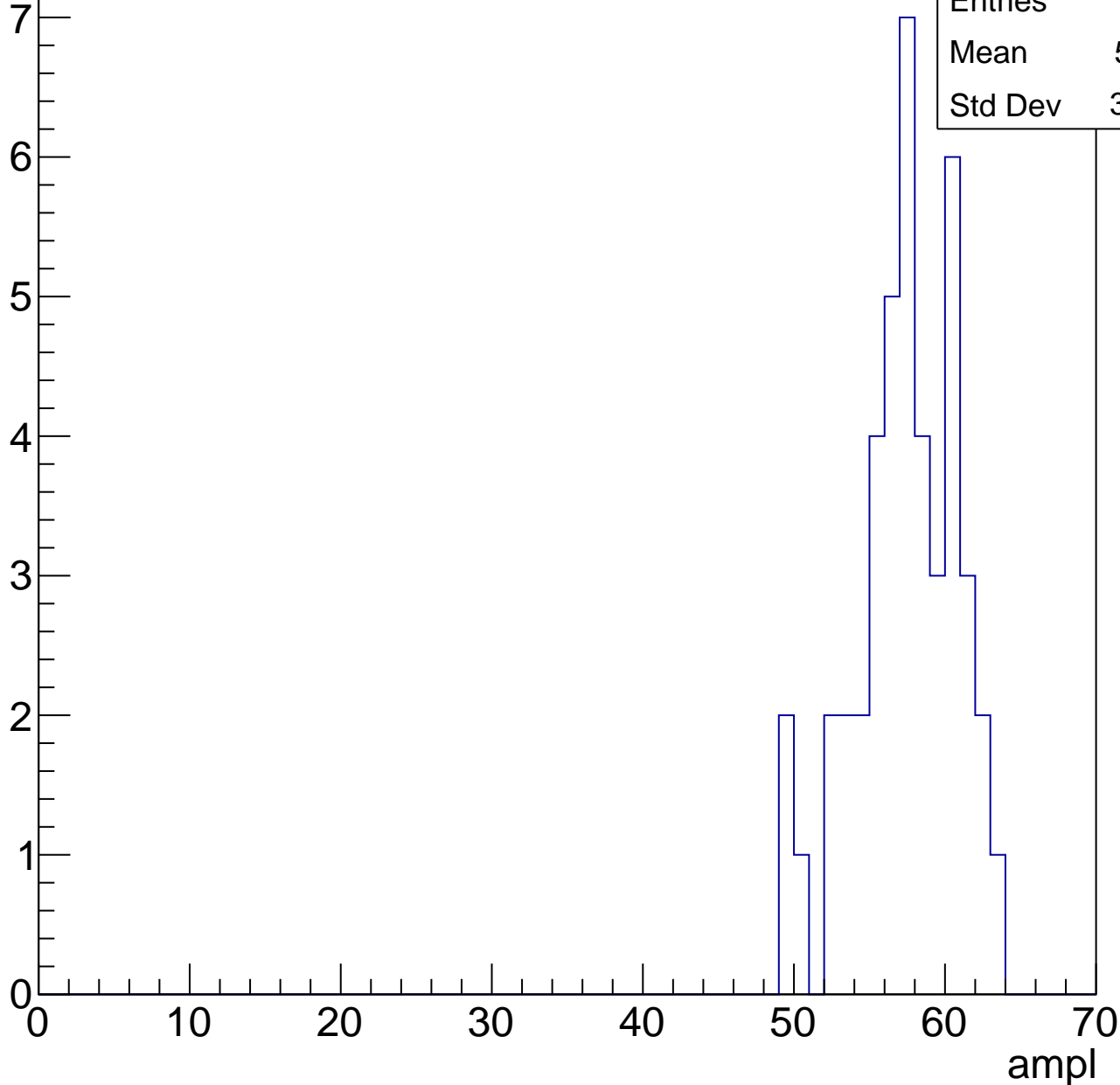
ampl



# B1L102S, U25-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

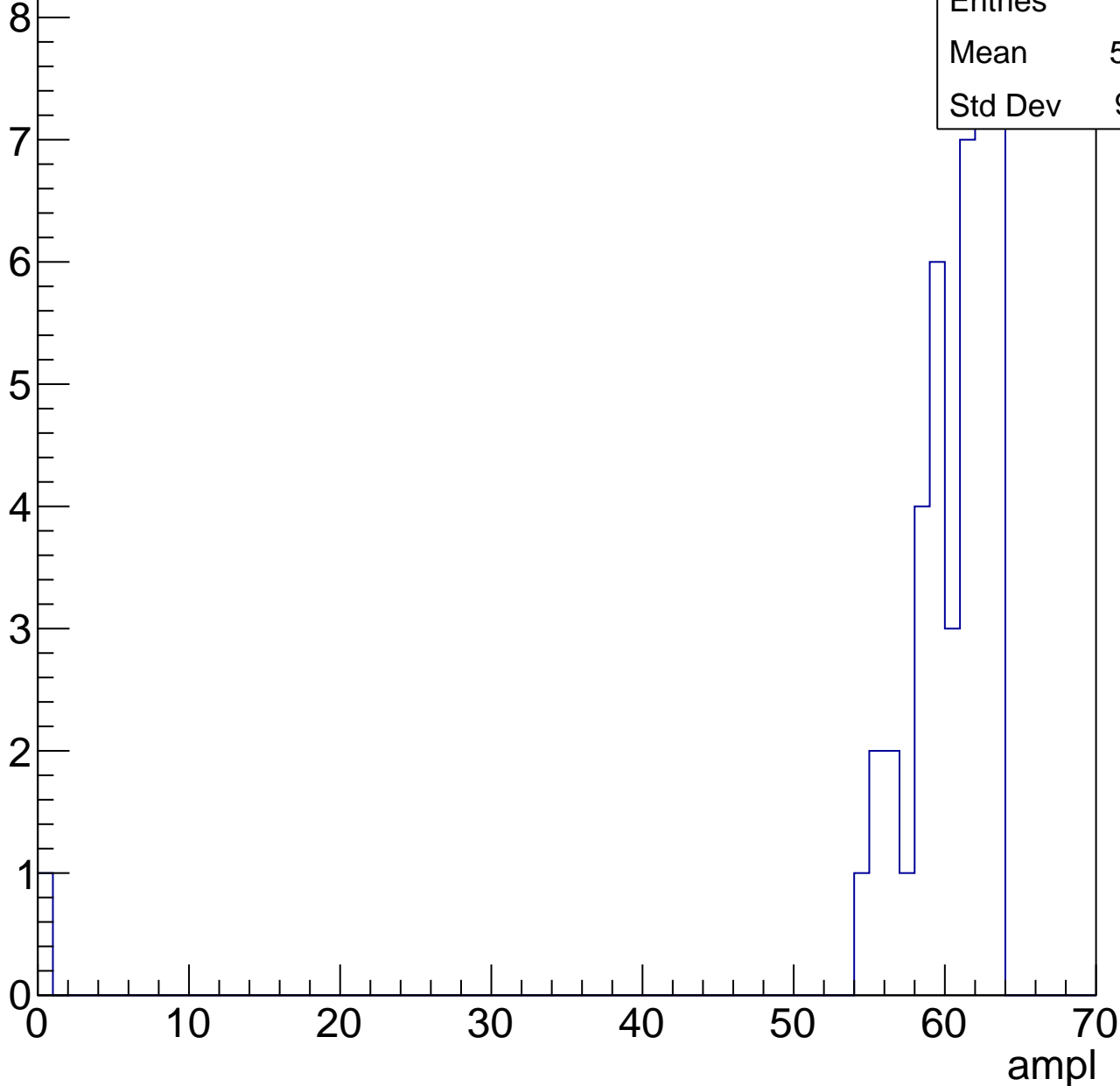


# B1L102S, U25-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.74
Std Dev	9.391



# B1L102S, U25-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	4
Mean	60
Std Dev	1.871



# B1L102S, U25-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch65, adc0

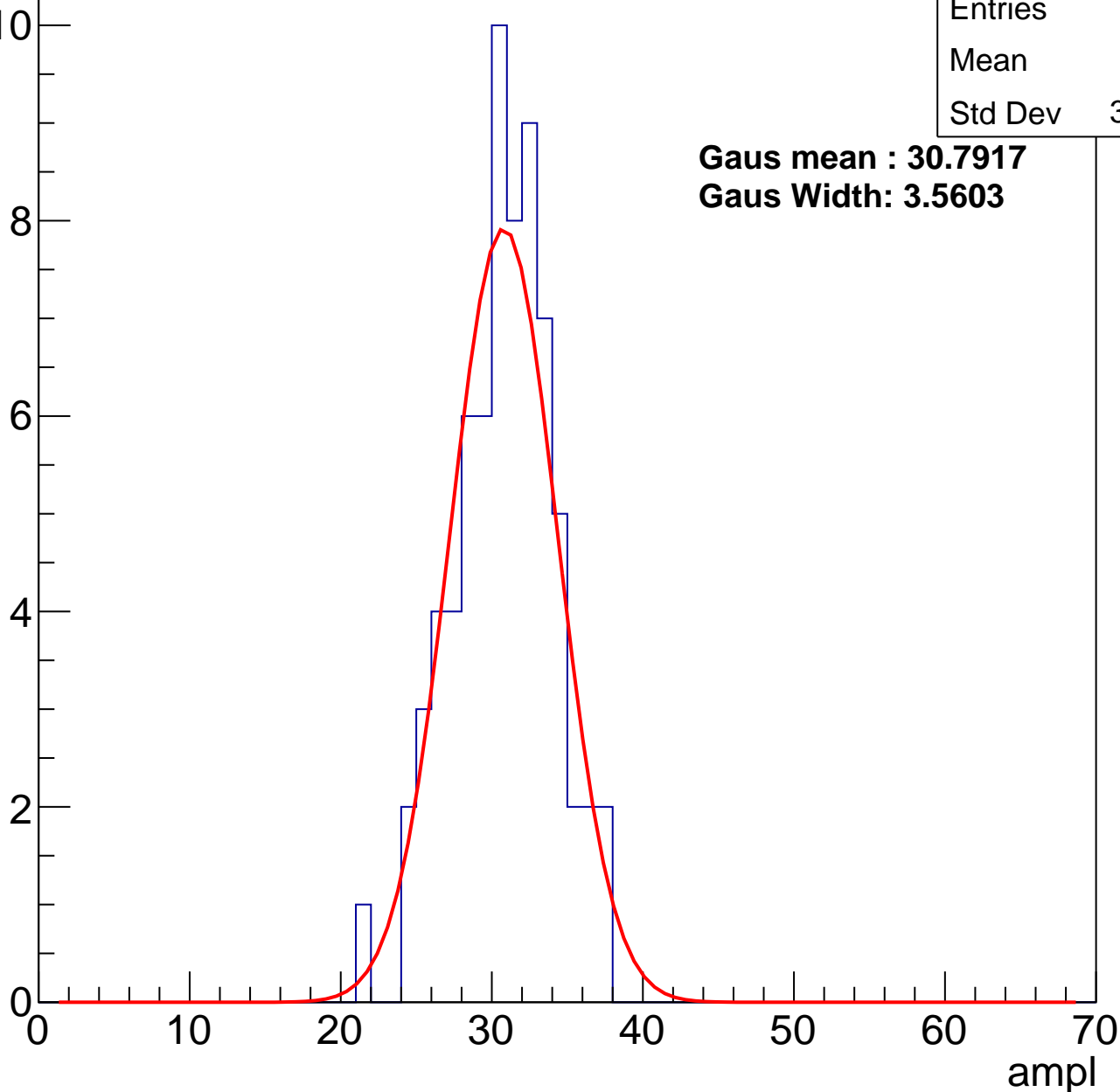
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	30.3
Std Dev	3.269

**Gaus mean : 30.7917**

**Gaus Width: 3.5603**



# B1L102S, U25-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.74
Std Dev	3.35

**Gaus mean : 38.3491**

**Gaus Width: 3.5954**

10

8

6

4

2

0

0

2

4

6

8

ampl

0

10

20

30

40

50

60

70

# B1L102S, U25-ch65, adc2

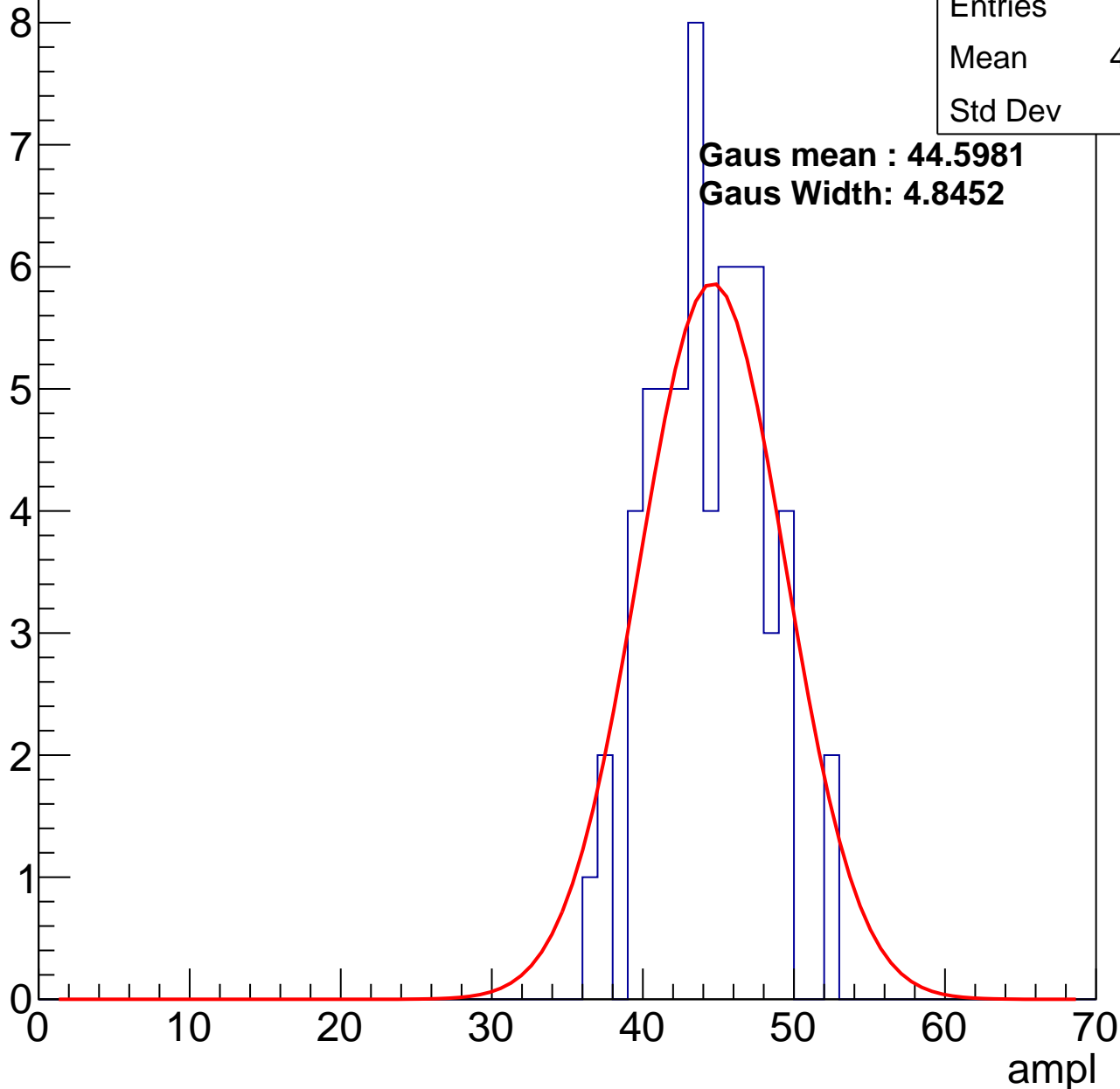
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.82
Std Dev	3.56

**Gaus mean : 44.5981**

**Gaus Width: 4.8452**

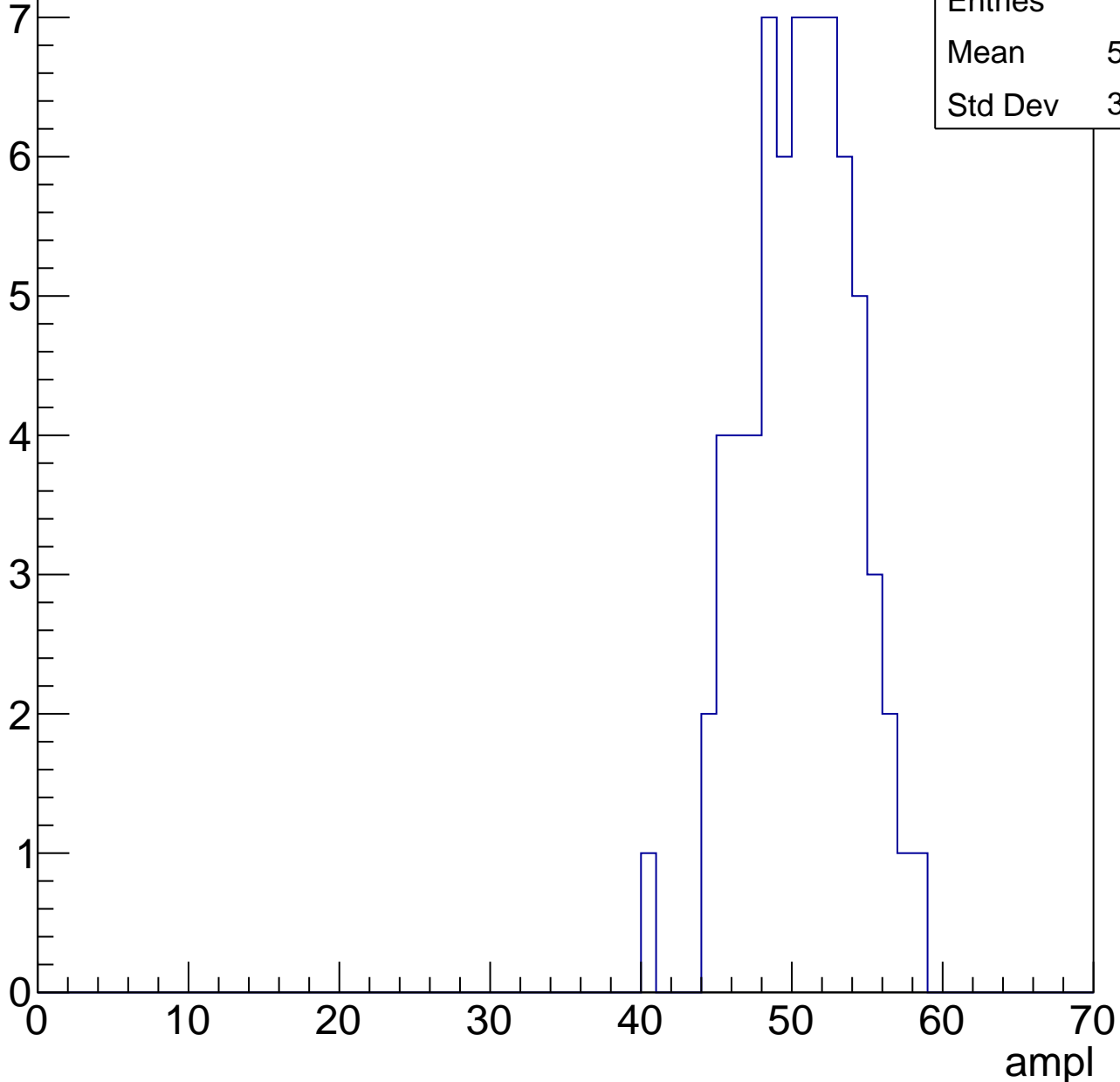


# B1L102S, U25-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50.16
Std Dev	3.526



# B1L102S, U25-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

52

Mean

56.4

Std Dev

3.17

ampl

0

10

20

30

40

50

60

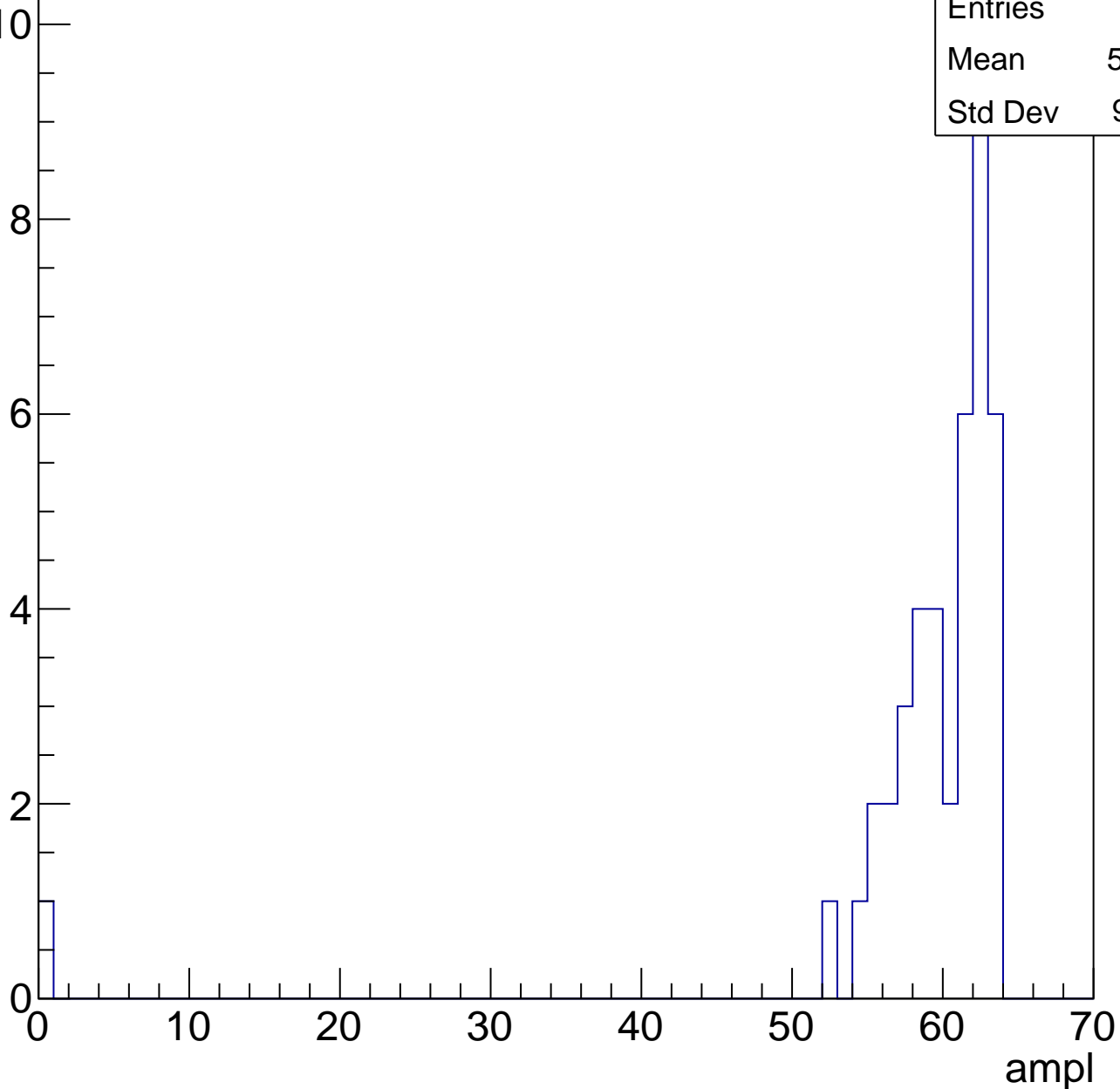
70

# B1L102S, U25-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

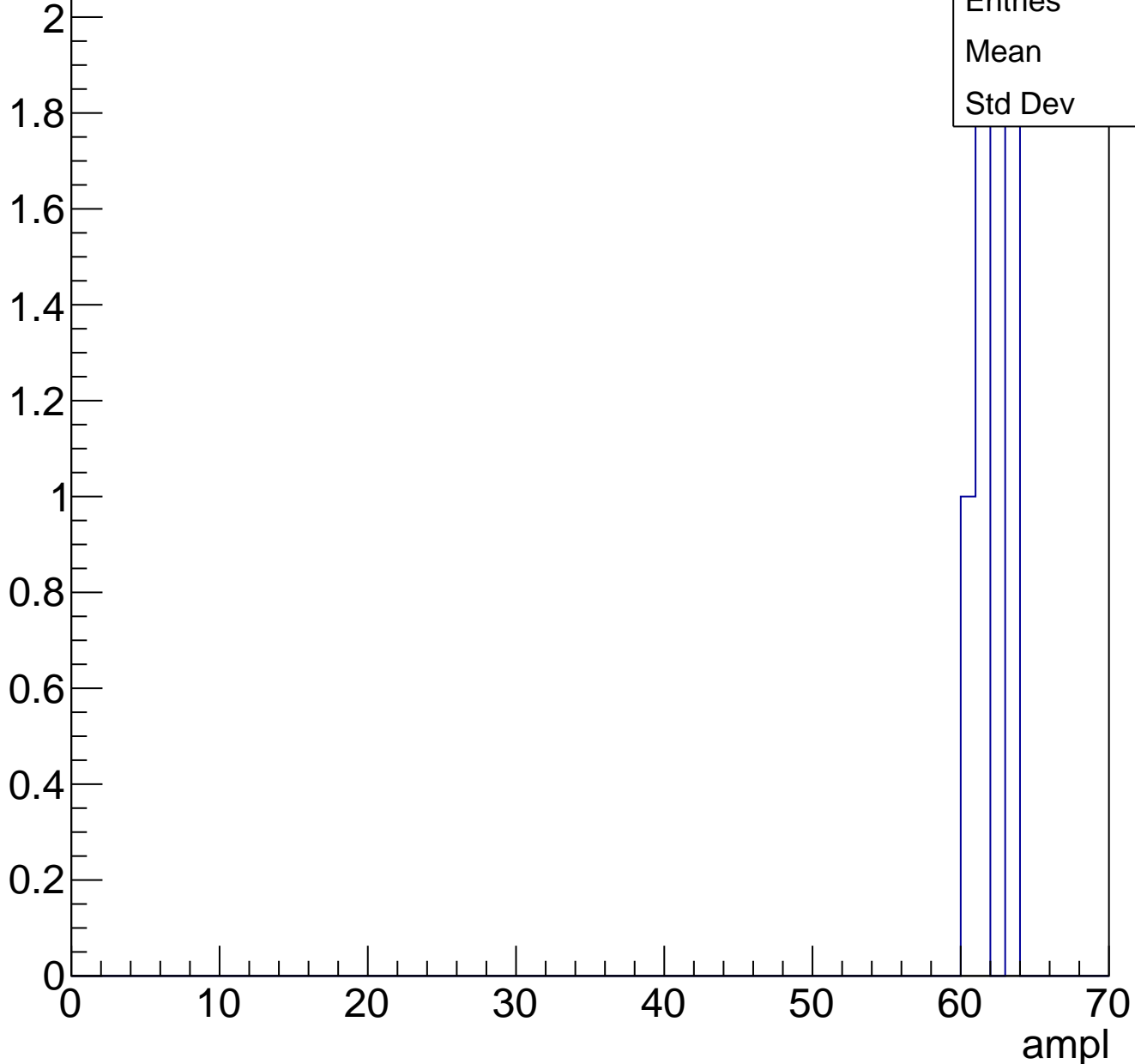
Entries	42
Mean	58.36
Std Dev	9.531



# B1L102S, U25-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch66, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	64
Mean	27.2
Std Dev	4.724

**Gaus mean : 28.2854**

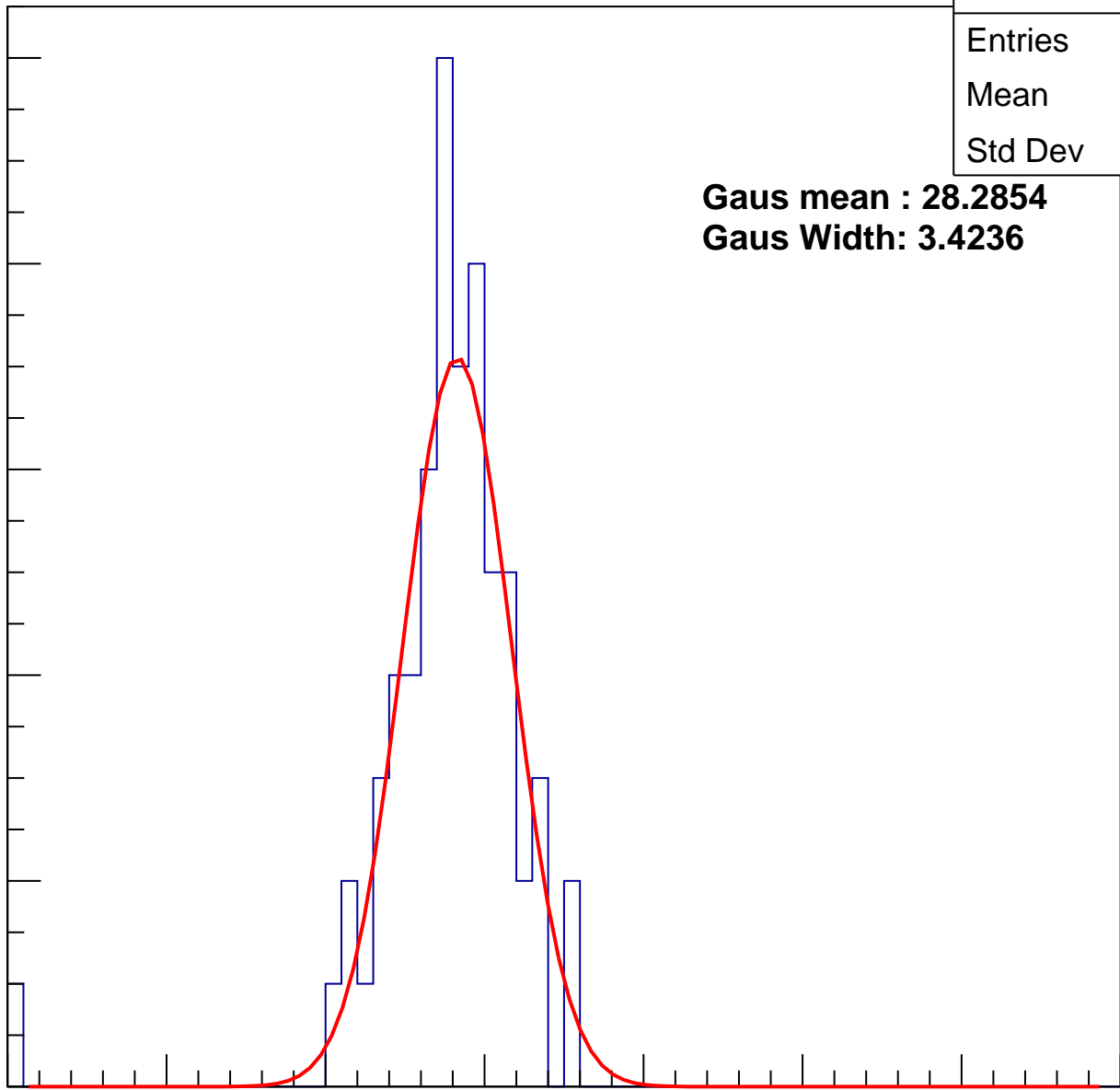
**Gaus Width: 3.4236**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch66, adc1

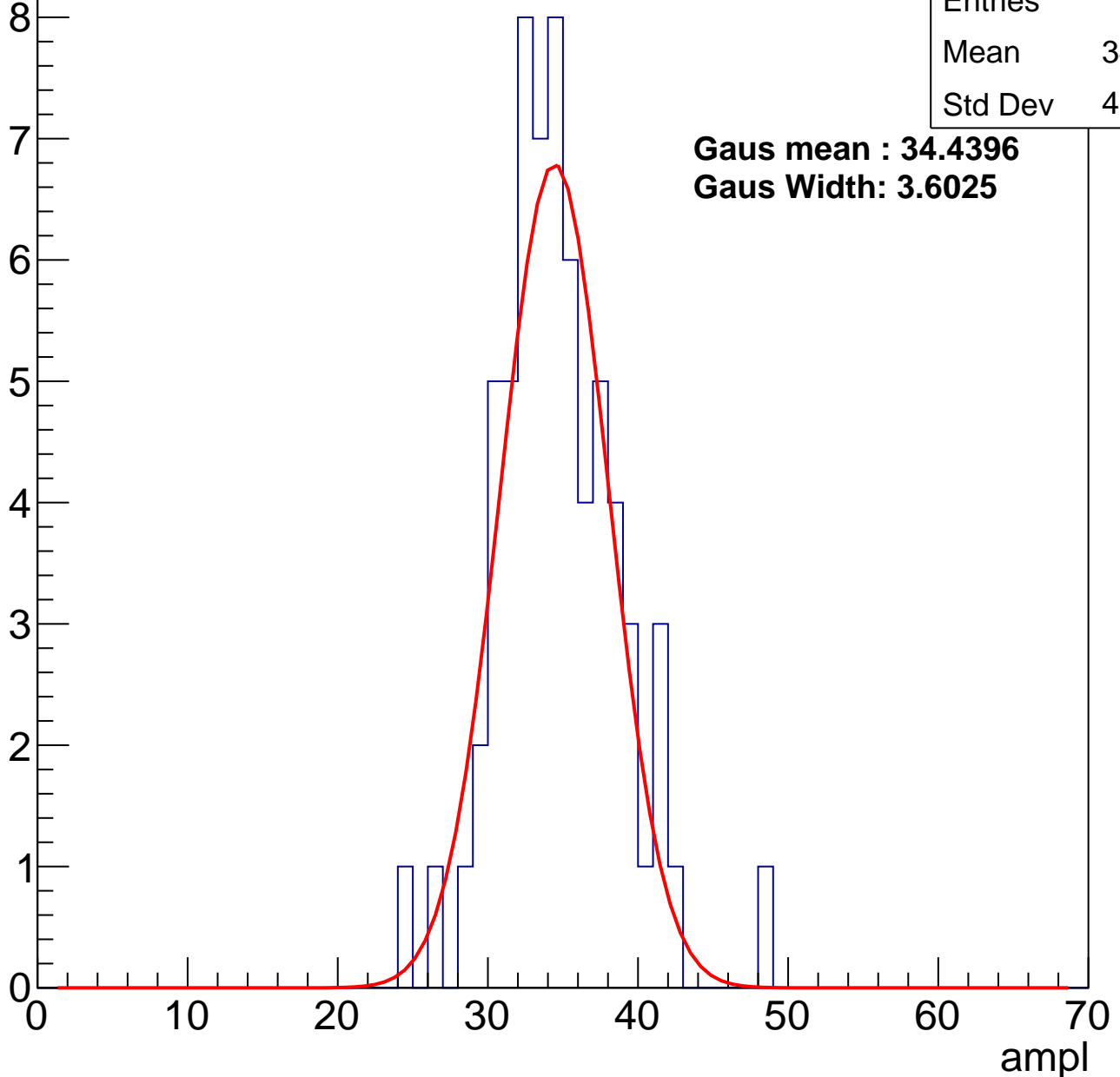
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.26
Std Dev	4.005

**Gaus mean : 34.4396**

**Gaus Width: 3.6025**



# B1L102S, U25-ch66, adc2

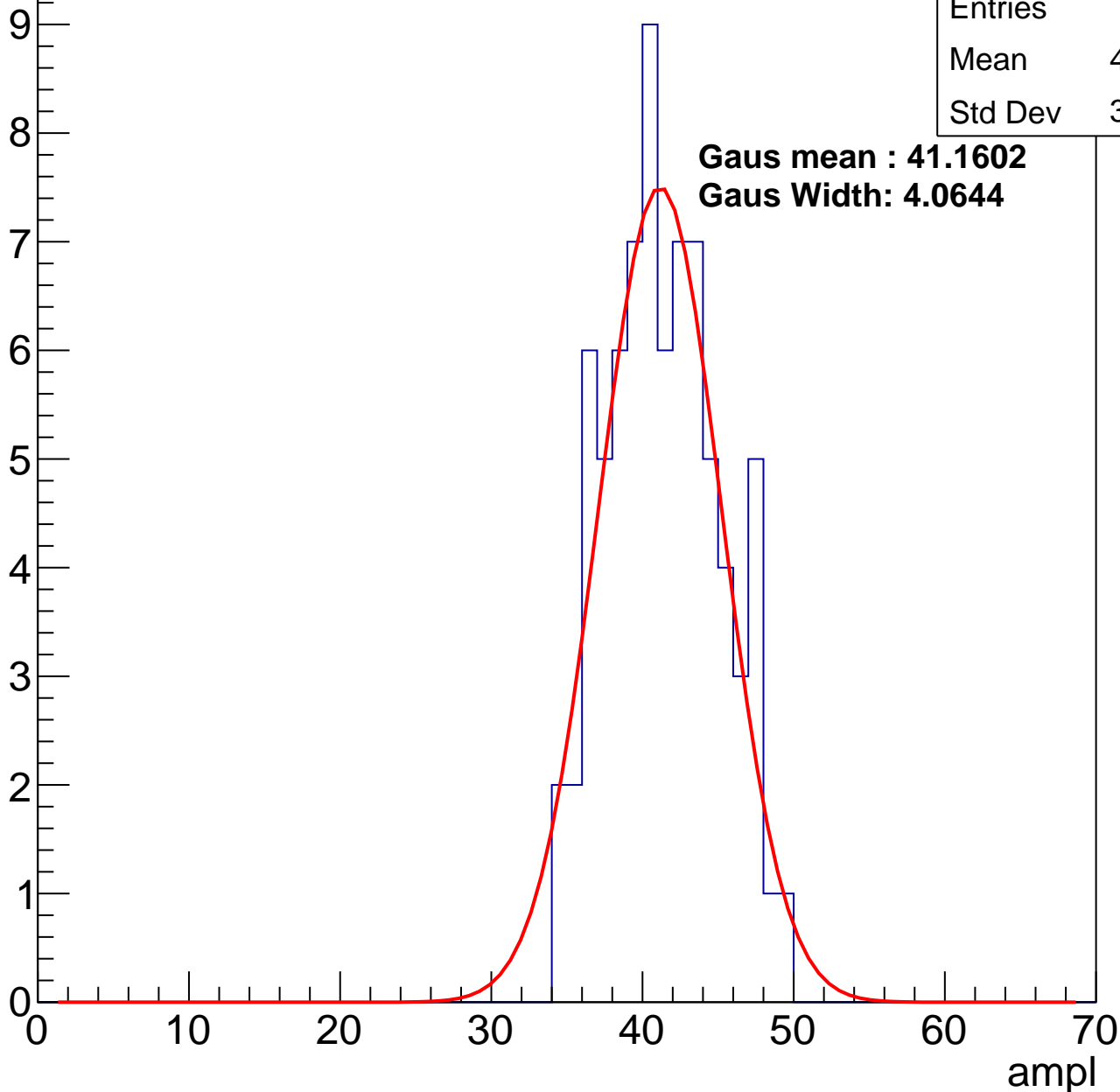
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	40.93
Std Dev	3.632

**Gaus mean : 41.1602**

**Gaus Width: 4.0644**

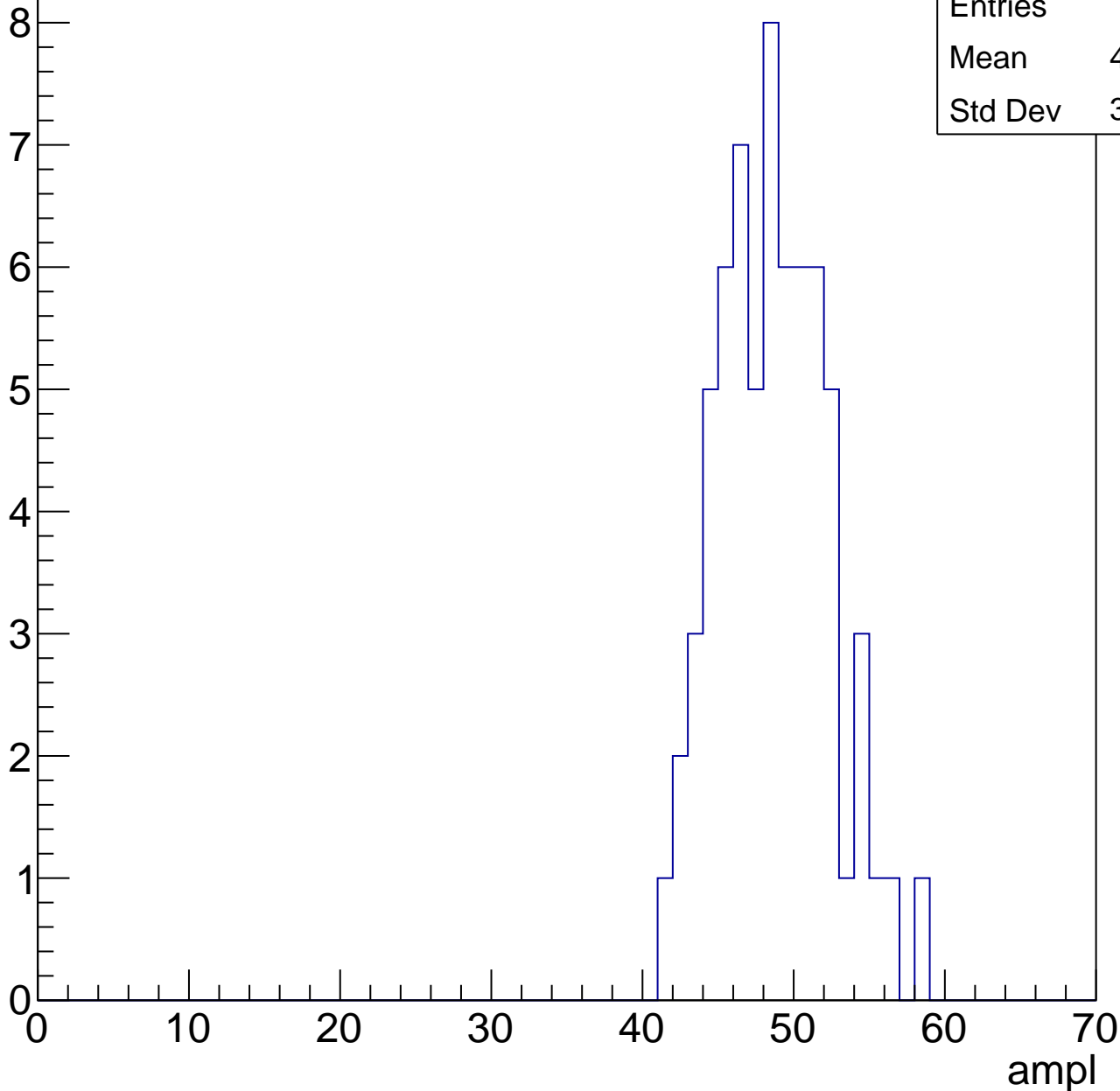


# B1L102S, U25-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.19
Std Dev	3.604

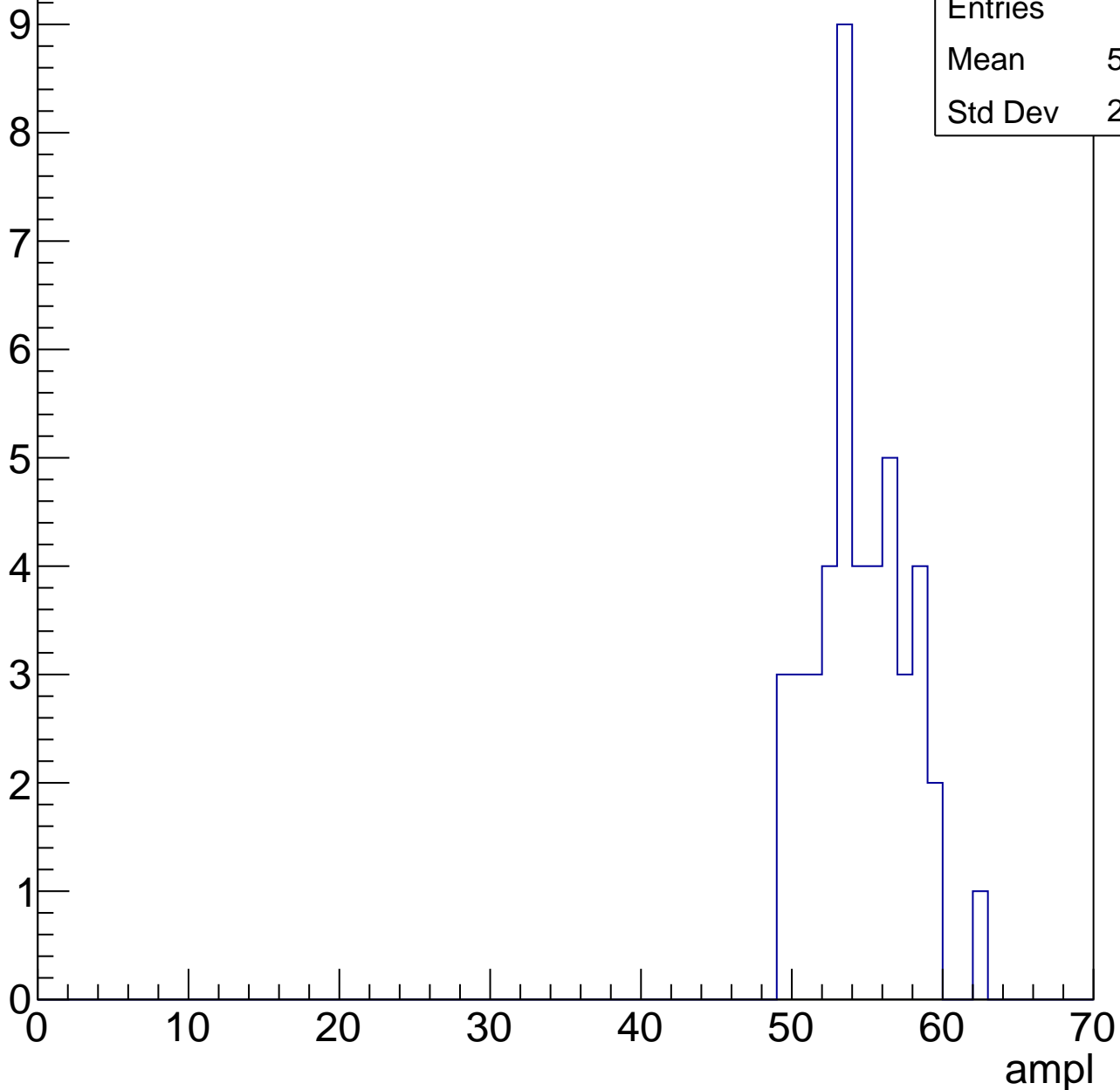


# B1L102S, U25-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	54.09
Std Dev	2.995

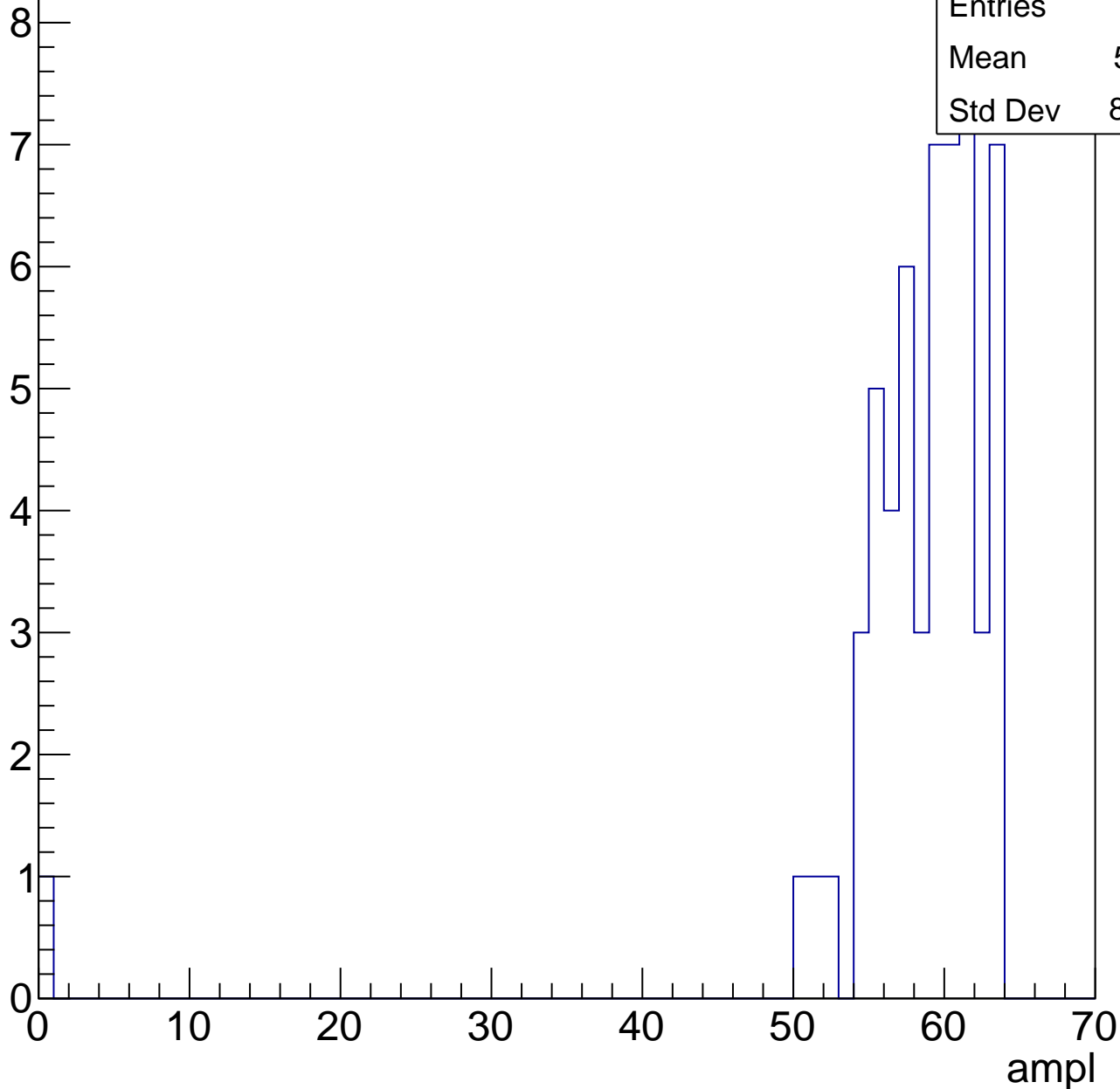


# B1L102S, U25-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.51
Std Dev	8.318

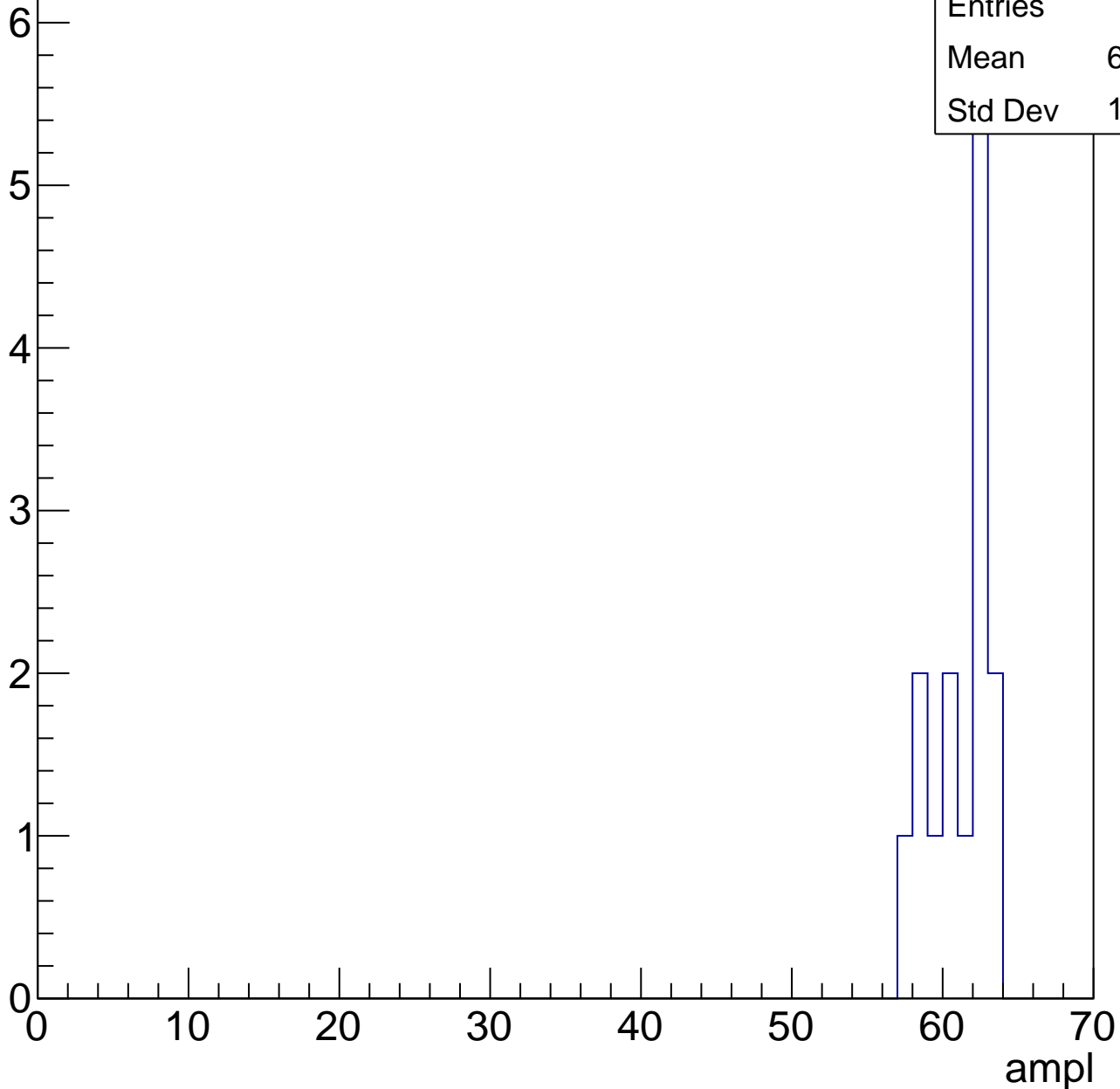


# B1L102S, U25-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	15
Mean	60.73
Std Dev	1.879

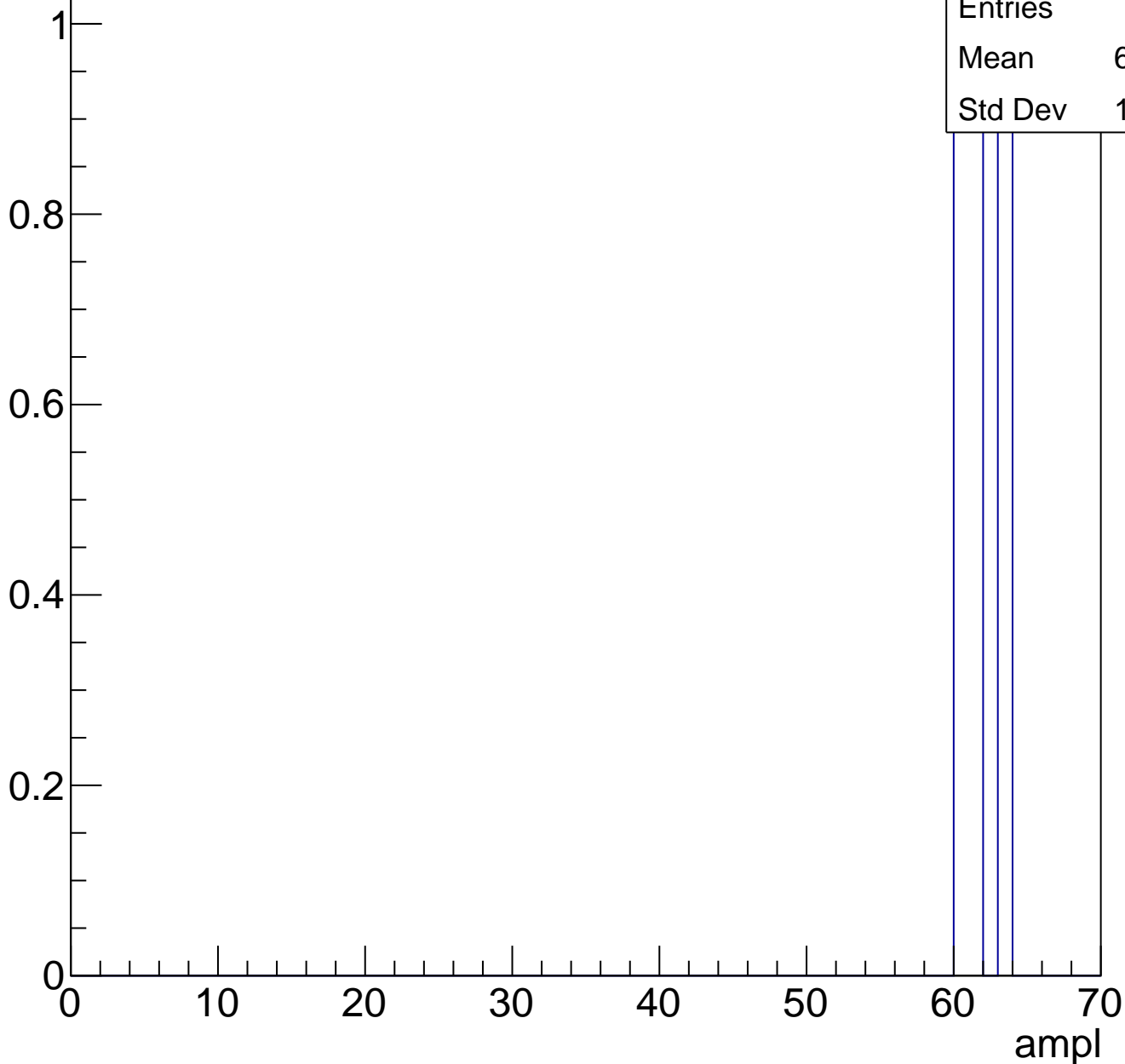




# B1L102S, U25-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch67, adc0

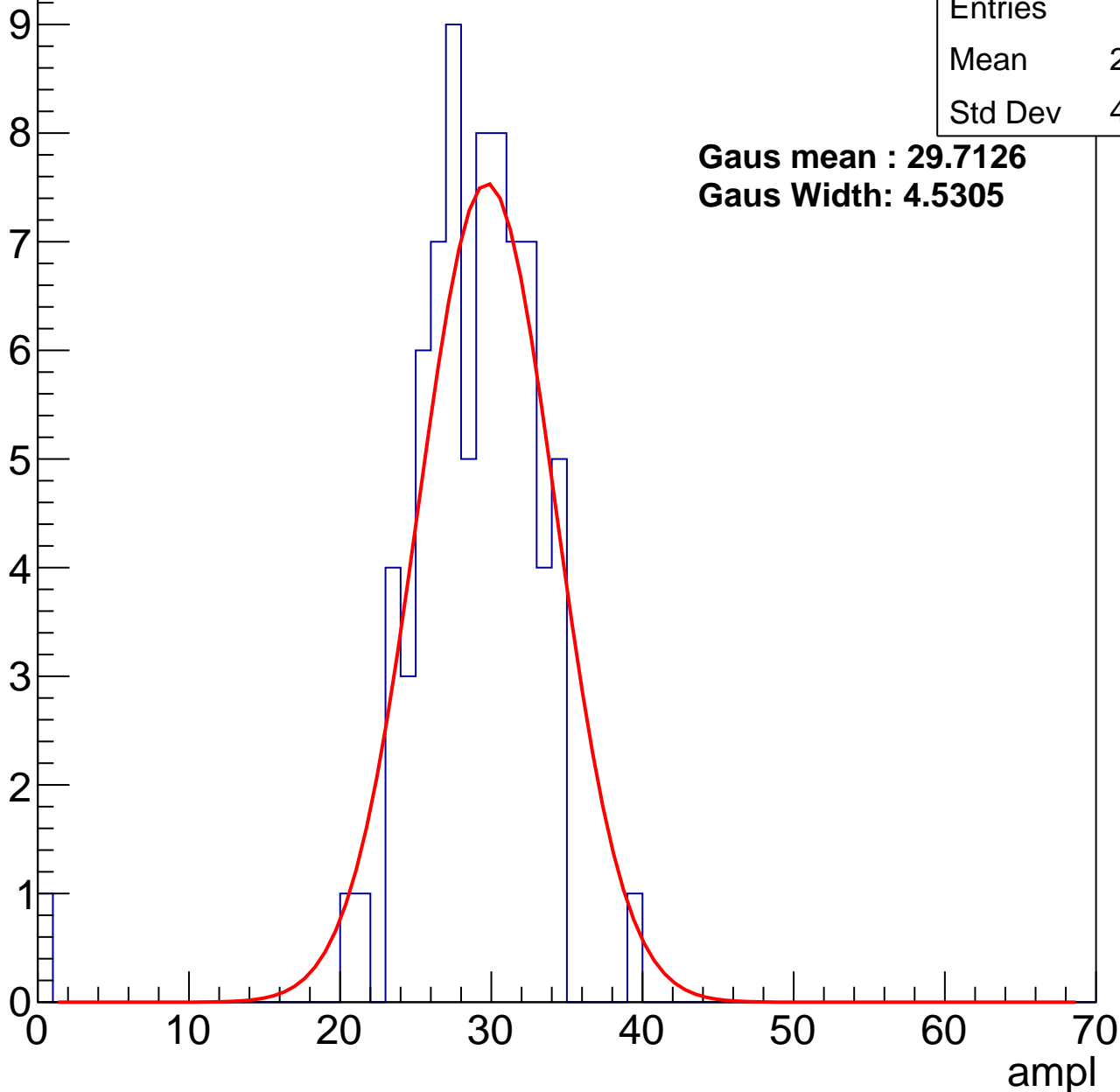
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.23
Std Dev	4.764

**Gaus mean : 29.7126**

**Gaus Width: 4.5305**



# B1L102S, U25-ch67, adc1

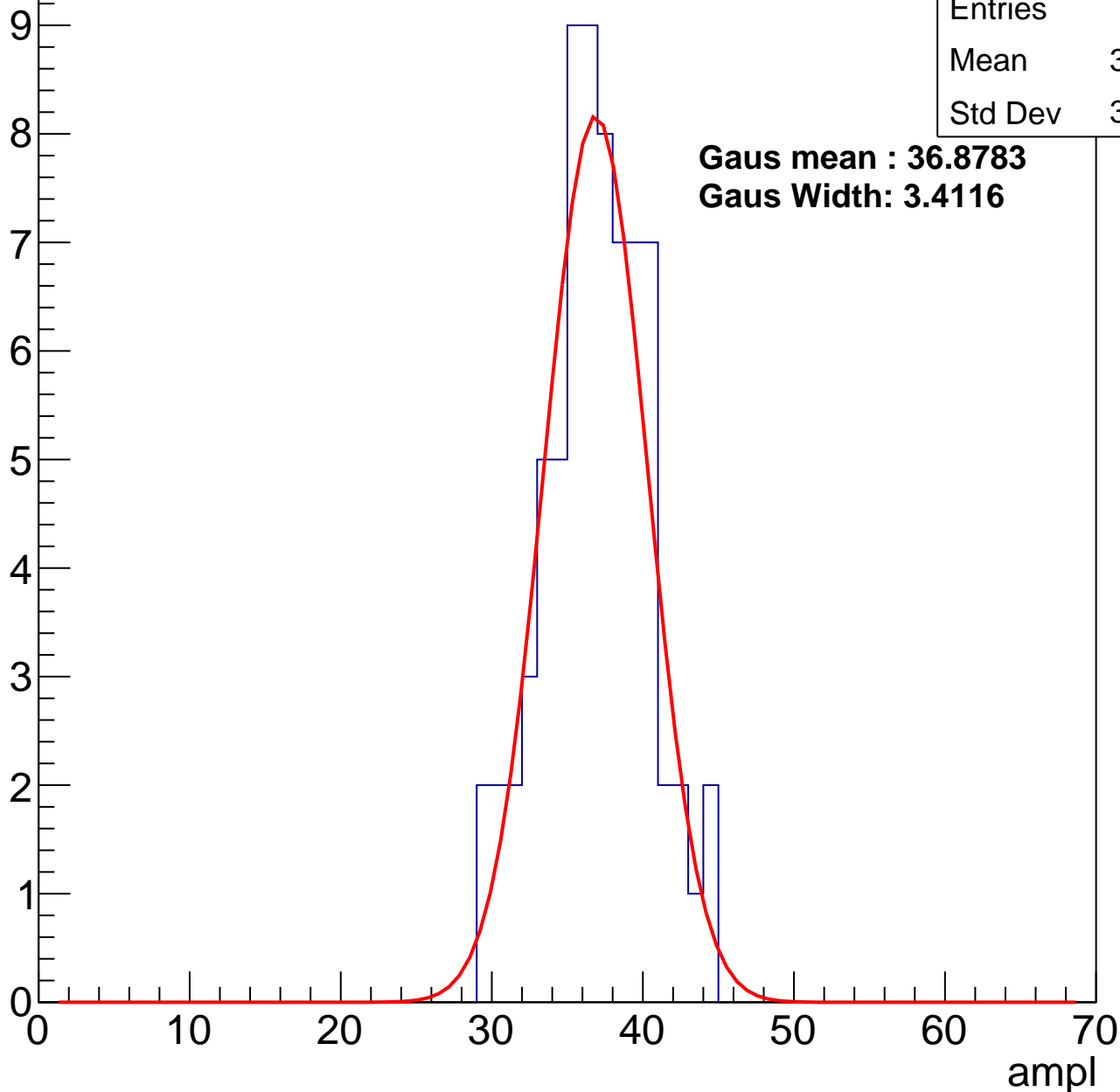
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.47
Std Dev	3.384

**Gaus mean : 36.8783**

**Gaus Width: 3.4116**



# B1L102S, U25-ch67, adc2

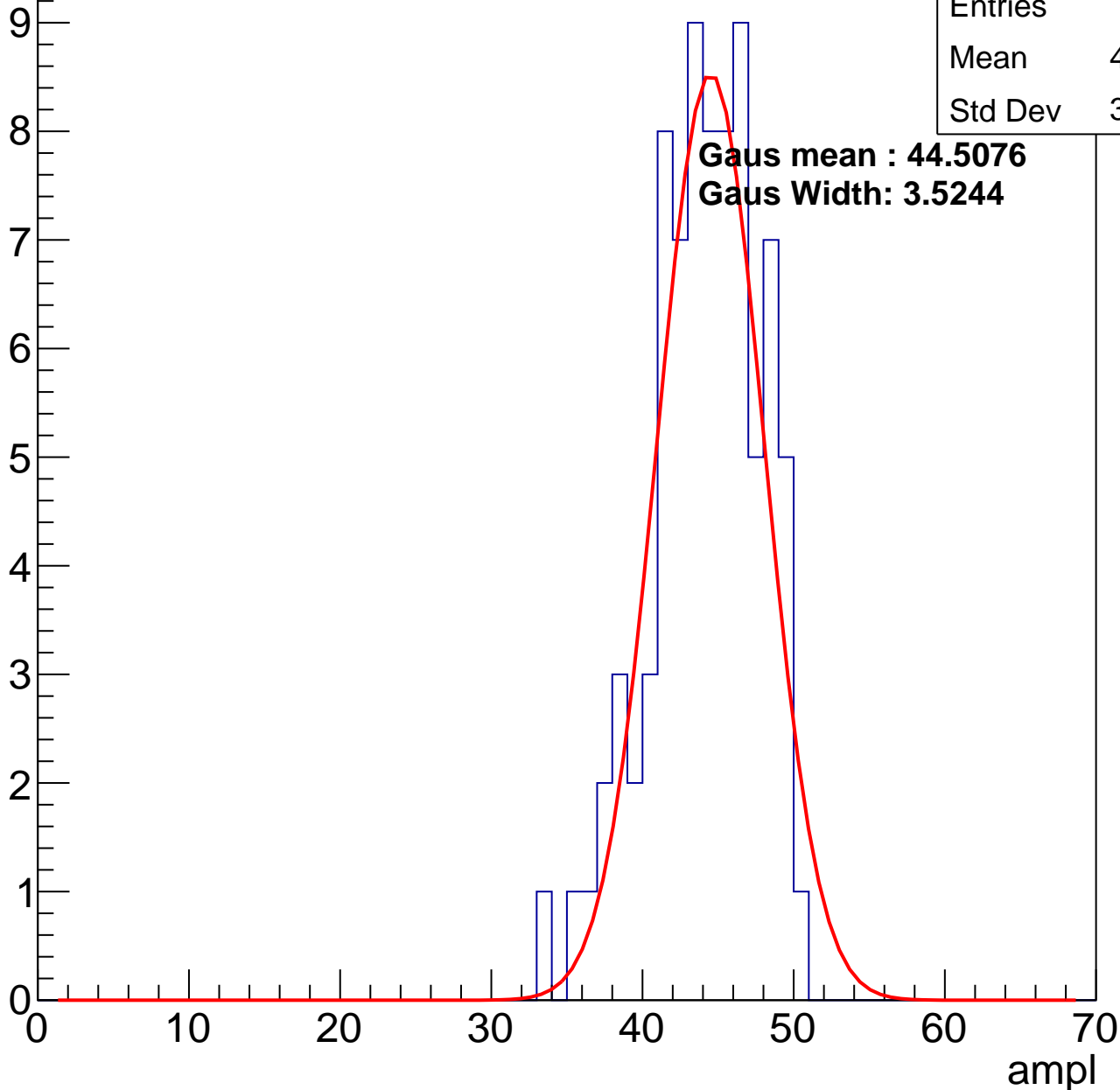
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.64
Std Dev	3.589

**Gaus mean : 44.5076**

**Gaus Width: 3.5244**

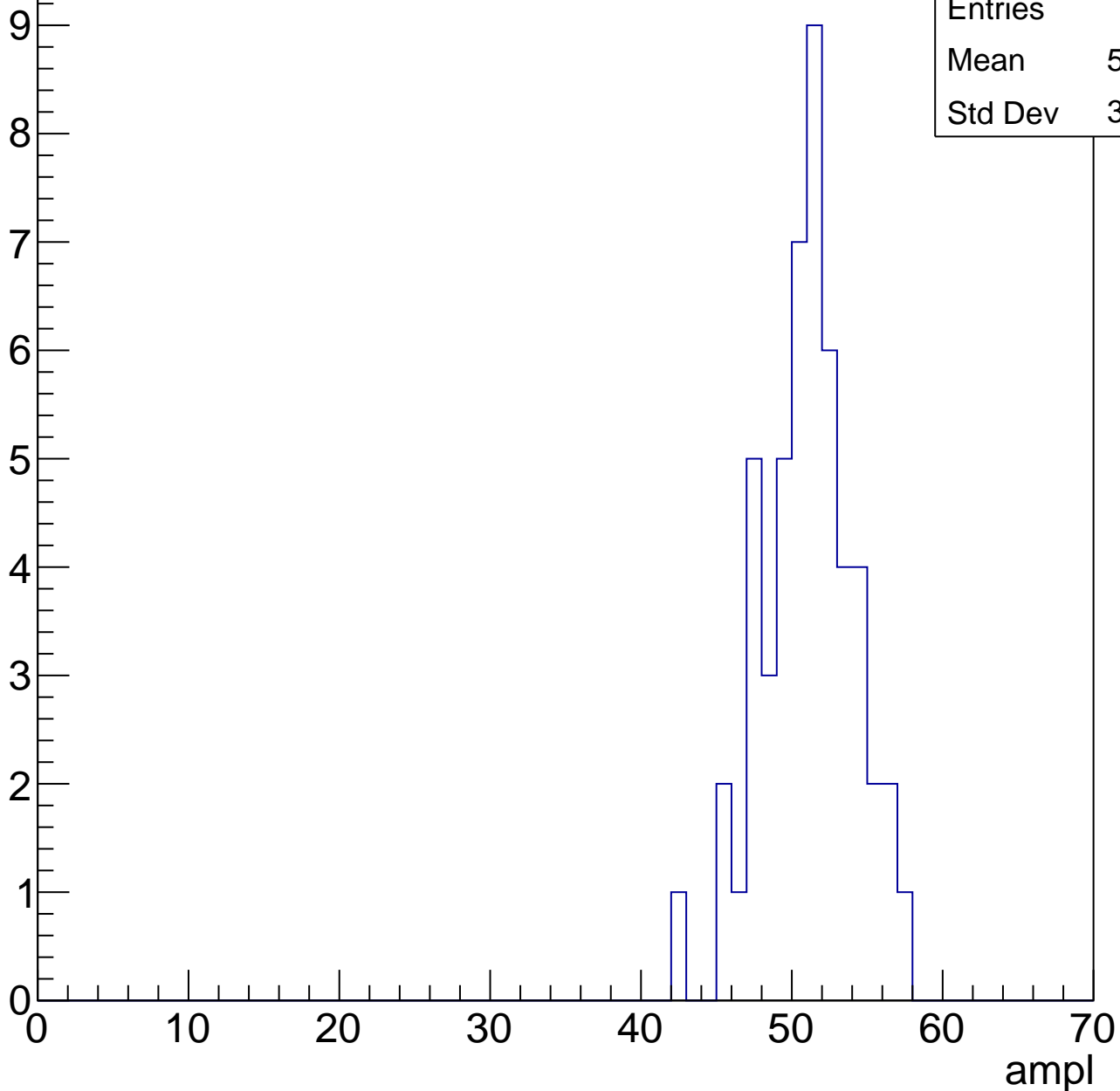


# B1L102S, U25-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	50.58
Std Dev	3.034

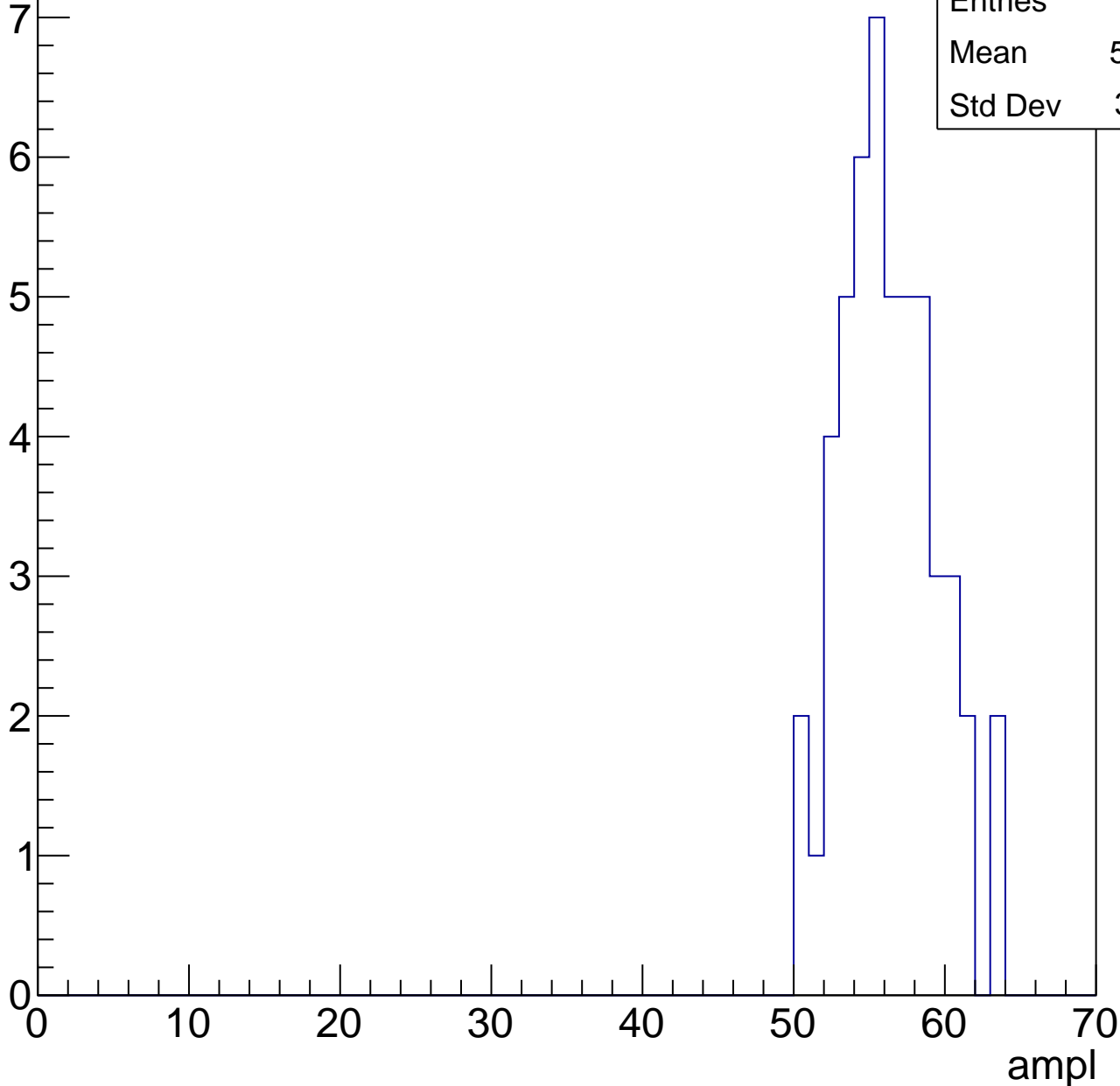


# B1L102S, U25-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.86
Std Dev	3.111

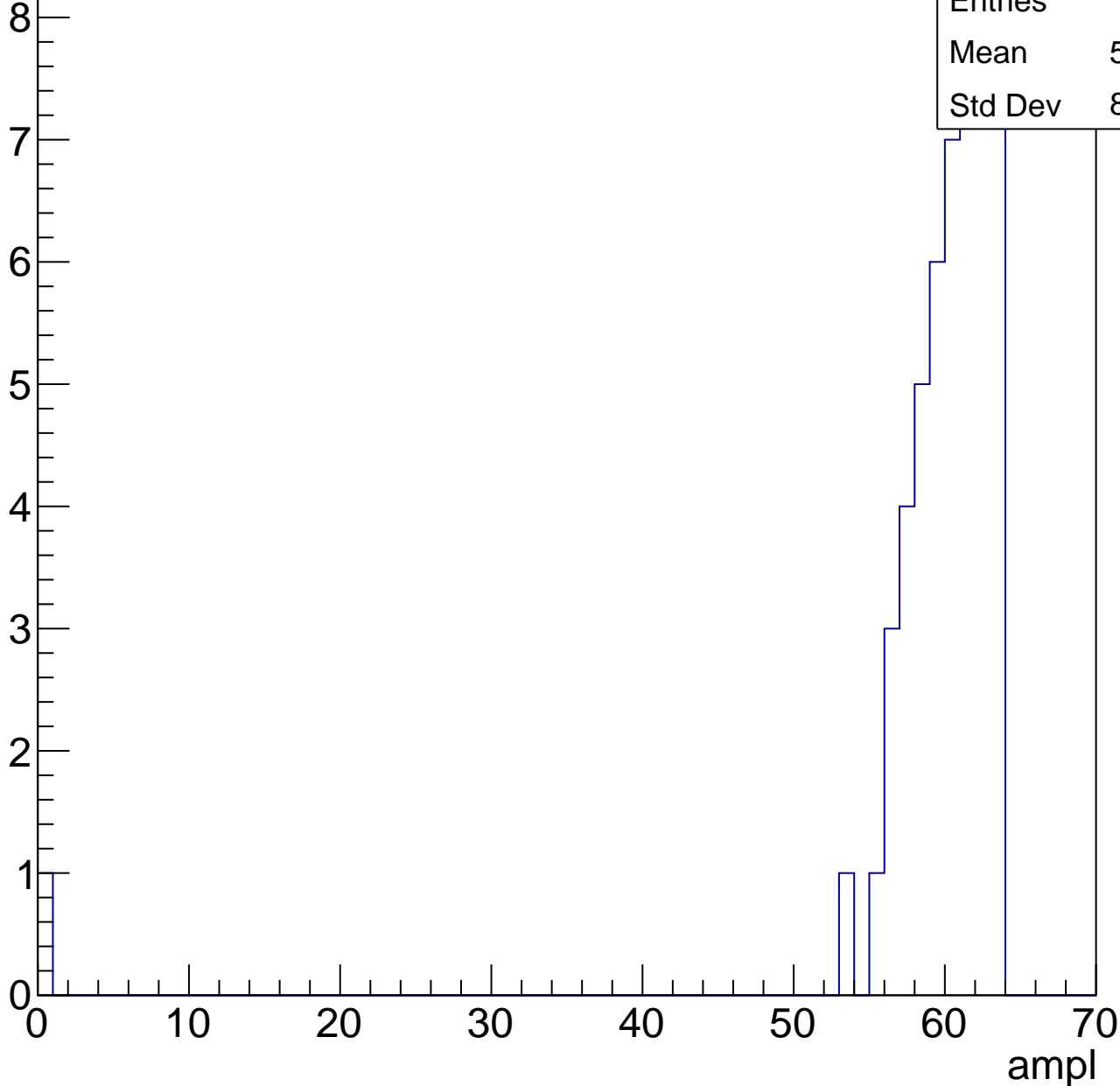


# B1L102S, U25-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

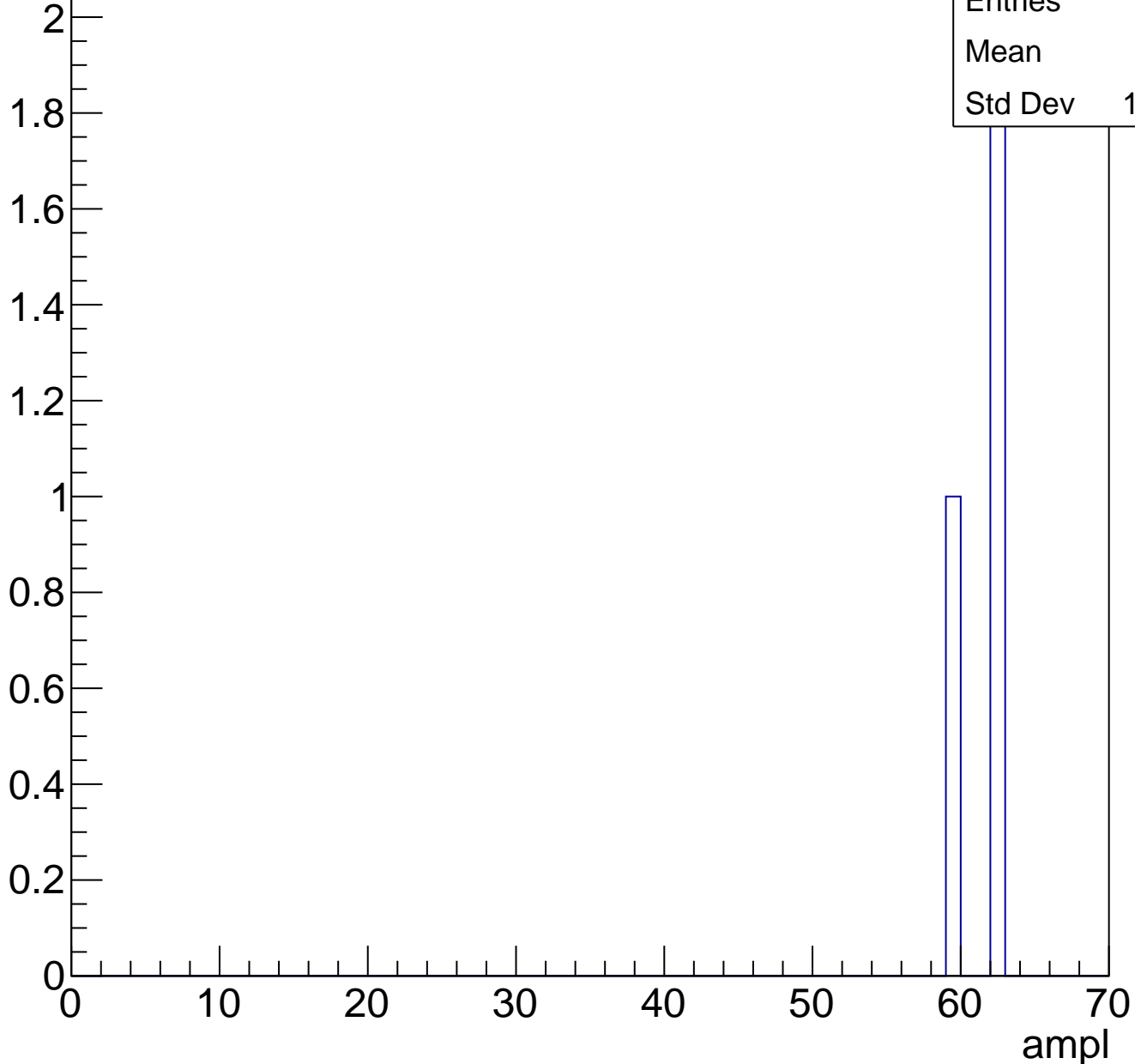
Entries	52
Mean	58.77
Std Dev	8.568



# B1L102S, U25-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

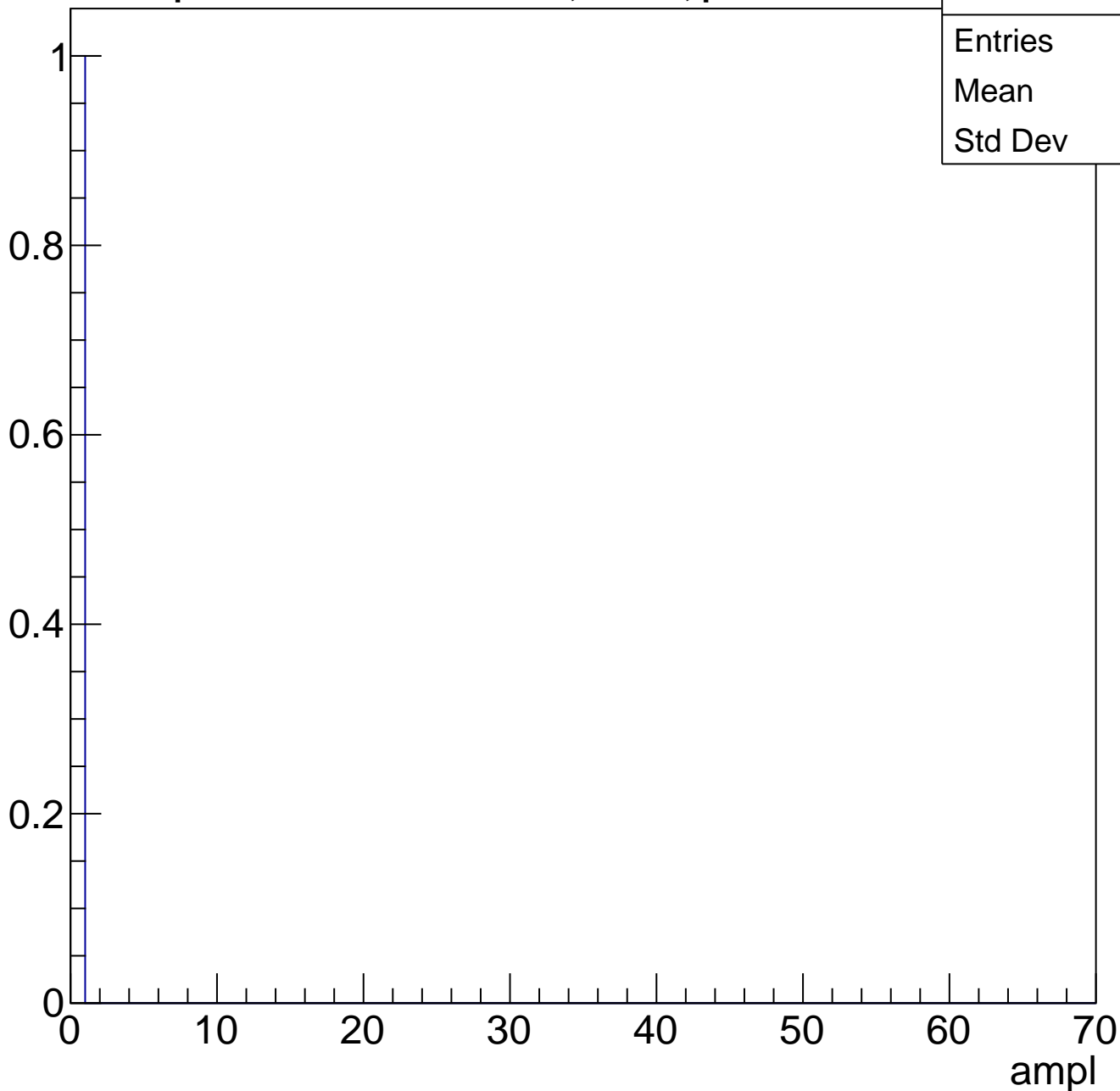




# B1L102S, U25-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch68, adc0

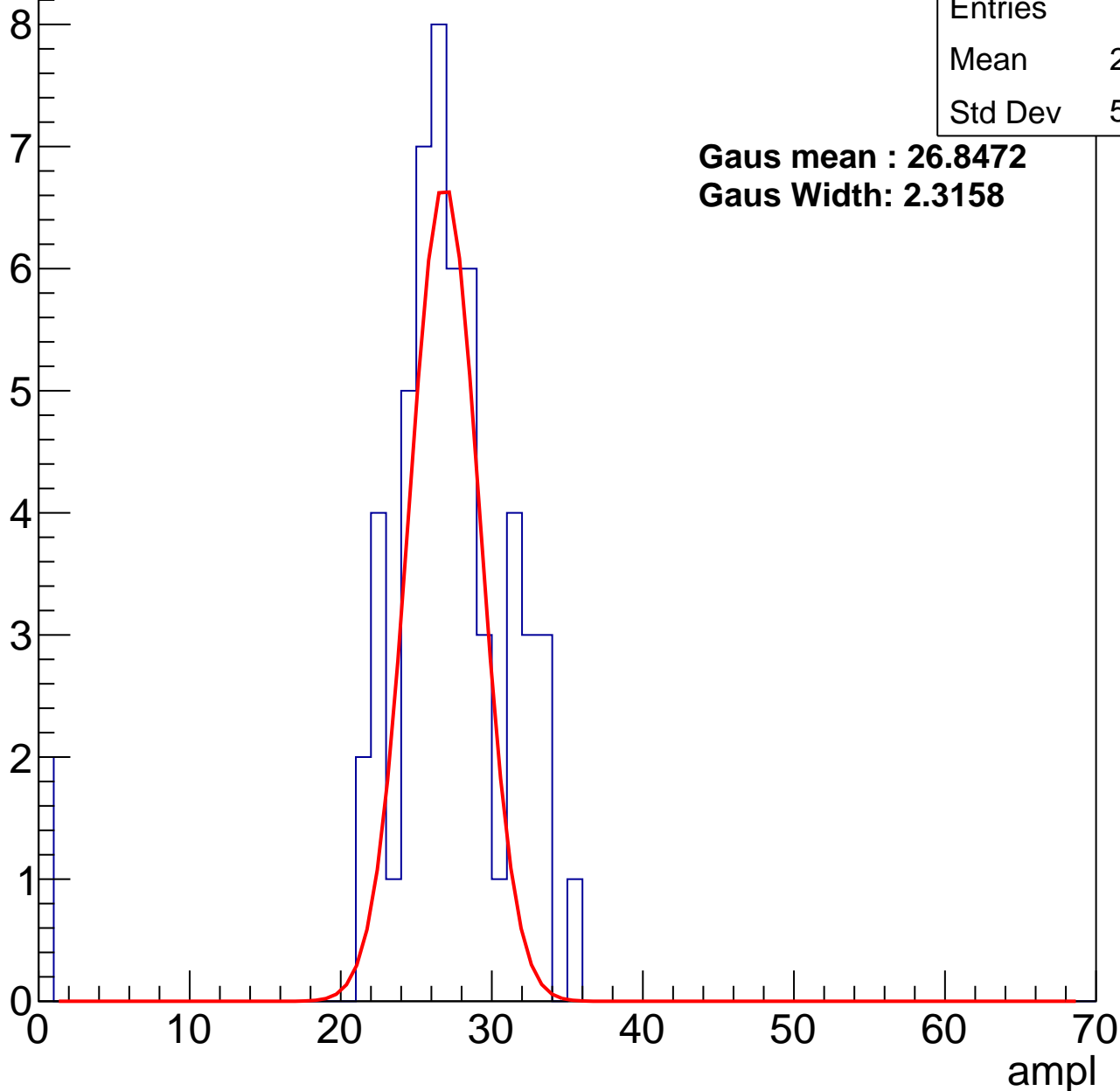
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	26.02
Std Dev	5.993

**Gaus mean : 26.8472**

**Gaus Width: 2.3158**



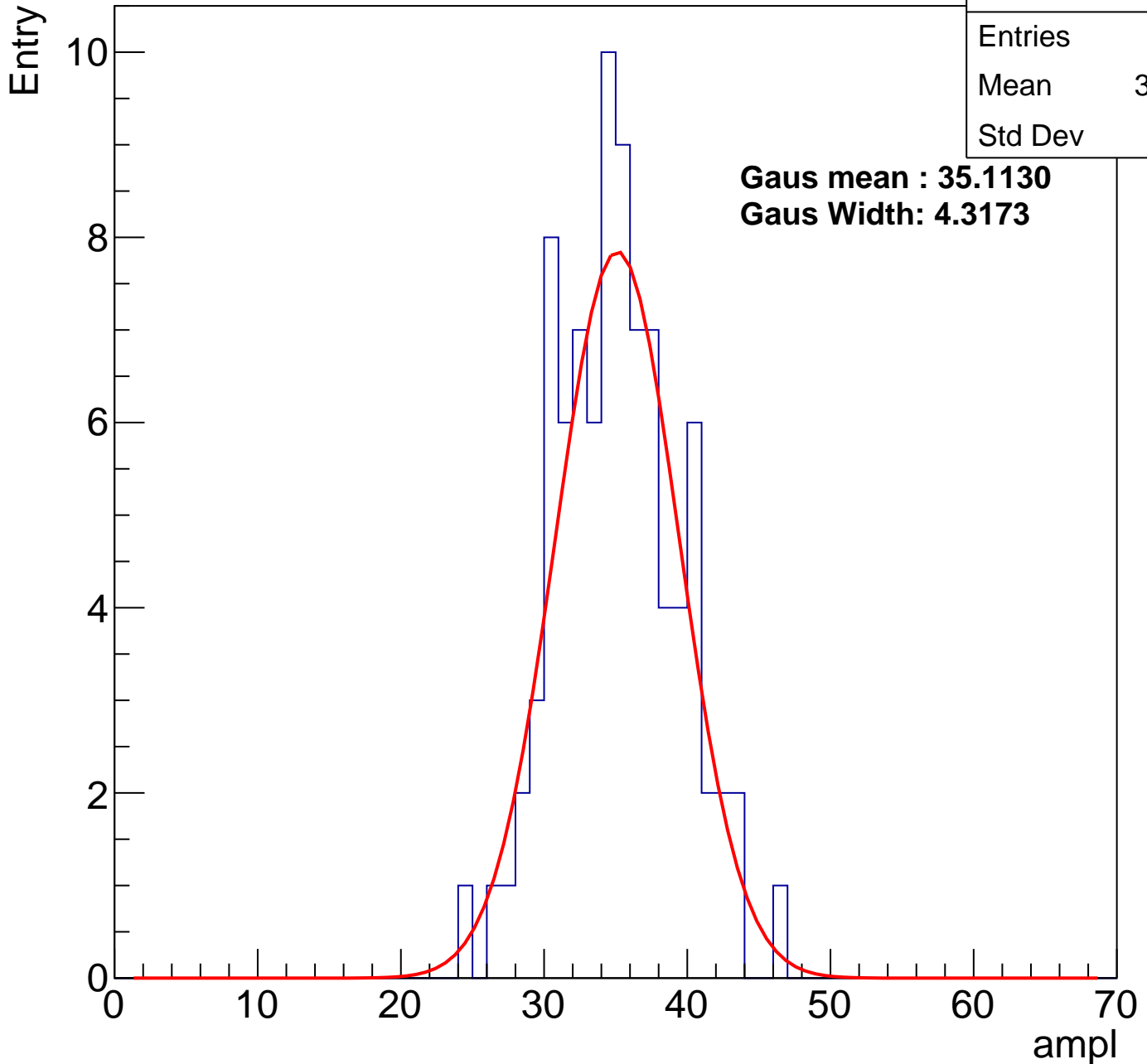
# B1L102S, U25-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	89
Mean	34.61
Std Dev	4.18

**Gaus mean : 35.1130**

**Gaus Width: 4.3173**



# B1L102S, U25-ch68, adc2

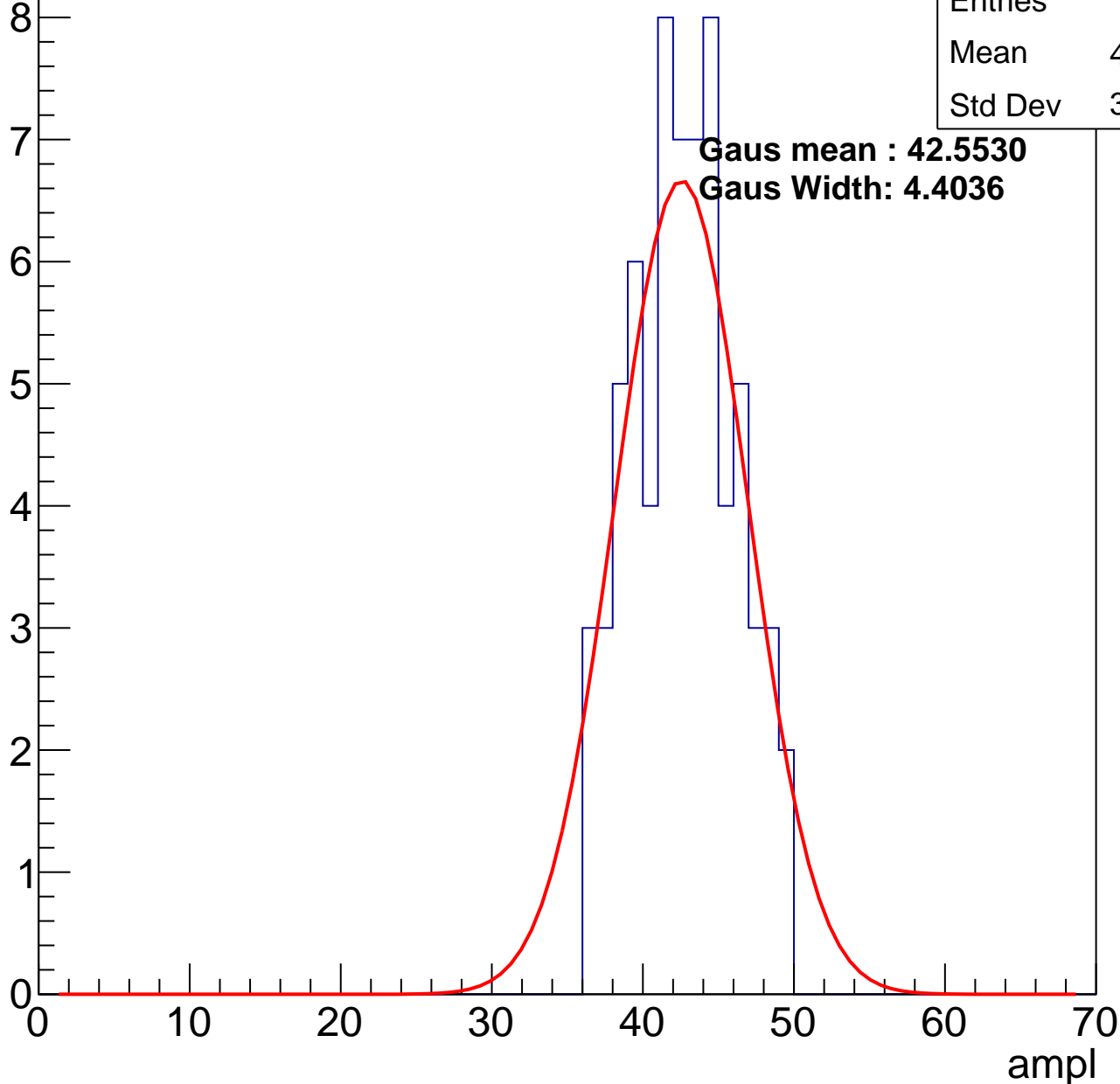
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.22
Std Dev	3.373

**Gaus mean : 42.5530**

**Gaus Width: 4.4036**

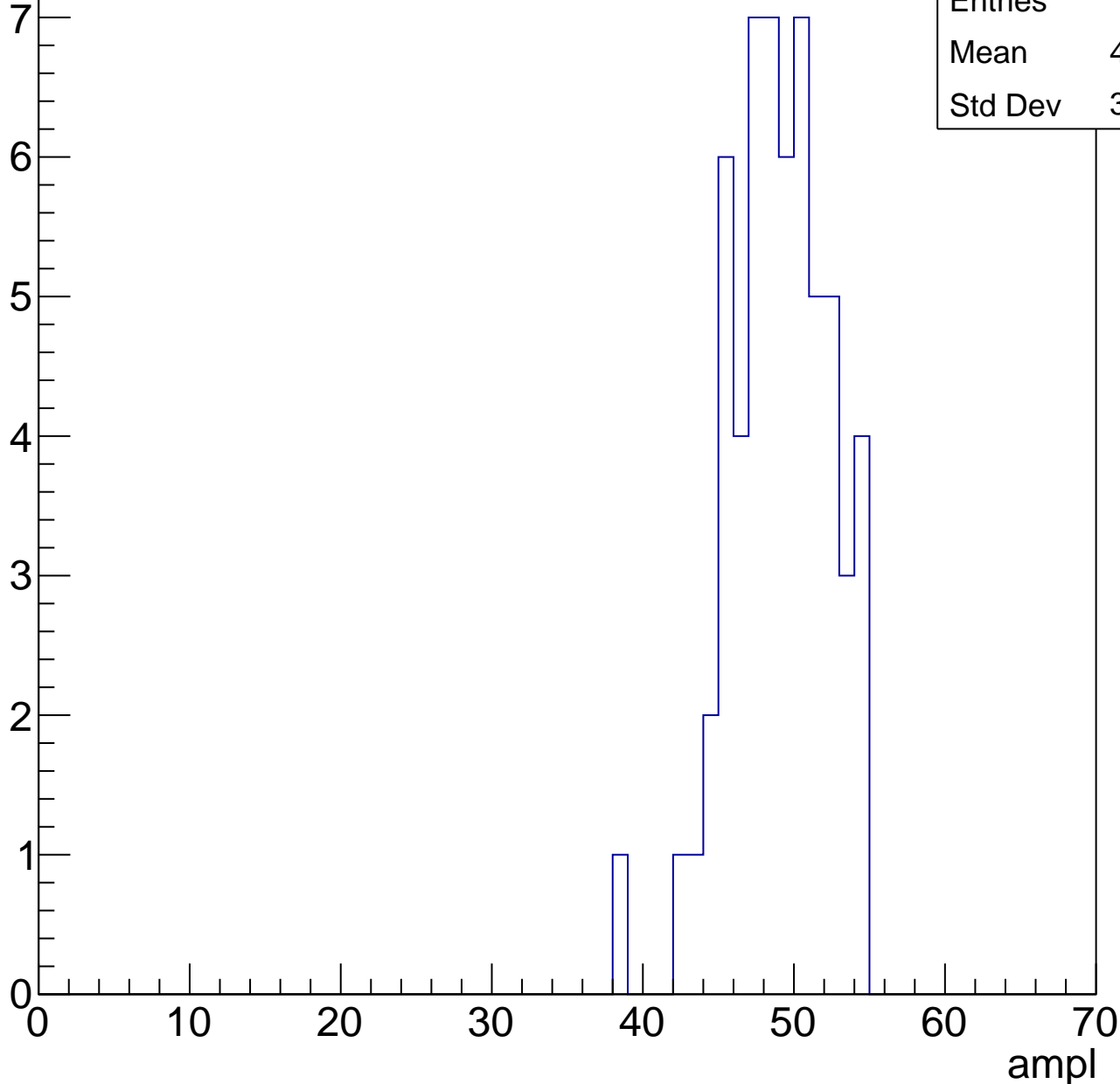


# B1L102S, U25-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.54
Std Dev	3.269

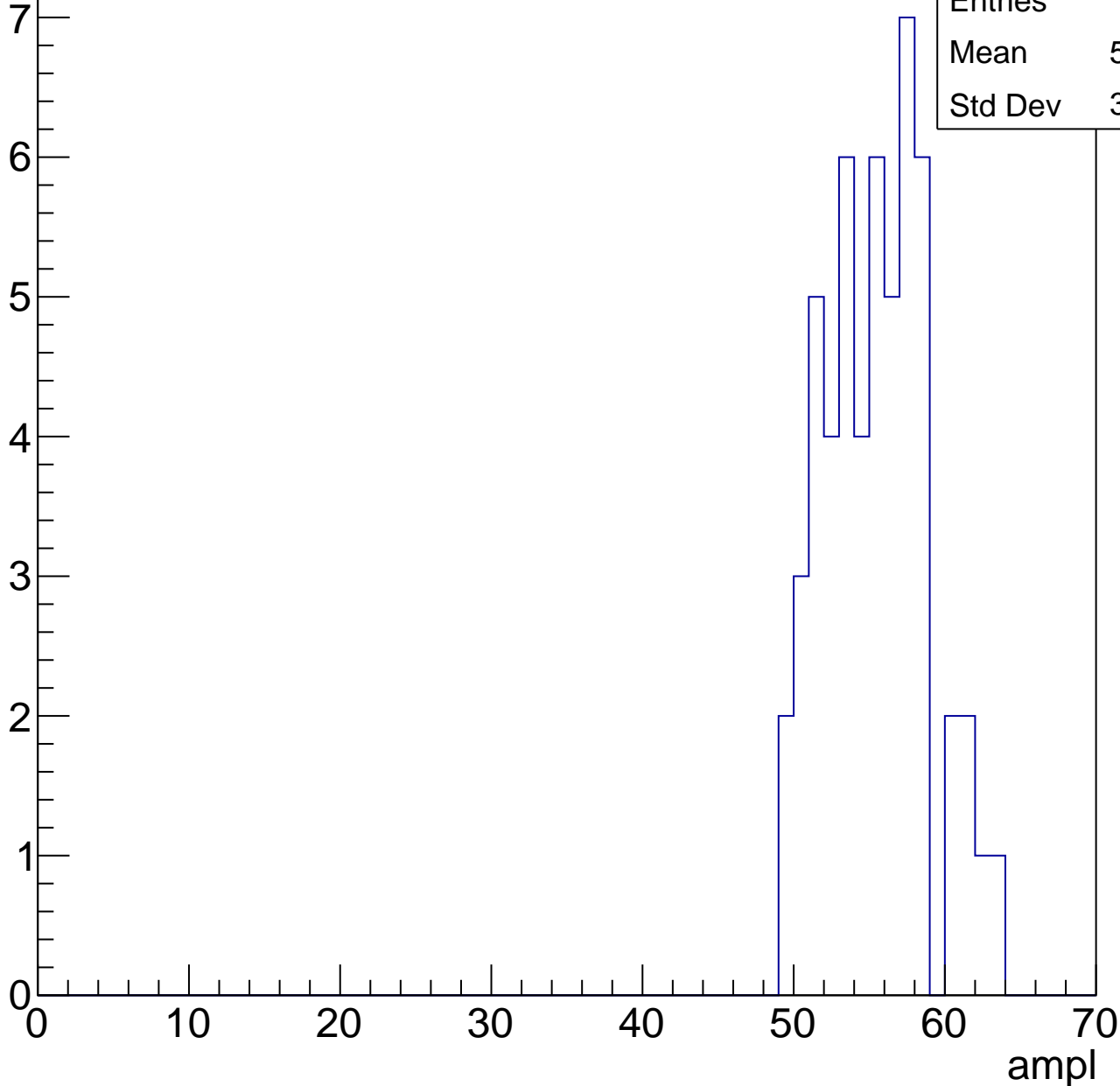


# B1L102S, U25-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.98
Std Dev	3.369



# B1L102S, U25-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

Entries 56

Mean 57.88

Std Dev 8.272

ampl

0

10

20

30

40

50

60

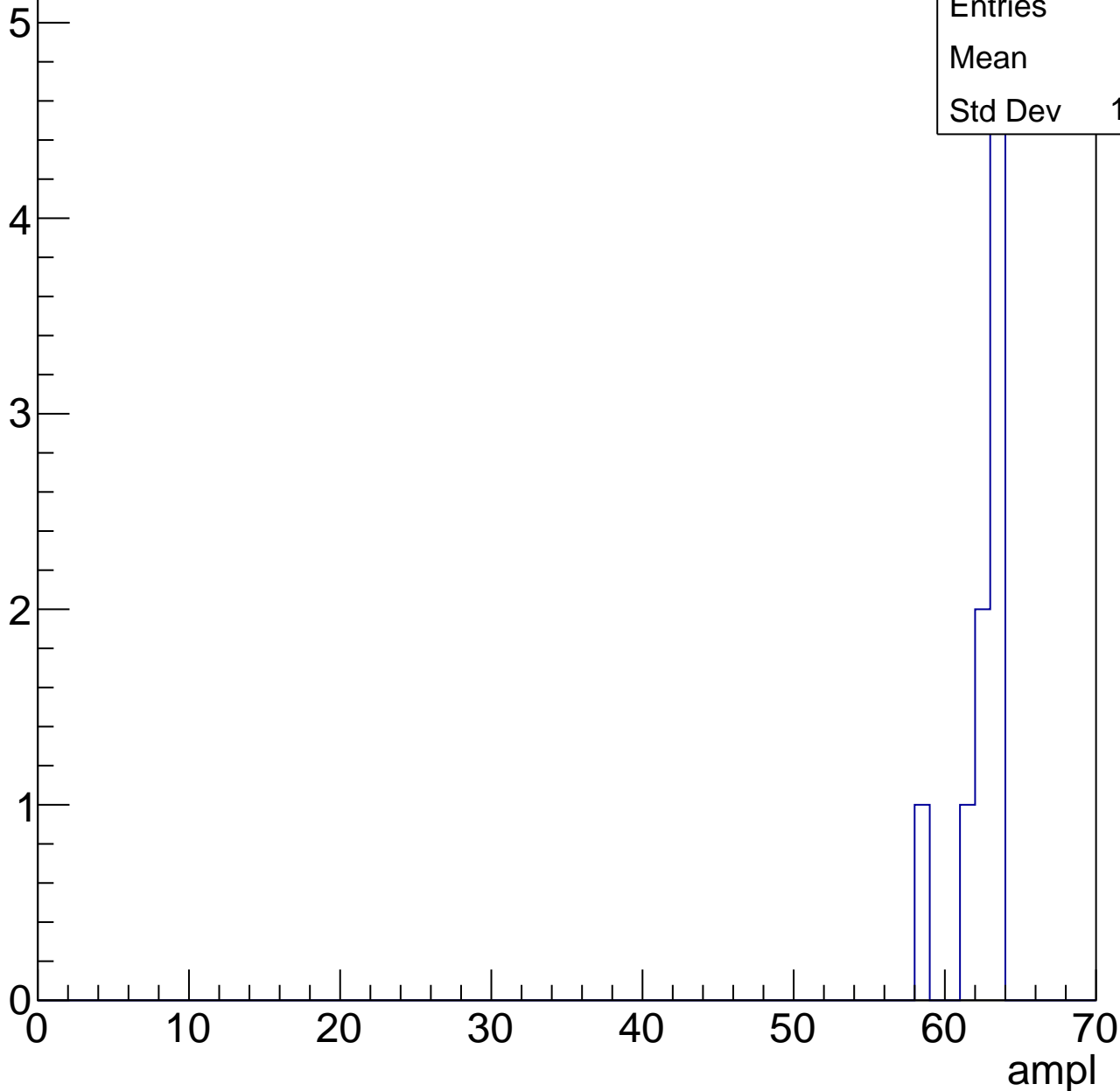
70

# B1L102S, U25-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62
Std Dev	1.563





# B1L102S, U25-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch69, adc0

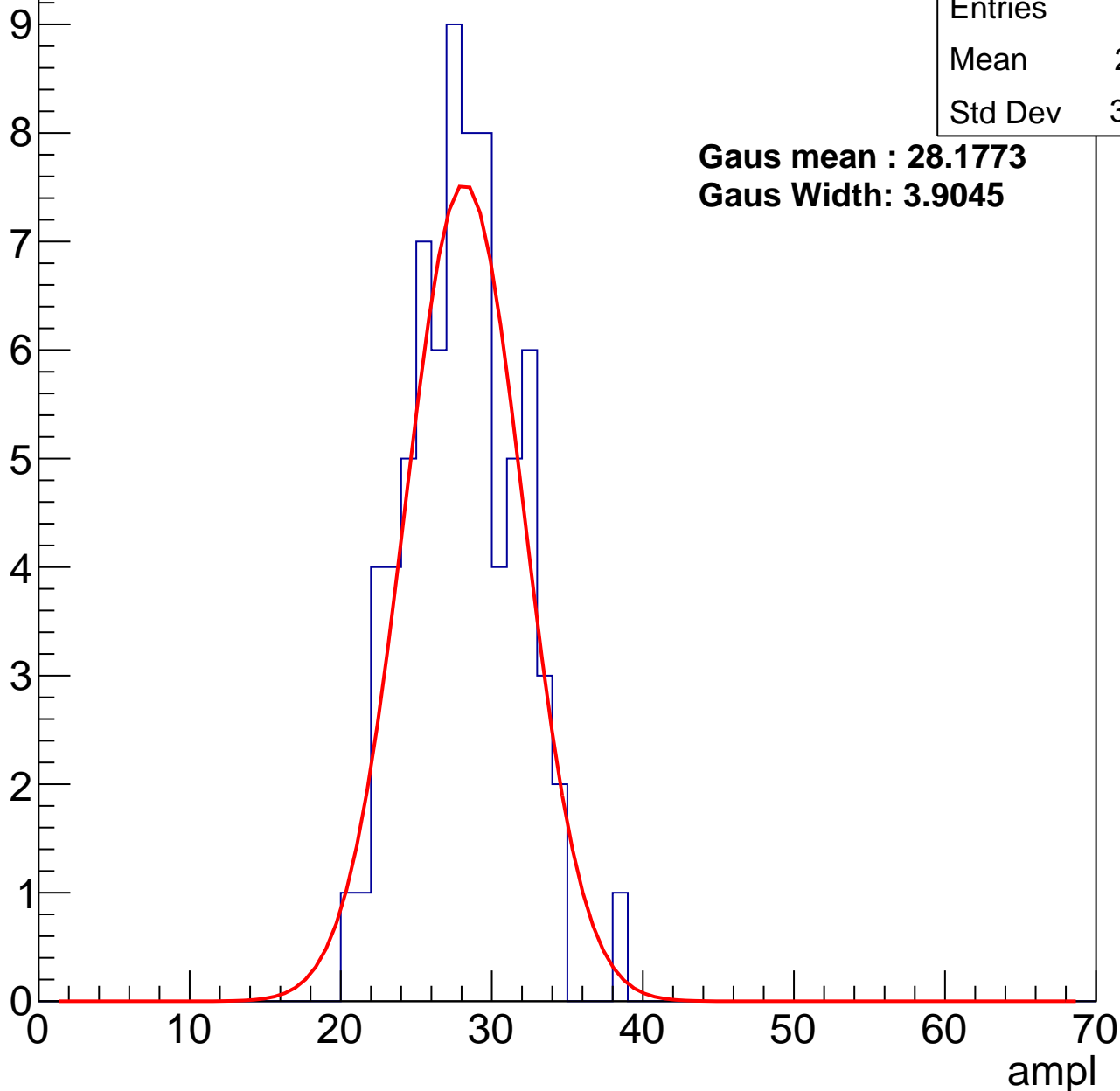
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.61
Std Dev	3.552

**Gaus mean : 28.1773**

**Gaus Width: 3.9045**



# B1L102S, U25-ch69, adc1

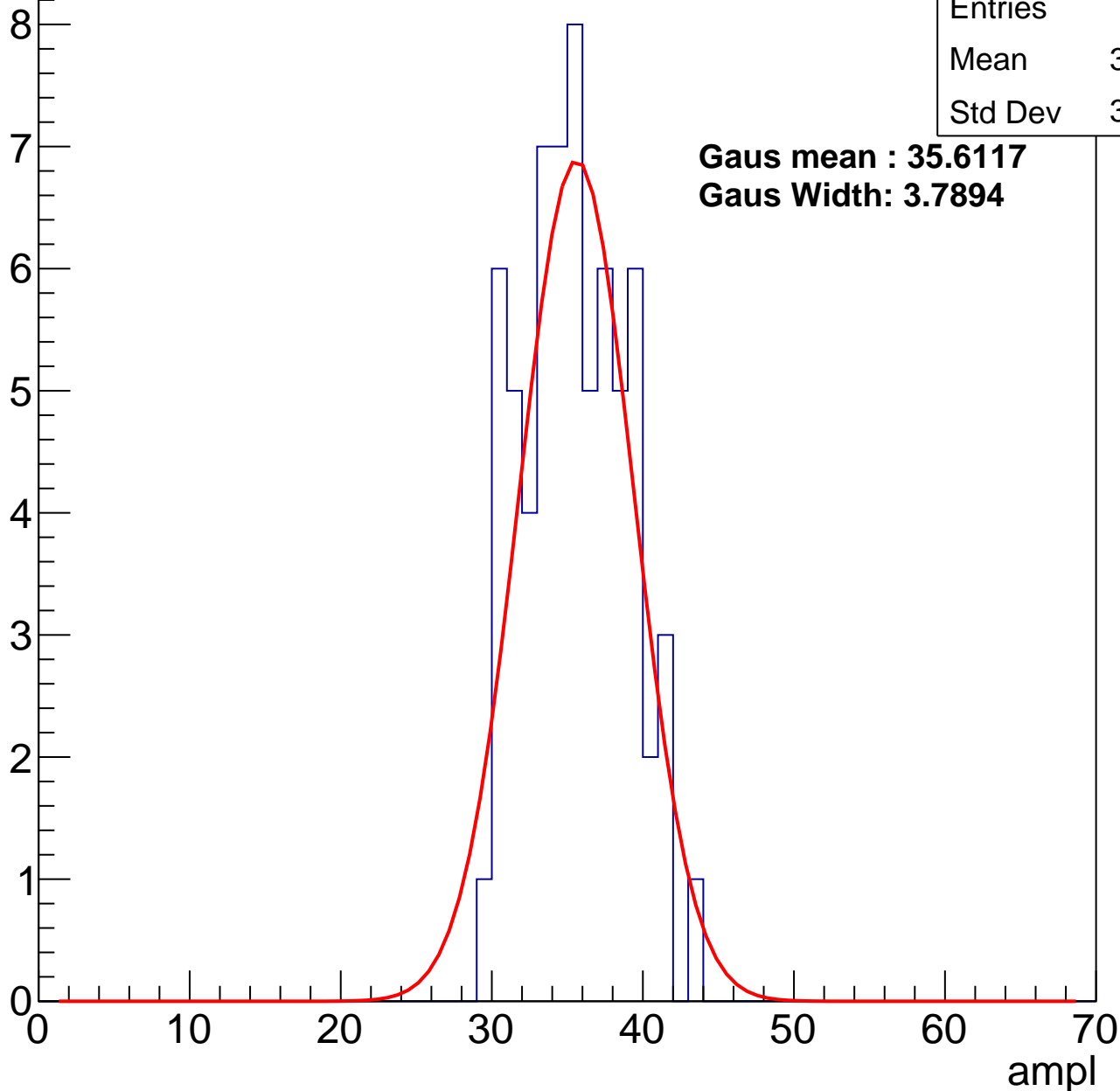
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.05
Std Dev	3.323

**Gaus mean : 35.6117**

**Gaus Width: 3.7894**



# B1L102S, U25-ch69, adc2

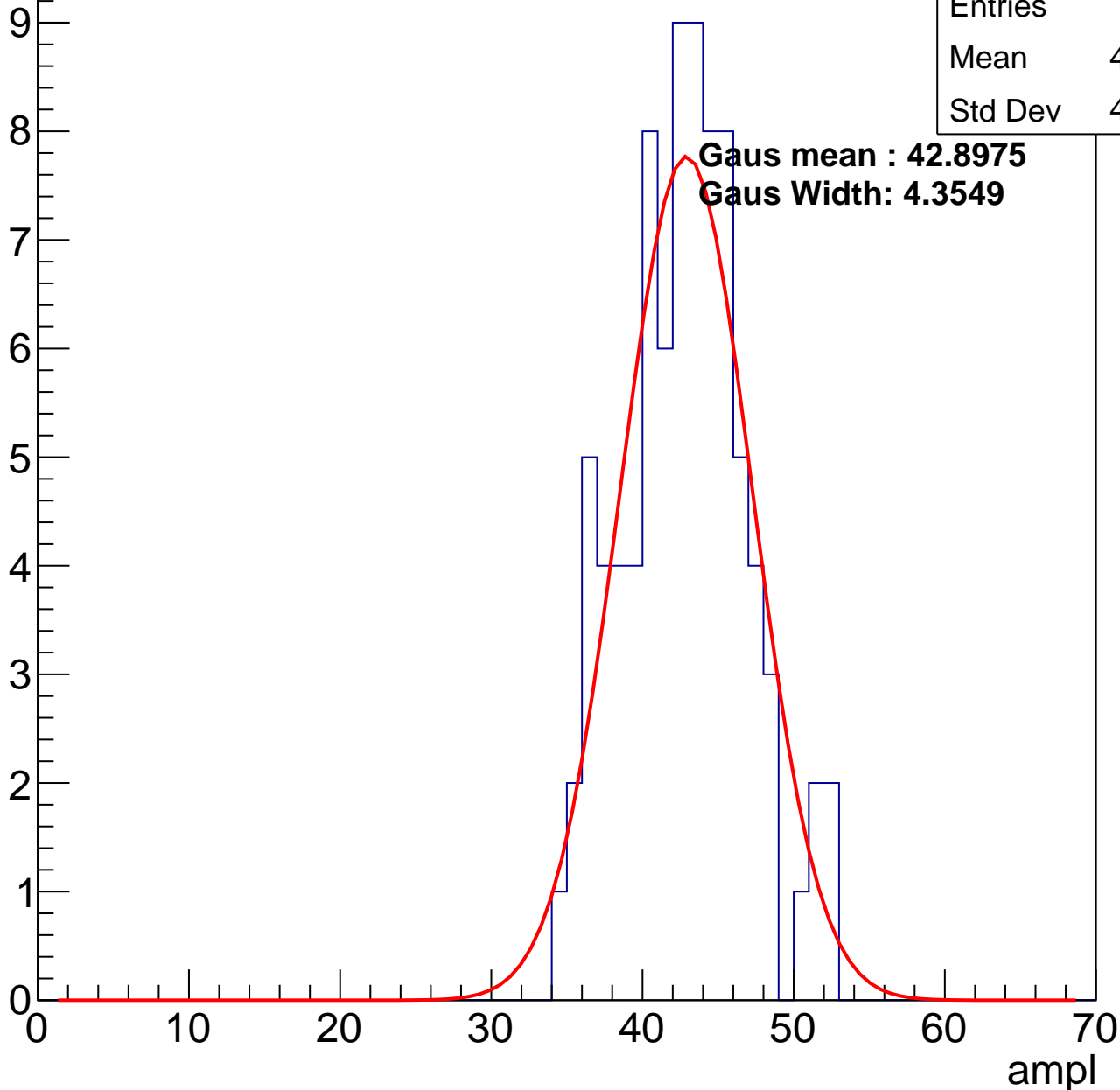
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	42.36
Std Dev	4.052

**Gaus mean : 42.8975**

**Gaus Width: 4.3549**

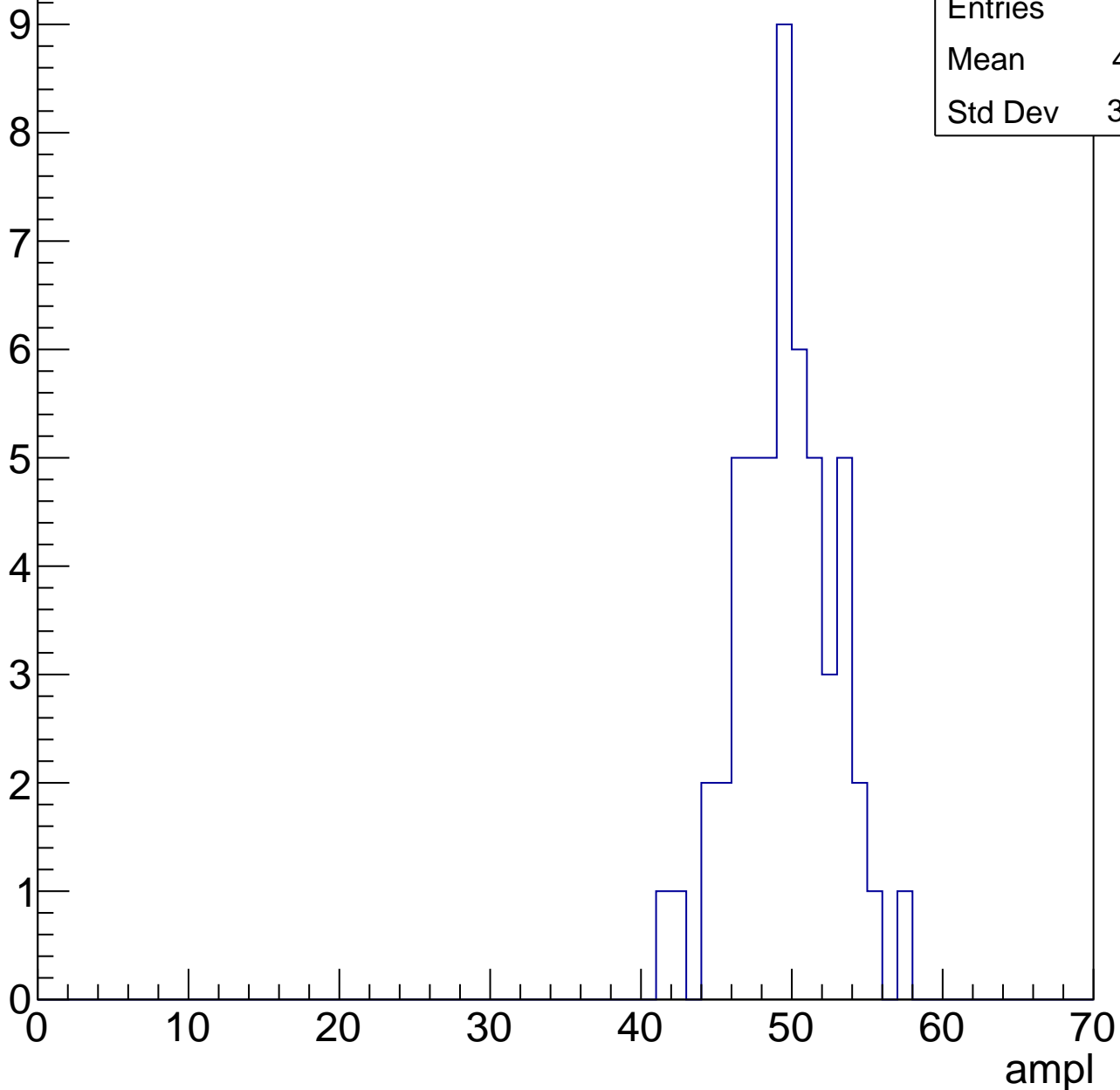


# B1L102S, U25-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	49.11
Std Dev	3.214

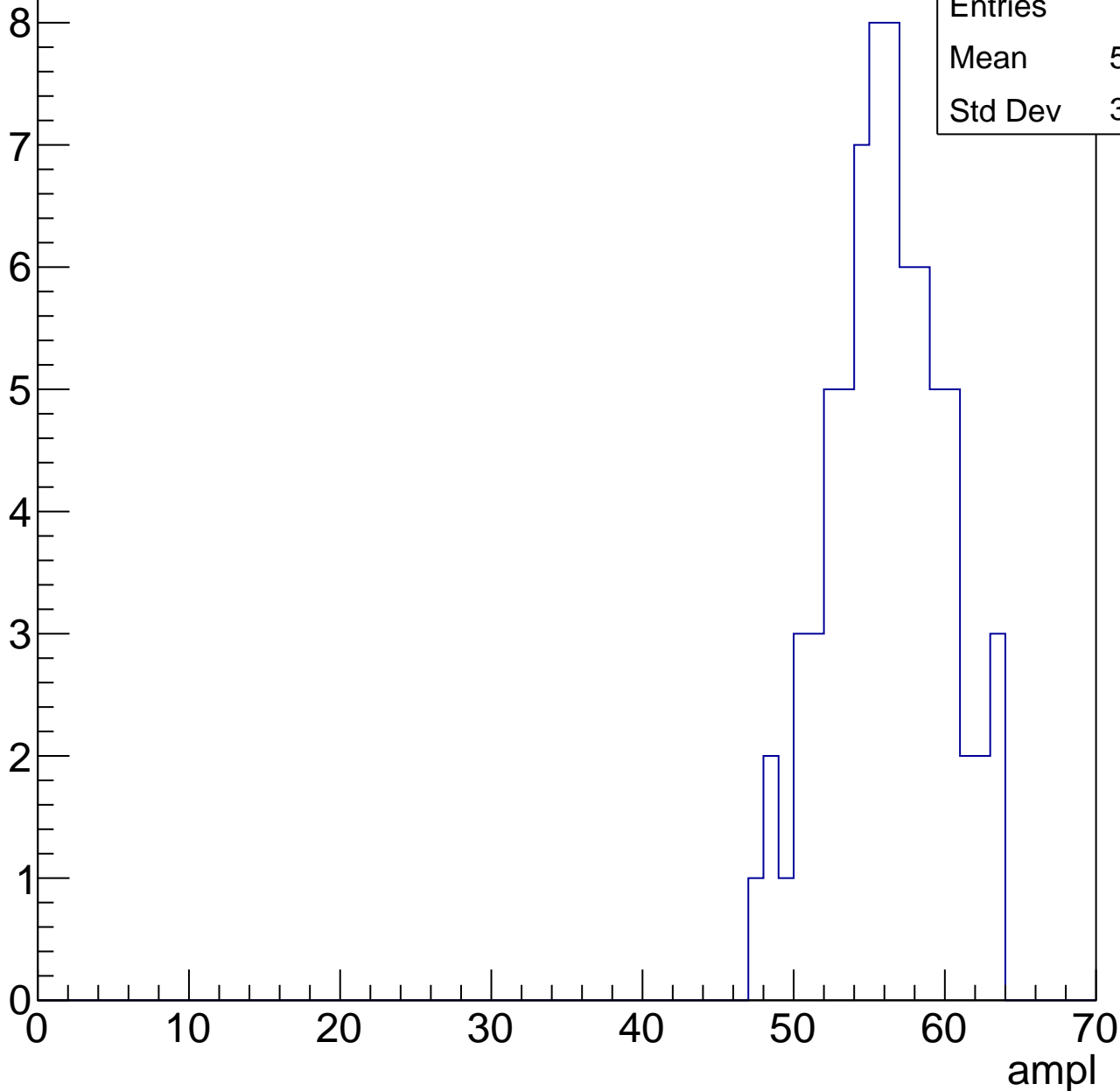


# B1L102S, U25-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	55.64
Std Dev	3.758

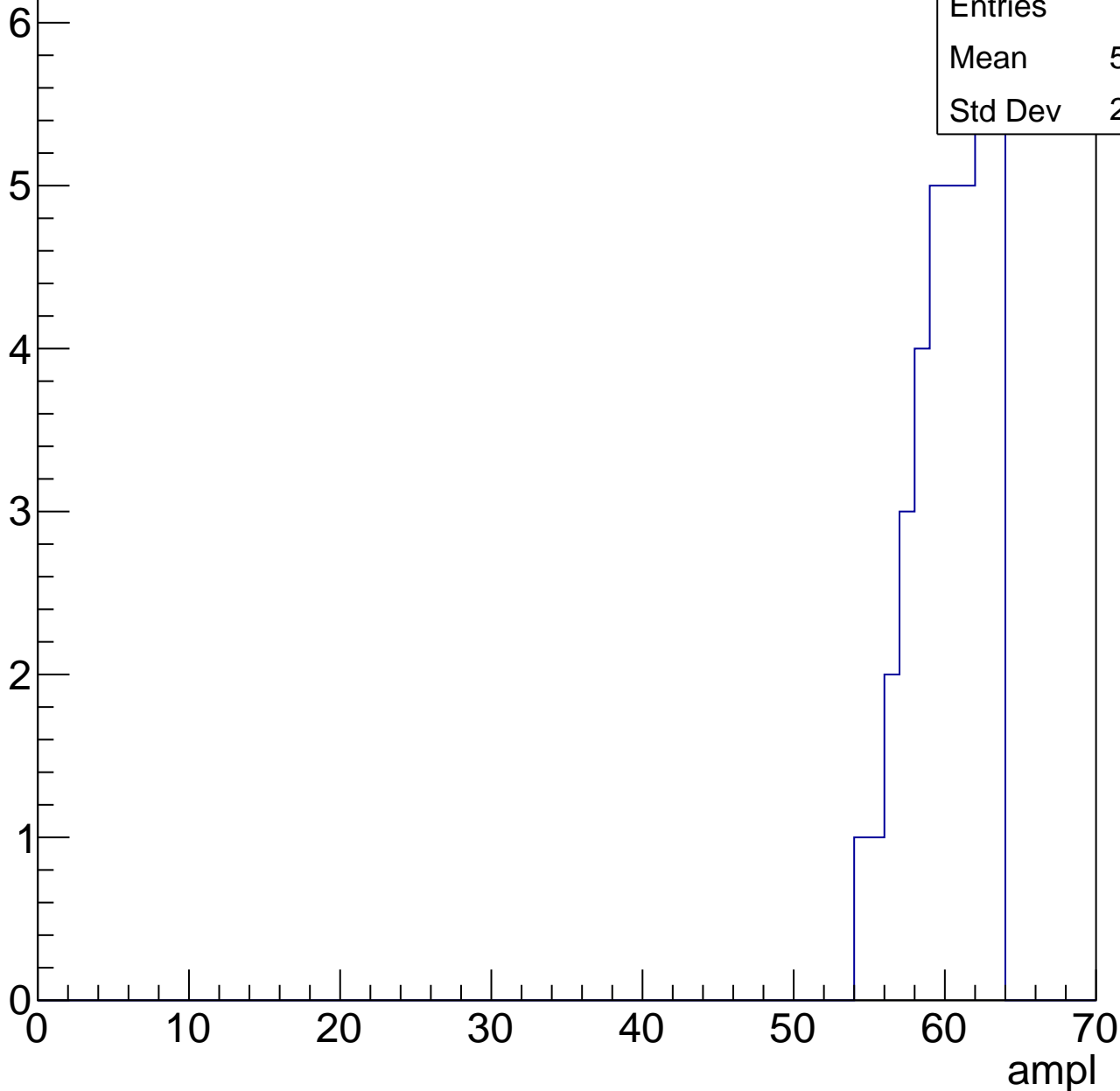


# B1L102S, U25-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

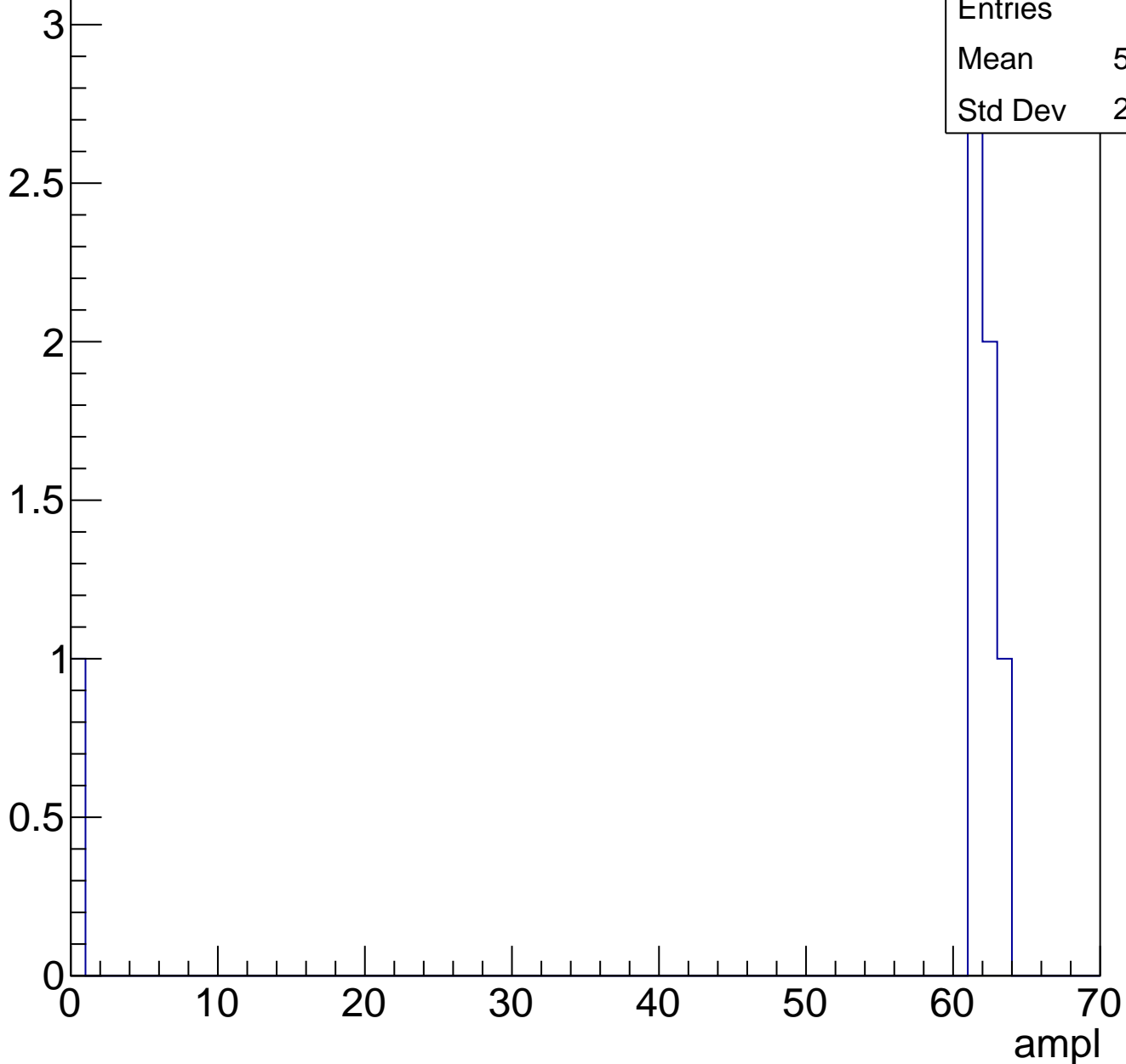
Entries	38
Mean	59.84
Std Dev	2.423



# B1L102S, U25-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch70, adc0

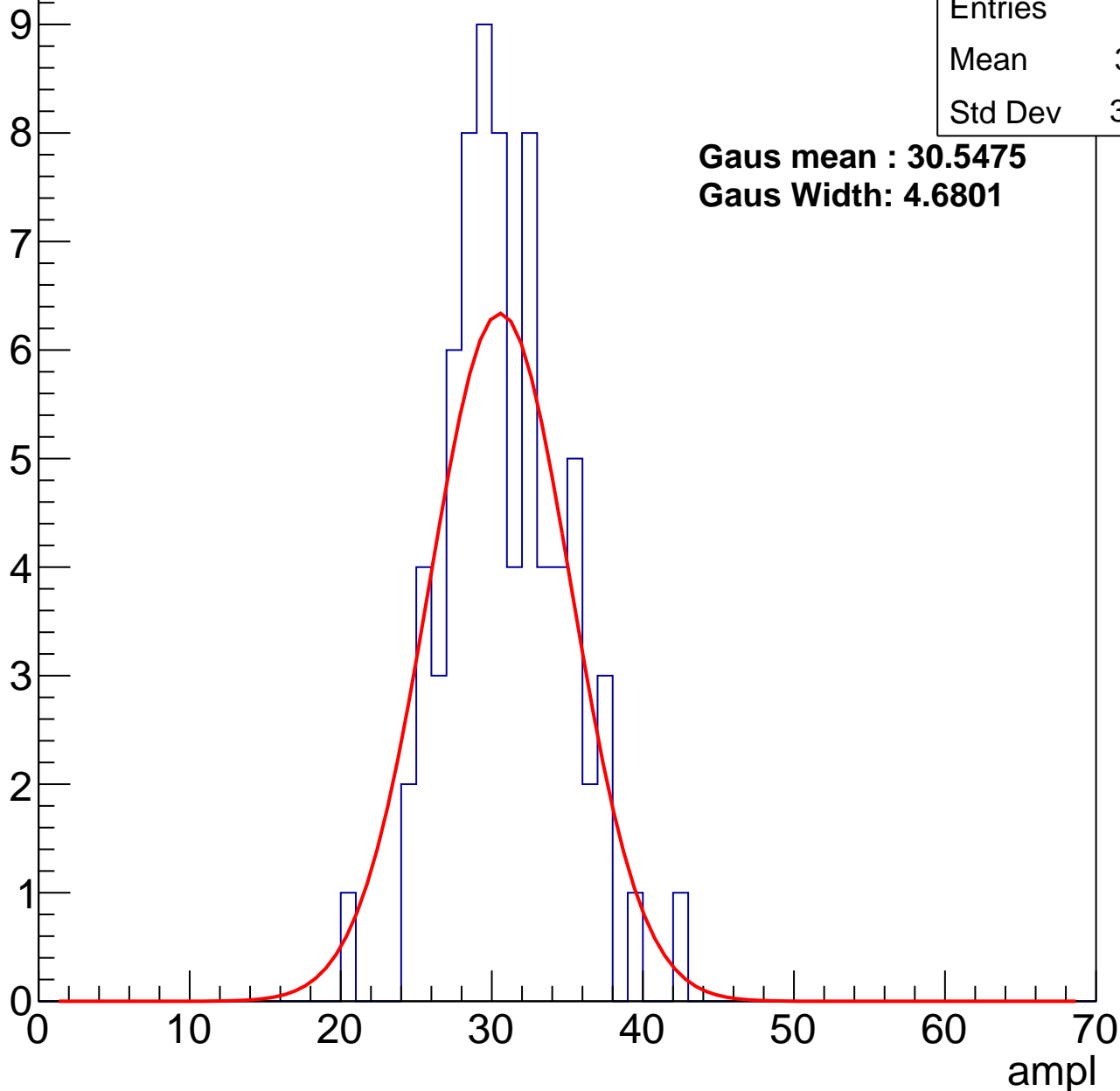
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	30.41
Std Dev	3.895

**Gaus mean : 30.5475**

**Gaus Width: 4.6801**



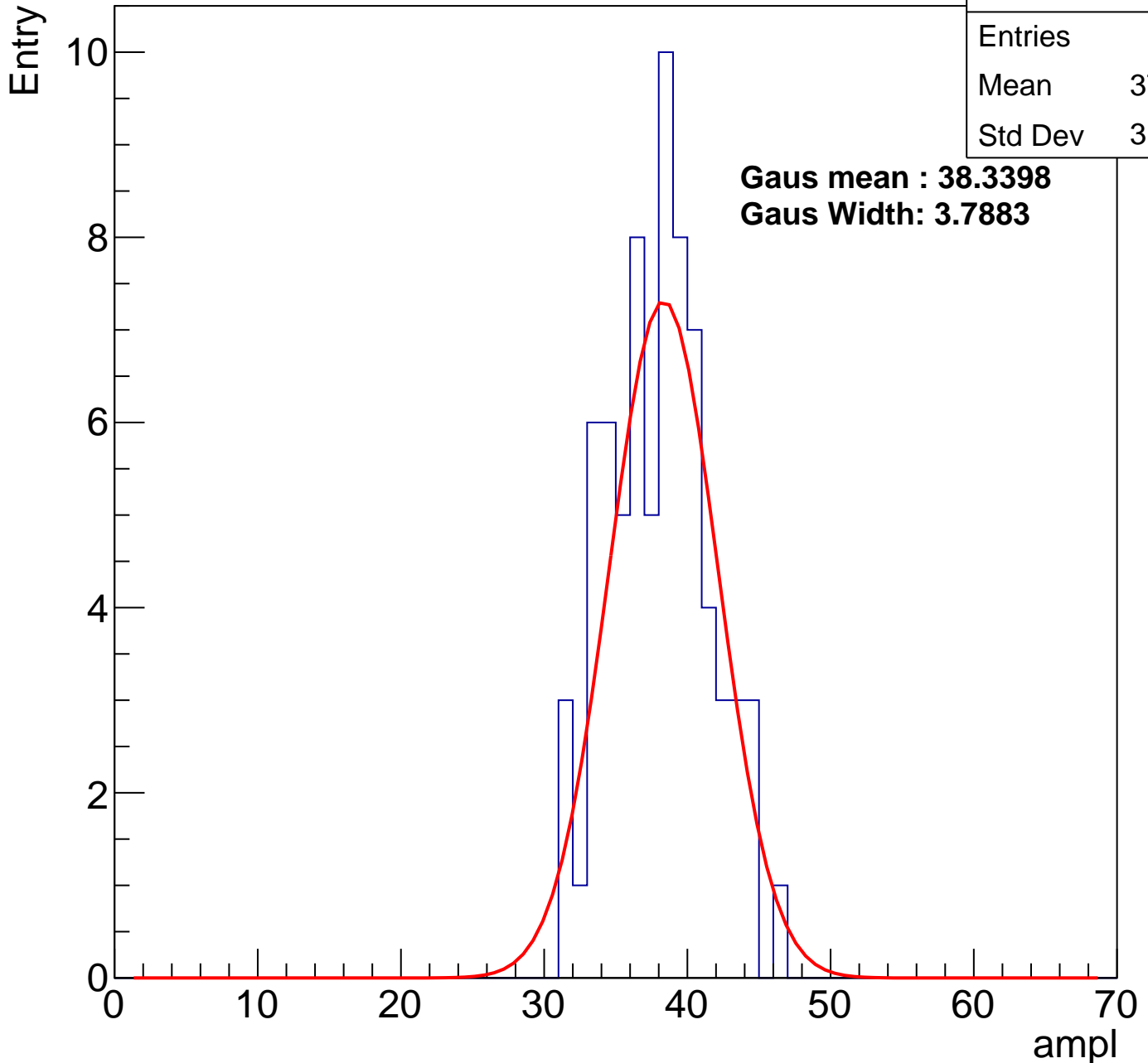
# B1L102S, U25-ch70, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	37.59
Std Dev	3.444

**Gaus mean : 38.3398**

**Gaus Width: 3.7883**



# B1L102S, U25-ch70, adc2

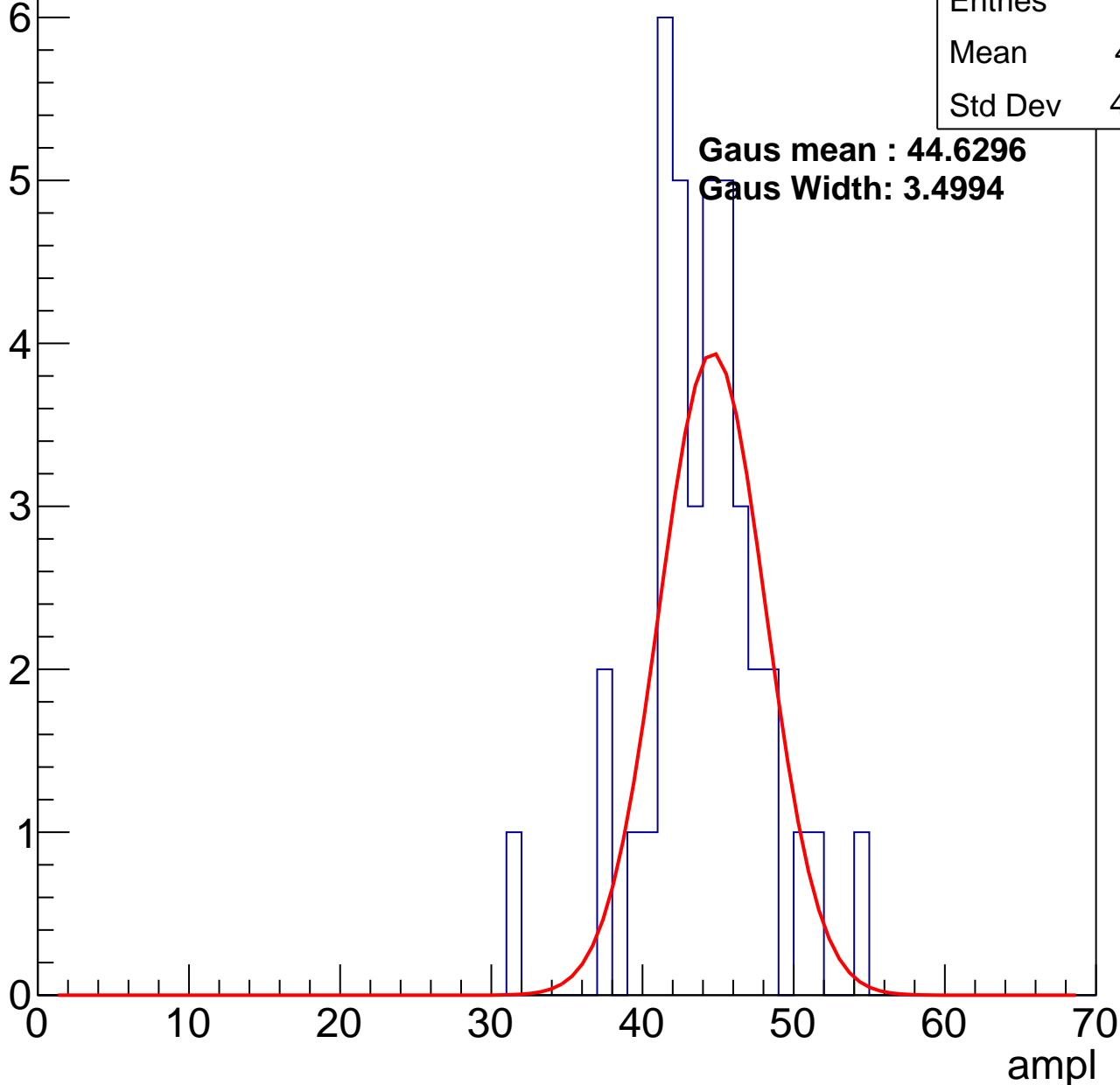
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	39
Mean	43.51
Std Dev	4.012

**Gaus mean : 44.6296**

**Gaus Width: 3.4994**

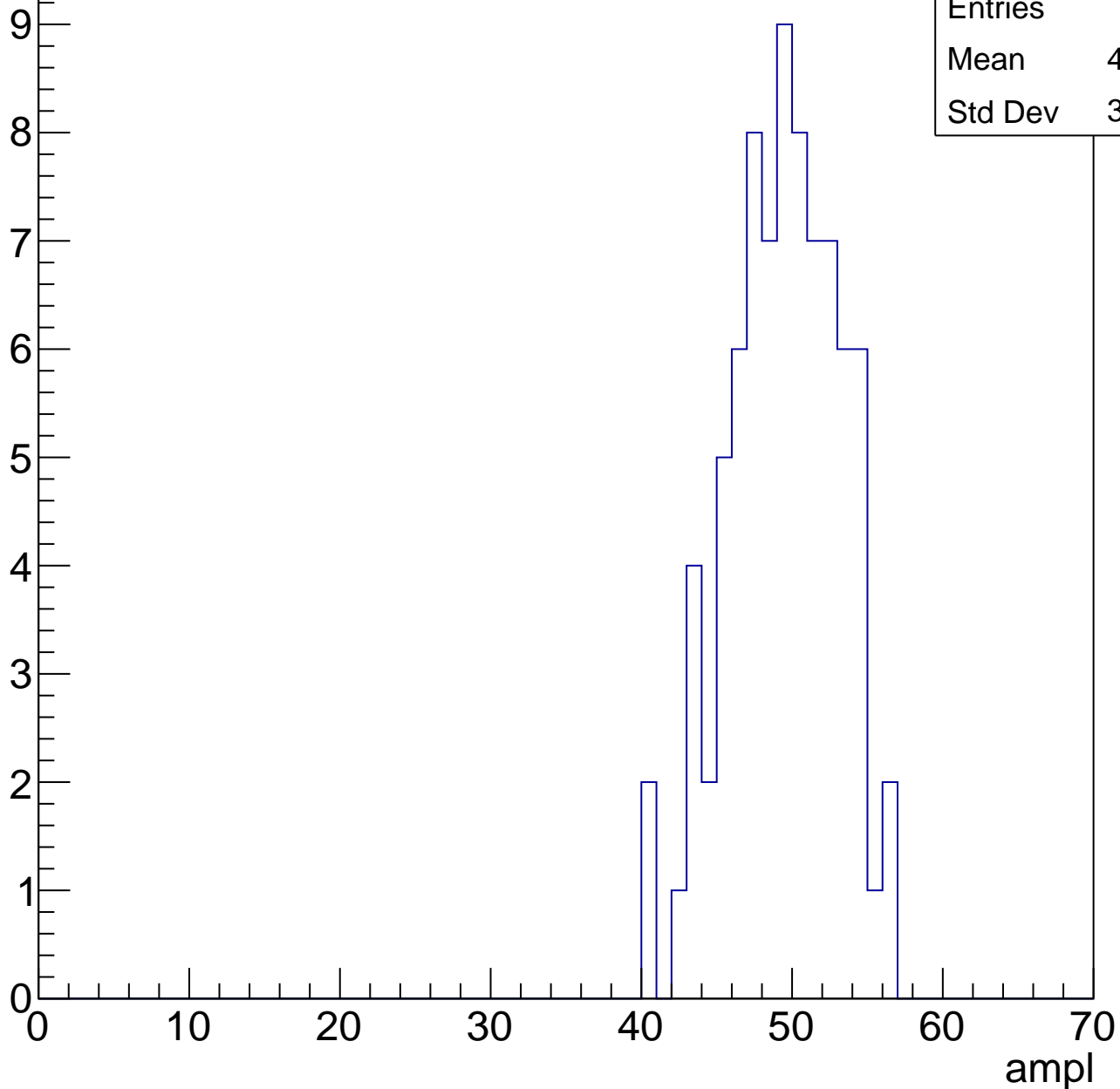


# B1L102S, U25-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	48.96
Std Dev	3.612

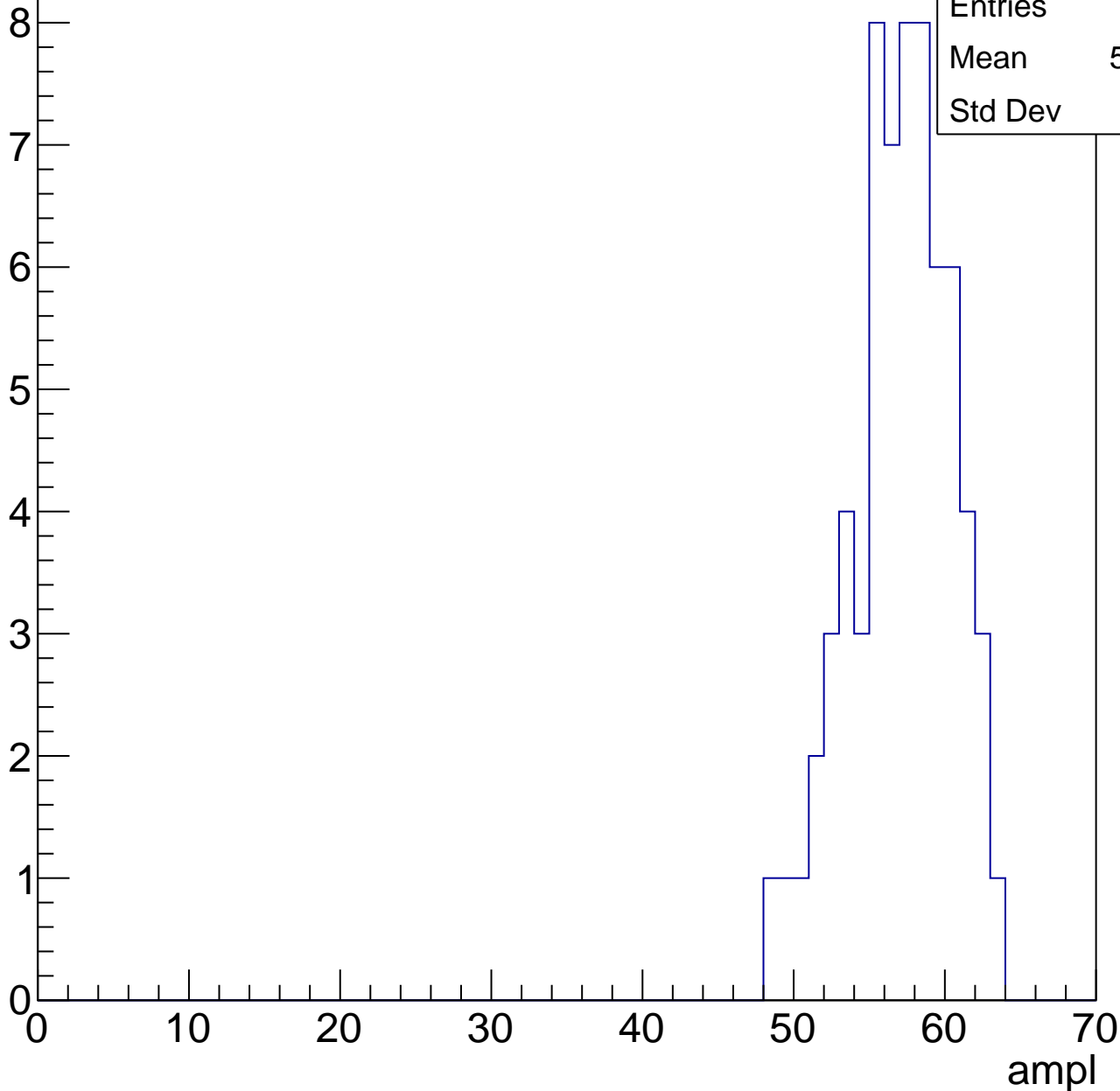


# B1L102S, U25-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	56.64
Std Dev	3.31



# B1L102S, U25-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	33
Mean	58.61
Std Dev	10.58

ampl

# B1L102S, U25-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

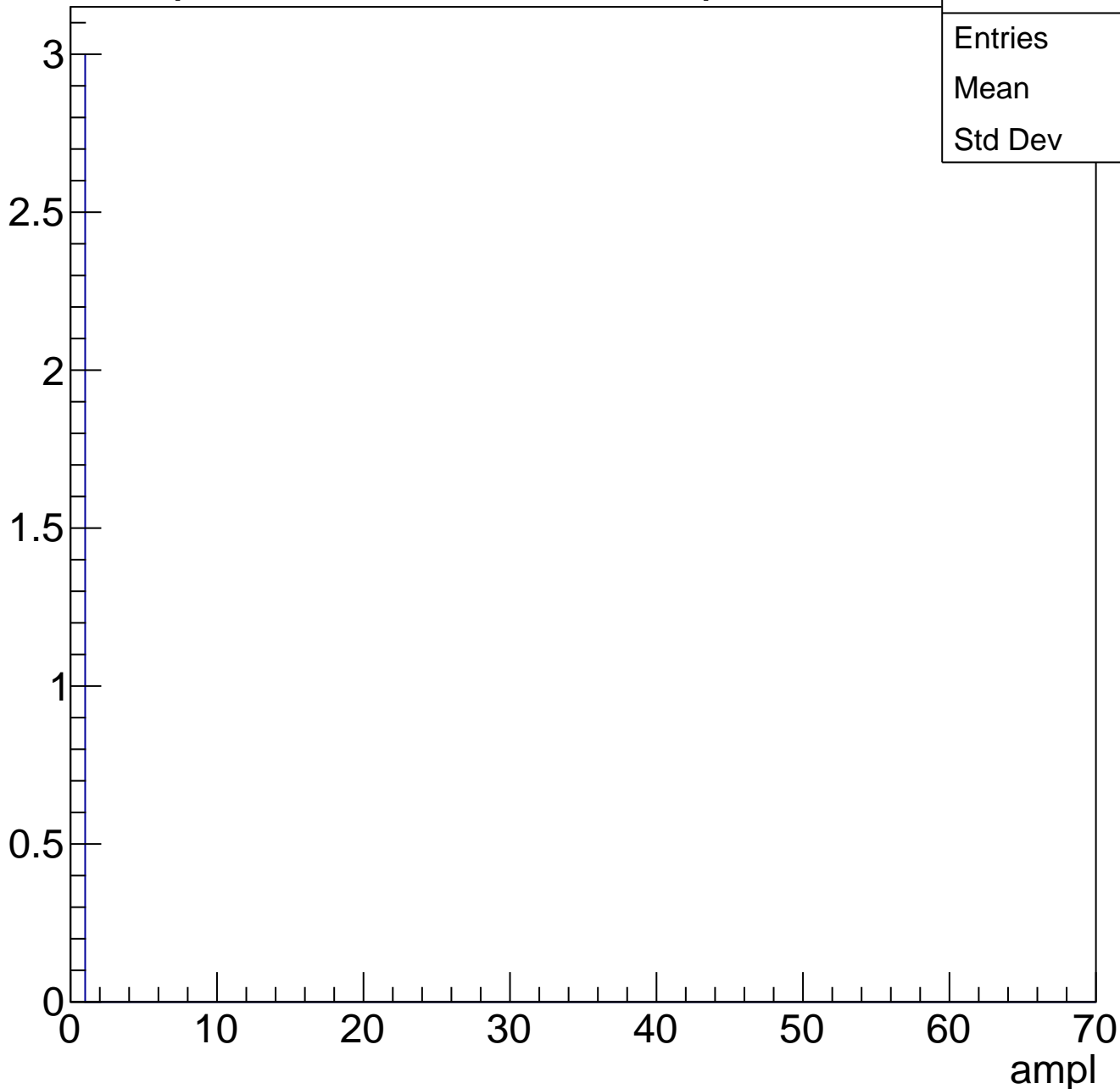
ampl



# B1L102S, U25-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L102S, U25-ch71, adc0

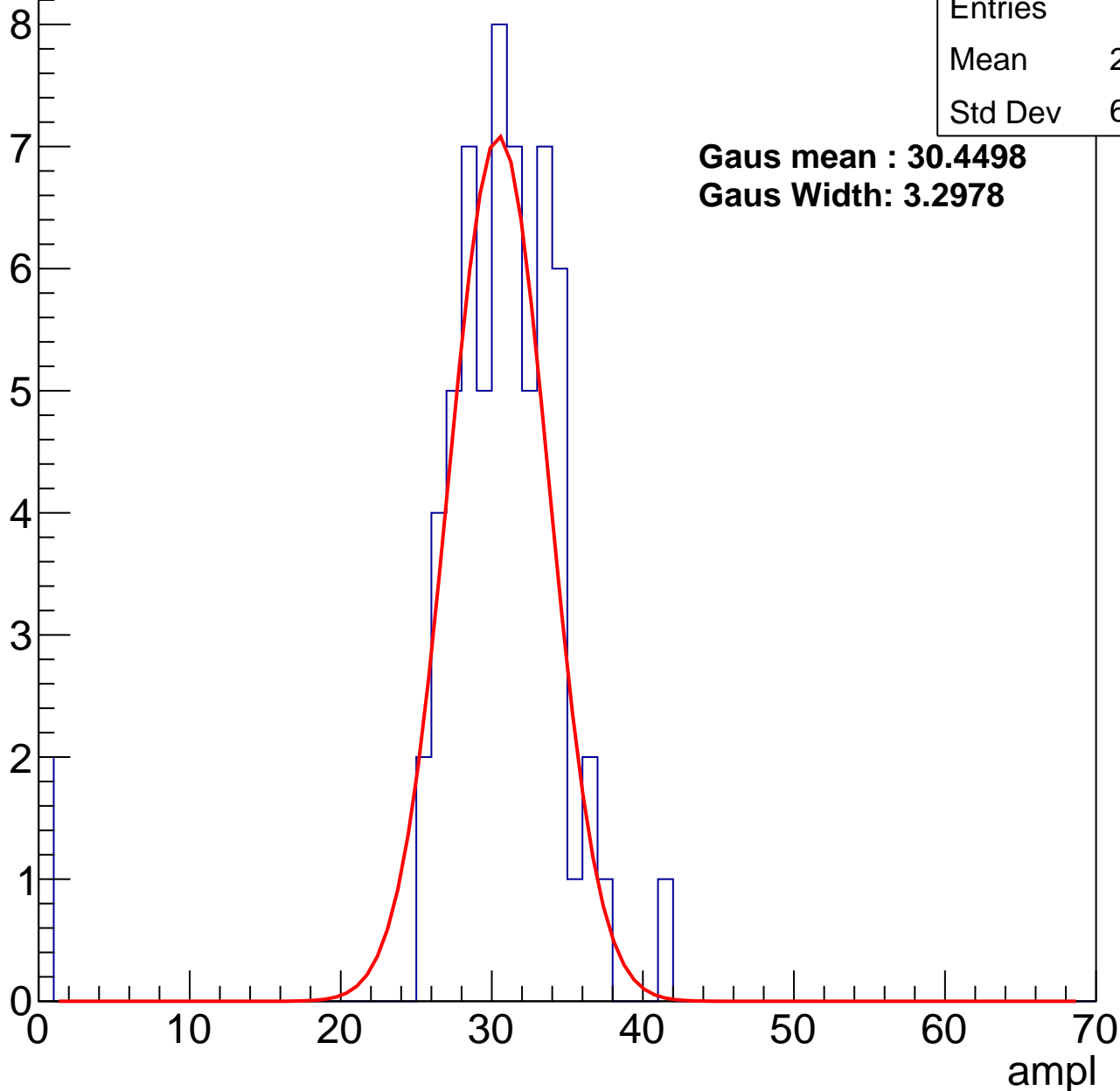
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.63
Std Dev	6.219

**Gaus mean : 30.4498**

**Gaus Width: 3.2978**



# B1L102S, U25-ch71, adc1

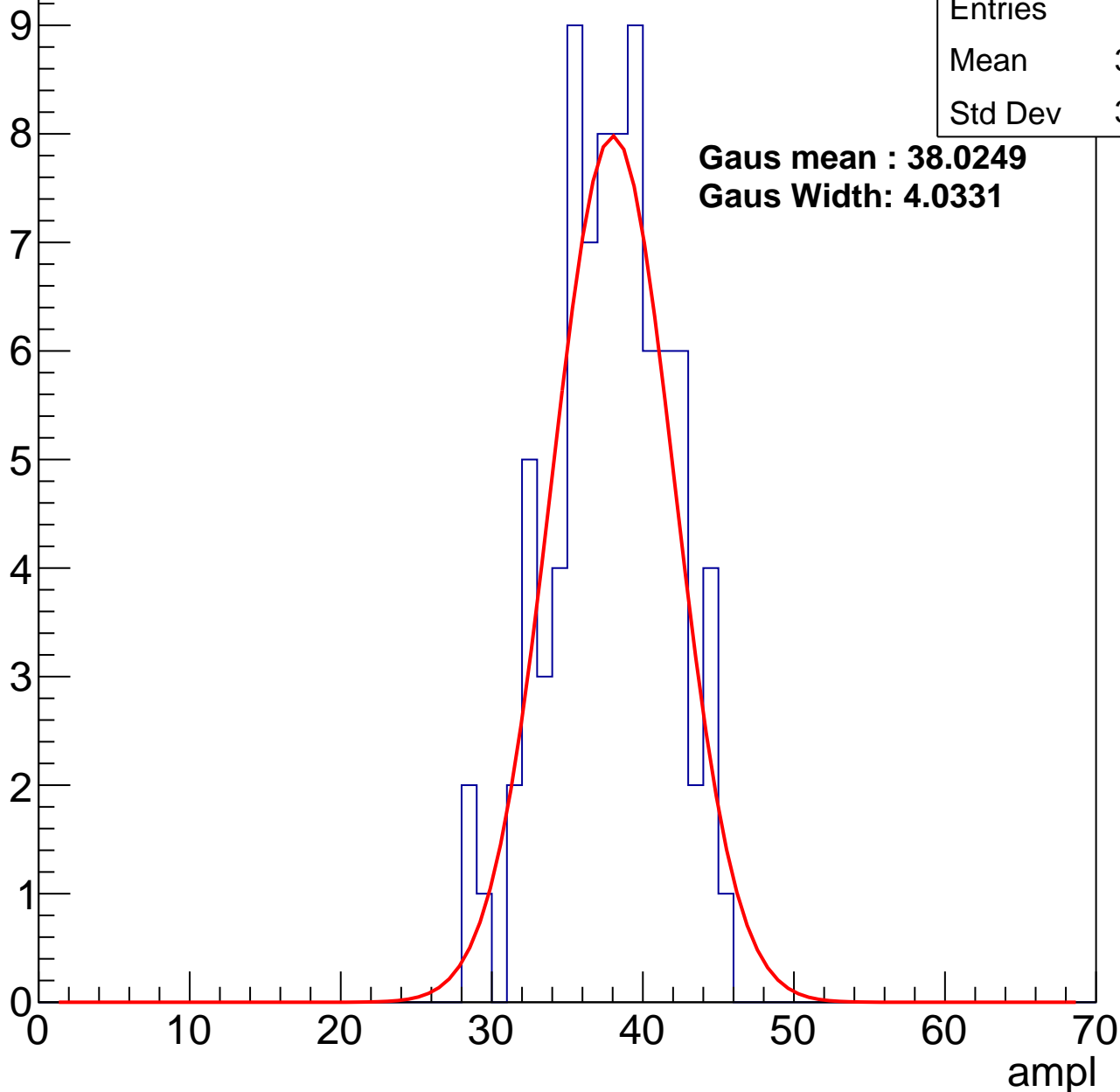
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	37.41
Std Dev	3.821

**Gaus mean : 38.0249**

**Gaus Width: 4.0331**



# B1L102S, U25-ch71, adc2

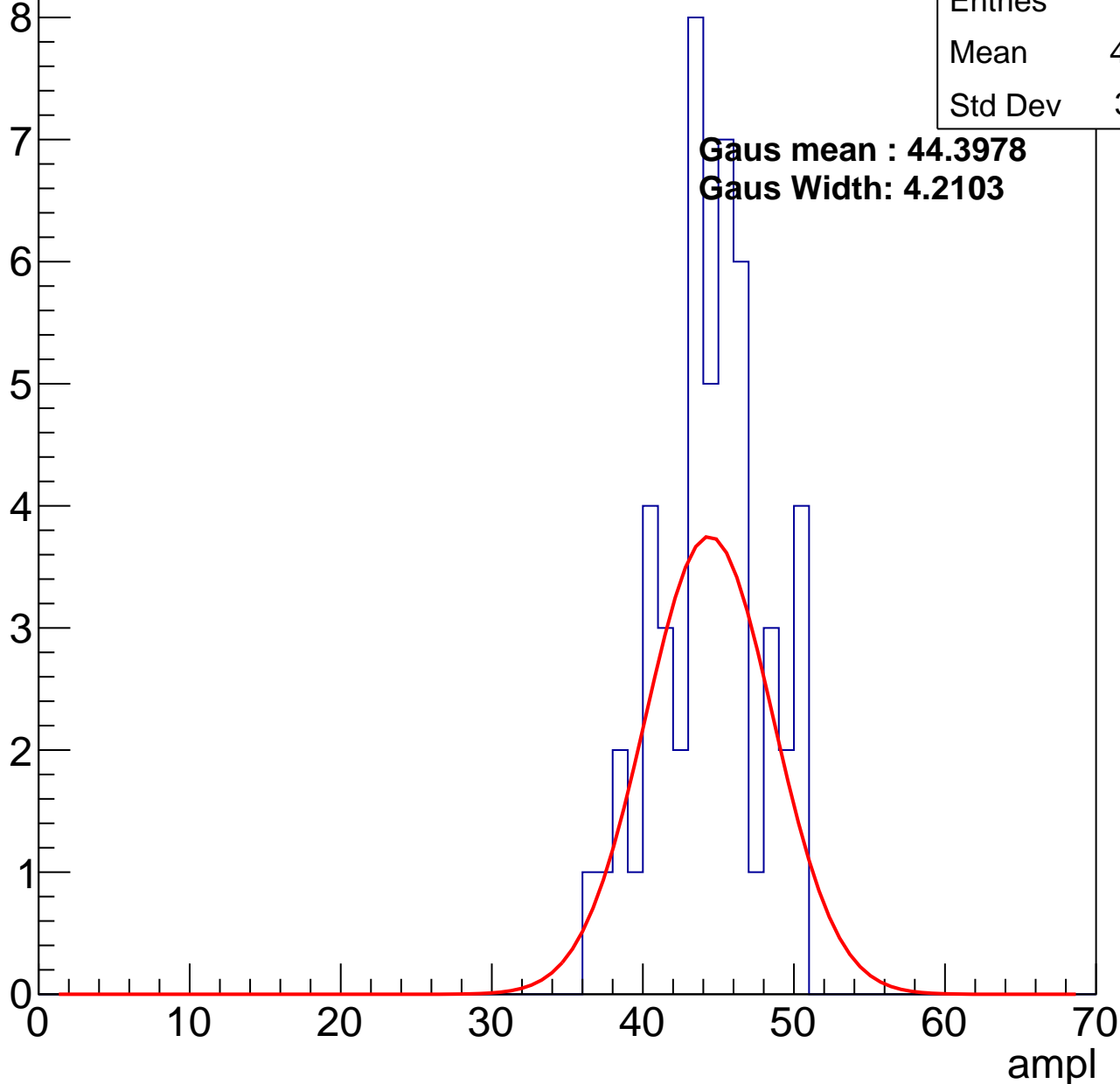
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.98
Std Dev	3.461

**Gaus mean : 44.3978**

**Gaus Width: 4.2103**

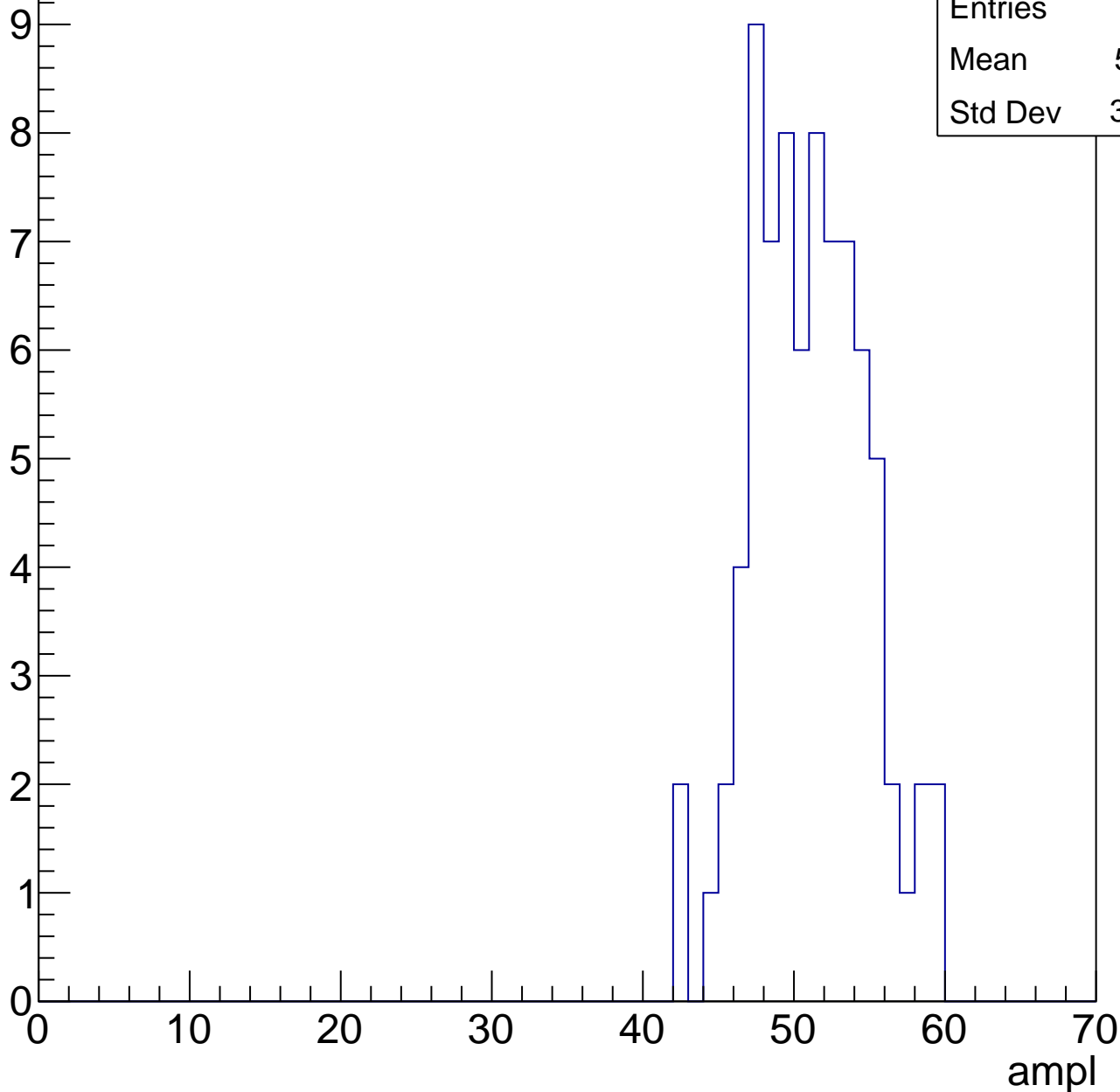


# B1L102S, U25-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50.61
Std Dev	3.726

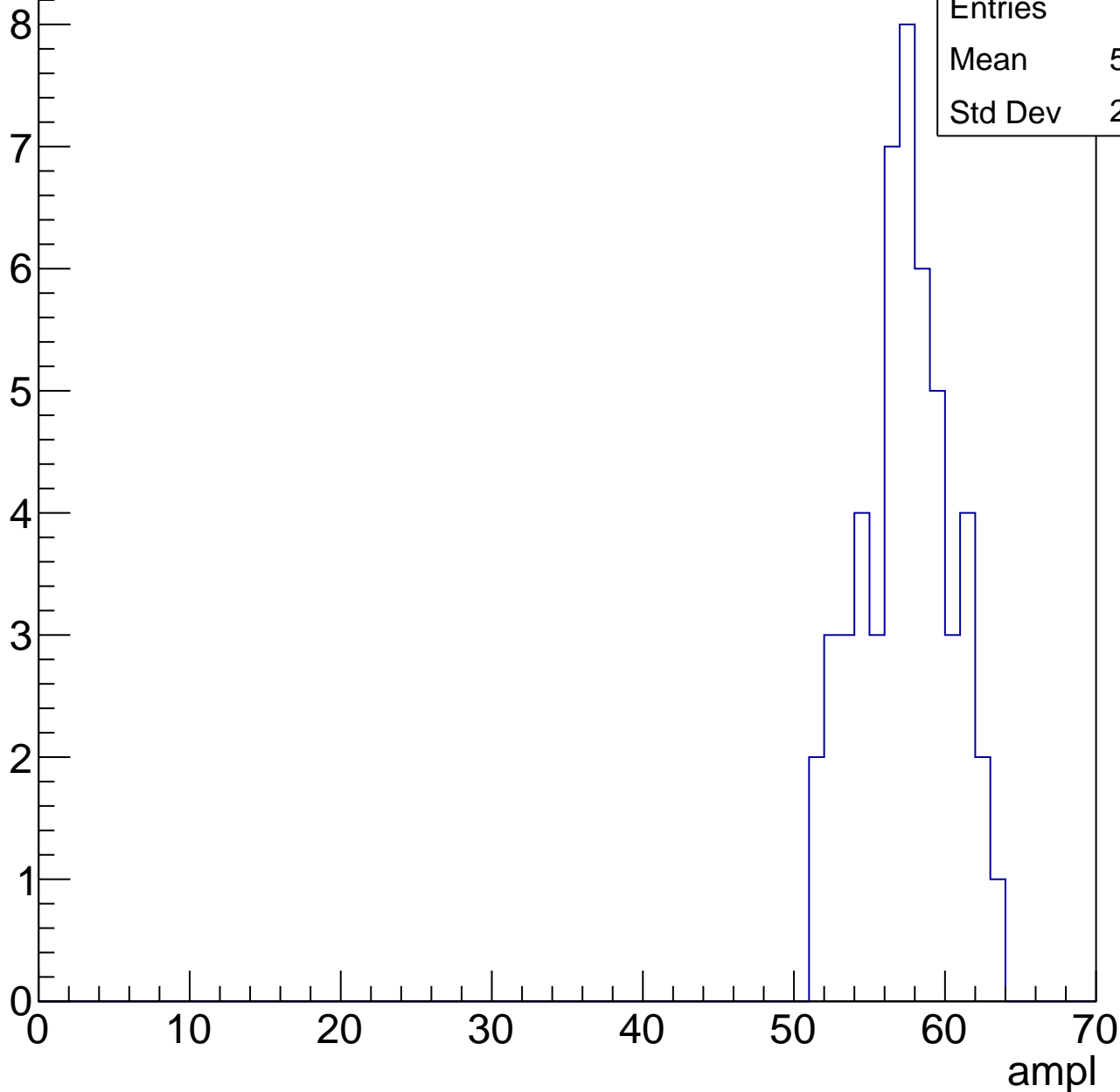


# B1L102S, U25-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.86
Std Dev	2.977

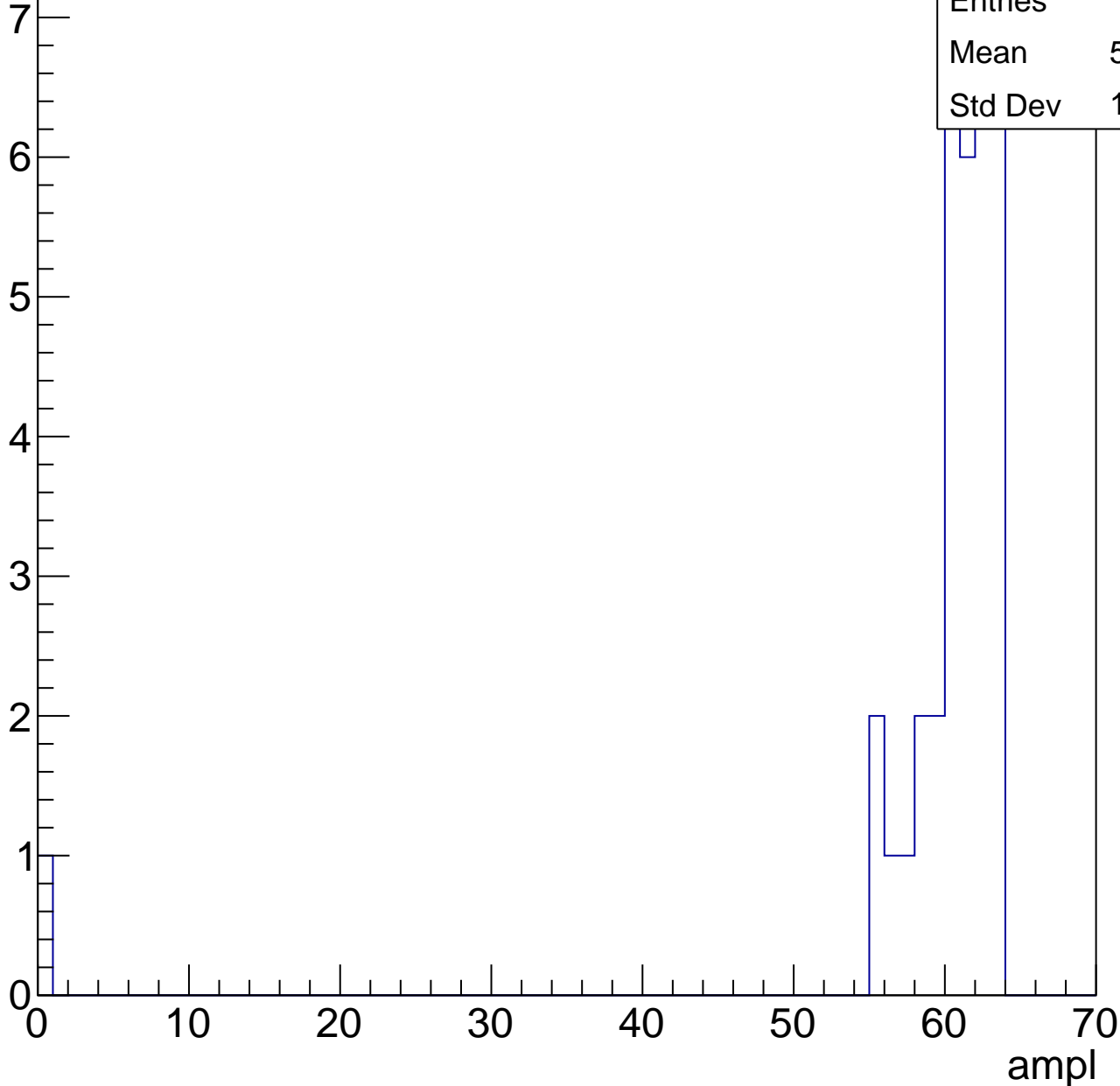


# B1L102S, U25-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.83
Std Dev	10.18



# B1L102S, U25-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B1L102S, U25-ch72, adc0

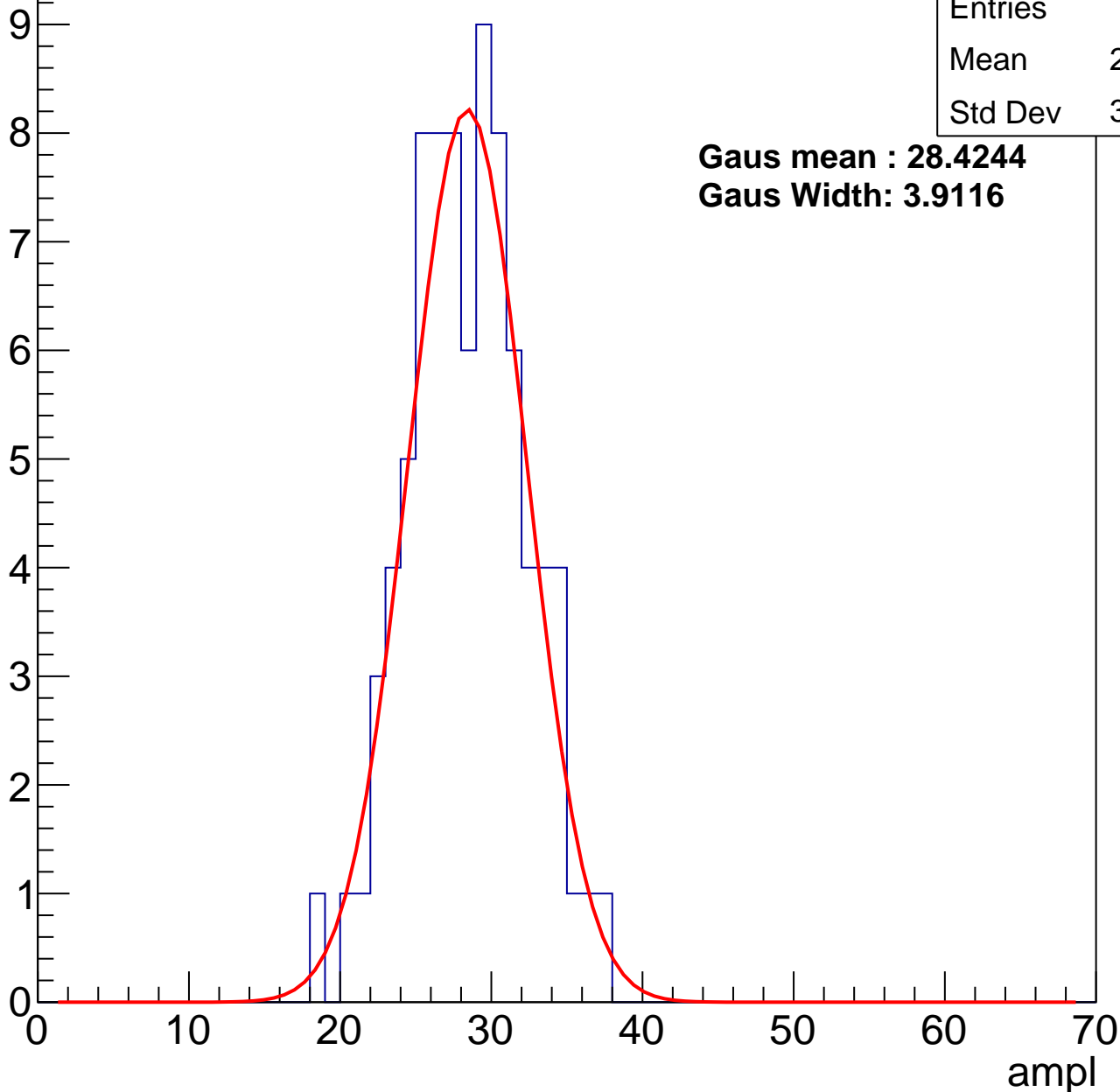
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.95
Std Dev	3.818

**Gaus mean : 28.4244**

**Gaus Width: 3.9116**



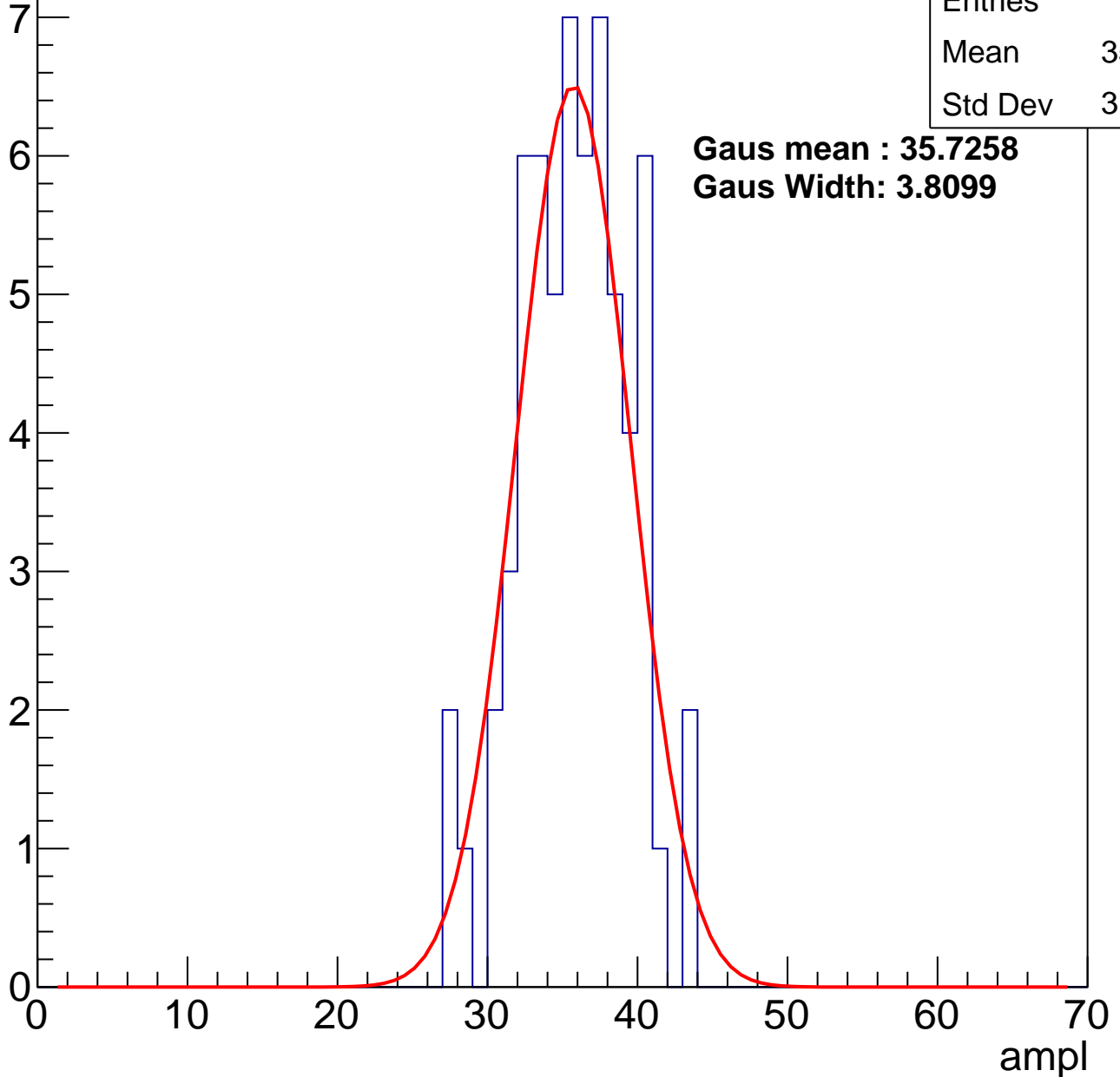
# B1L102S, U25-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.37
Std Dev	3.578

**Gaus mean : 35.7258**  
**Gaus Width: 3.8099**



# B1L102S, U25-ch72, adc2

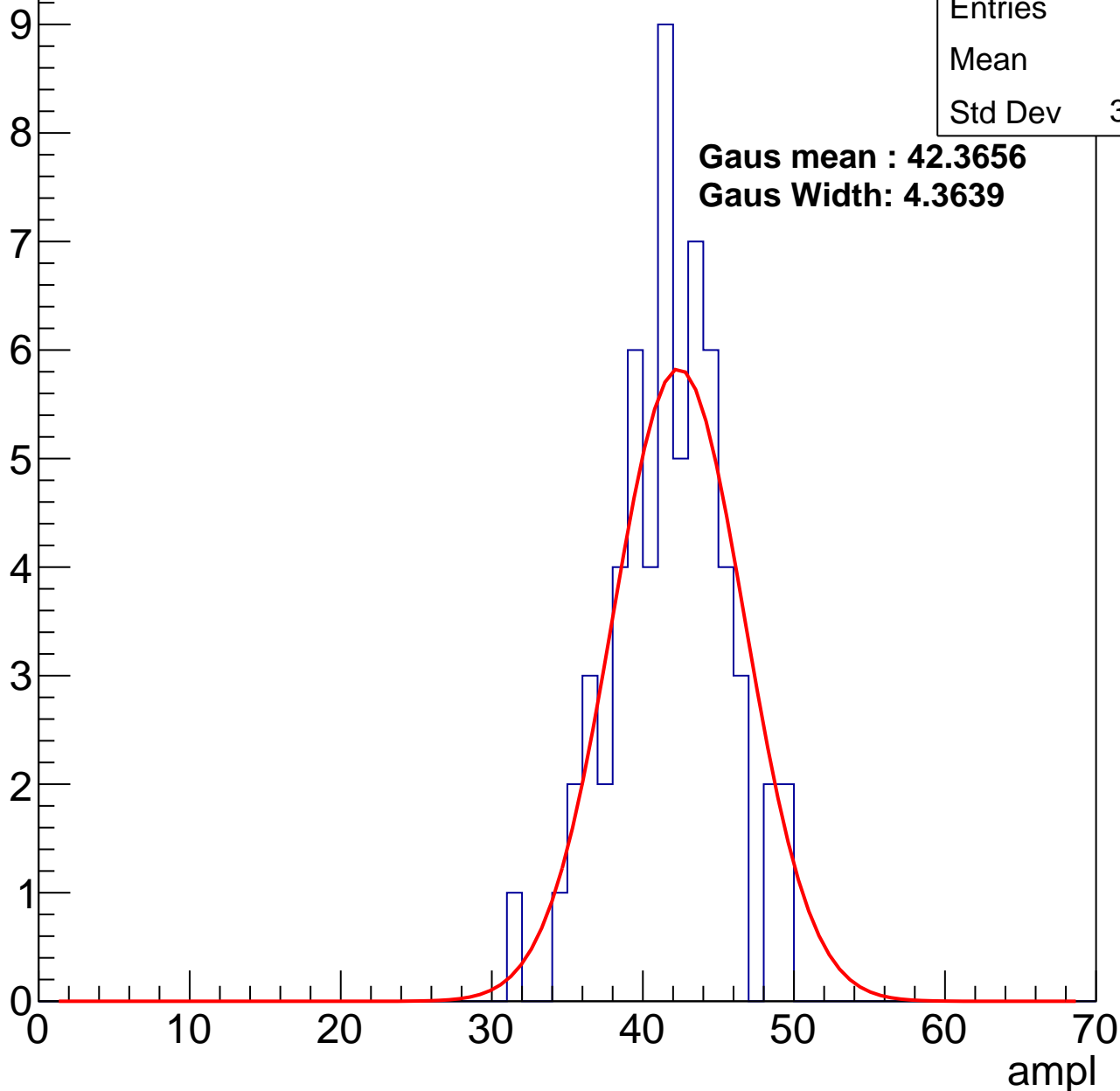
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.3
Std Dev	3.708

**Gaus mean : 42.3656**

**Gaus Width: 4.3639**



# B1L102S, U25-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	80
Mean	48.71
Std Dev	4.154

Entry

10

8

6

4

2

0

0

10

20

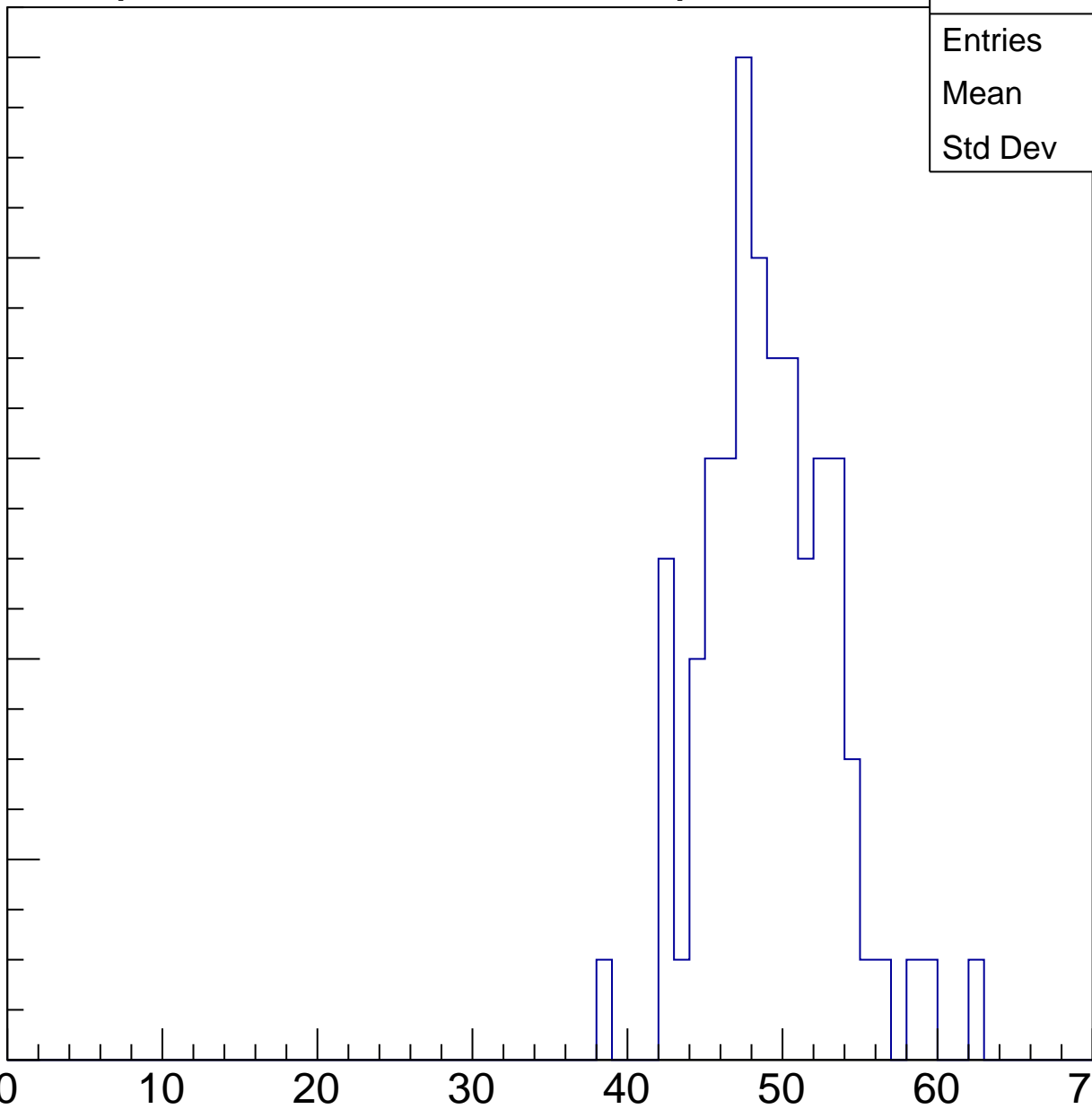
30

40

50

60

ampl

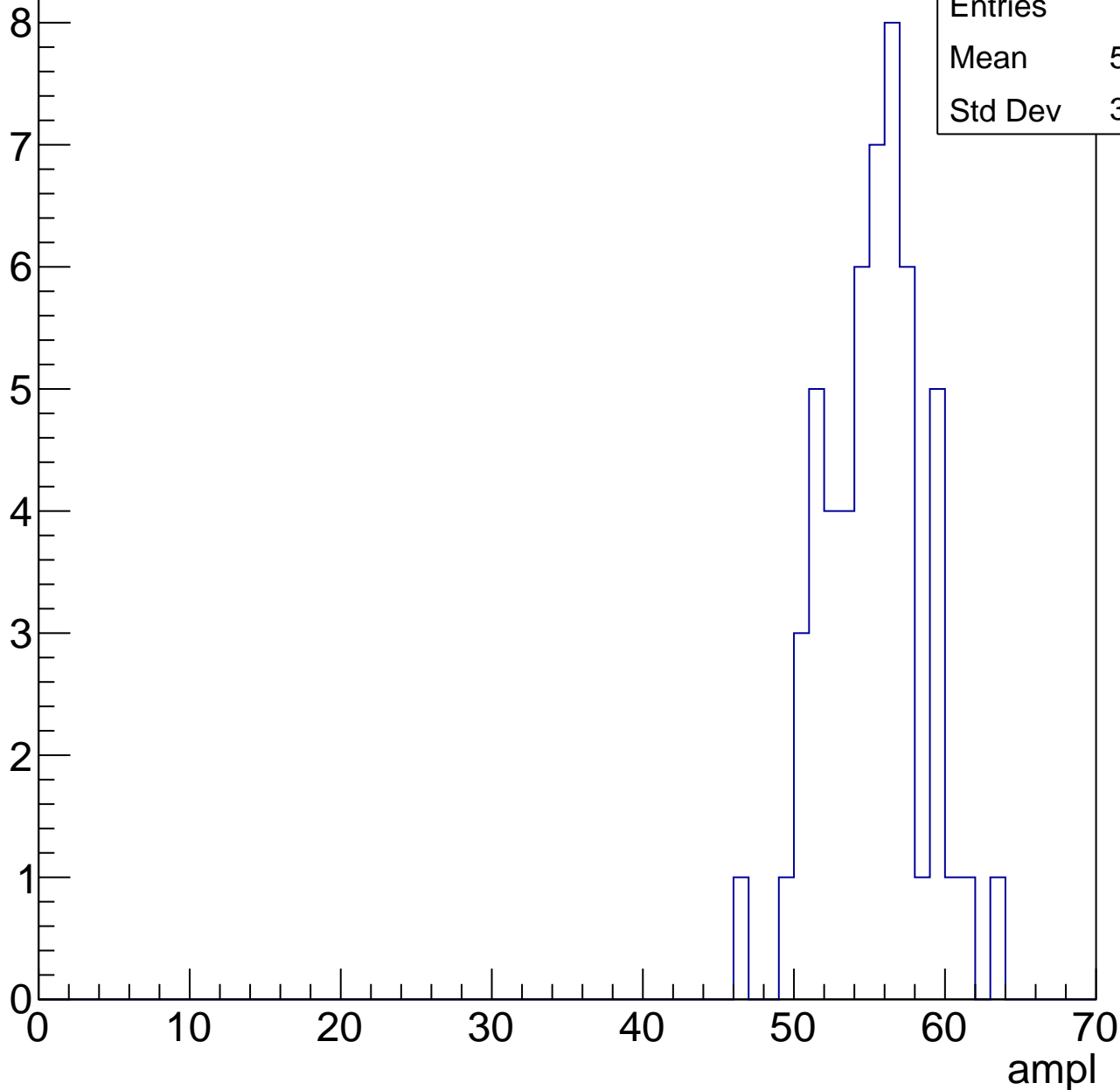


# B1L102S, U25-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

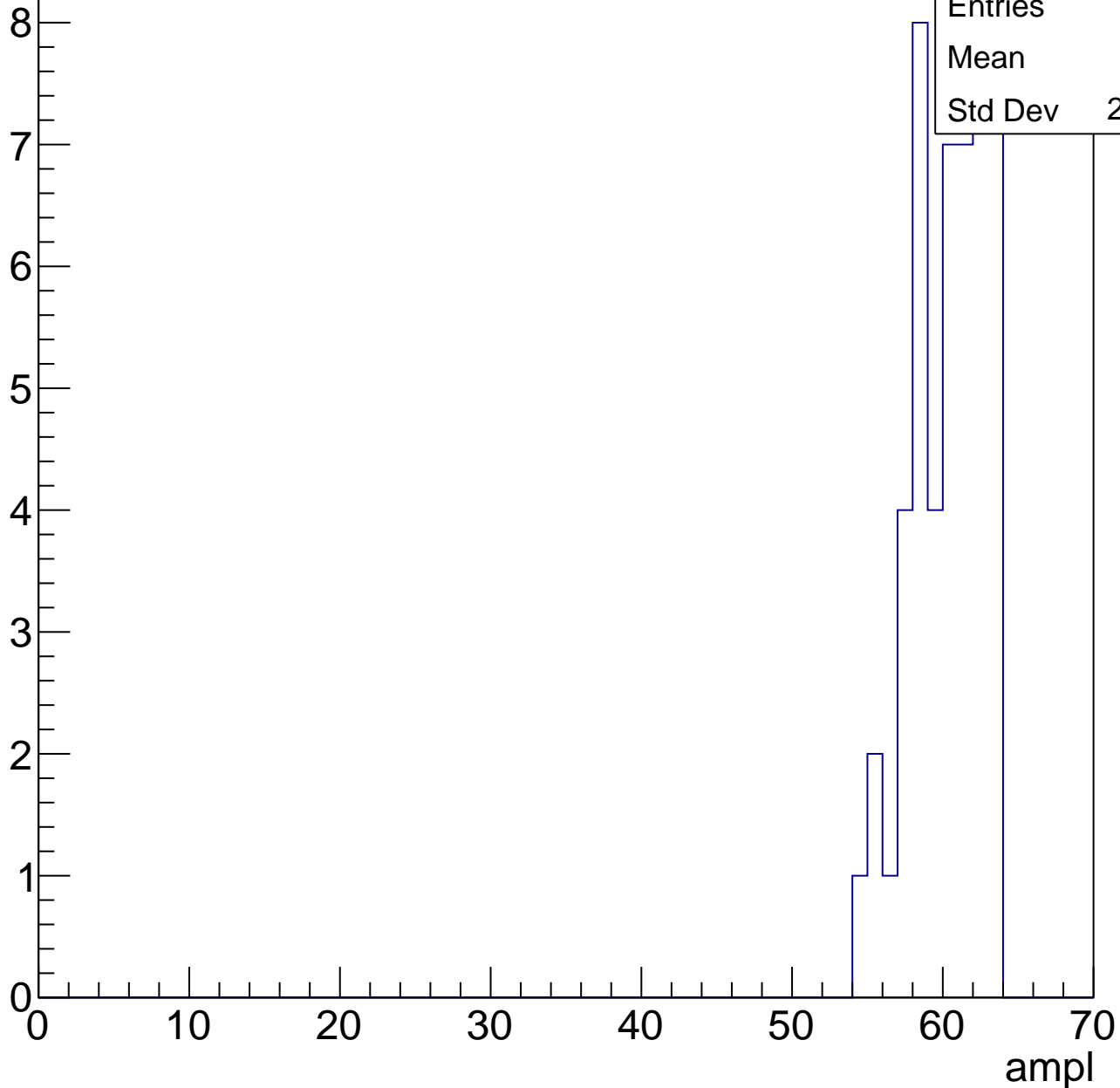
Entries	54
Mean	54.74
Std Dev	3.256



# B1L102S, U25-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

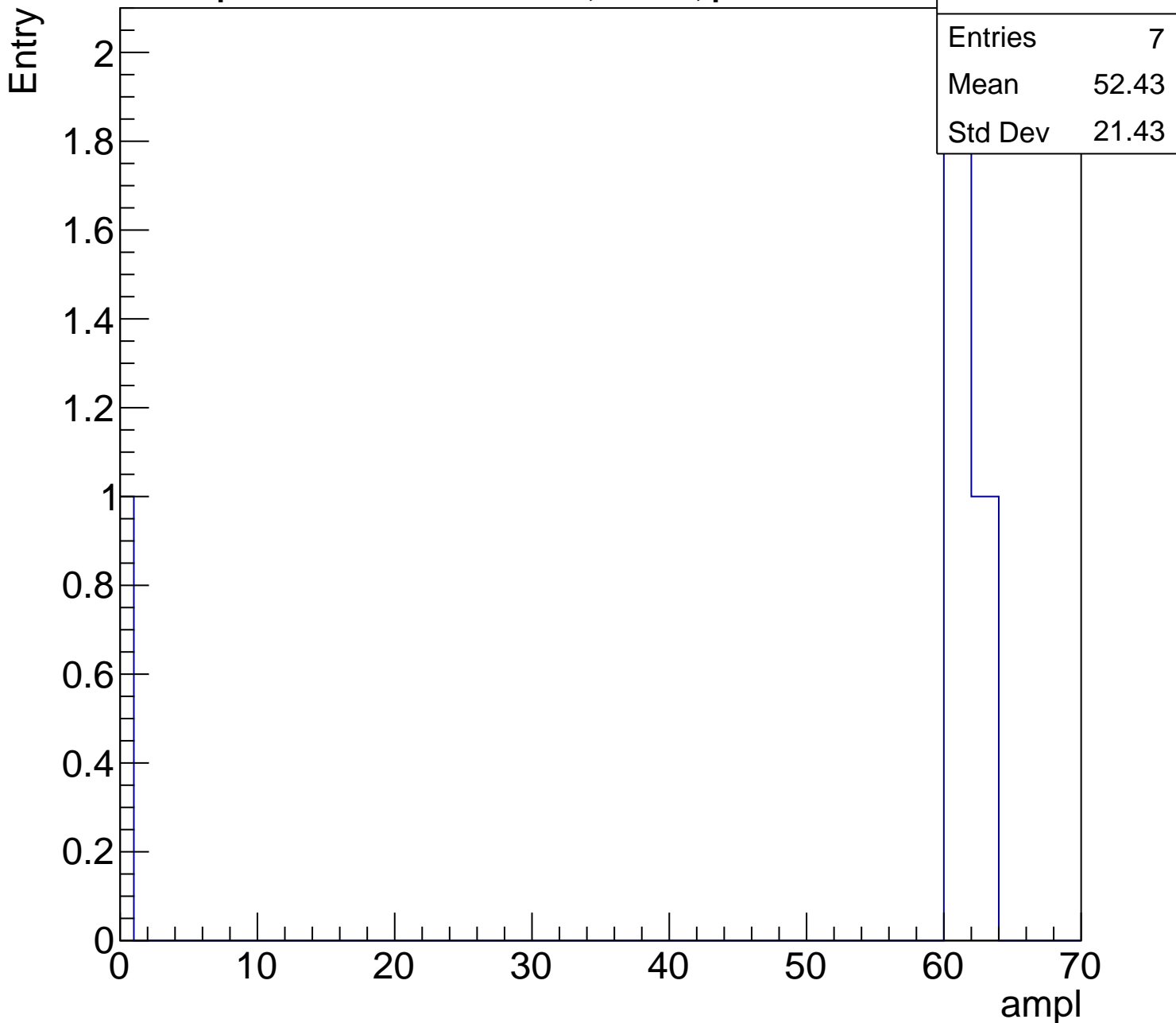
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.43
Std Dev	21.43

0 10 20 30 40 50 60 70

ampl





# B1L102S, U25-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch73, adc0

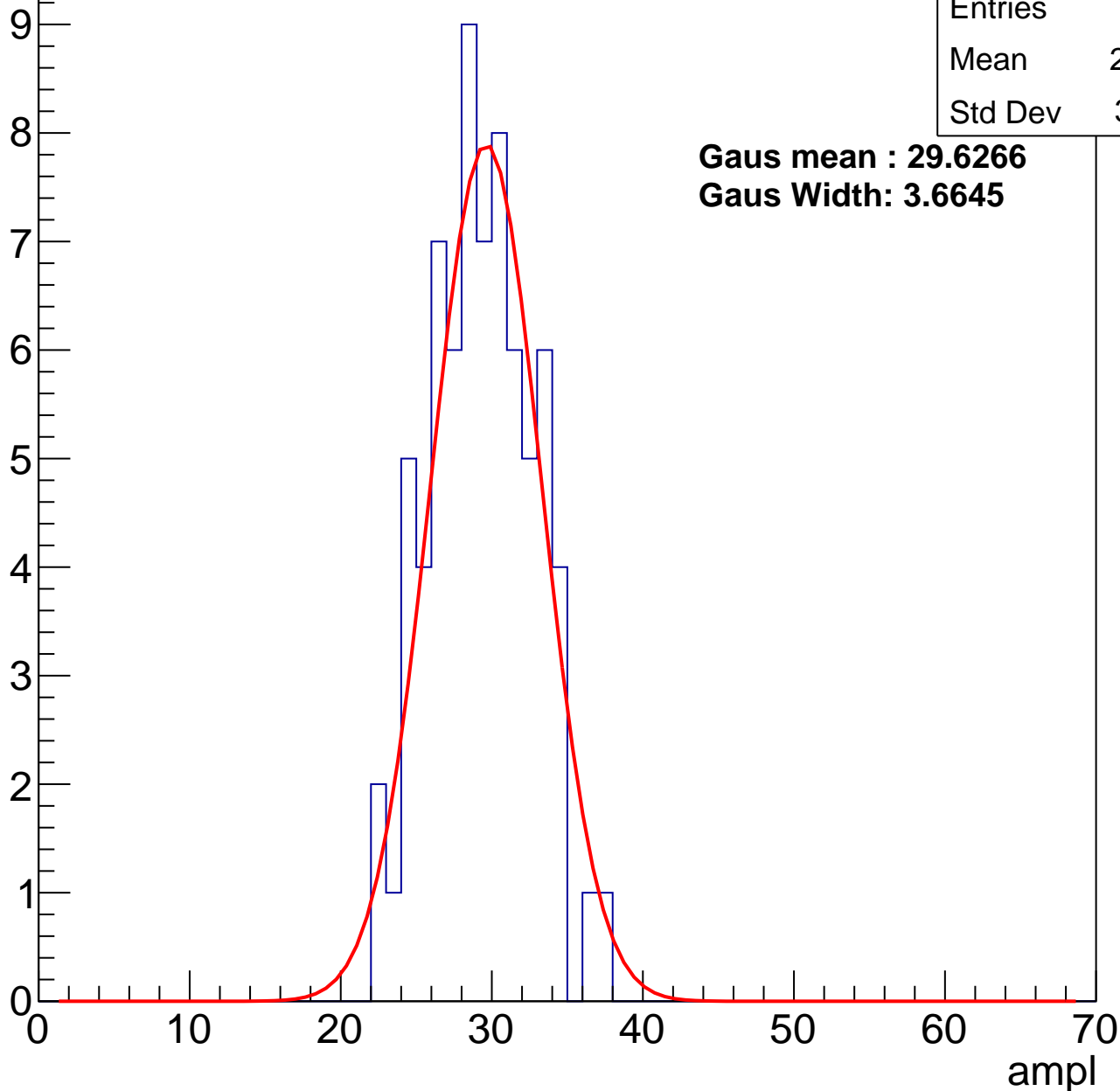
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.88
Std Dev	3.341

**Gaus mean : 29.6266**

**Gaus Width: 3.6645**



# B1L102S, U25-ch73, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	85
Mean	36.99
Std Dev	3.553

**Gaus mean : 37.5197**

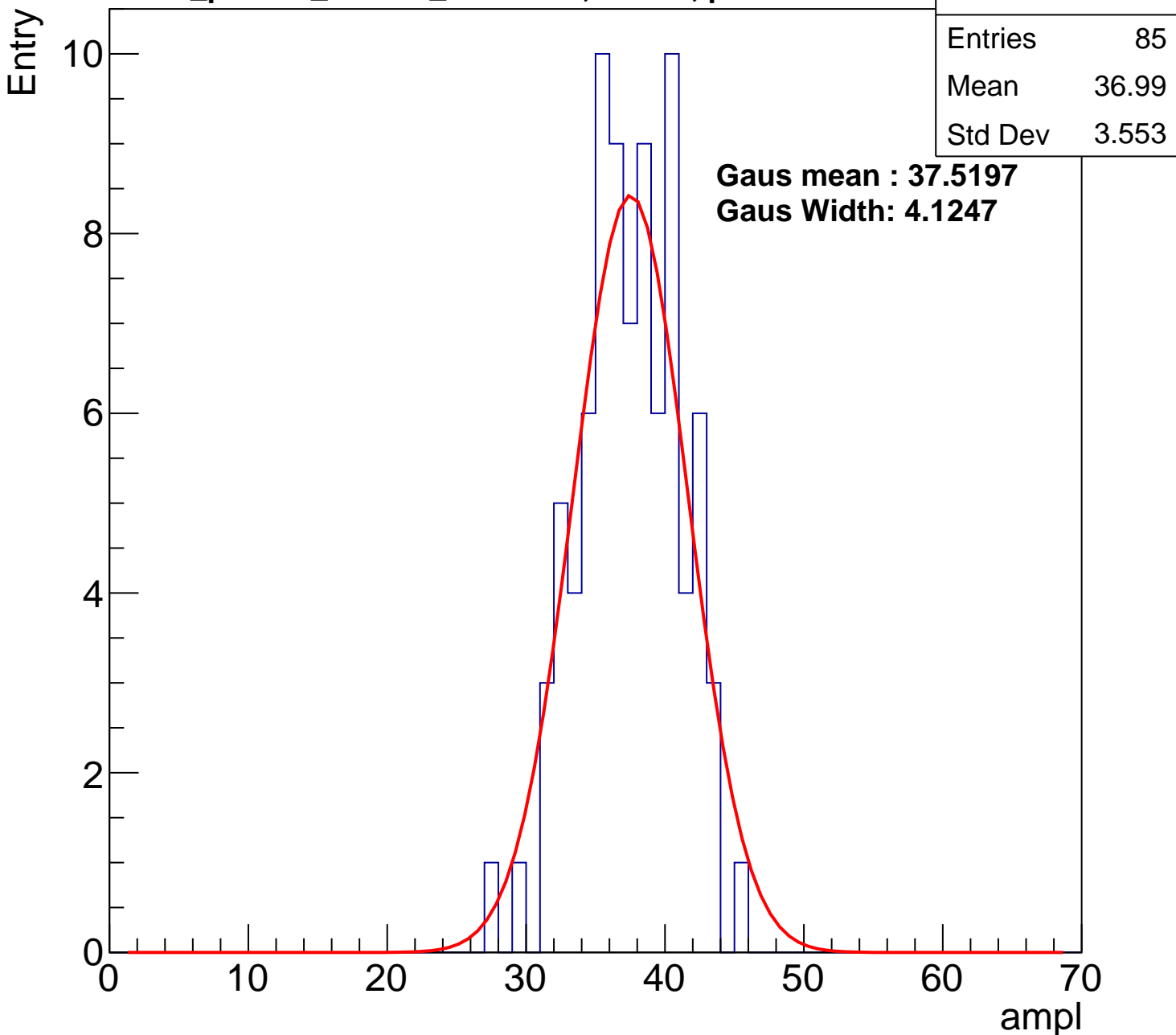
**Gaus Width: 4.1247**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L102S, U25-ch73, adc2

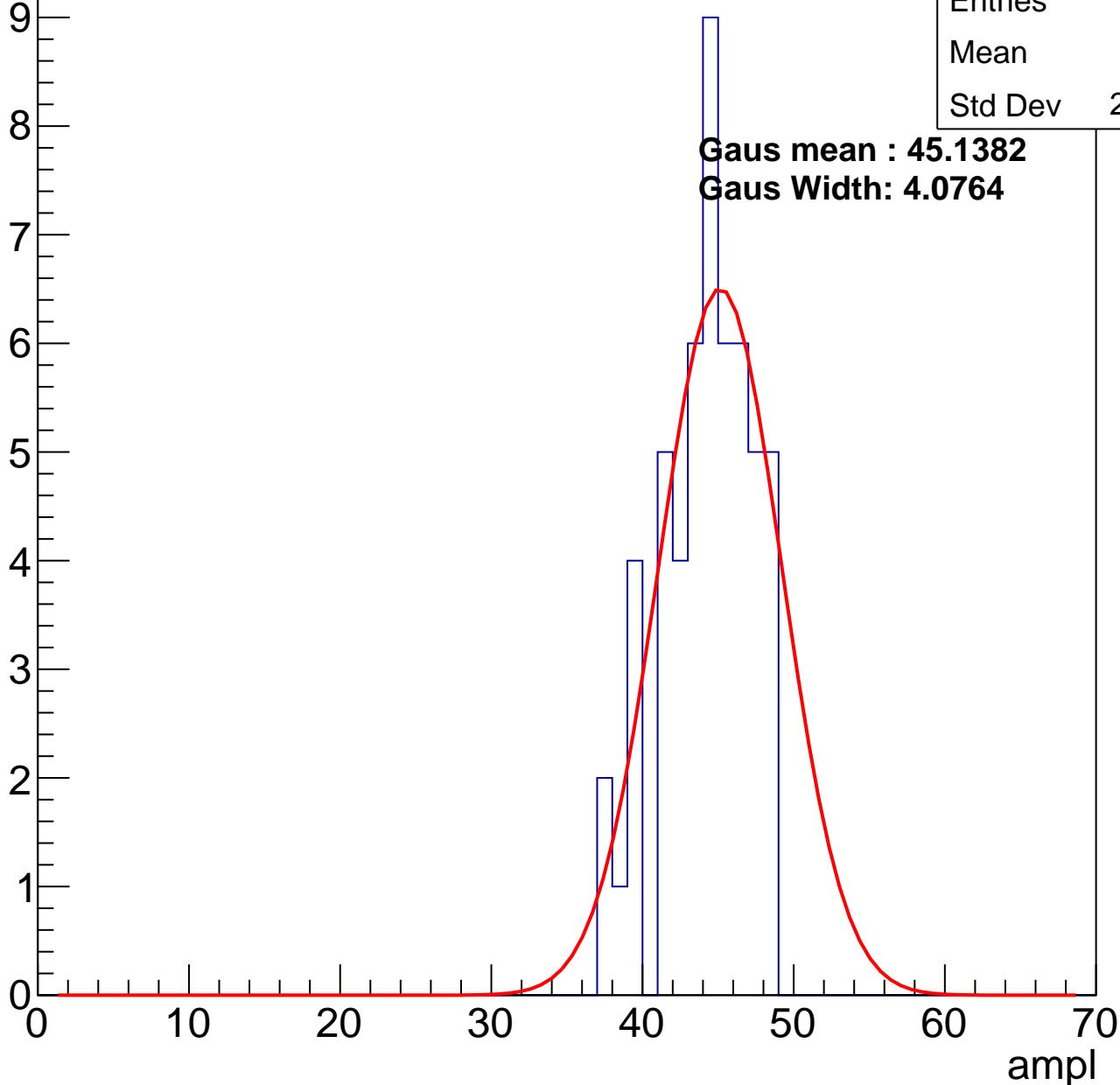
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.7
Std Dev	2.918

**Gaus mean : 45.1382**

**Gaus Width: 4.0764**

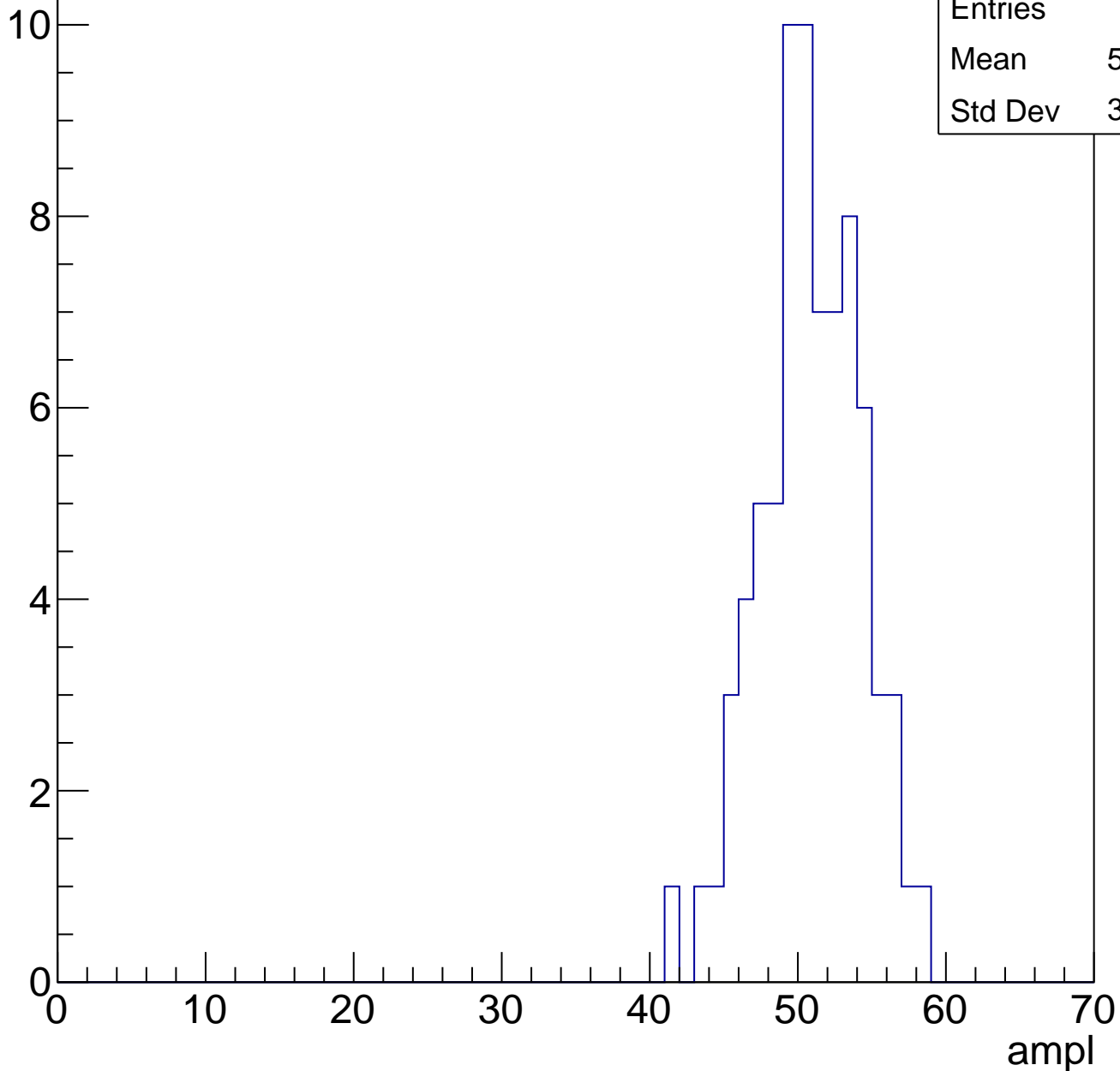


# B1L102S, U25-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	76
Mean	50.38
Std Dev	3.379

Entry

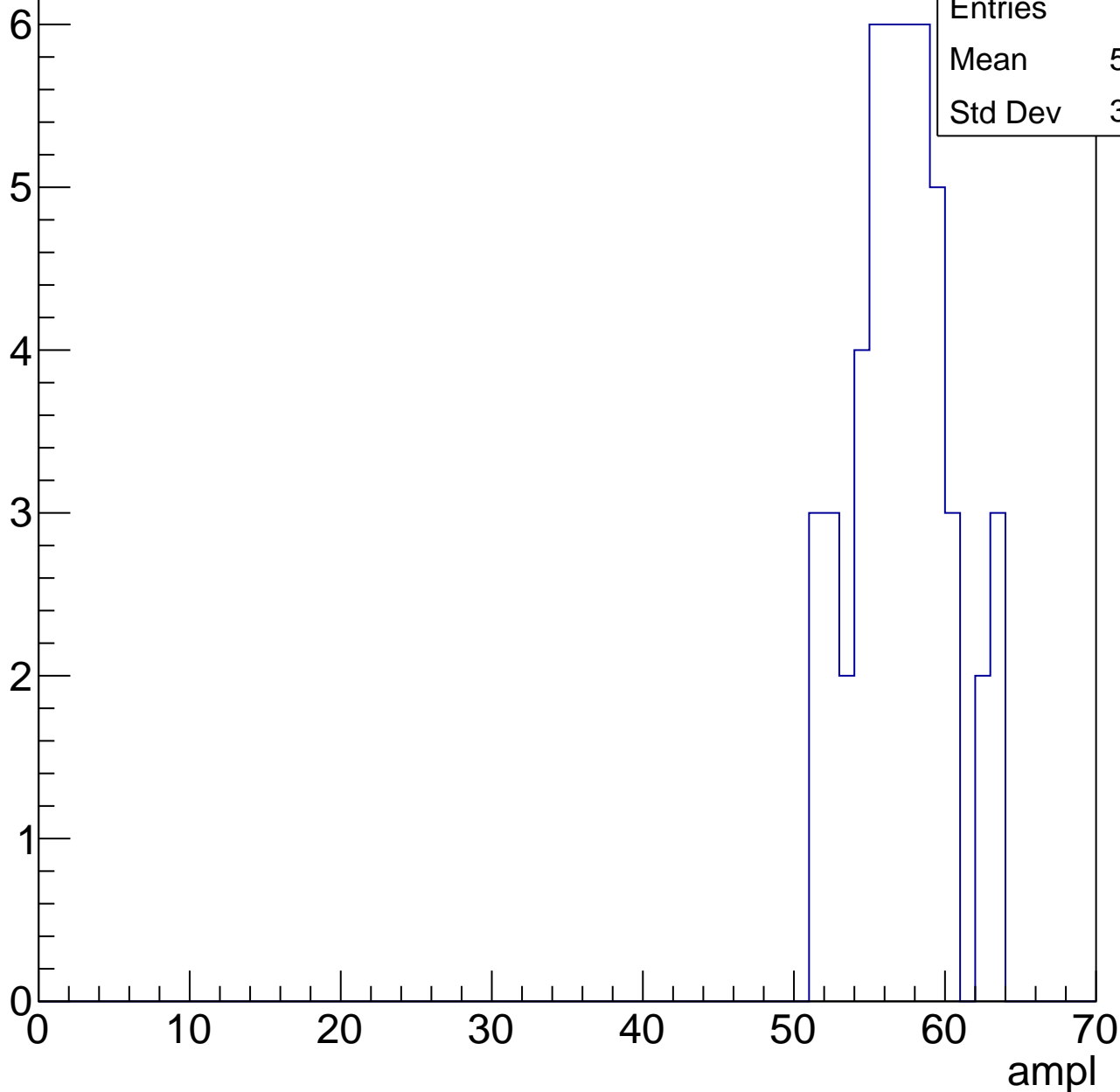


# B1L102S, U25-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	56.63
Std Dev	3.147

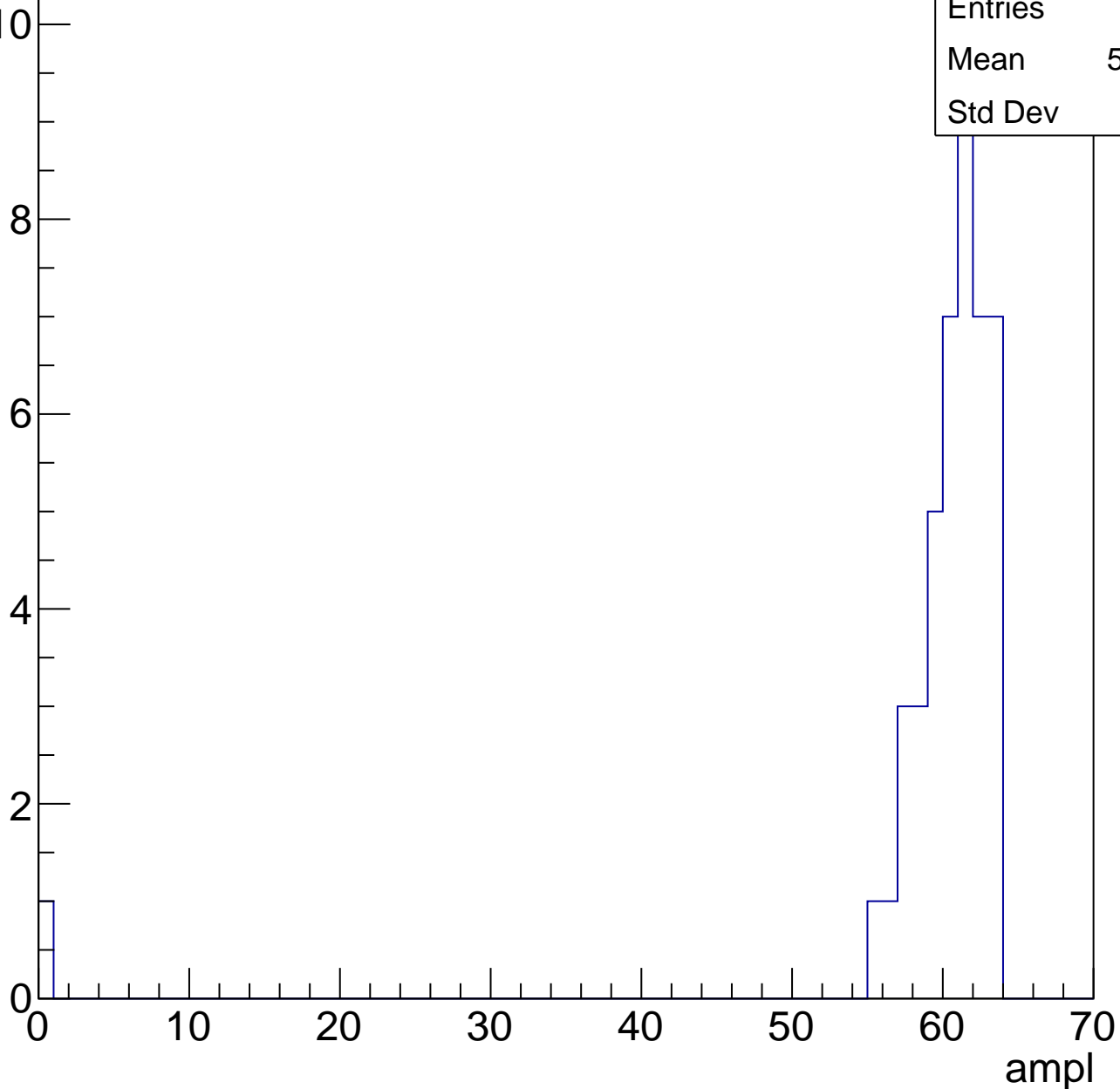


# B1L102S, U25-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.02
Std Dev	9.12



# B1L102S, U25-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch74, adc0

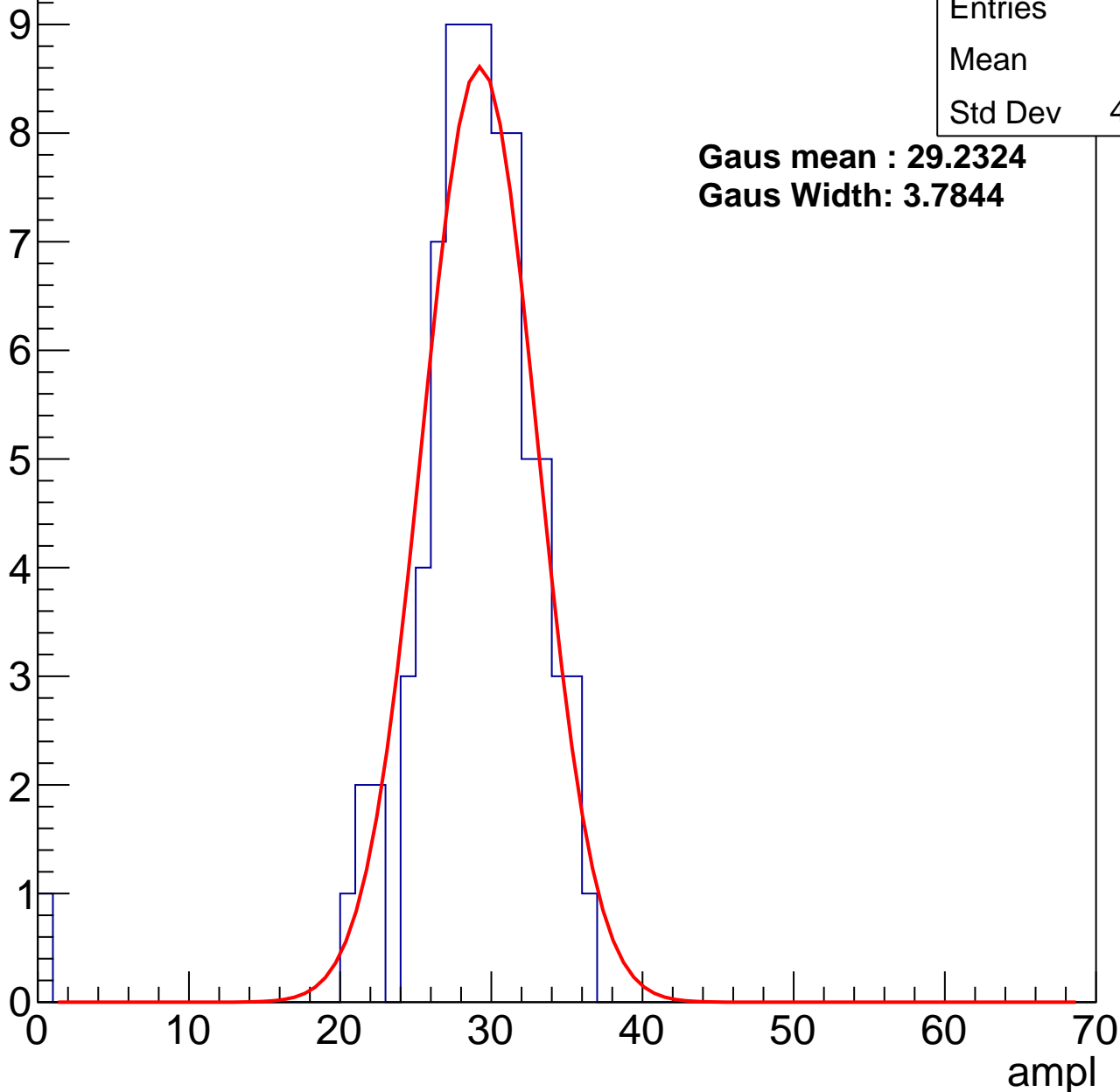
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.4
Std Dev	4.695

**Gaus mean : 29.2324**

**Gaus Width: 3.7844**



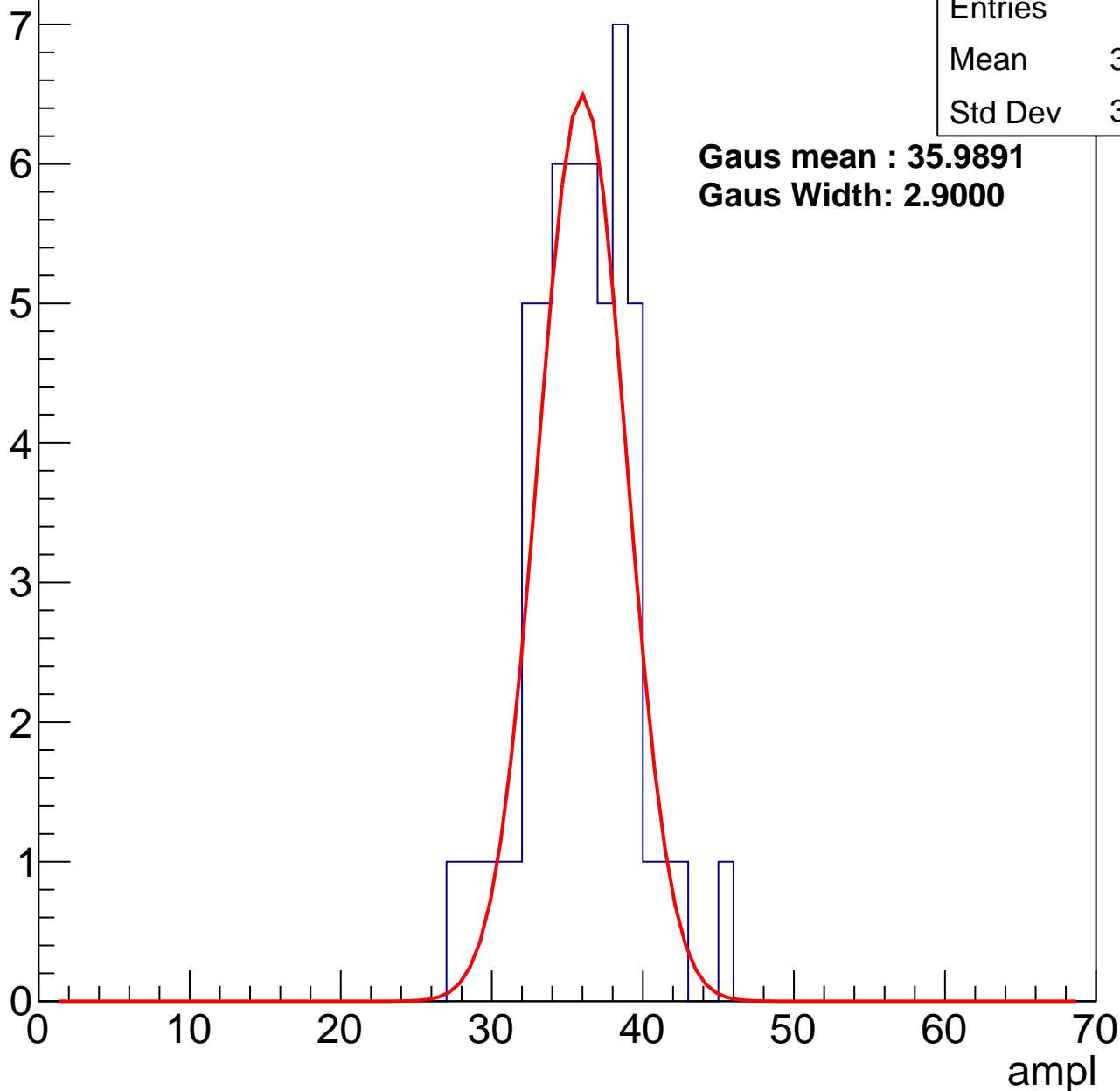
# B1L102S, U25-ch74, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	35.44
Std Dev	3.414

**Gaus mean : 35.9891**  
**Gaus Width: 2.9000**



# B1L102S, U25-ch74, adc2

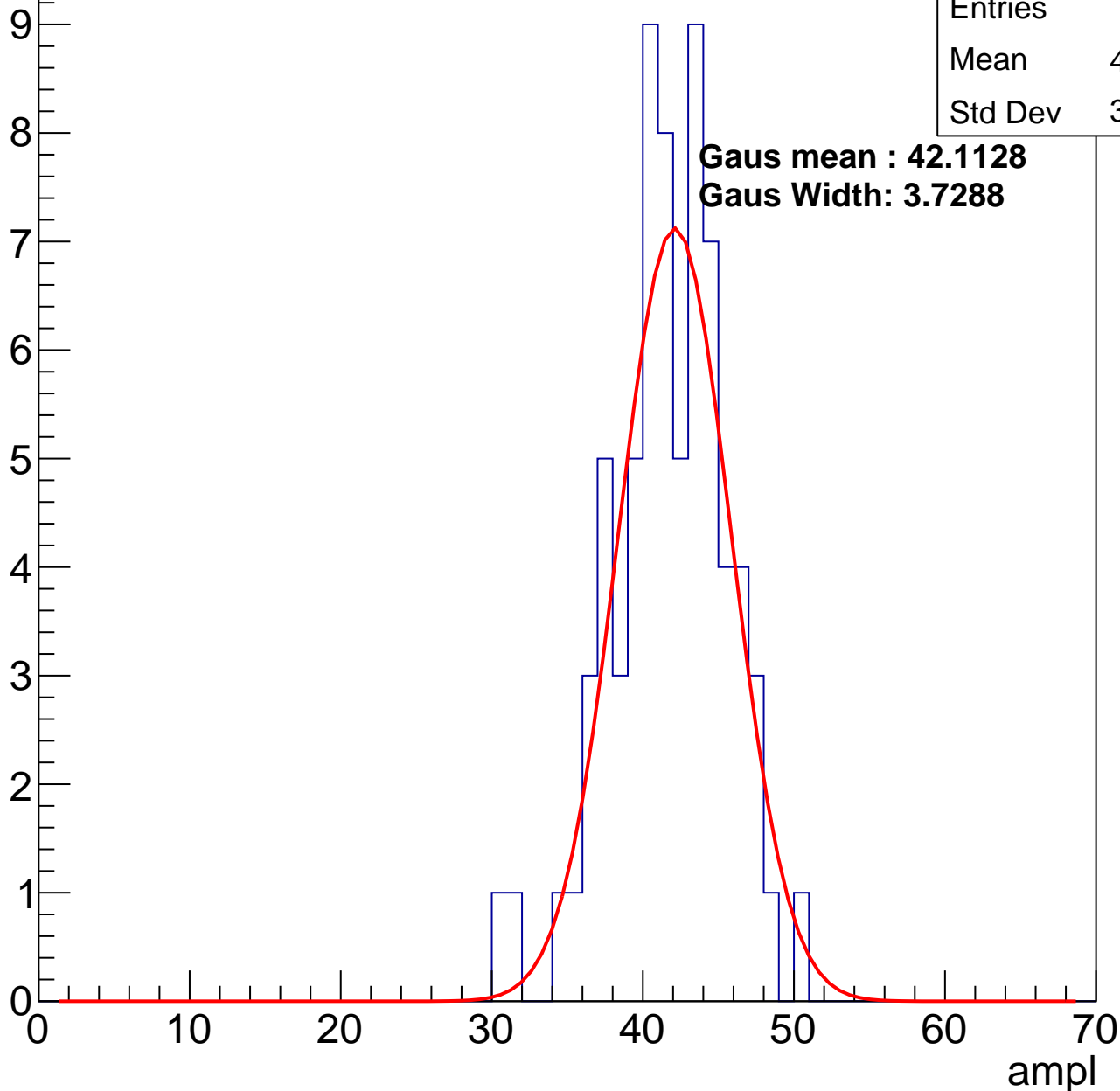
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.24
Std Dev	3.788

**Gaus mean : 42.1128**

**Gaus Width: 3.7288**

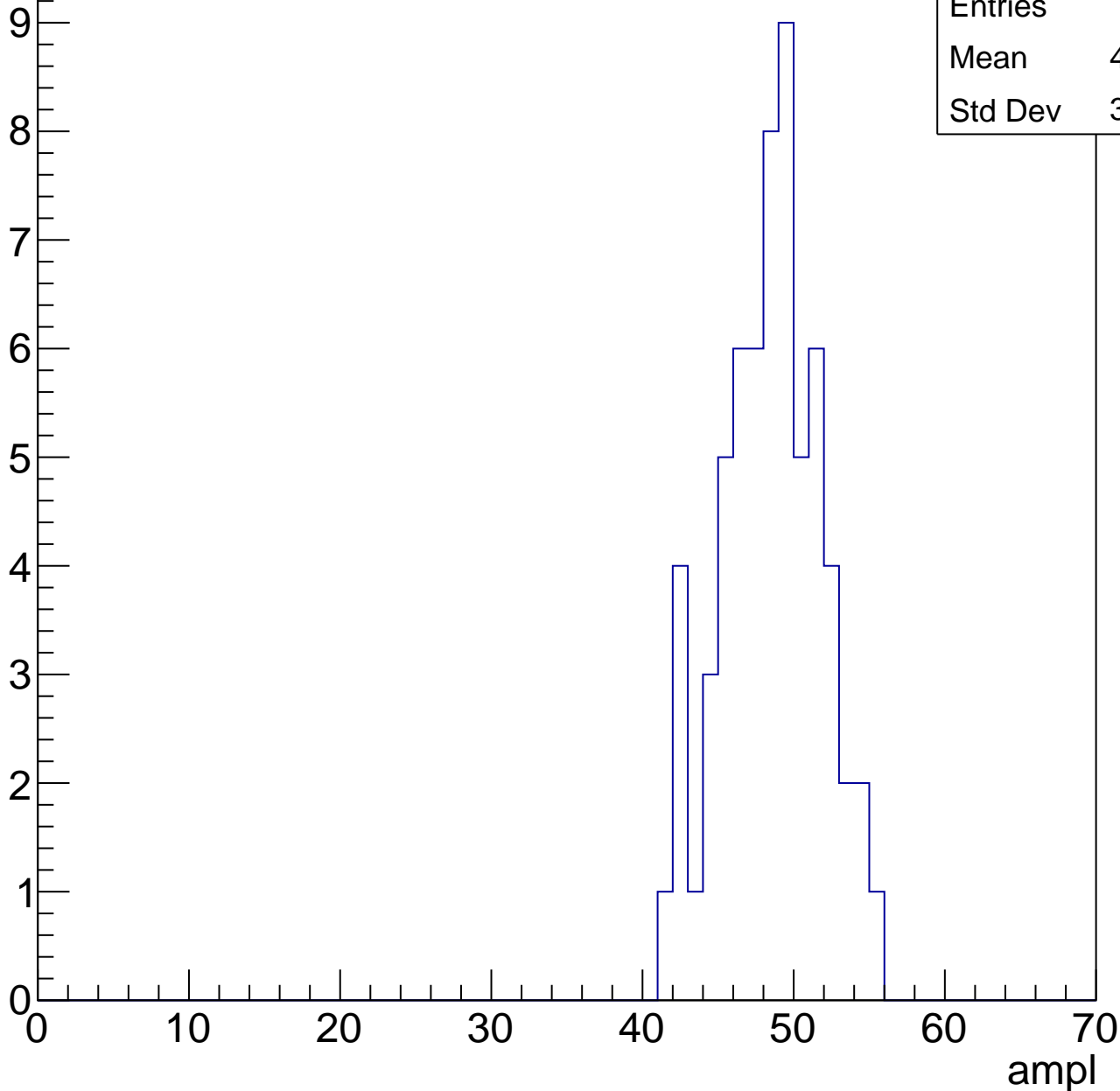


# B1L102S, U25-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.02
Std Dev	3.234

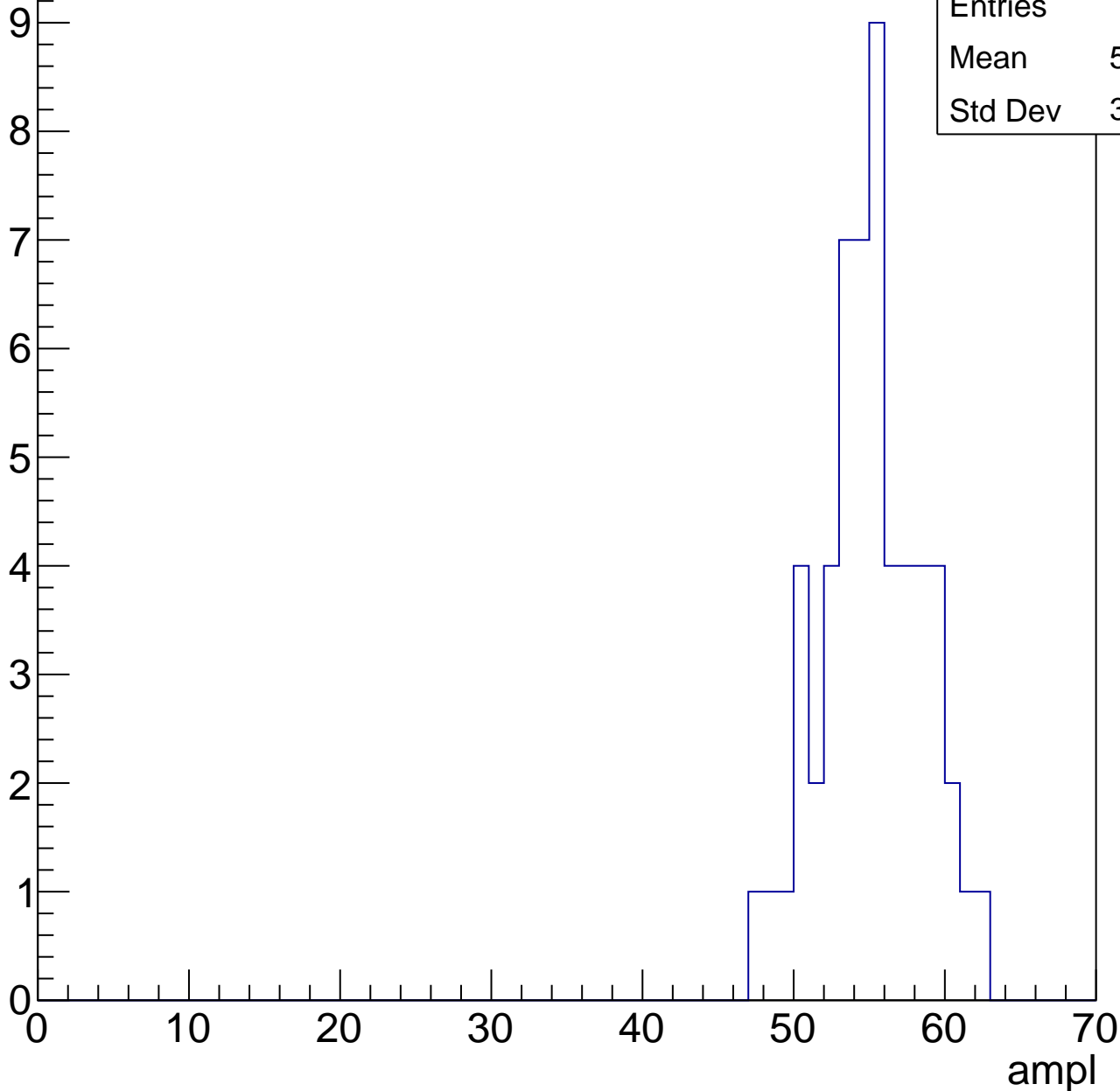


# B1L102S, U25-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.66
Std Dev	3.275

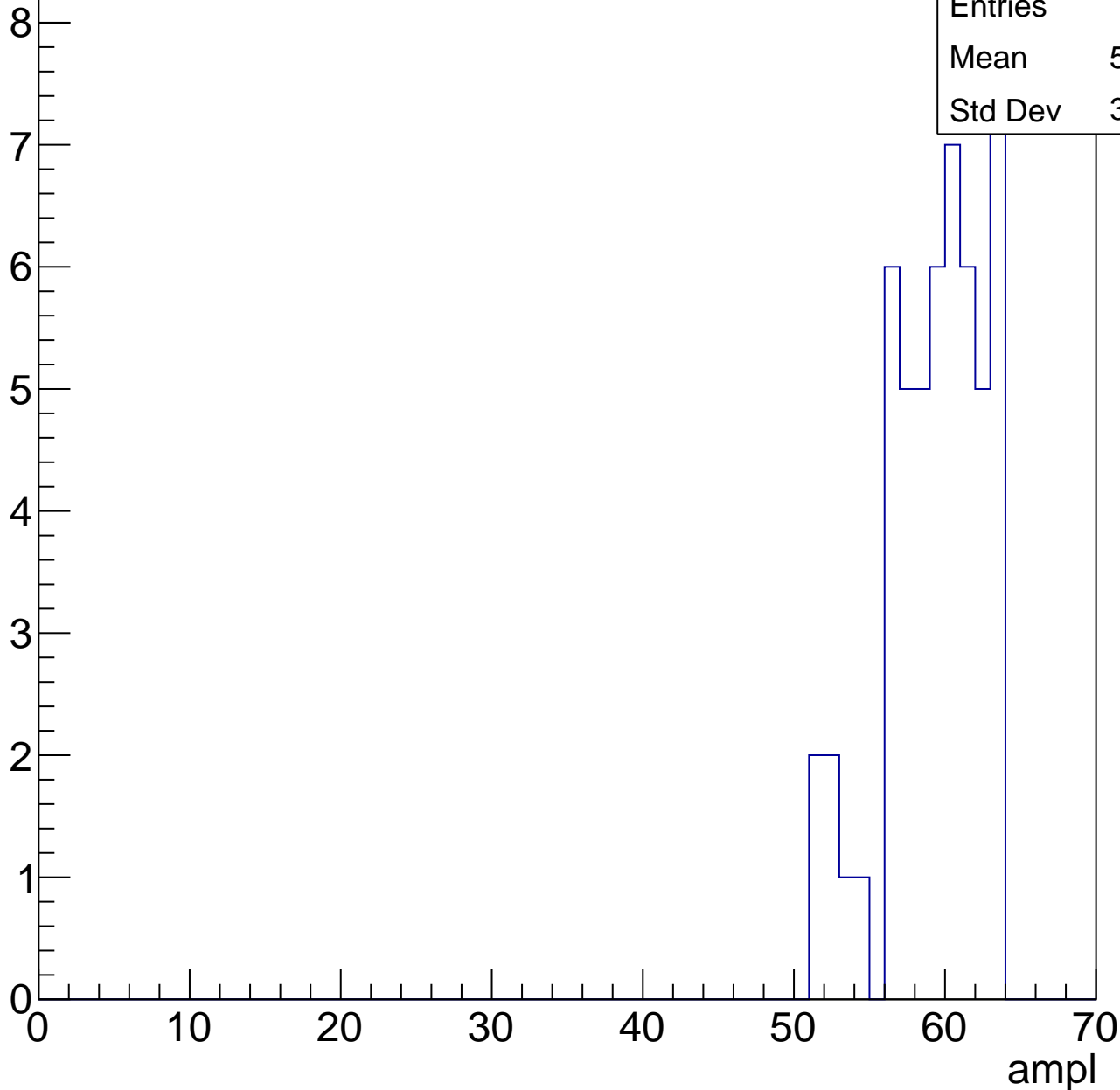


# B1L102S, U25-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

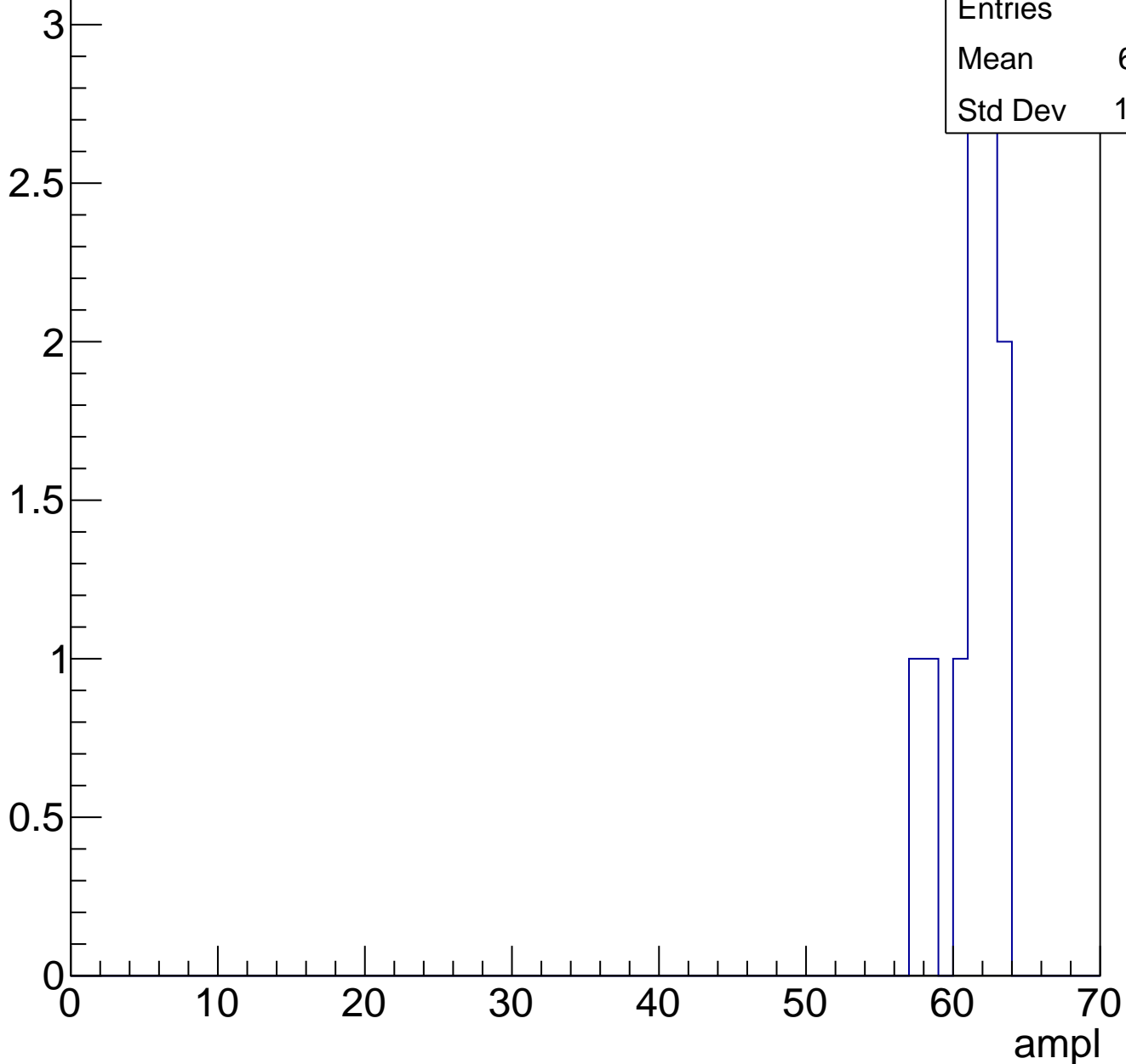
Entries	54
Mean	58.85
Std Dev	3.246



# B1L102S, U25-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B1L102S, U25-ch75, adc0

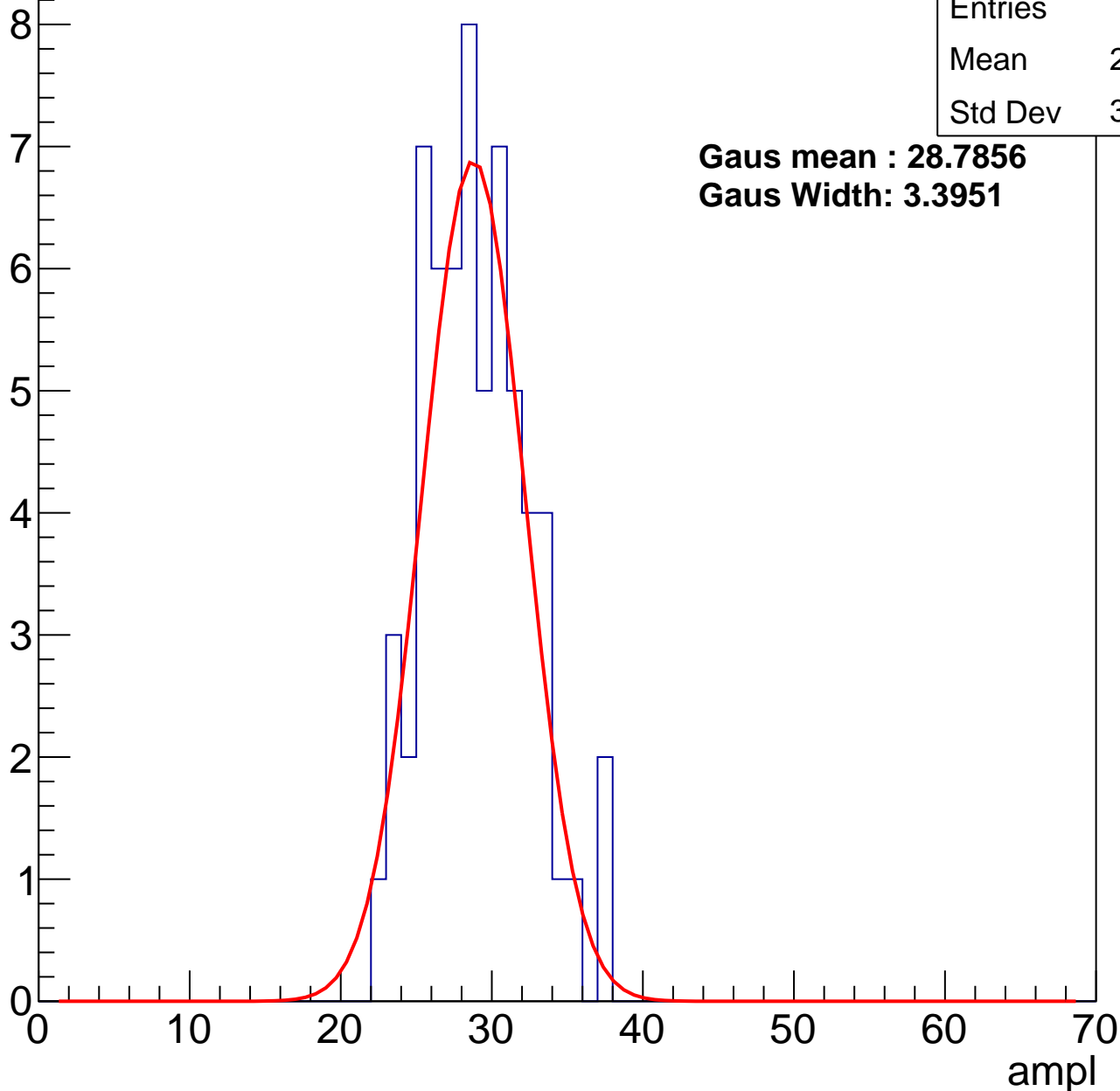
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.53
Std Dev	3.378

**Gaus mean : 28.7856**

**Gaus Width: 3.3951**



# B1L102S, U25-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	89
Mean	36.06
Std Dev	3.901

**Gaus mean : 36.5932**

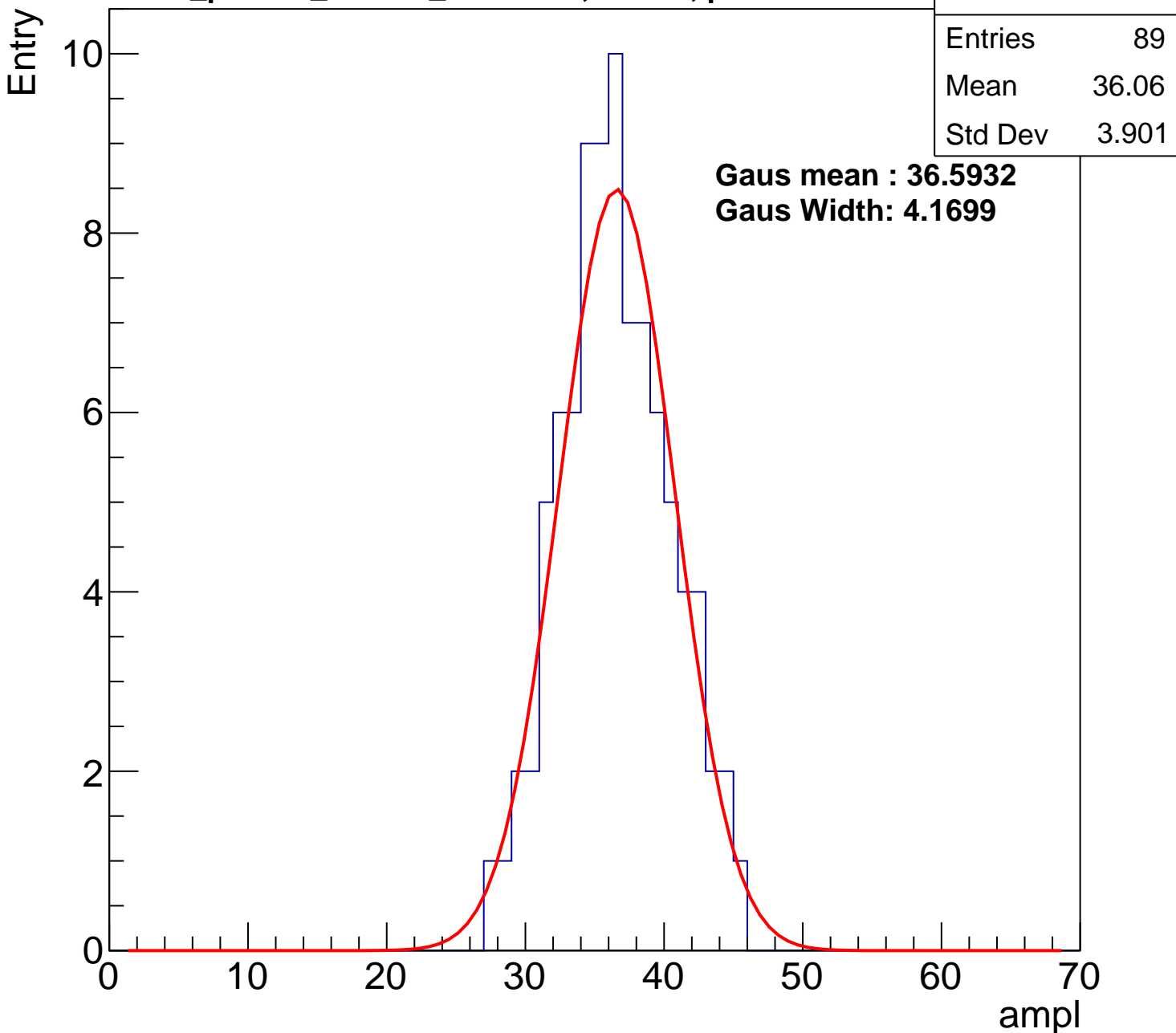
**Gaus Width: 4.1699**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch75, adc2

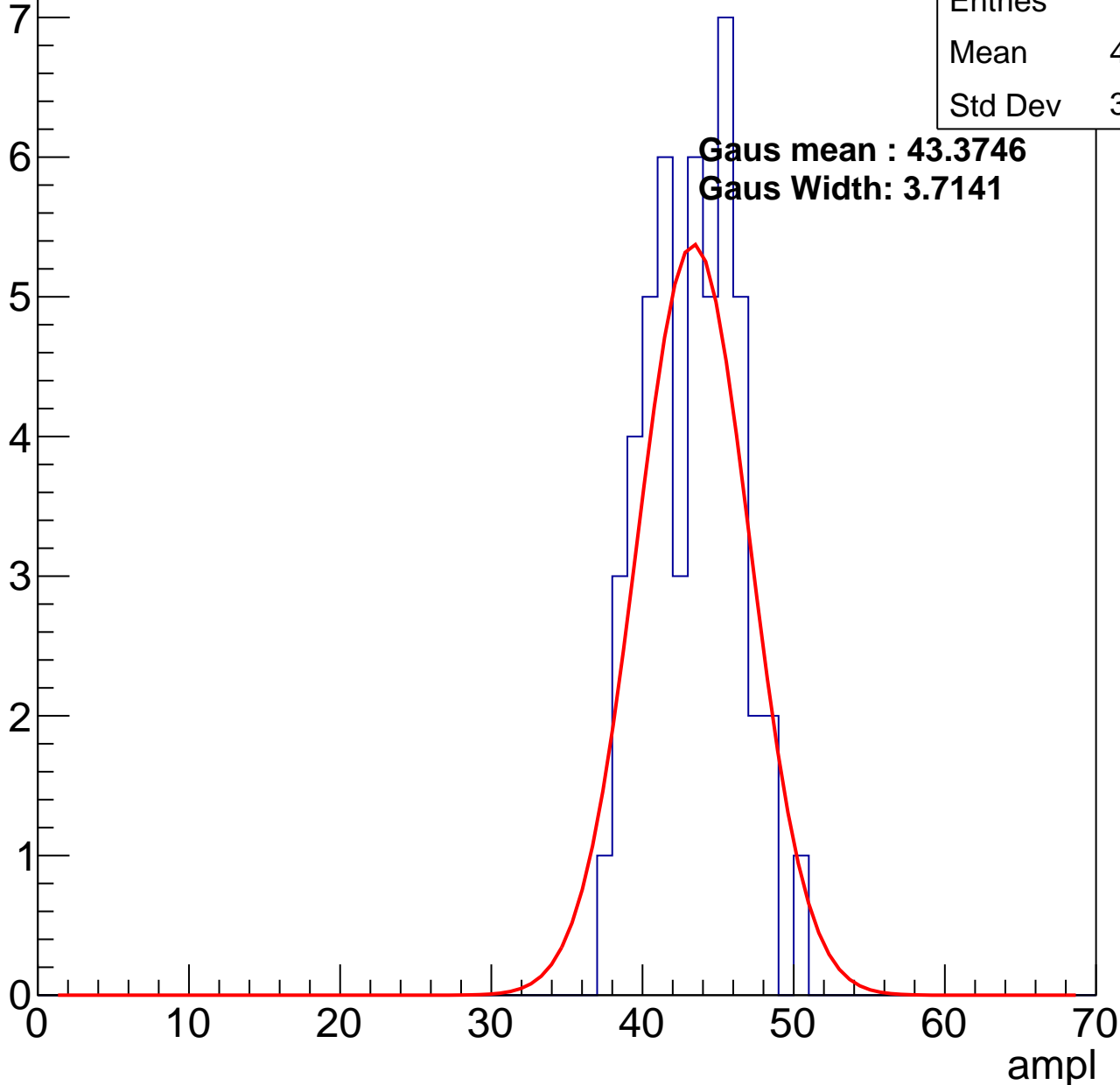
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	42.84
Std Dev	3.016

**Gaus mean : 43.3746**

**Gaus Width: 3.7141**

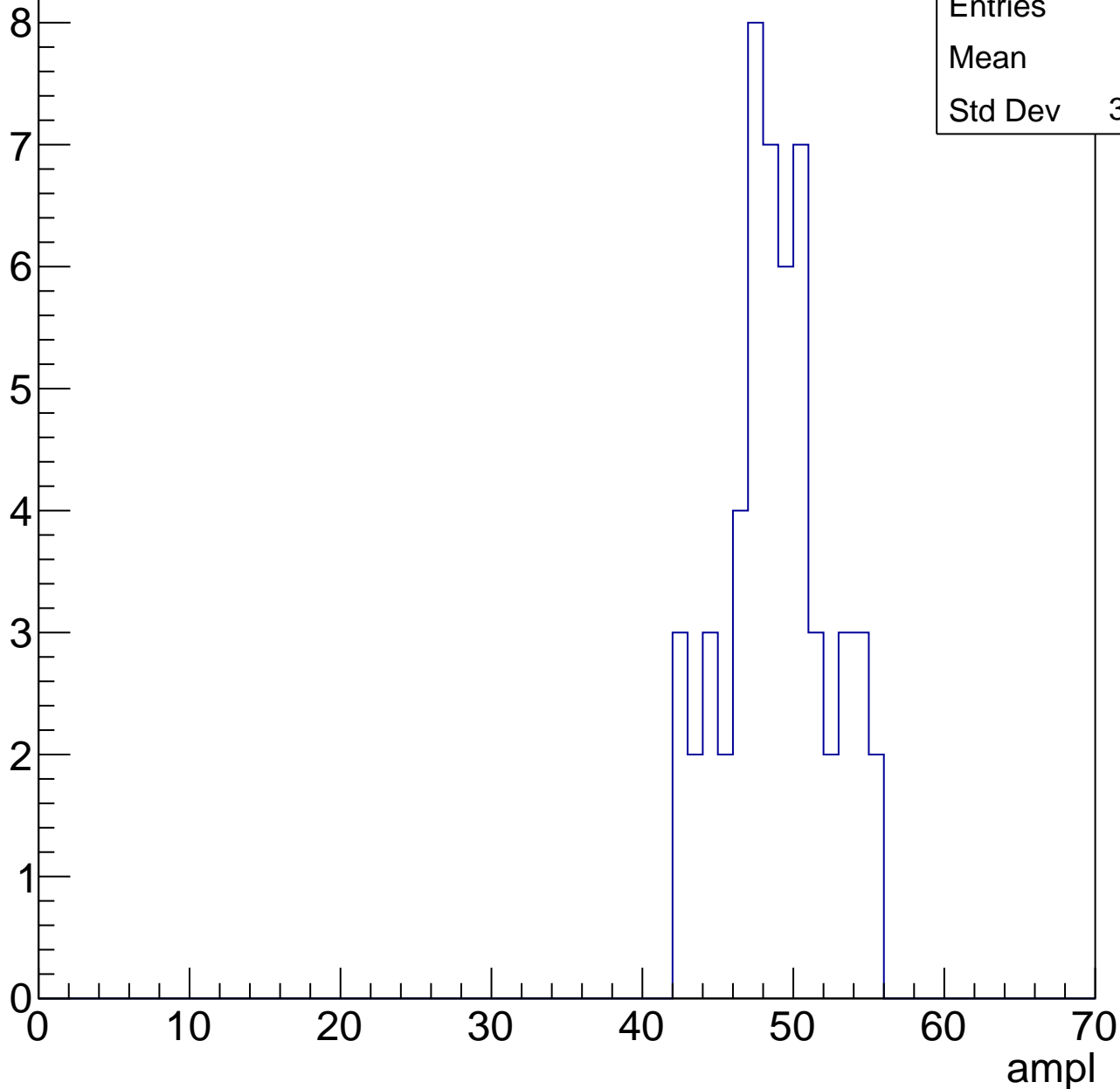


# B1L102S, U25-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

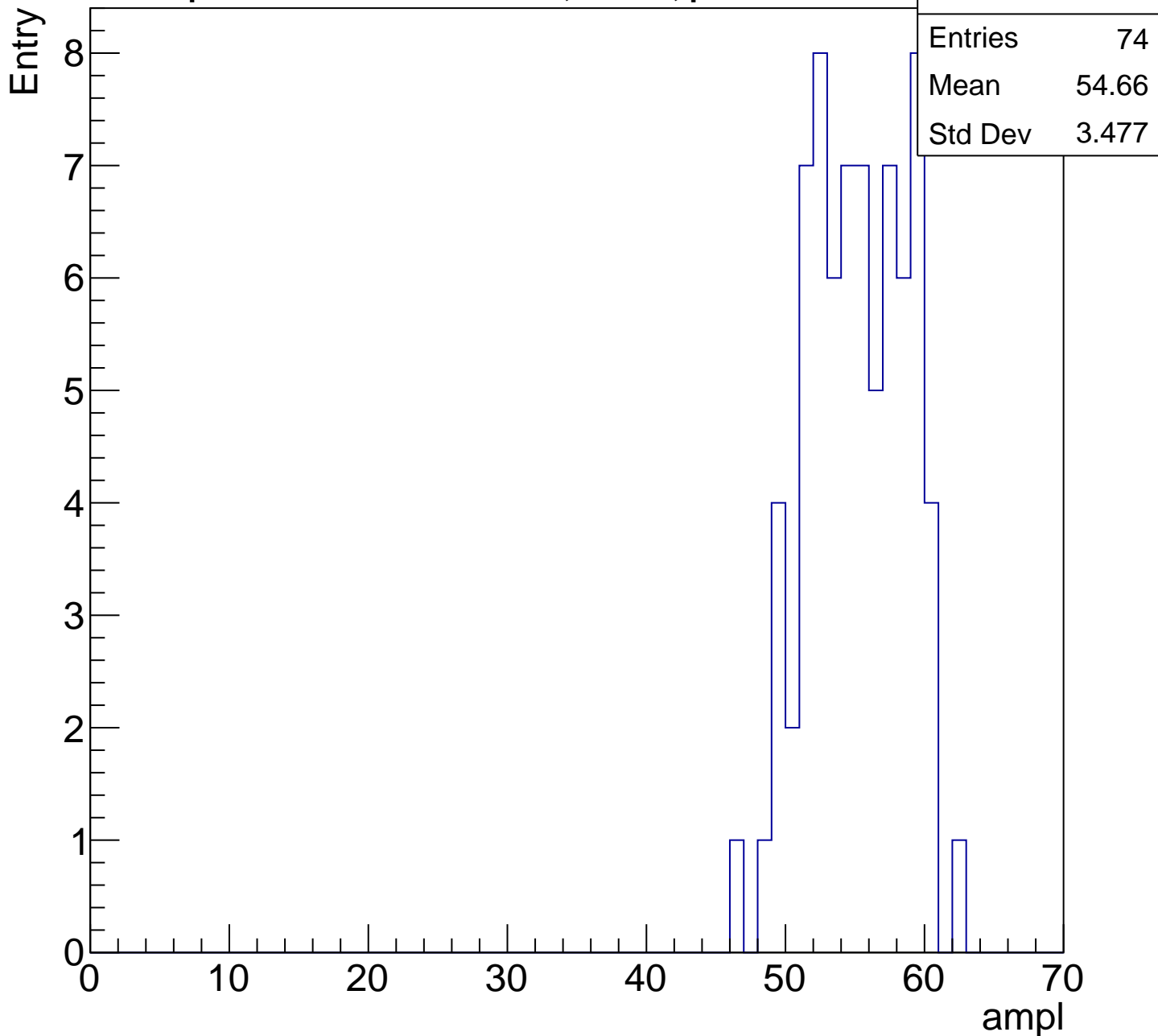
Entry

Entries	55
Mean	48.4
Std Dev	3.339



# B1L102S, U25-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

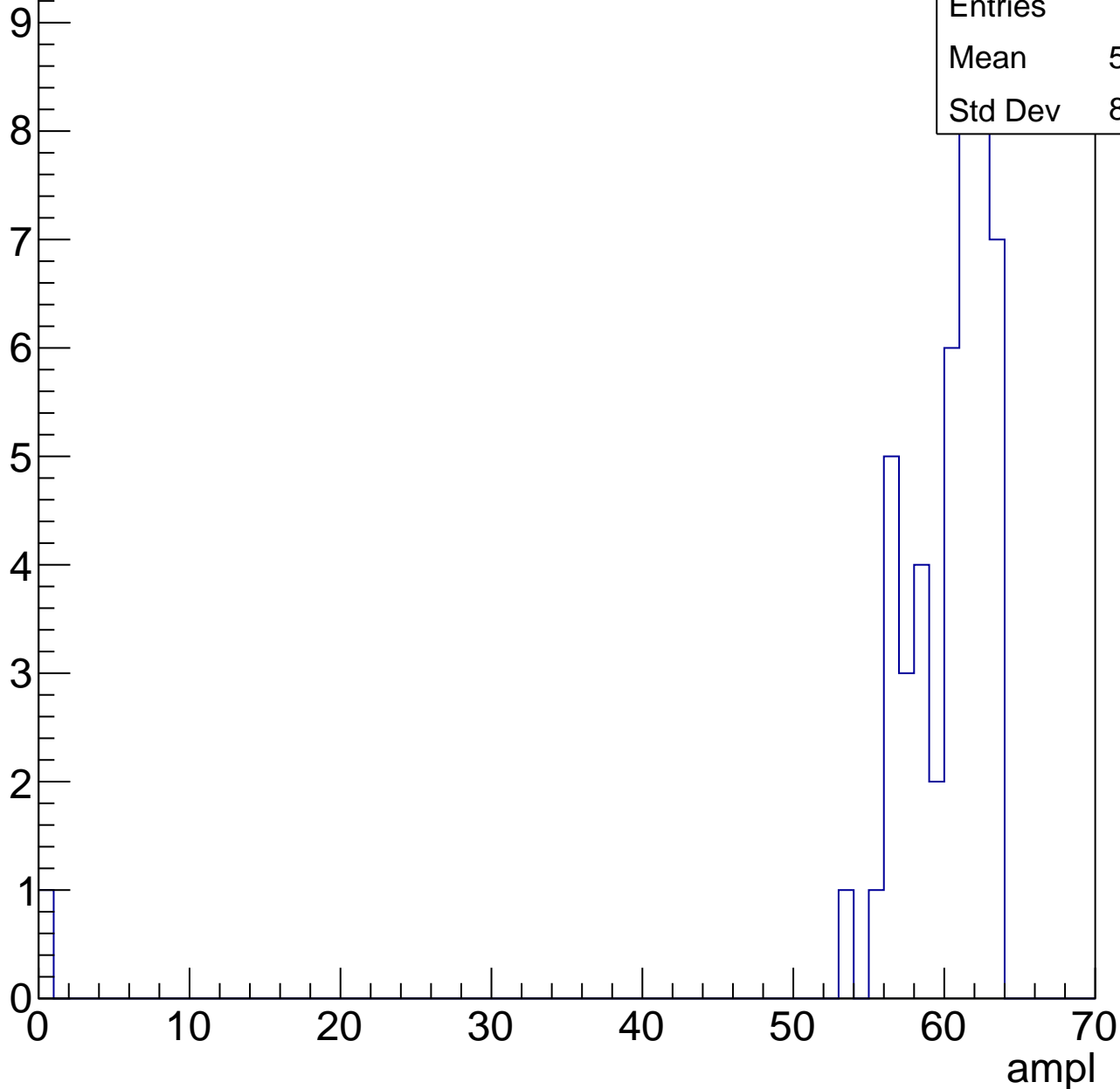


# B1L102S, U25-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.69
Std Dev	8.926



# B1L102S, U25-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

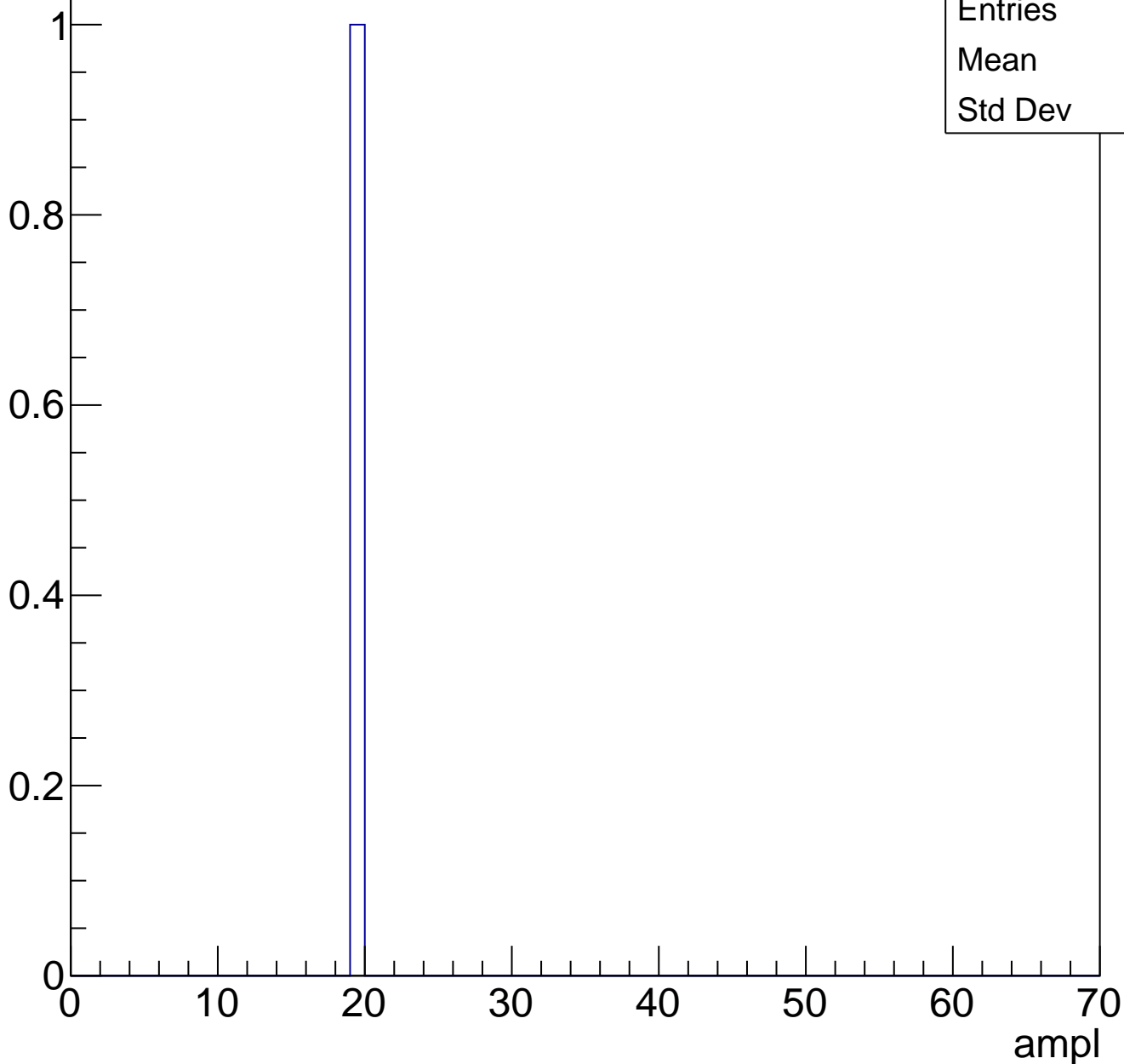




# B1L102S, U25-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch76, adc0

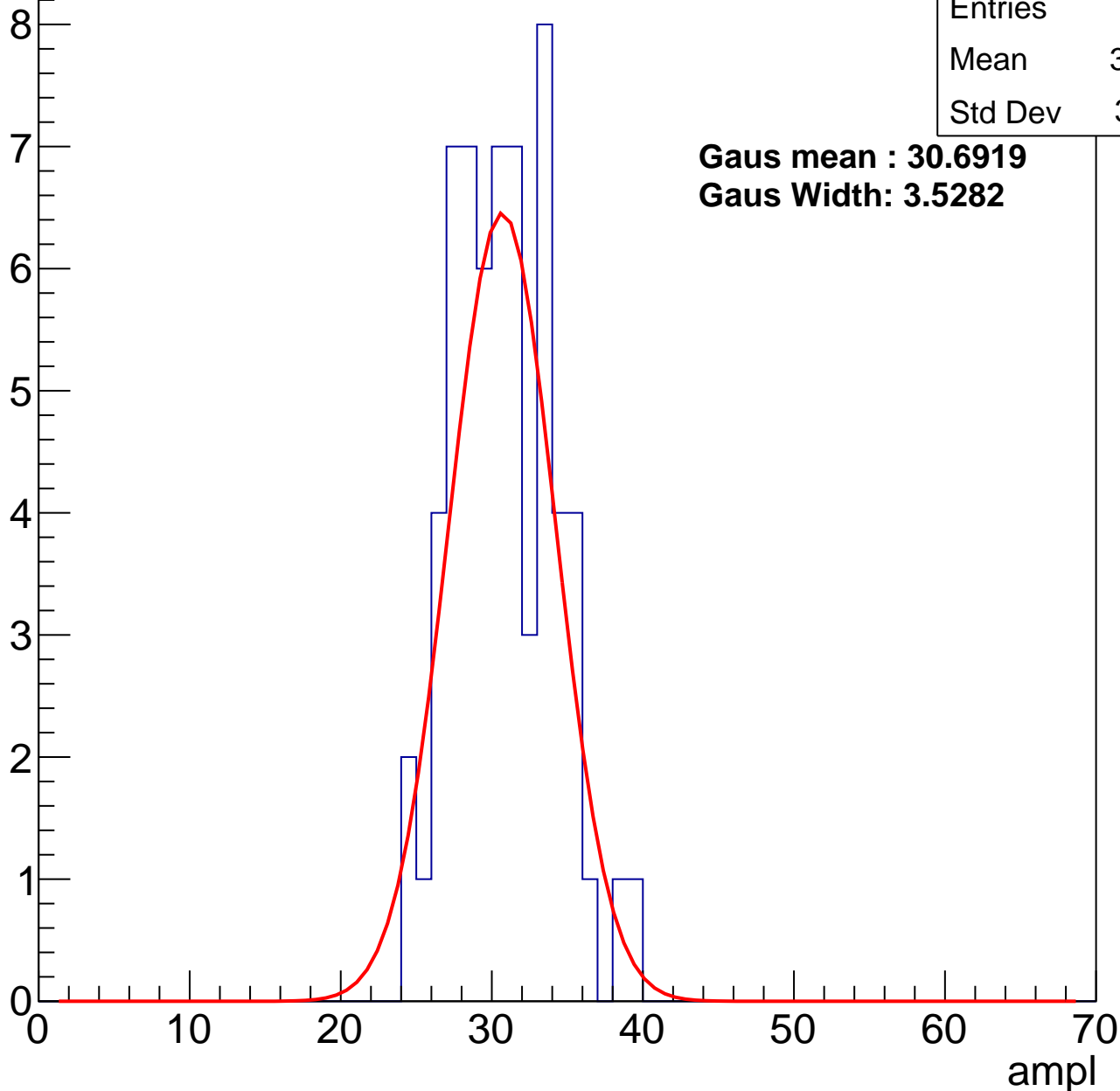
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	30.35
Std Dev	3.301

**Gaus mean : 30.6919**

**Gaus Width: 3.5282**



# B1L102S, U25-ch76, adc1

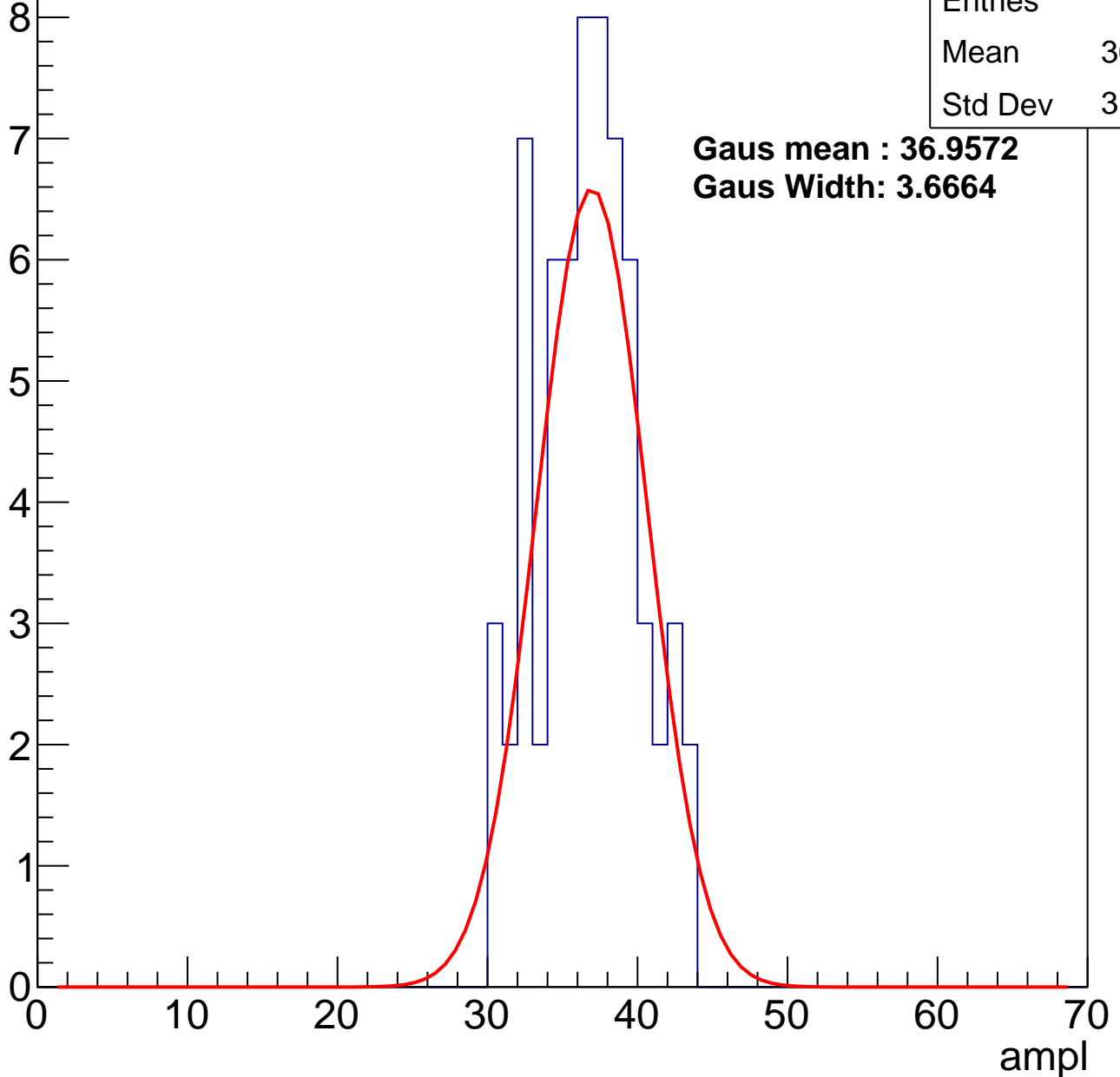
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.22
Std Dev	3.303

**Gaus mean : 36.9572**

**Gaus Width: 3.6664**



# B1L102S, U25-ch76, adc2

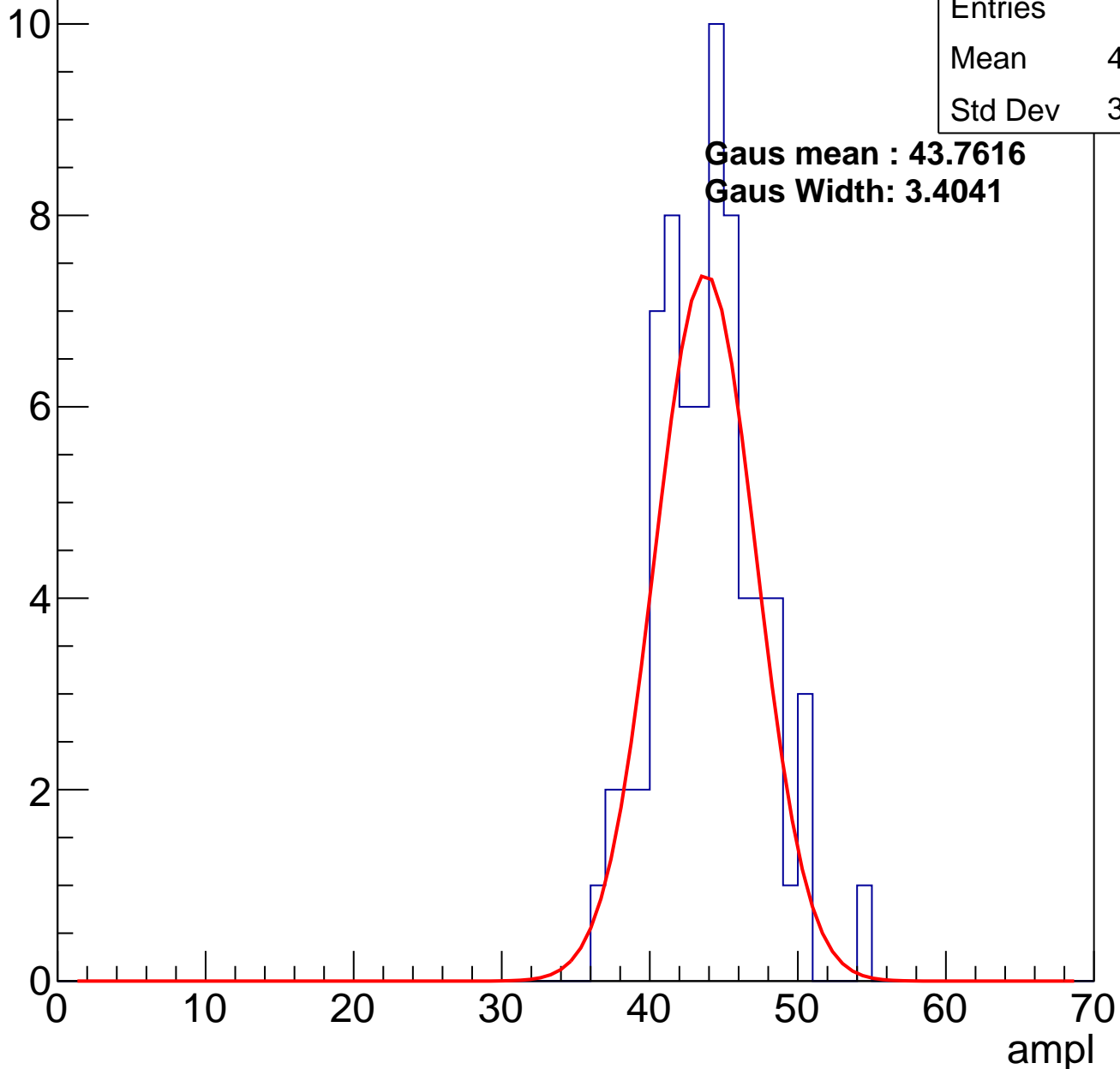
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	69
Mean	43.46
Std Dev	3.479

**Gaus mean : 43.7616**

**Gaus Width: 3.4041**

Entry



# B1L102S, U25-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	74
Mean	50.04
Std Dev	3.678

Entry

10

8

6

4

2

0

0

10

20

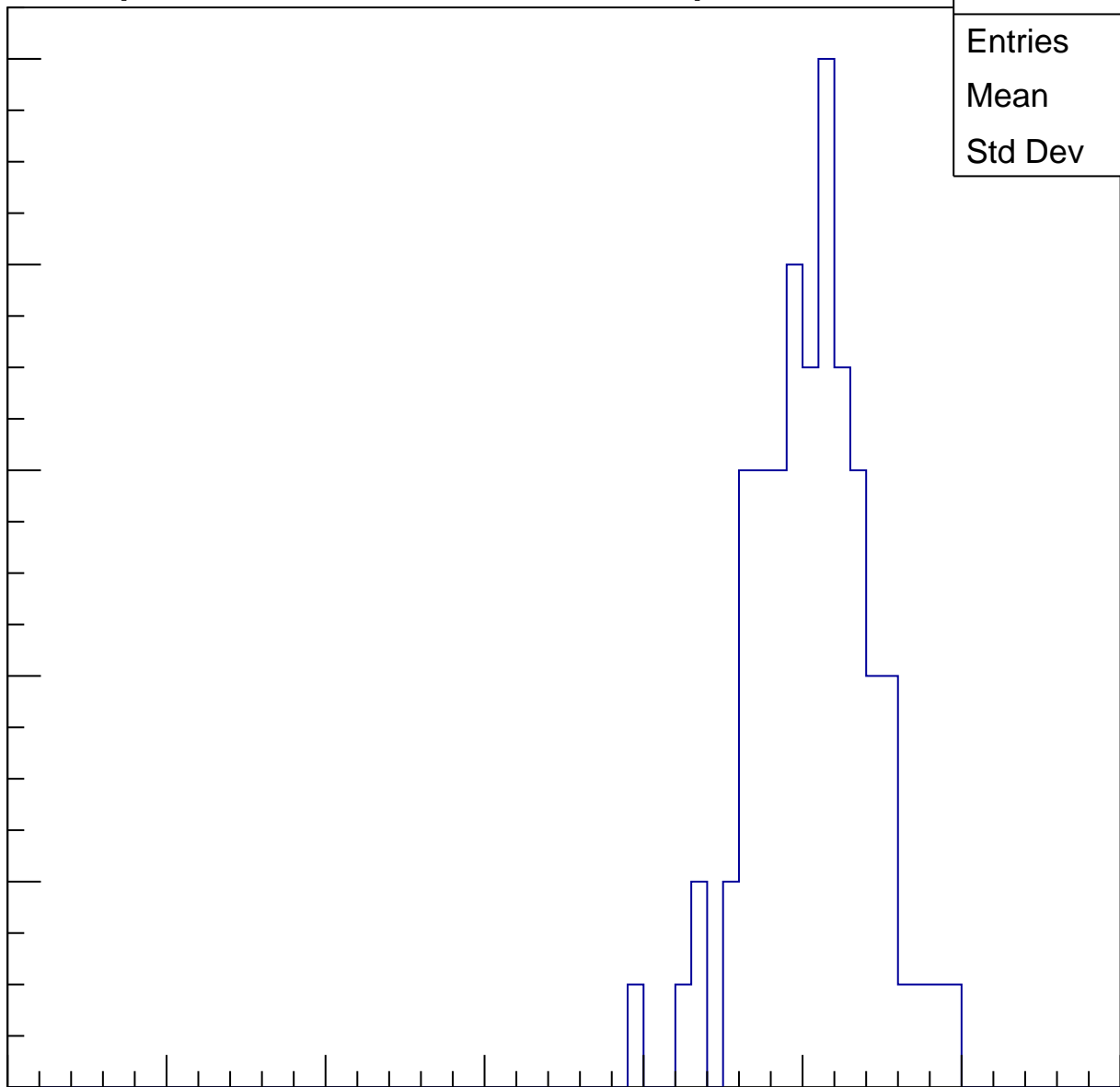
30

40

50

60

ampl

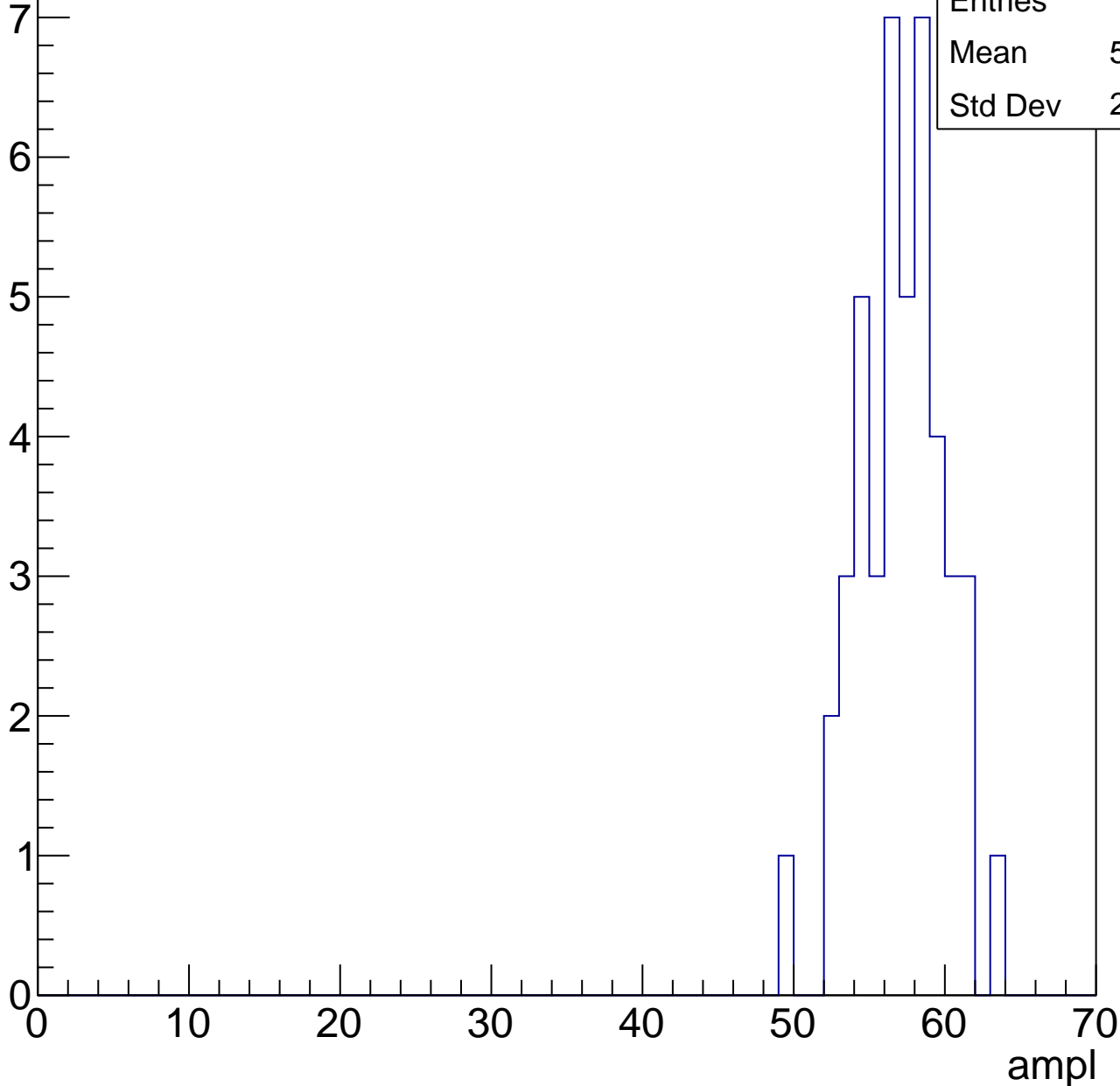


# B1L102S, U25-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.64
Std Dev	2.837

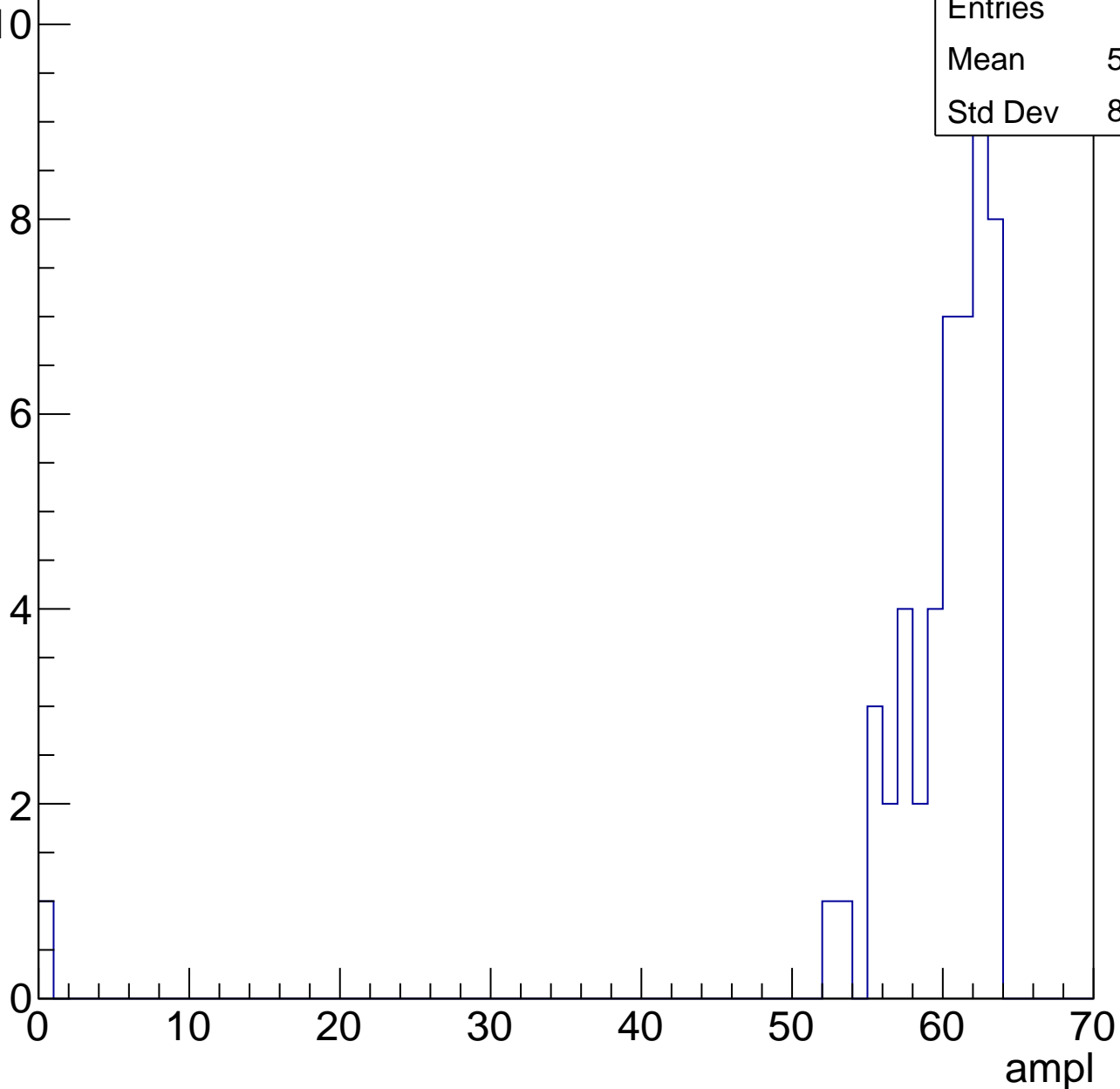


# B1L102S, U25-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.66
Std Dev	8.829



# B1L102S, U25-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch77, adc0

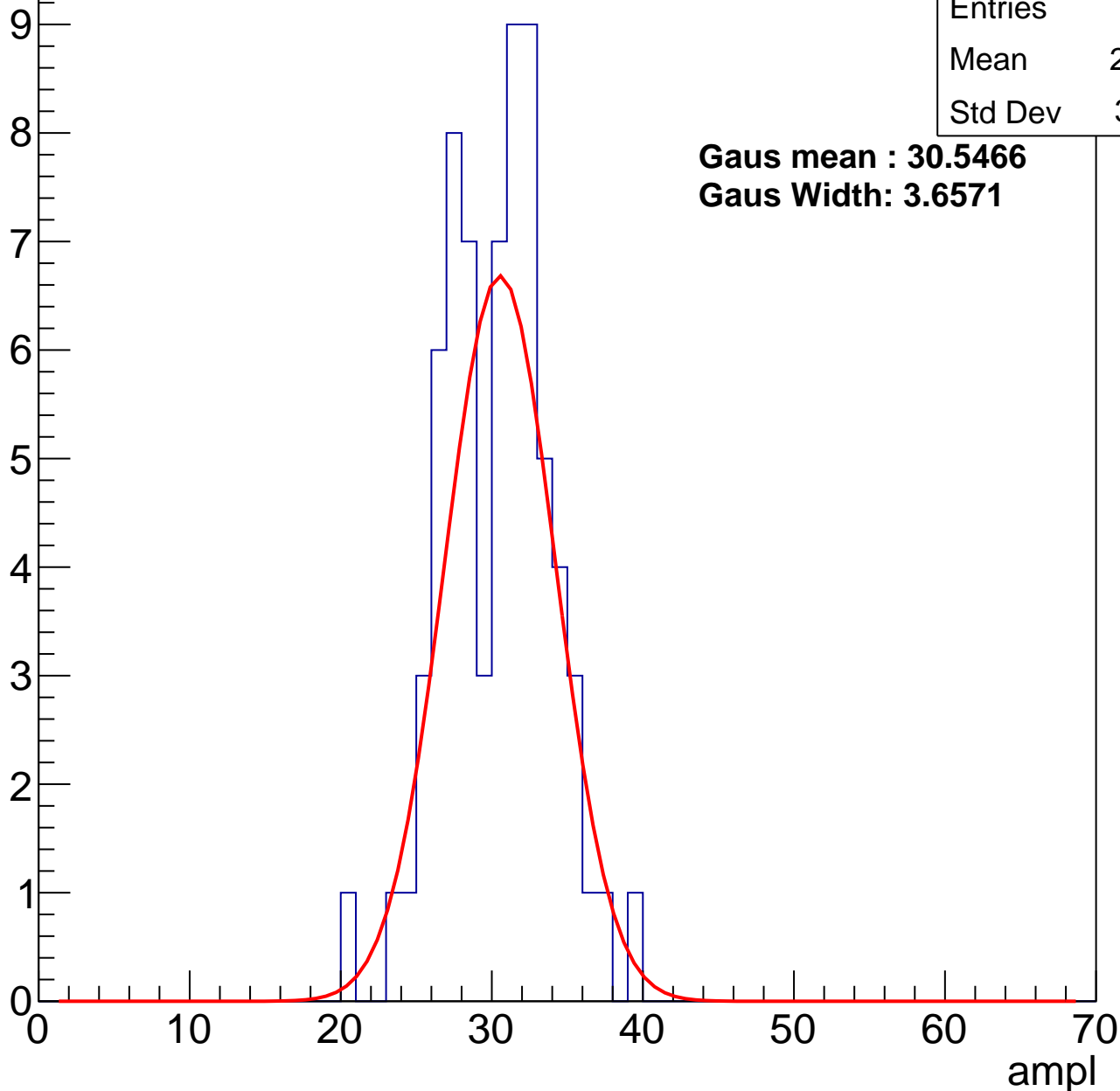
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.89
Std Dev	3.491

**Gaus mean : 30.5466**

**Gaus Width: 3.6571**



# B1L102S, U25-ch77, adc1

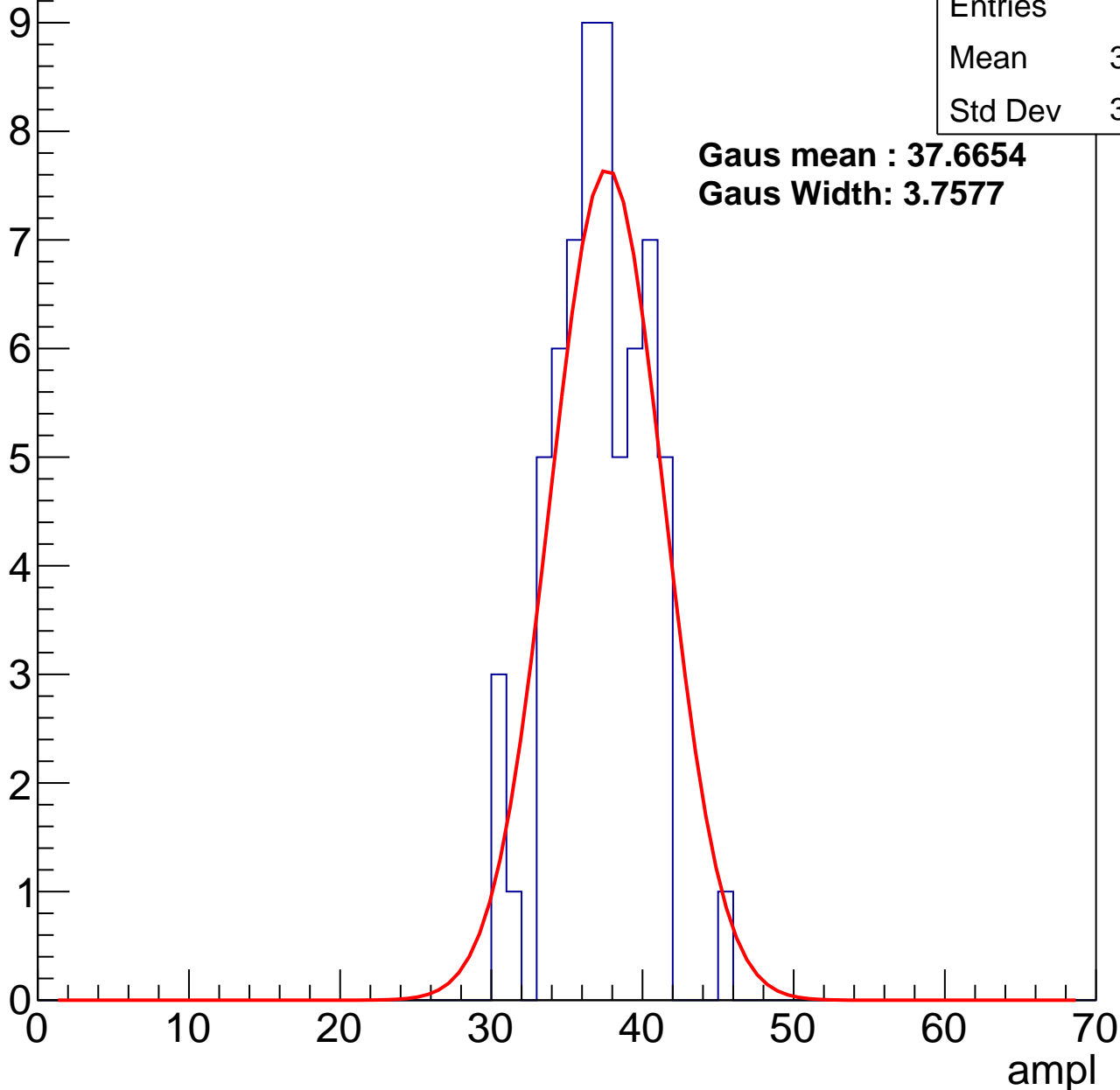
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.66
Std Dev	3.017

**Gaus mean : 37.6654**

**Gaus Width: 3.7577**



# B1L102S, U25-ch77, adc2

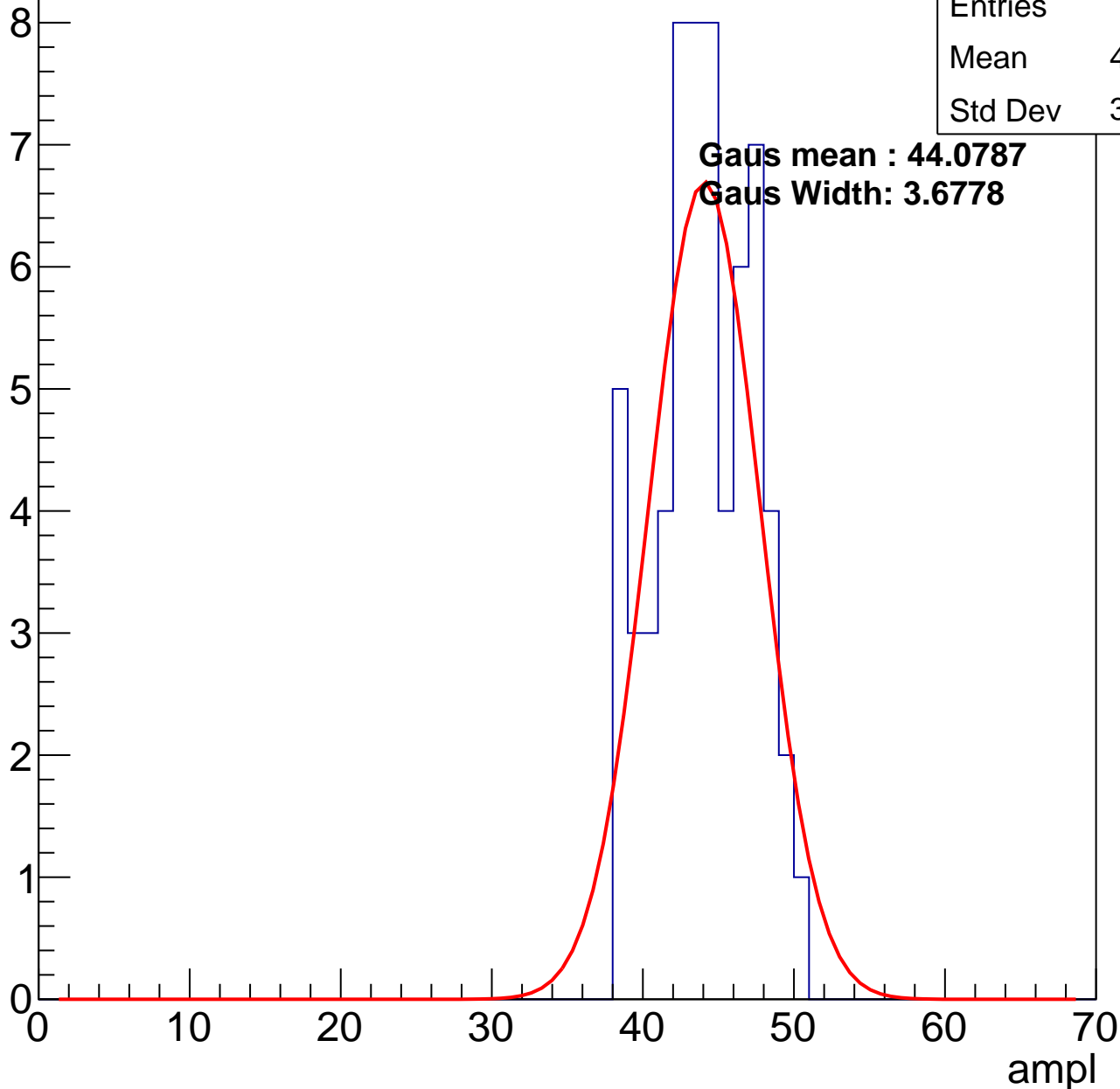
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.62
Std Dev	3.114

**Gaus mean : 44.0787**

**Gaus Width: 3.6778**

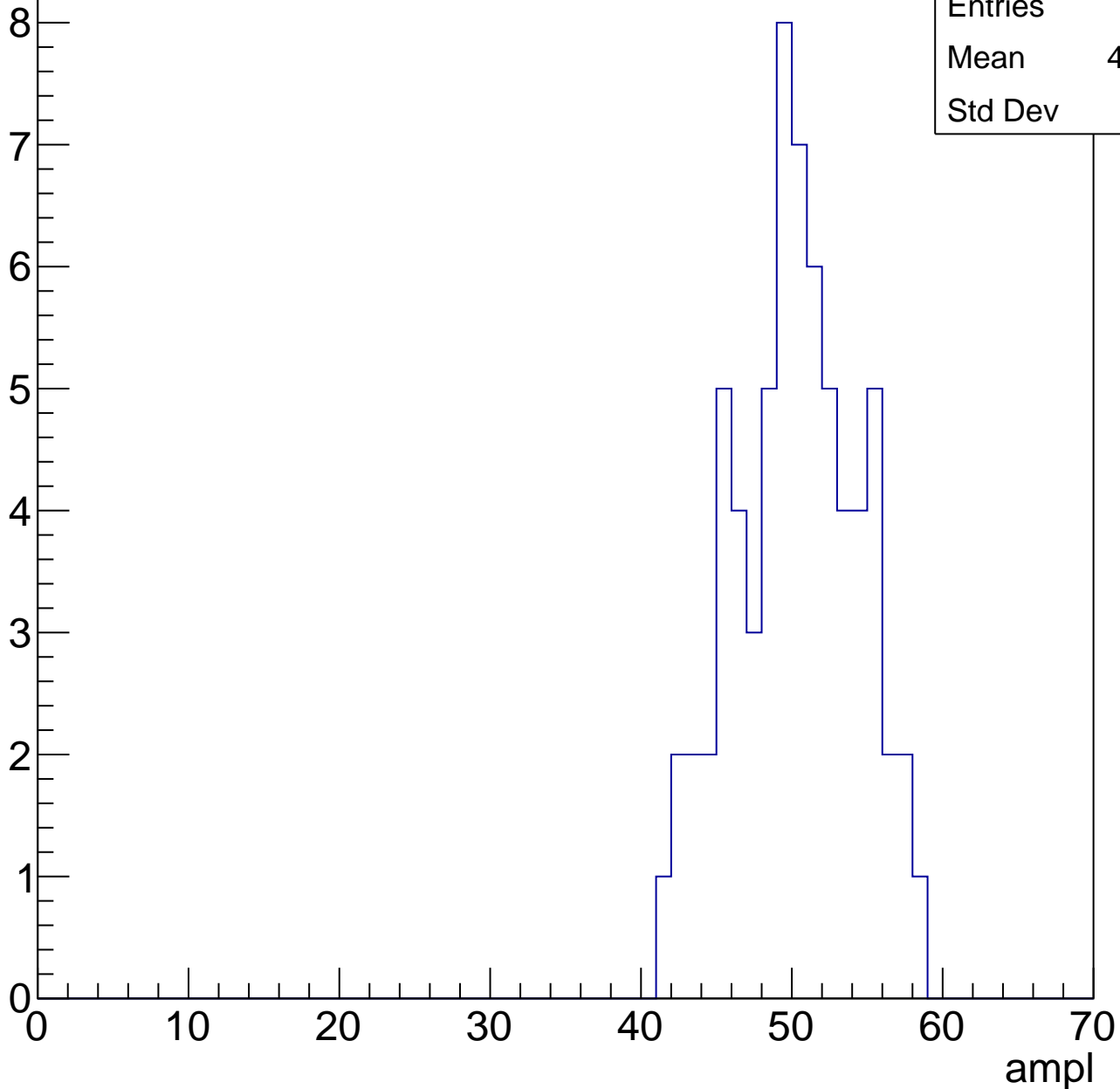


# B1L102S, U25-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.76
Std Dev	4.03

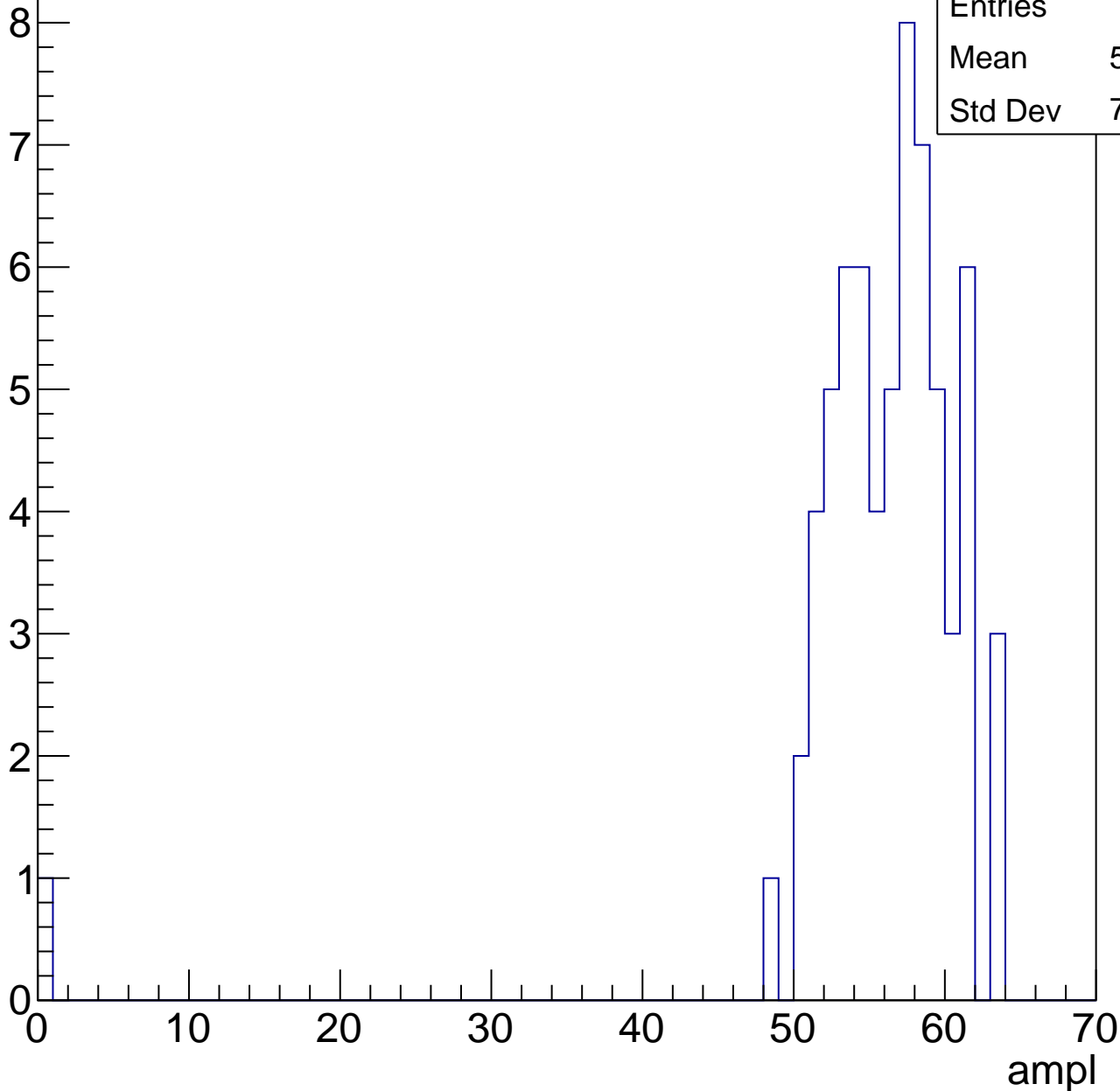


# B1L102S, U25-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.24
Std Dev	7.709

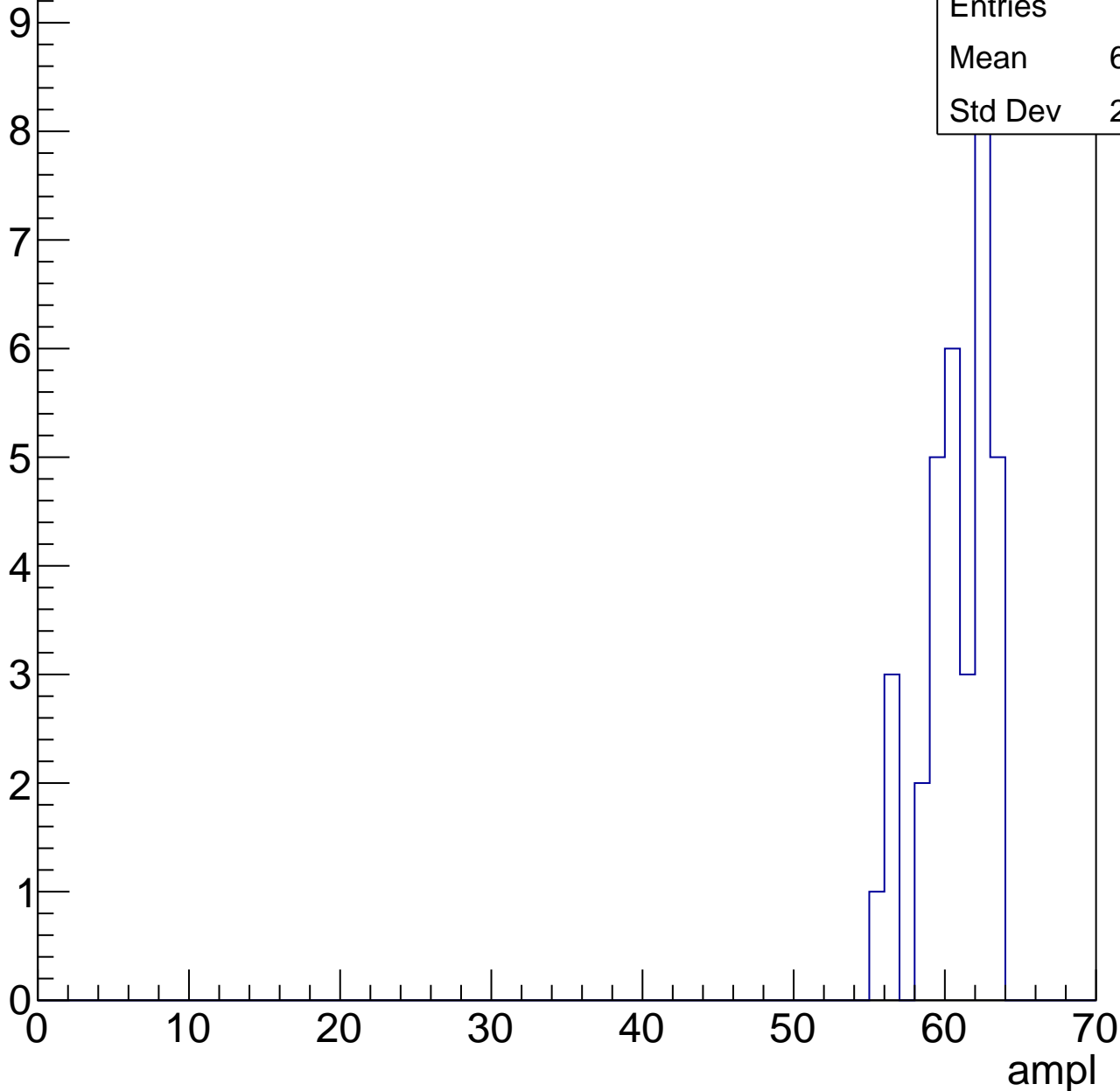


# B1L102S, U25-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	34
Mean	60.29
Std Dev	2.217



# B1L102S, U25-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

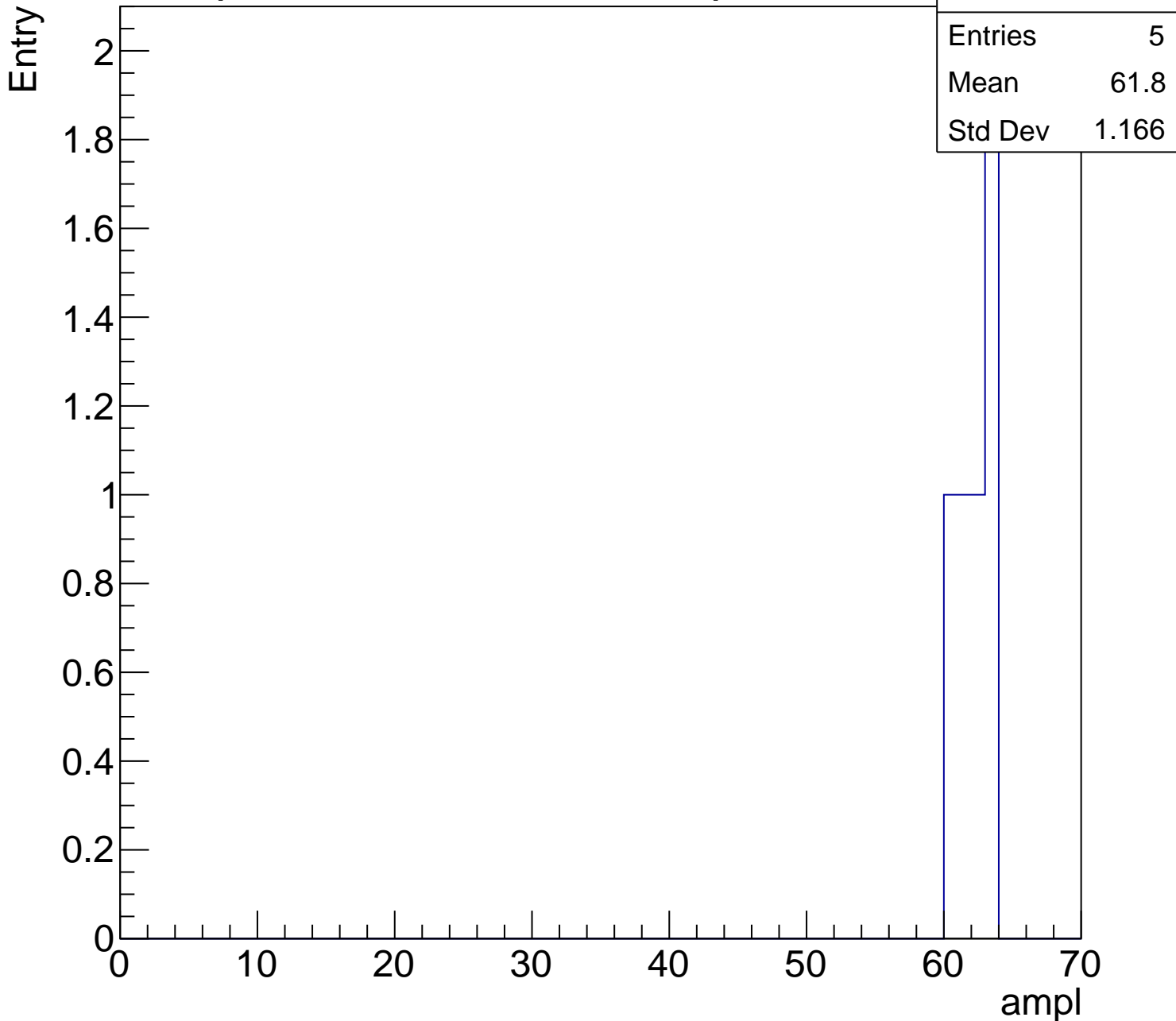
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.8
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl





# B1L102S, U25-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch78, adc0

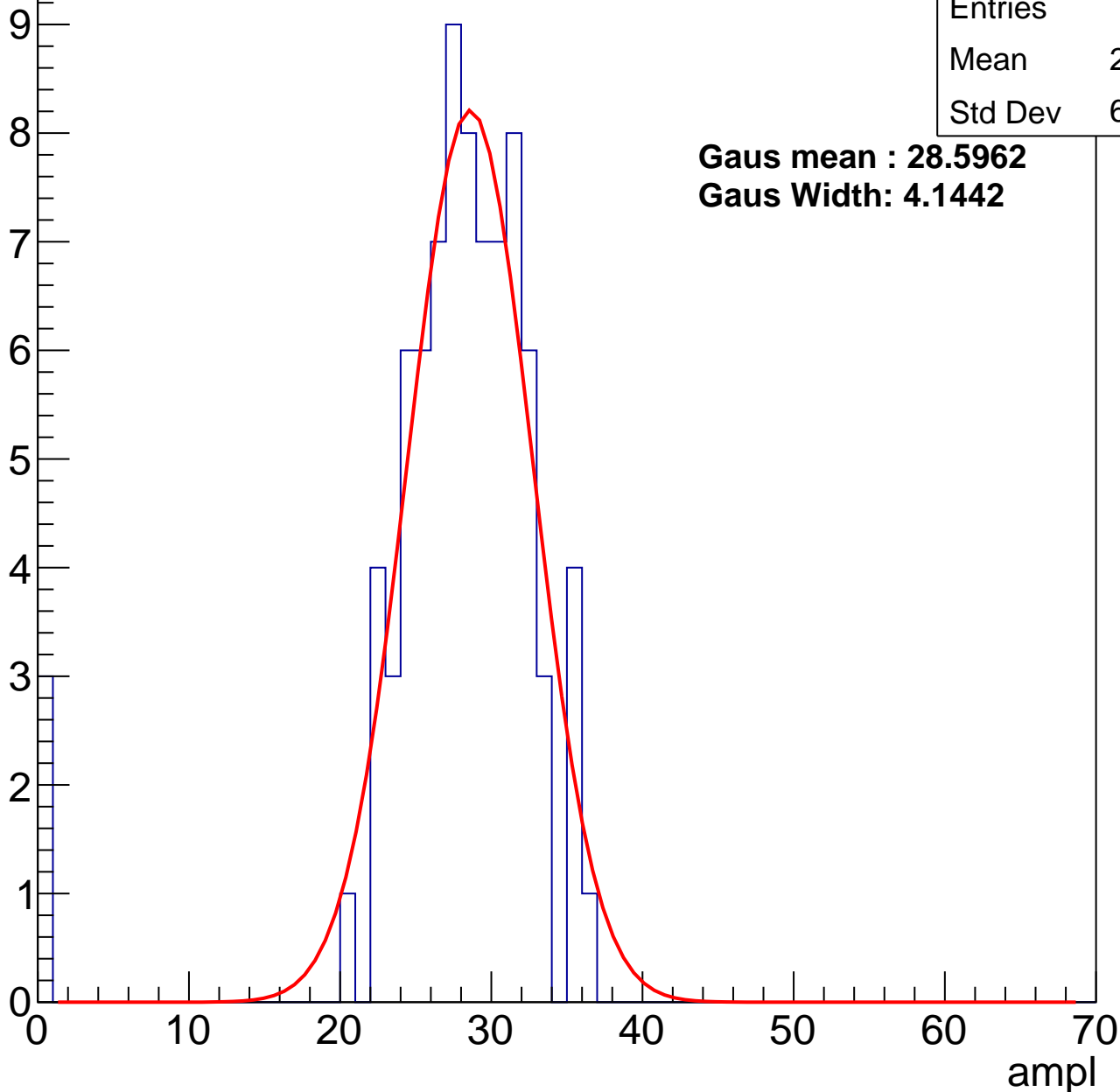
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.08
Std Dev	6.296

**Gaus mean : 28.5962**

**Gaus Width: 4.1442**



# B1L102S, U25-ch78, adc1

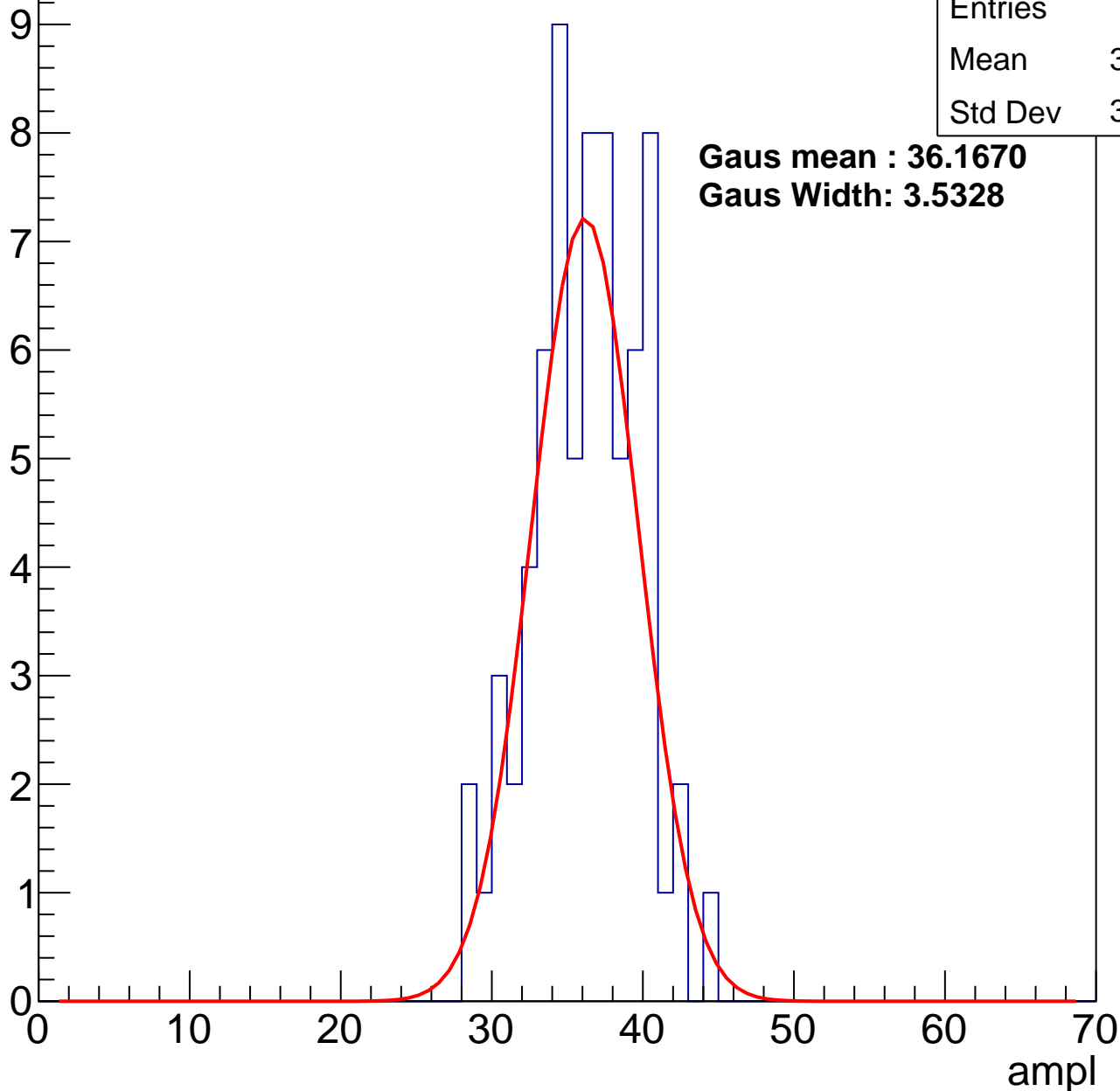
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.79
Std Dev	3.484

**Gaus mean : 36.1670**

**Gaus Width: 3.5328**



# B1L102S, U25-ch78, adc2

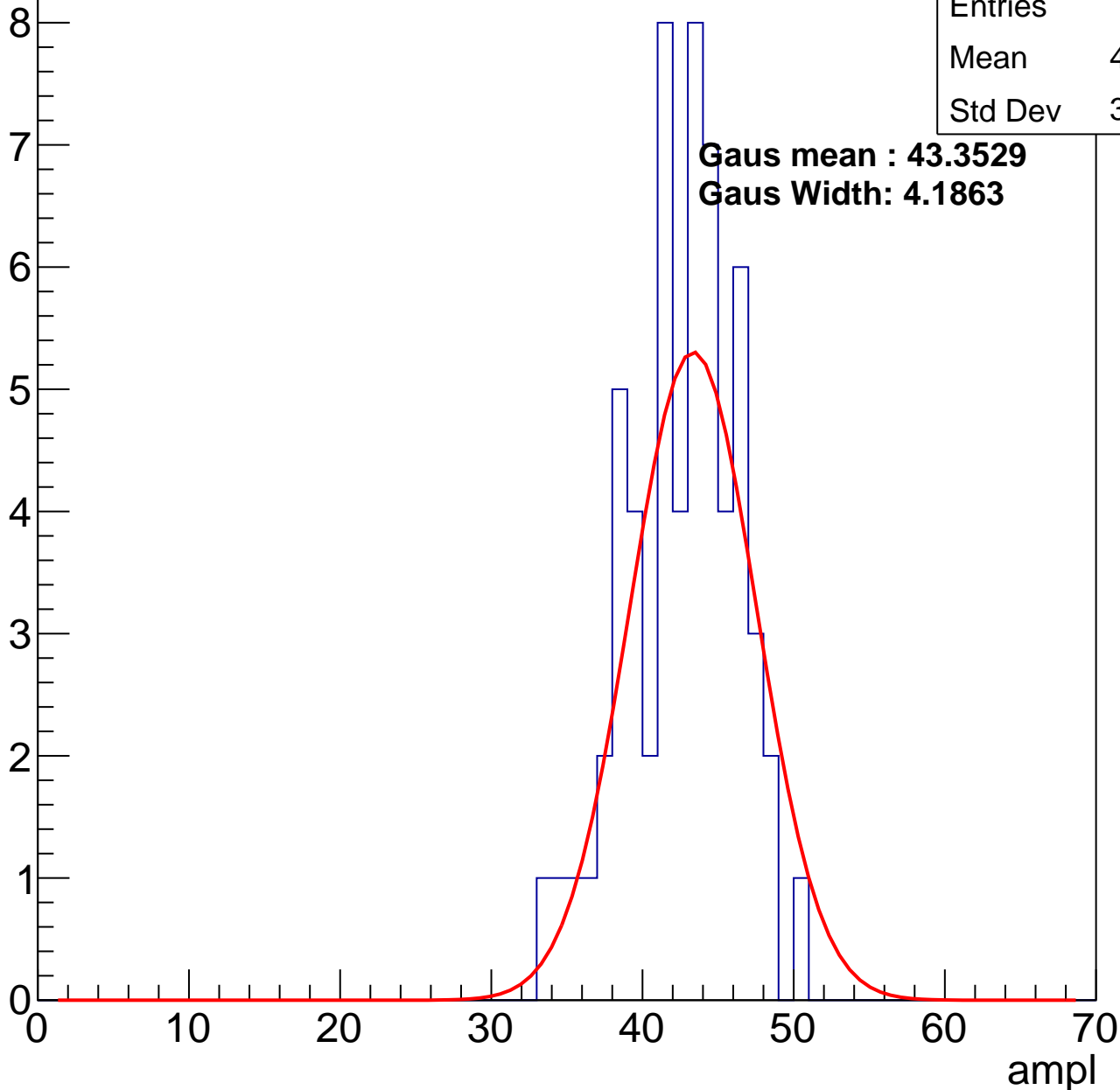
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.15
Std Dev	3.628

**Gaus mean : 43.3529**

**Gaus Width: 4.1863**

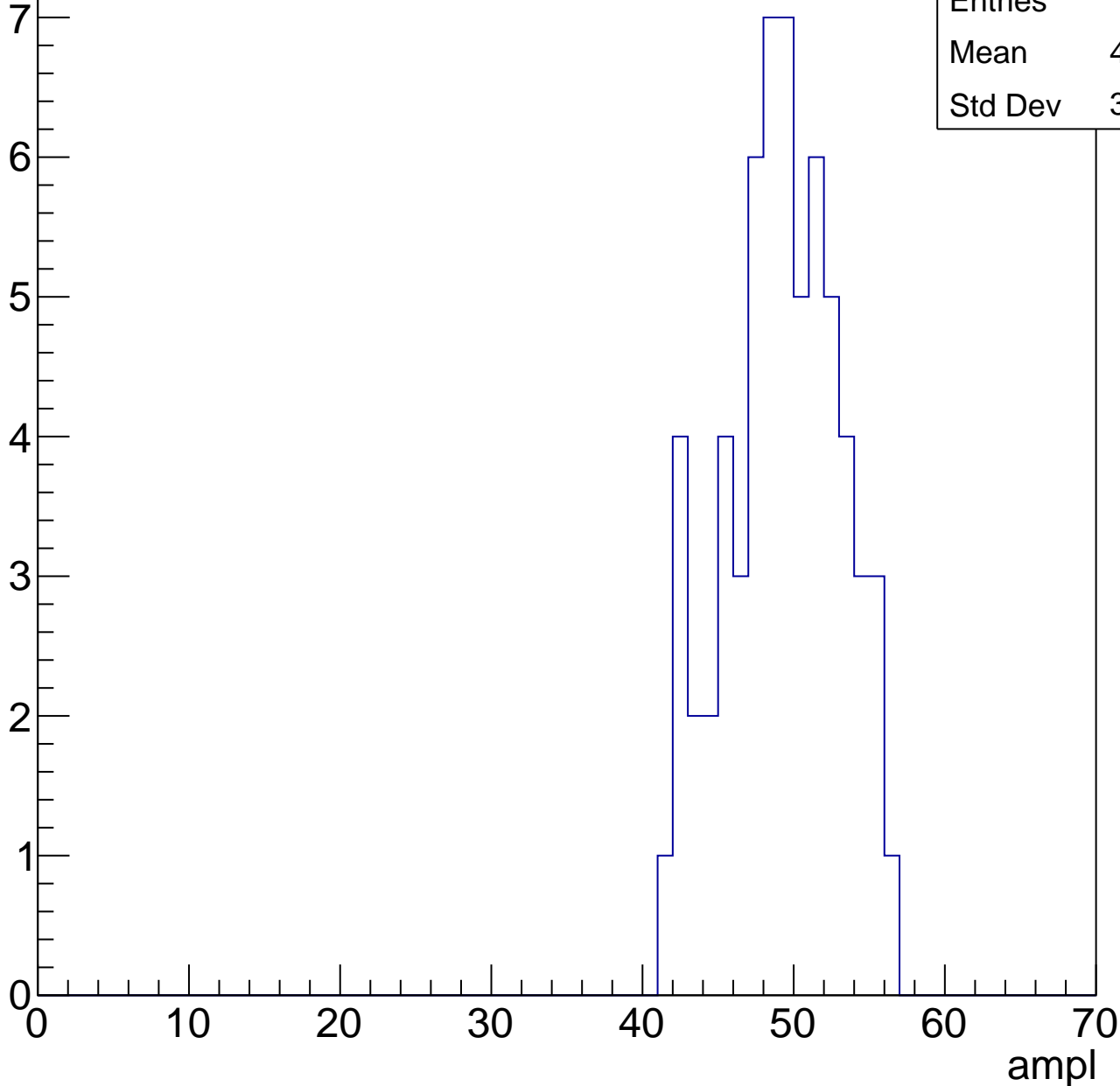


# B1L102S, U25-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.78
Std Dev	3.718

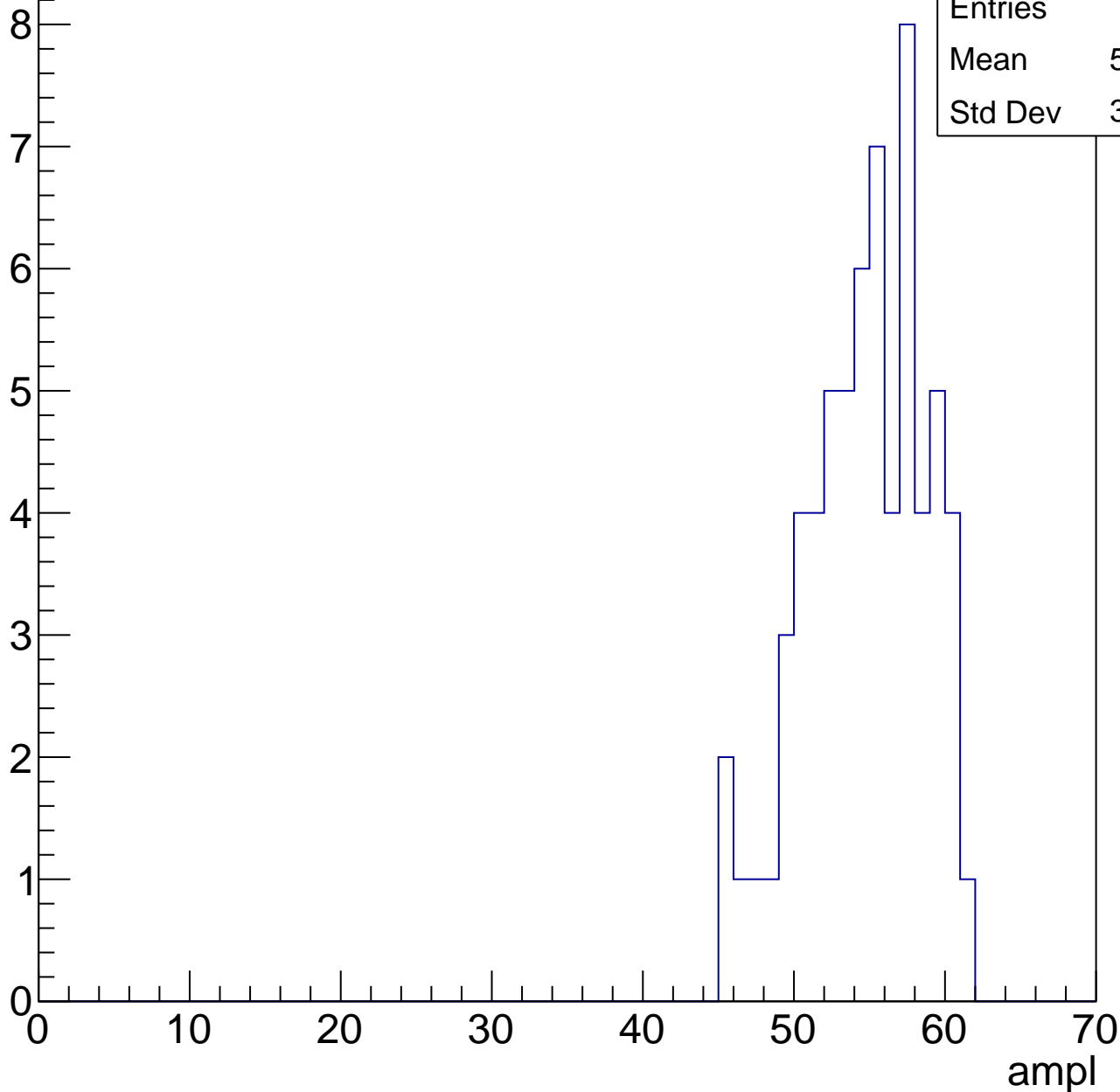


# B1L102S, U25-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	54.22
Std Dev	3.893

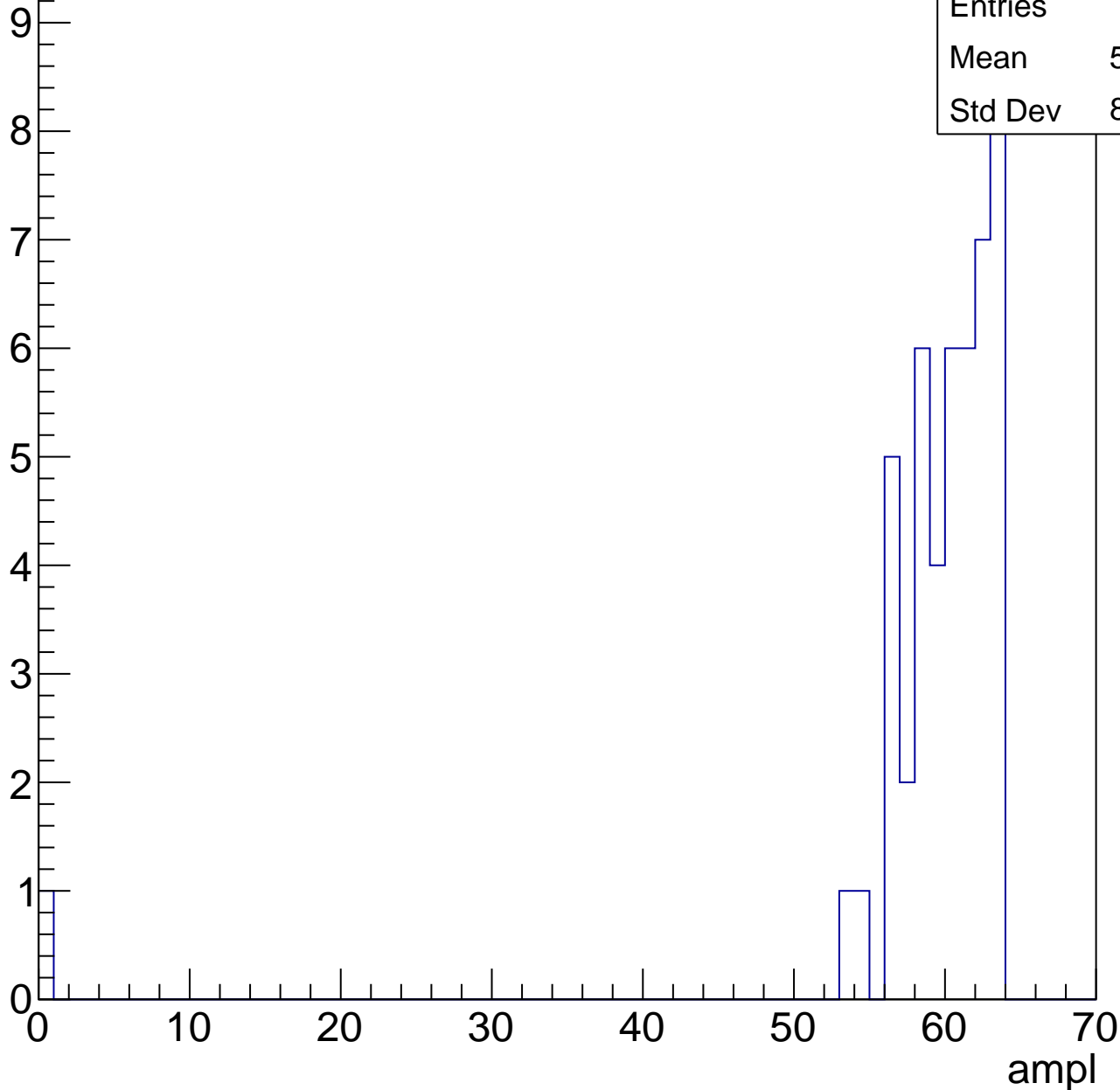


# B1L102S, U25-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

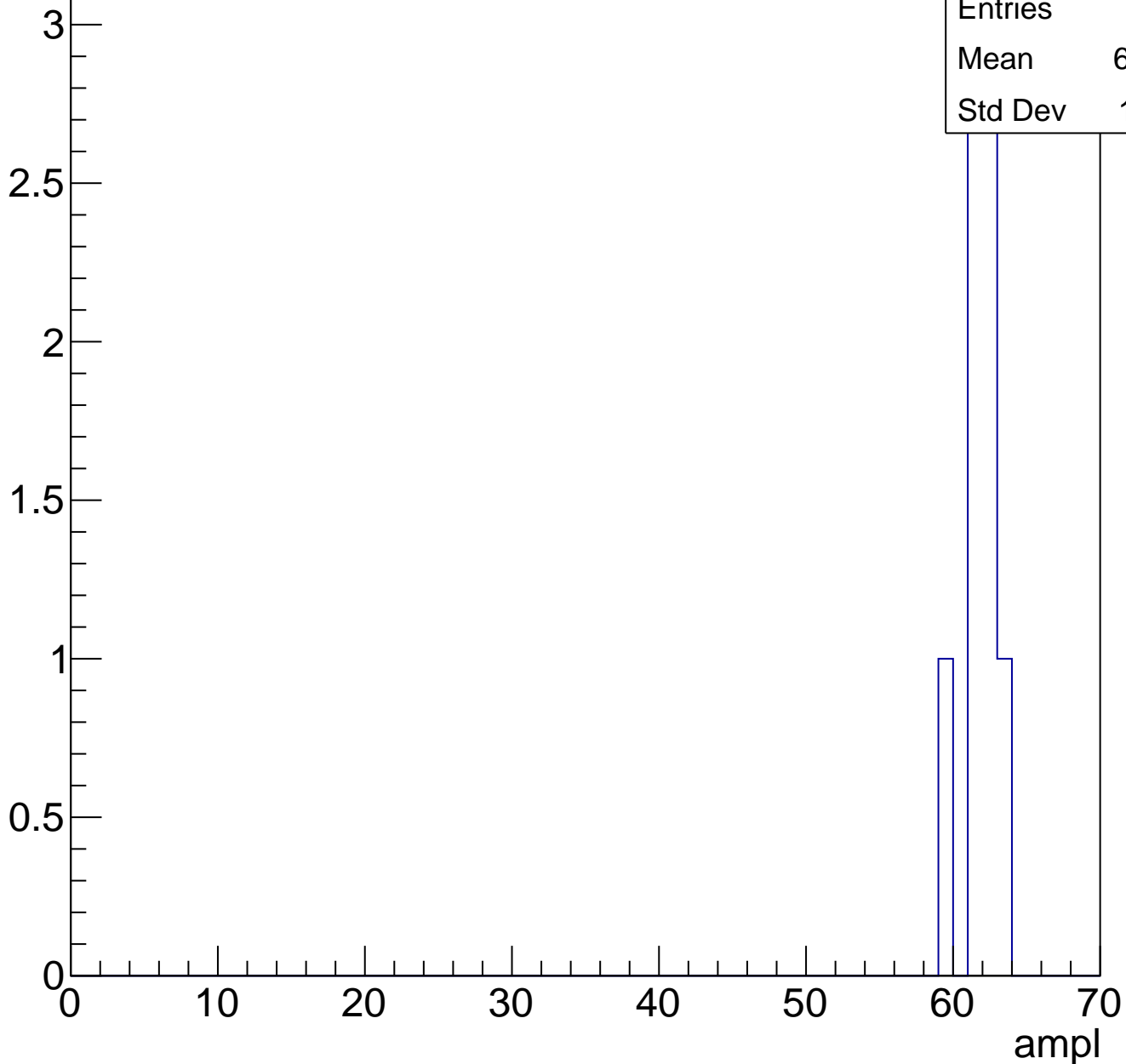
Entries	48
Mean	58.58
Std Dev	8.932



# B1L102S, U25-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch79, adc0

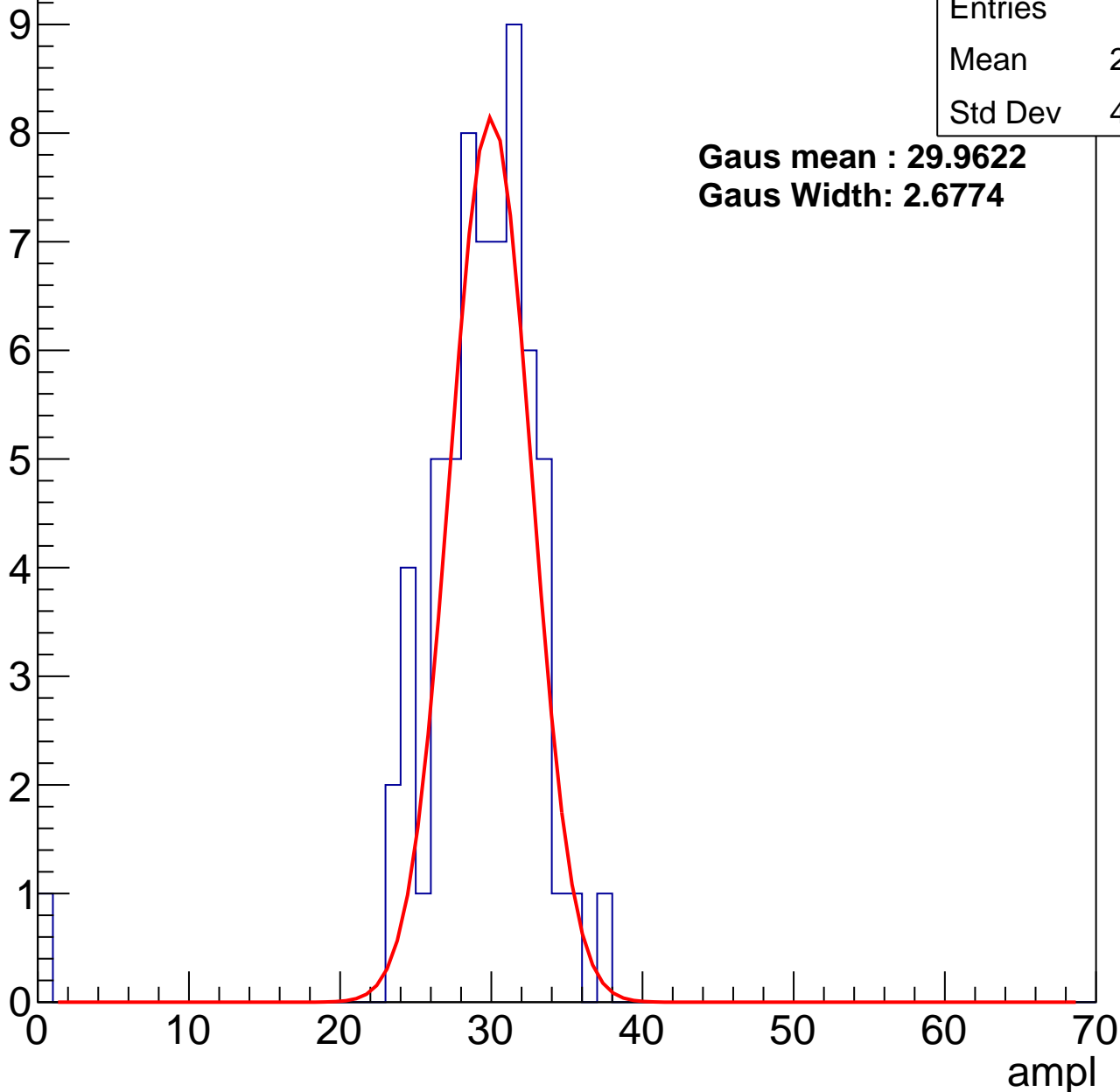
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.75
Std Dev	4.714

**Gaus mean : 29.9622**

**Gaus Width: 2.6774**



# B1L102S, U25-ch79, adc1

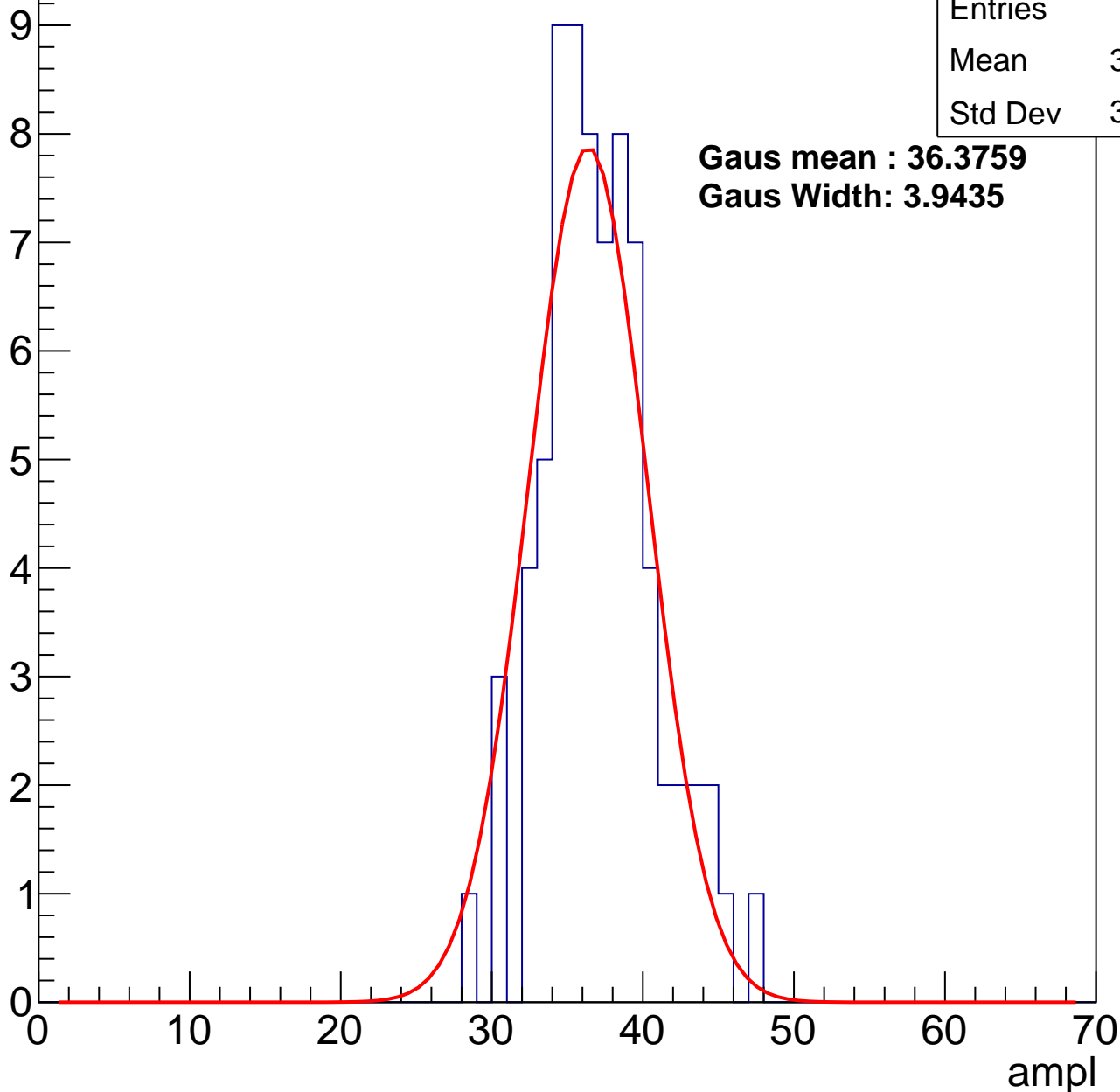
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.64
Std Dev	3.672

**Gaus mean : 36.3759**

**Gaus Width: 3.9435**



# B1L102S, U25-ch79, adc2

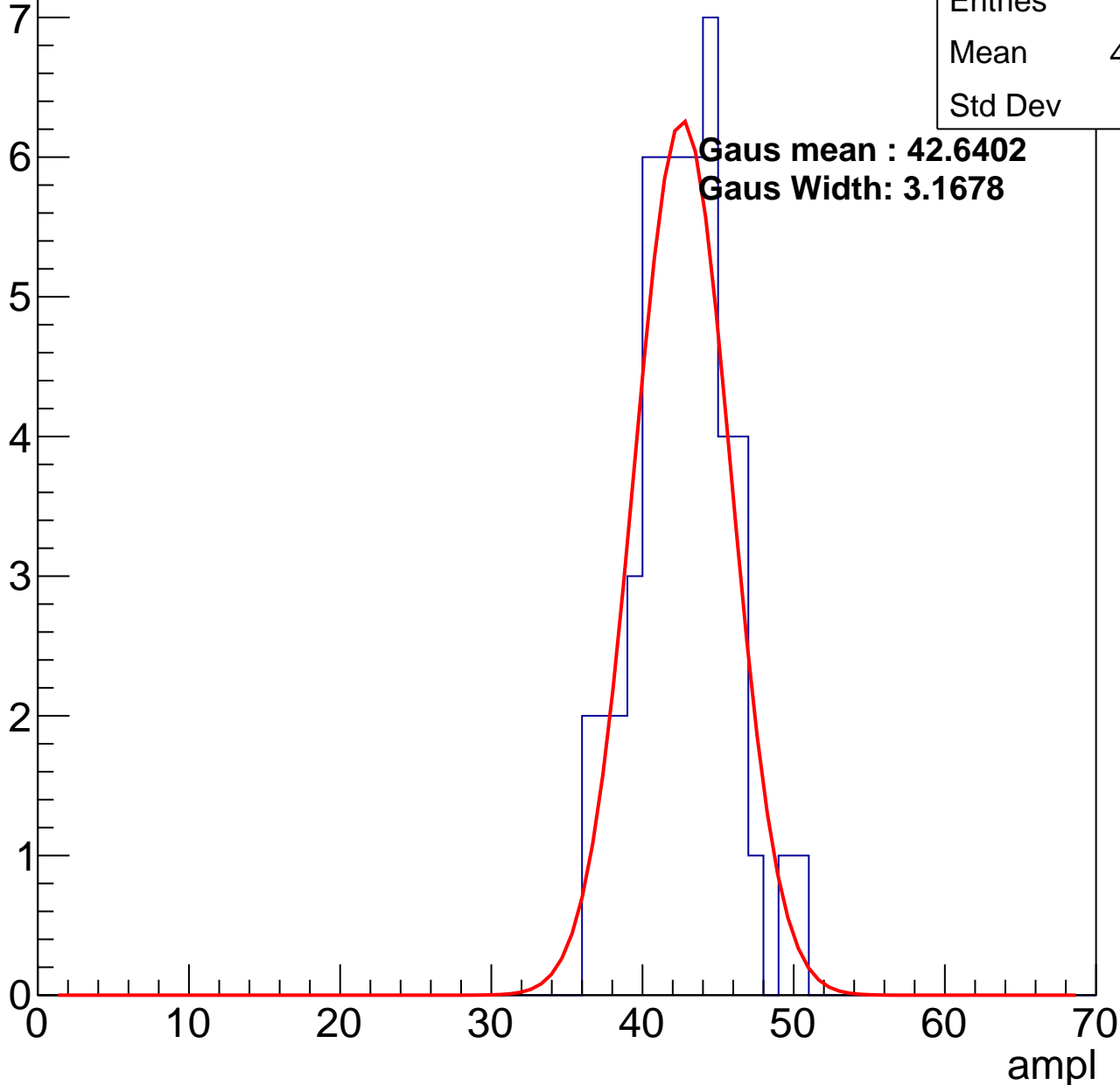
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.22
Std Dev	3.07

**Gaus mean : 42.6402**

**Gaus Width: 3.1678**

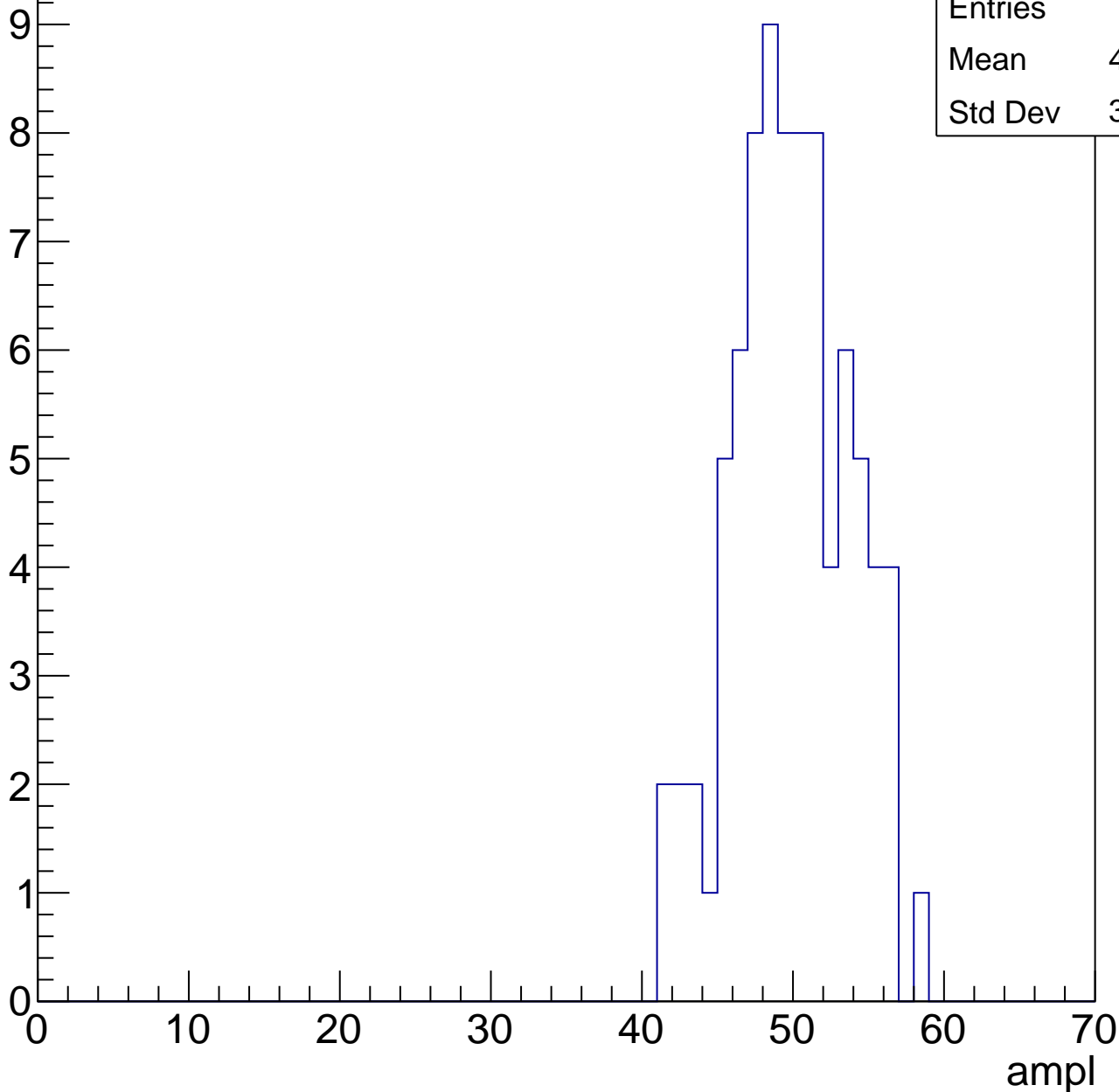


# B1L102S, U25-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	83
Mean	49.43
Std Dev	3.803

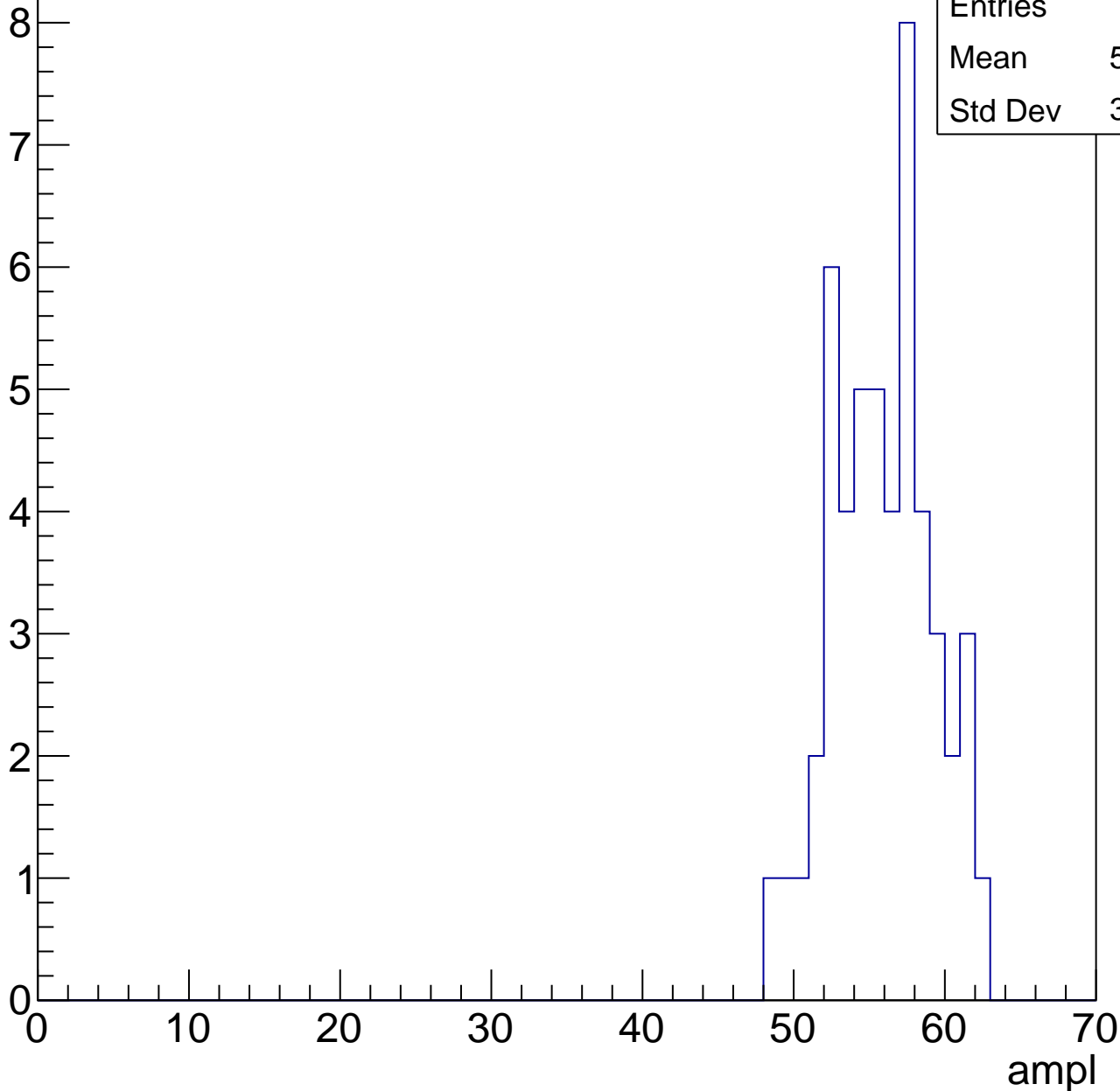


# B1L102S, U25-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.44
Std Dev	3.269

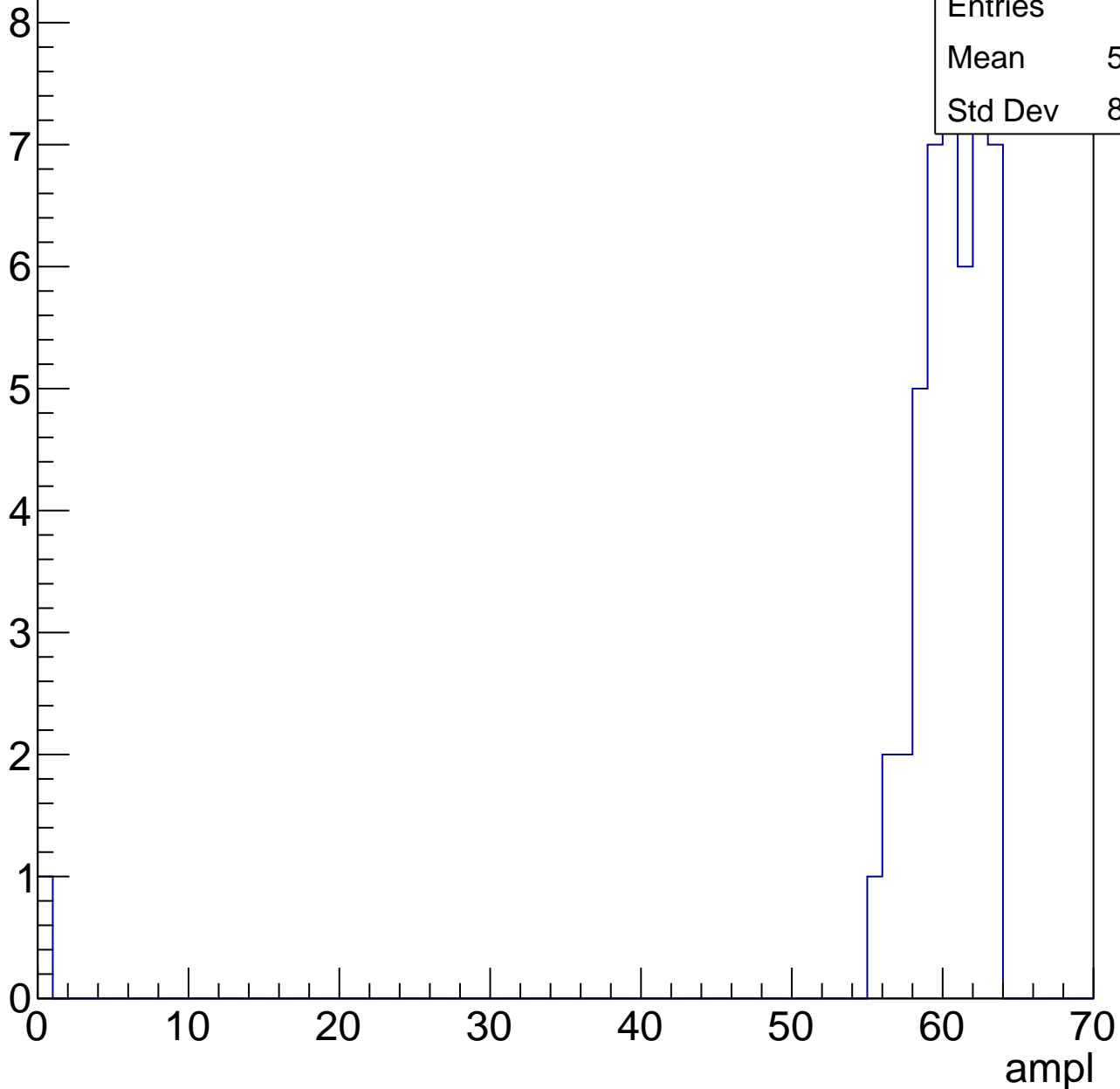


# B1L102S, U25-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.87
Std Dev	8.924



# B1L102S, U25-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B1L102S, U25-ch80, adc0

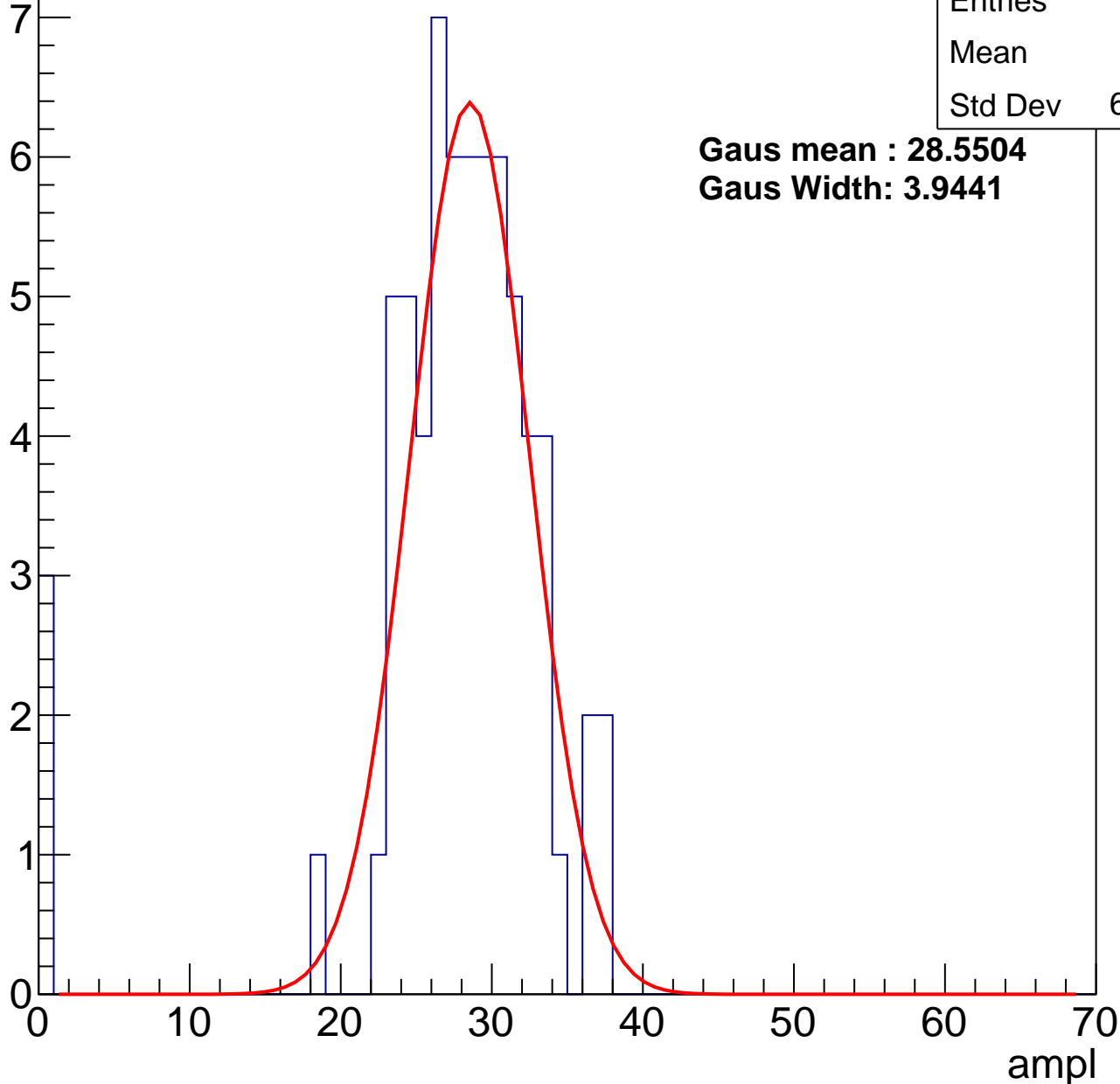
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27
Std Dev	6.924

**Gaus mean : 28.5504**

**Gaus Width: 3.9441**



# B1L102S, U25-ch80, adc1

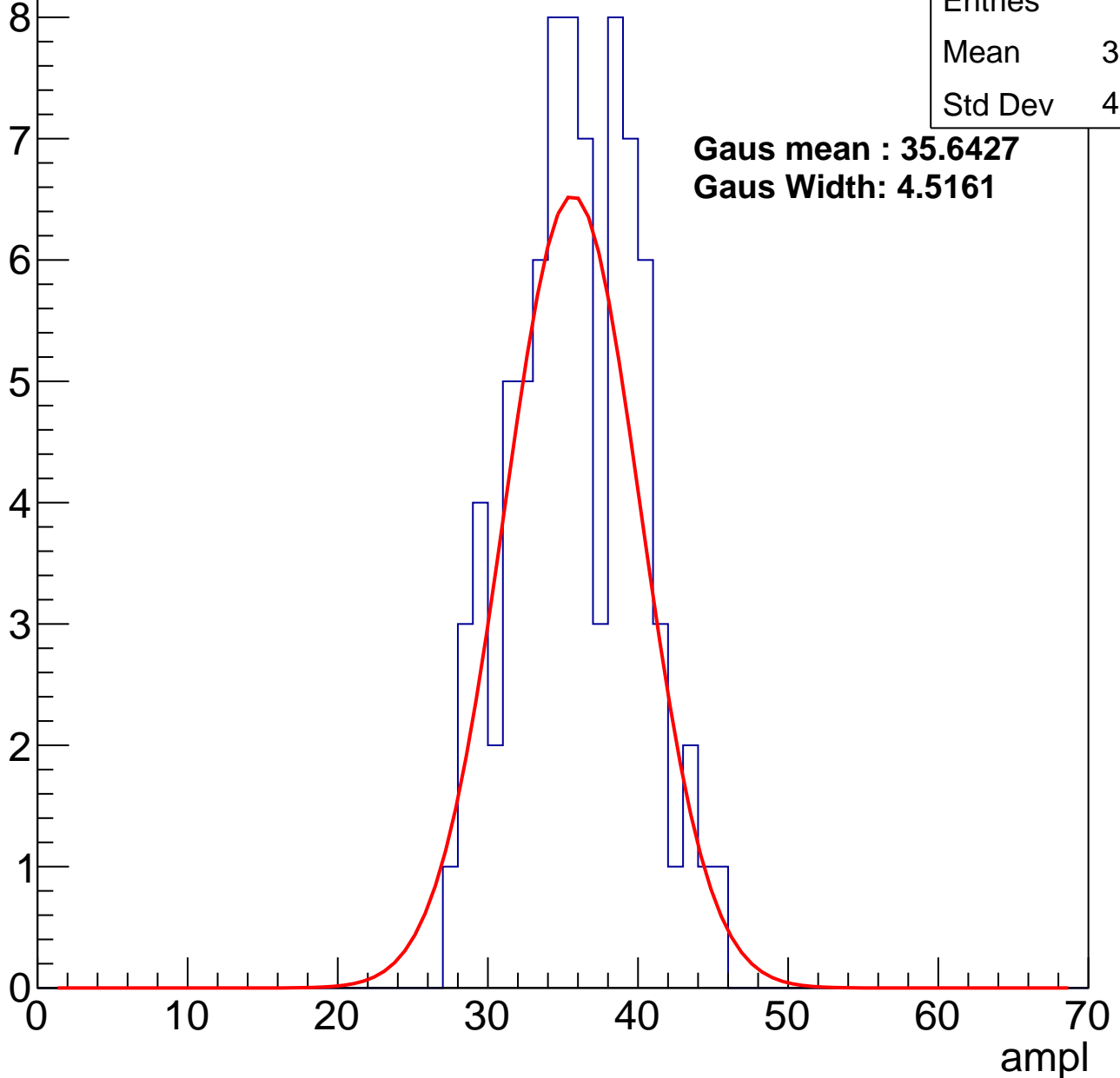
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.46
Std Dev	4.104

**Gaus mean : 35.6427**

**Gaus Width: 4.5161**



# B1L102S, U25-ch80, adc2

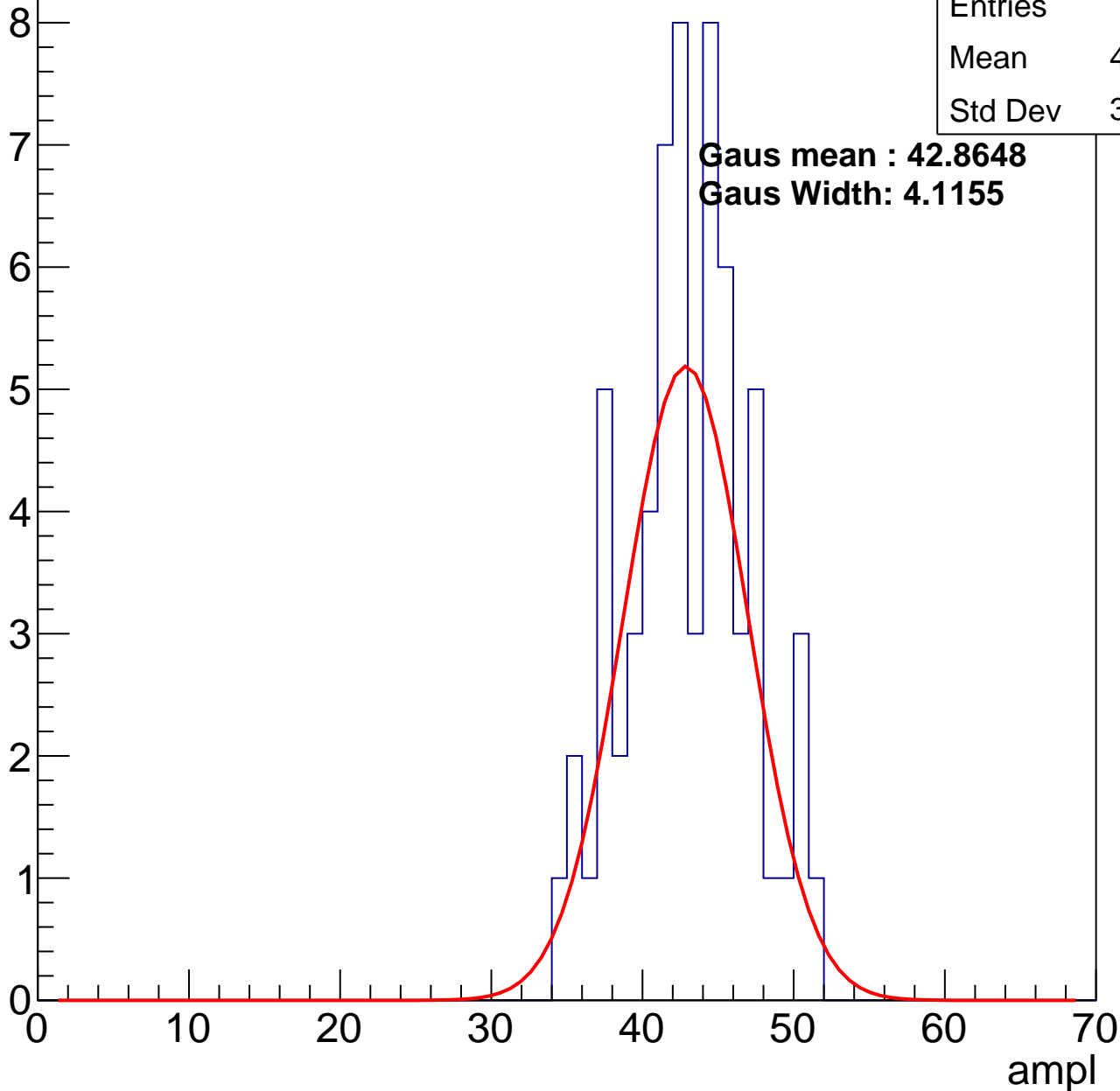
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.55
Std Dev	3.964

**Gaus mean : 42.8648**

**Gaus Width: 4.1155**

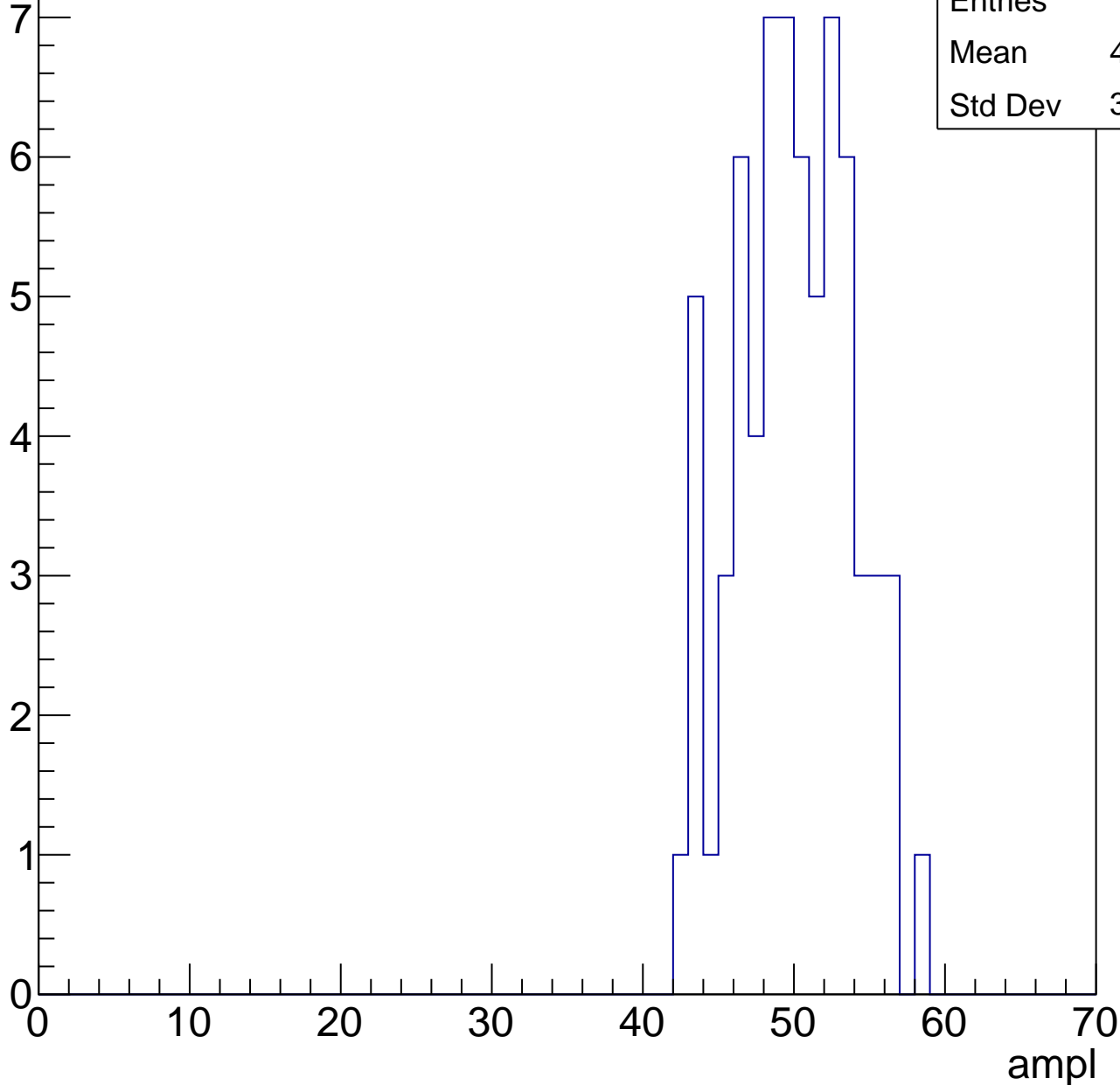


# B1L102S, U25-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.54
Std Dev	3.747

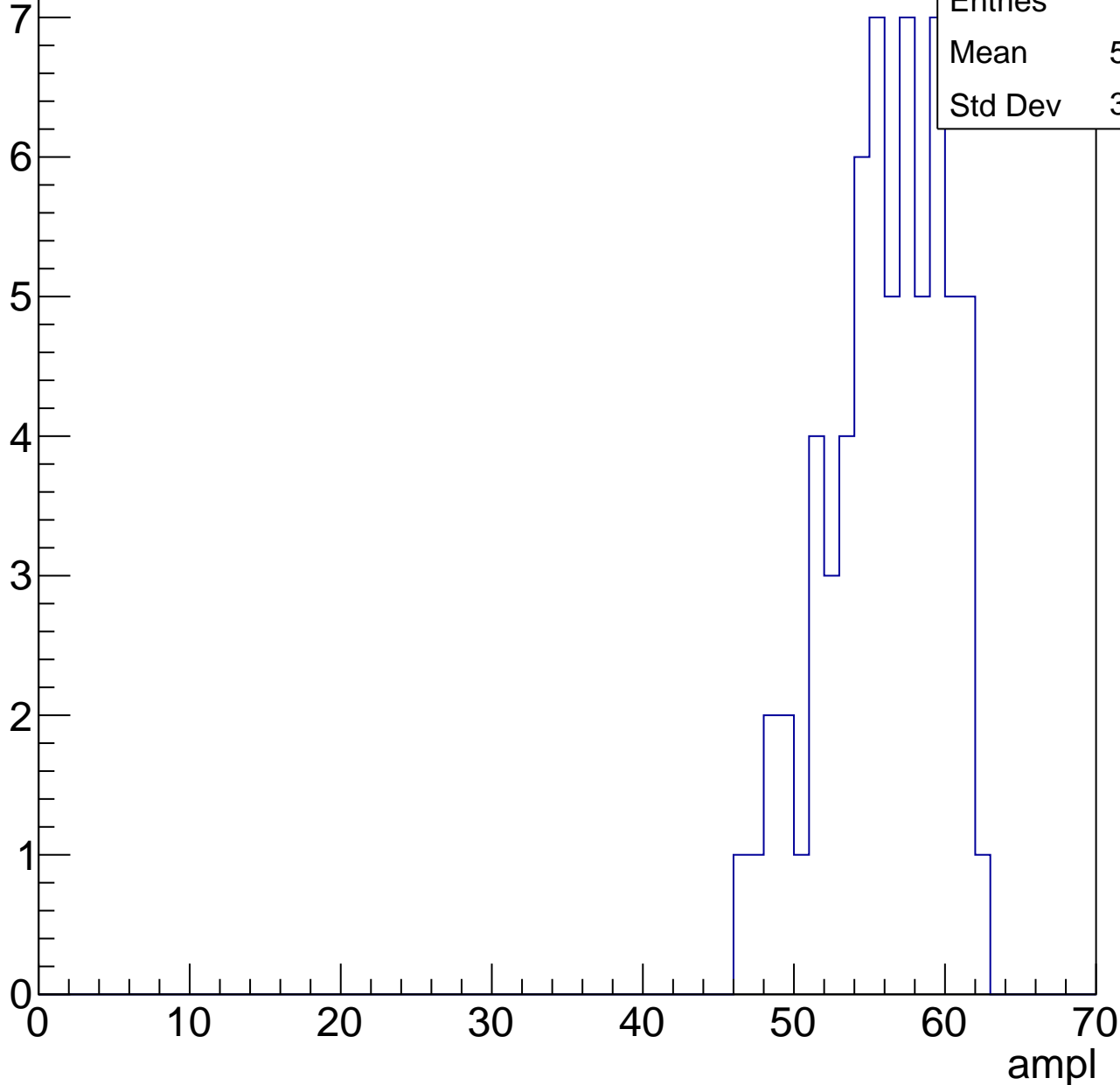


# B1L102S, U25-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.56
Std Dev	3.846

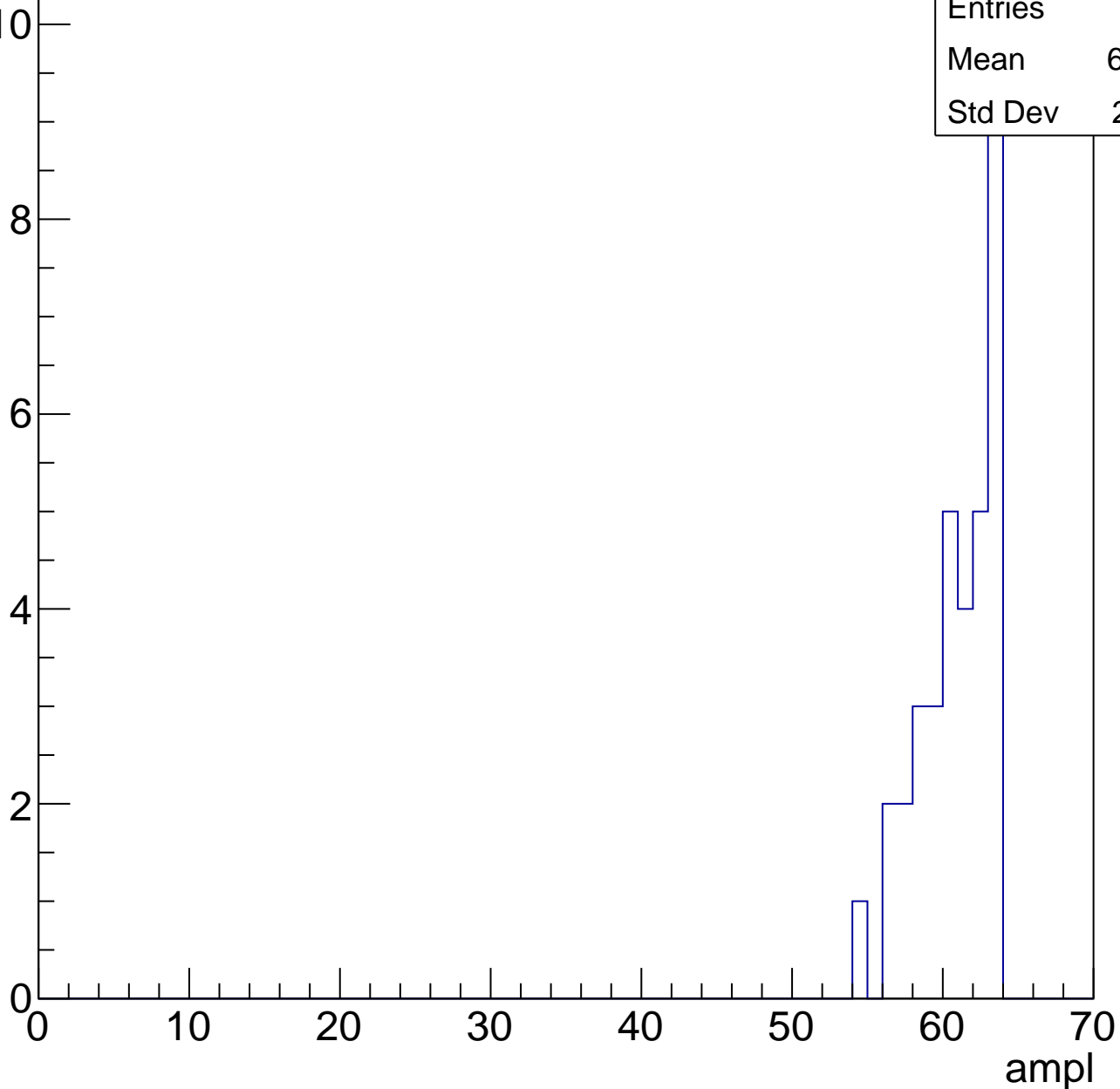


# B1L102S, U25-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

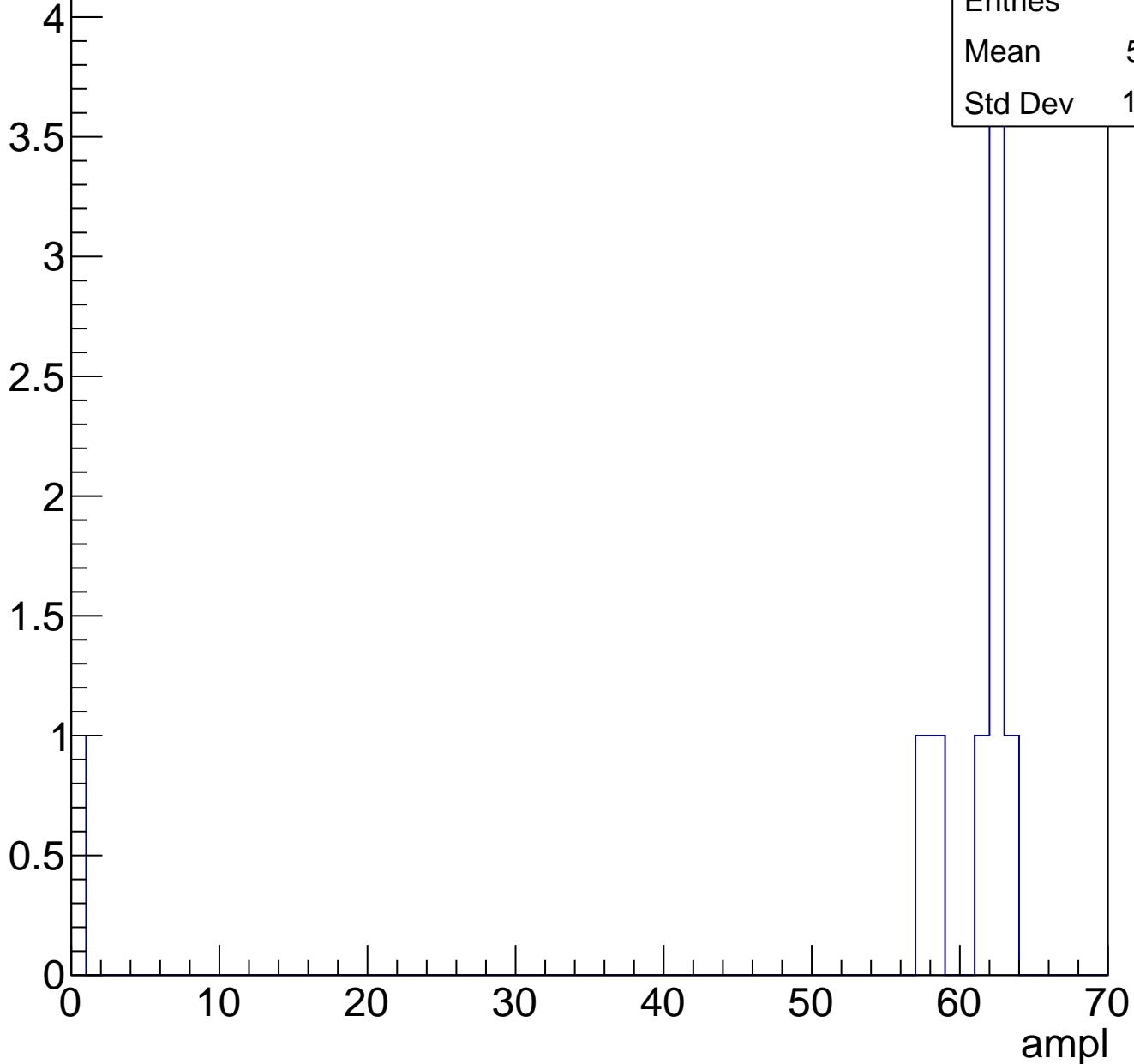
Entries	35
Mean	60.43
Std Dev	2.441



# B1L102S, U25-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch81, adc0

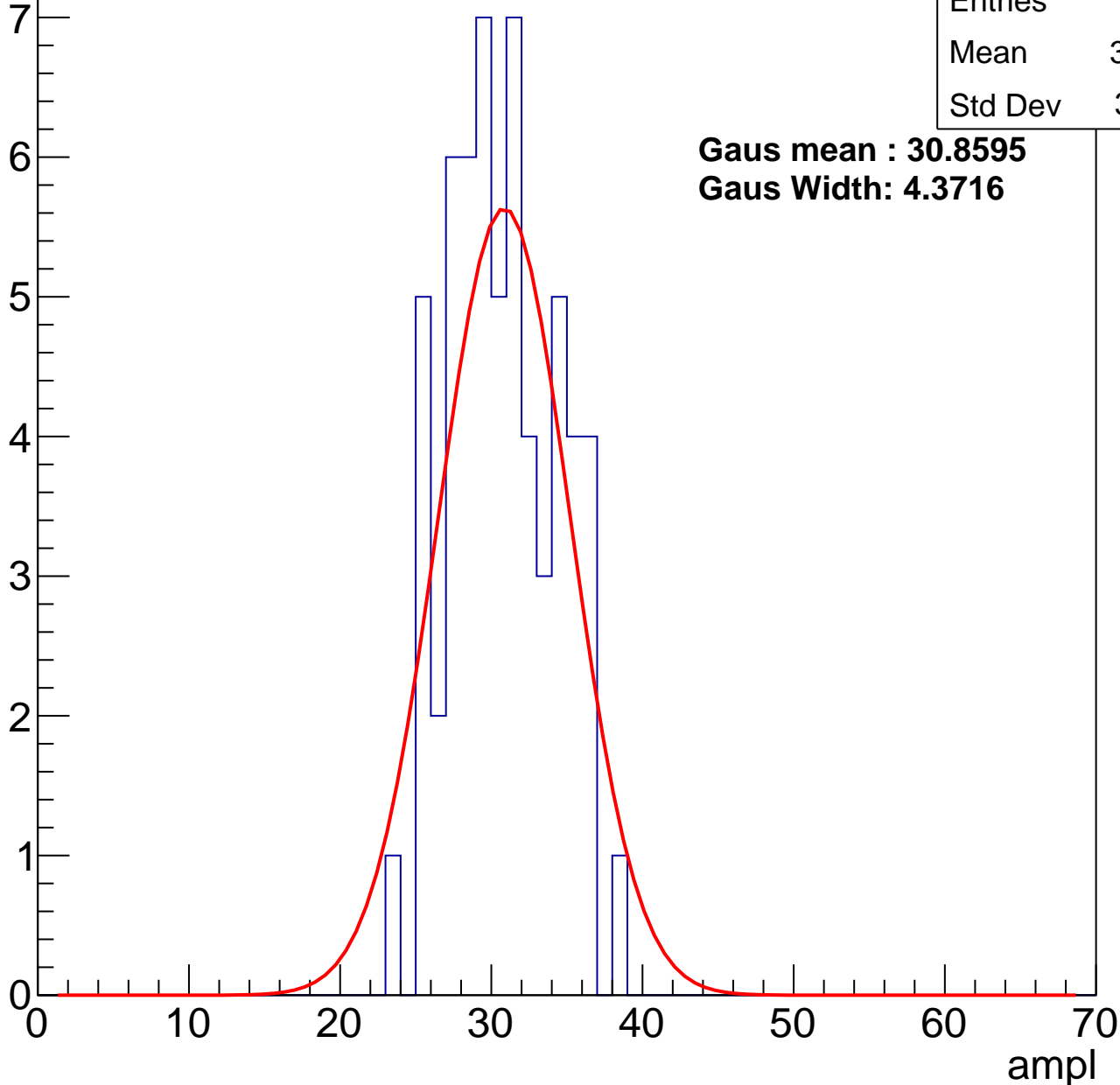
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	30.32
Std Dev	3.471

**Gaus mean : 30.8595**

**Gaus Width: 4.3716**

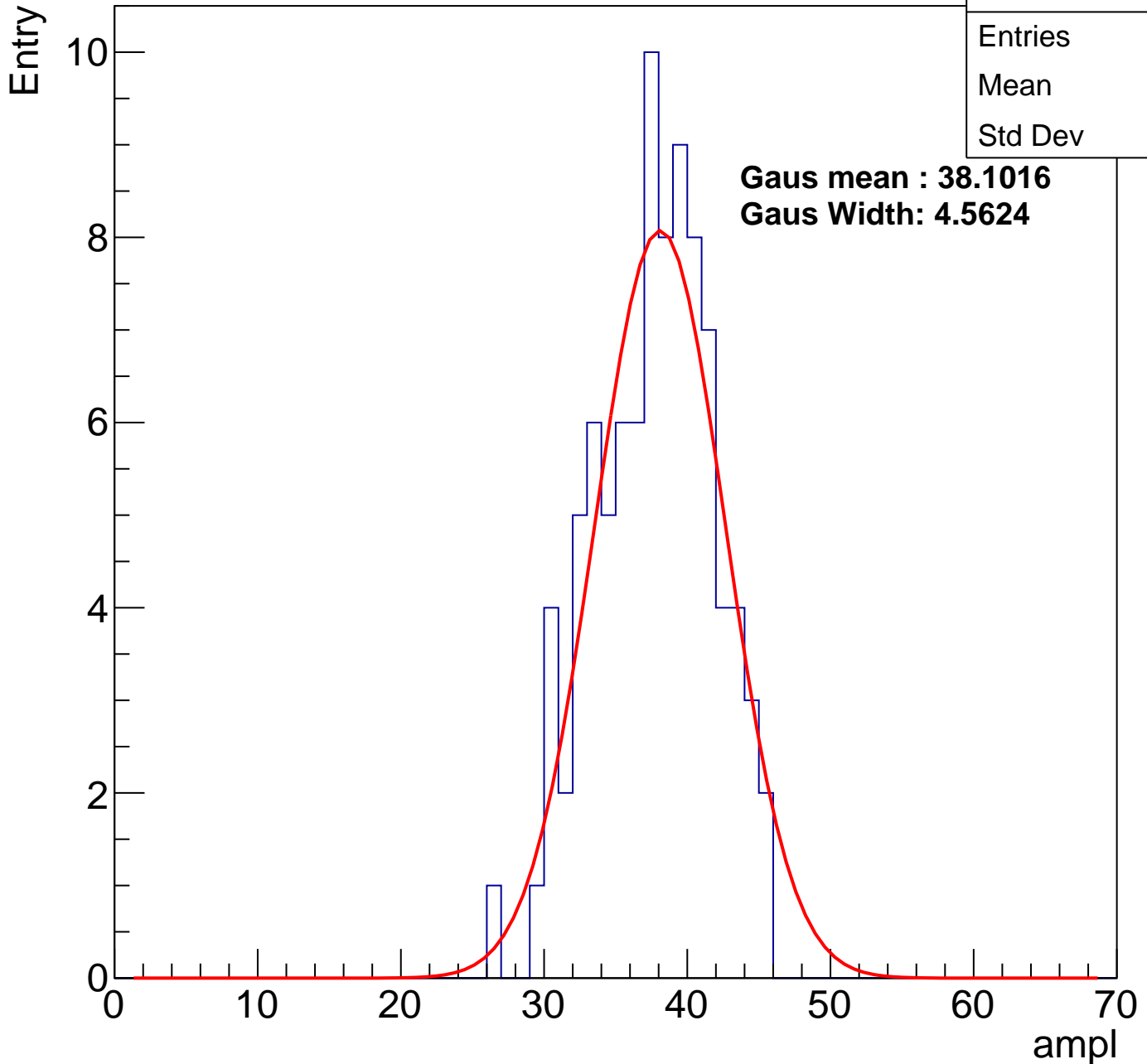


# B1L102S, U25-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	91
Mean	37.2
Std Dev	4.05

**Gaus mean : 38.1016**  
**Gaus Width: 4.5624**



# B1L102S, U25-ch81, adc2

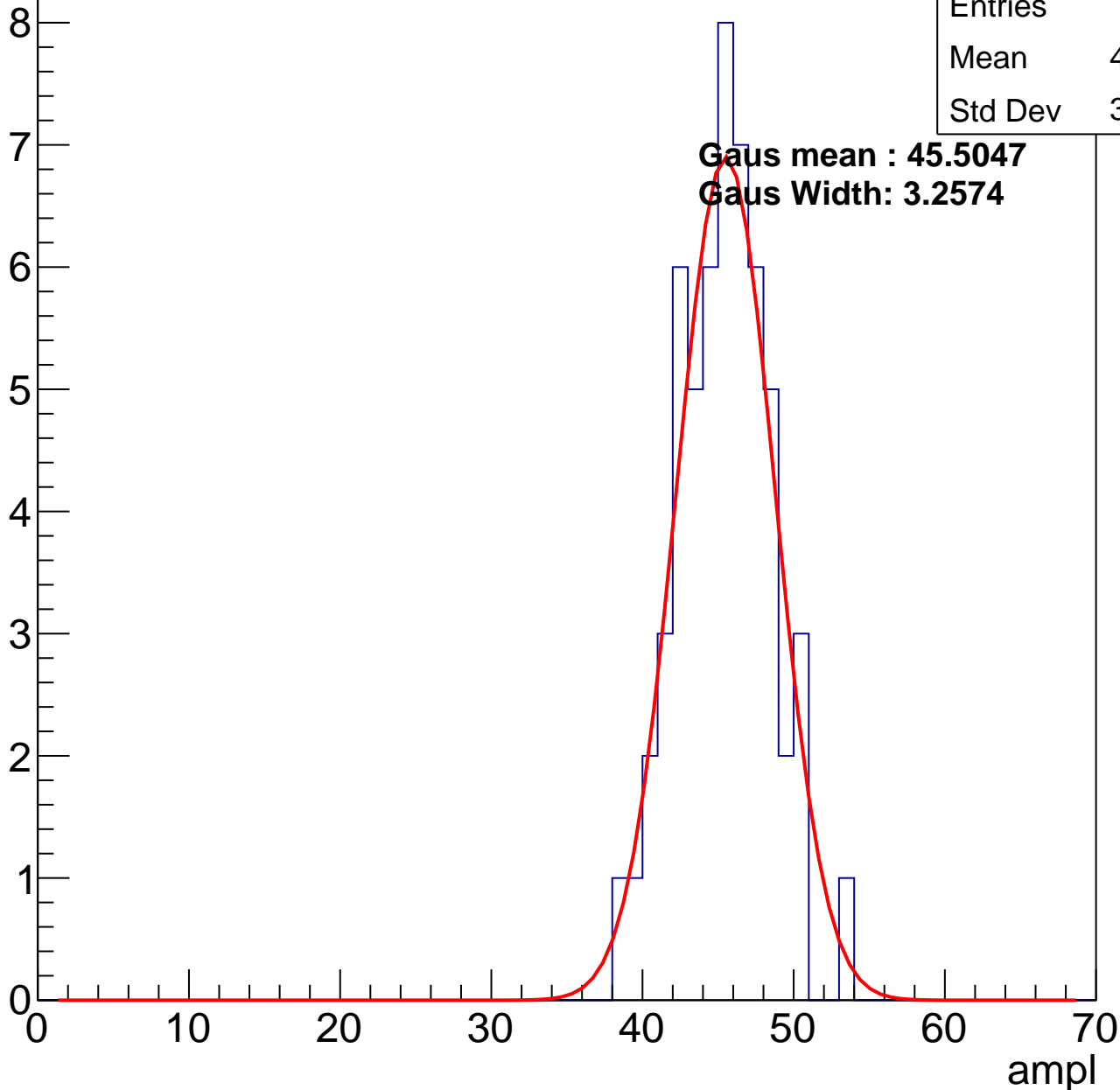
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	44.93
Std Dev	3.017

**Gaus mean : 45.5047**

**Gaus Width: 3.2574**

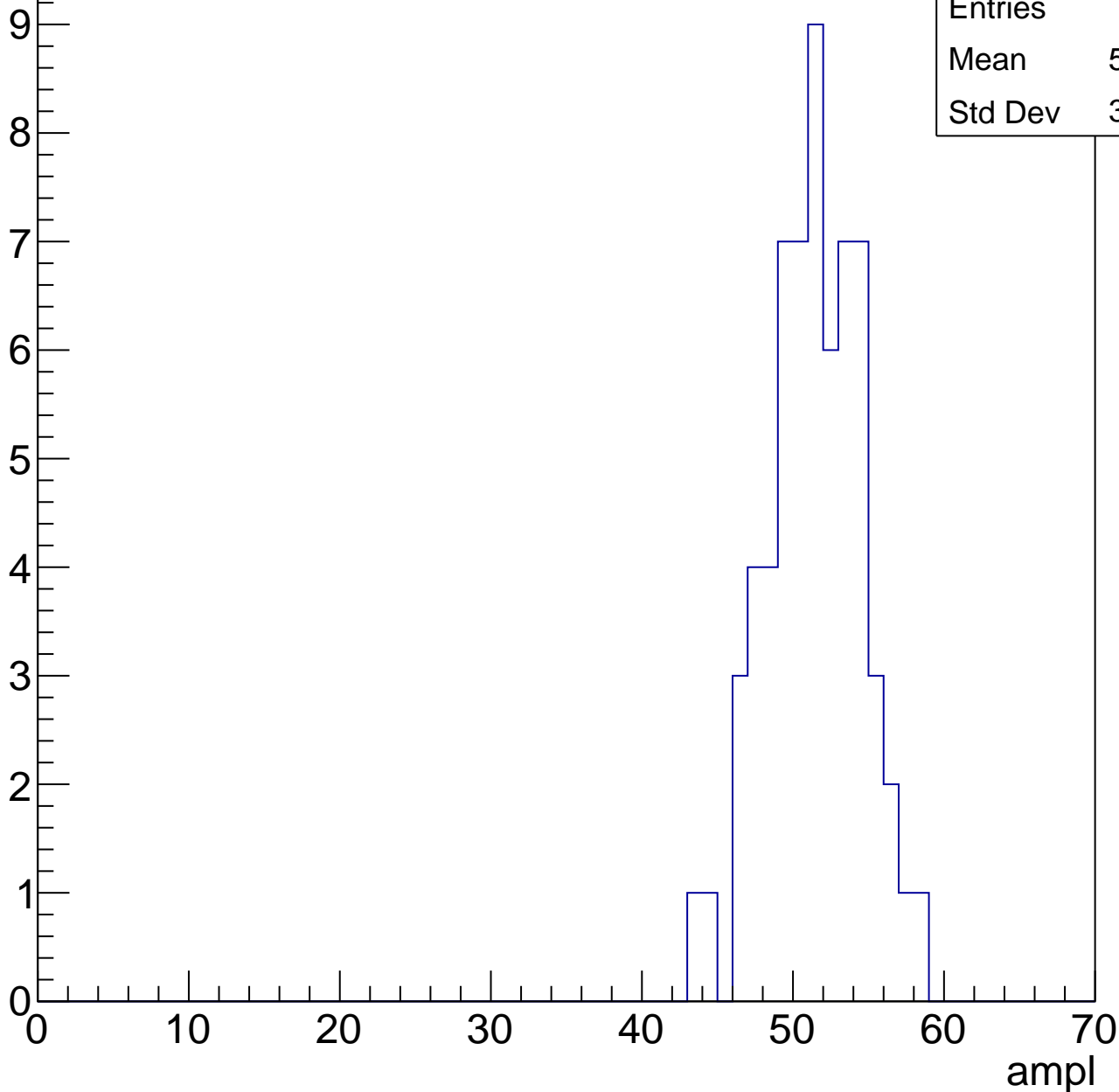


# B1L102S, U25-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.95
Std Dev	3.093

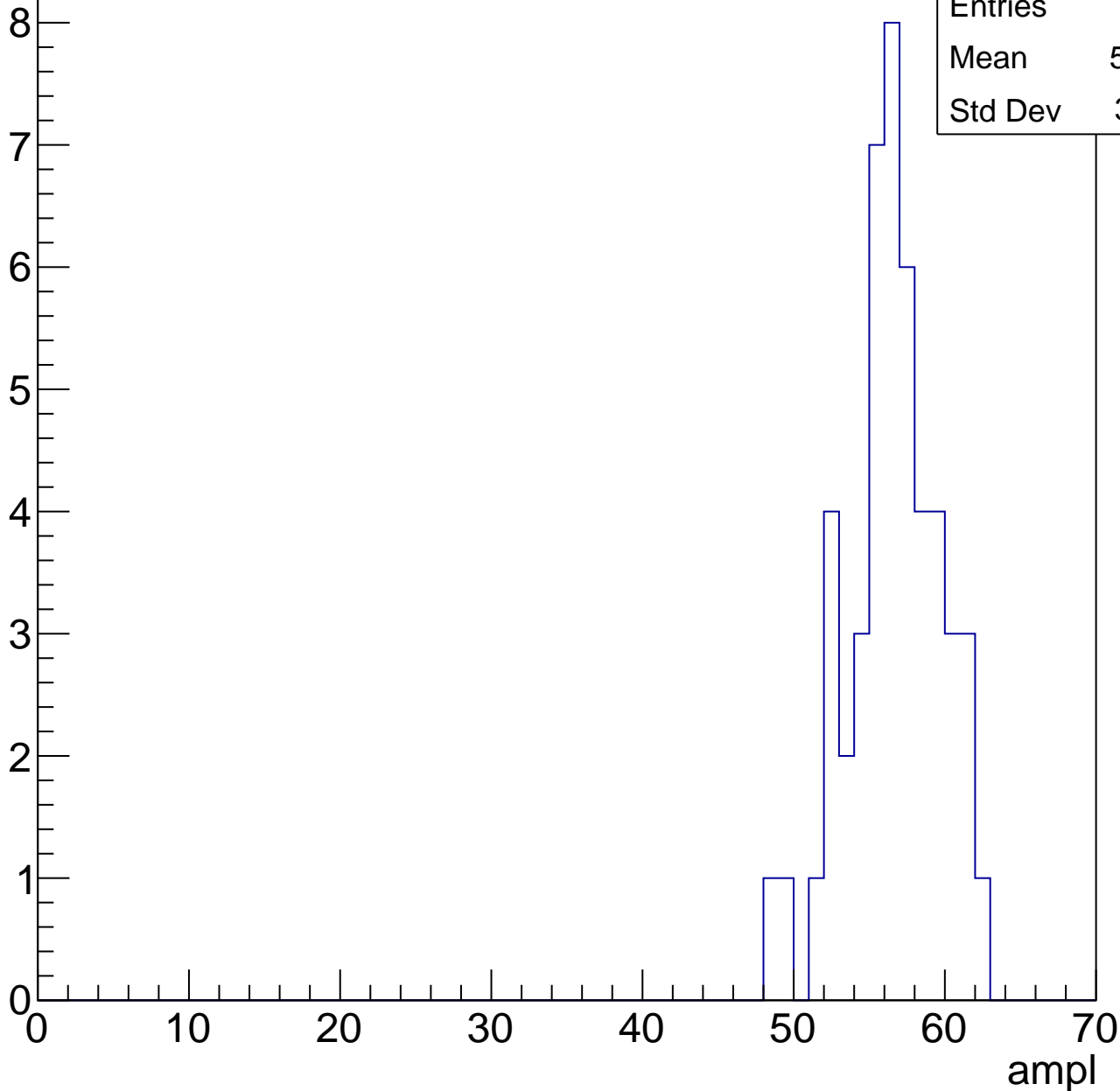


# B1L102S, U25-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	56.08
Std Dev	3.081

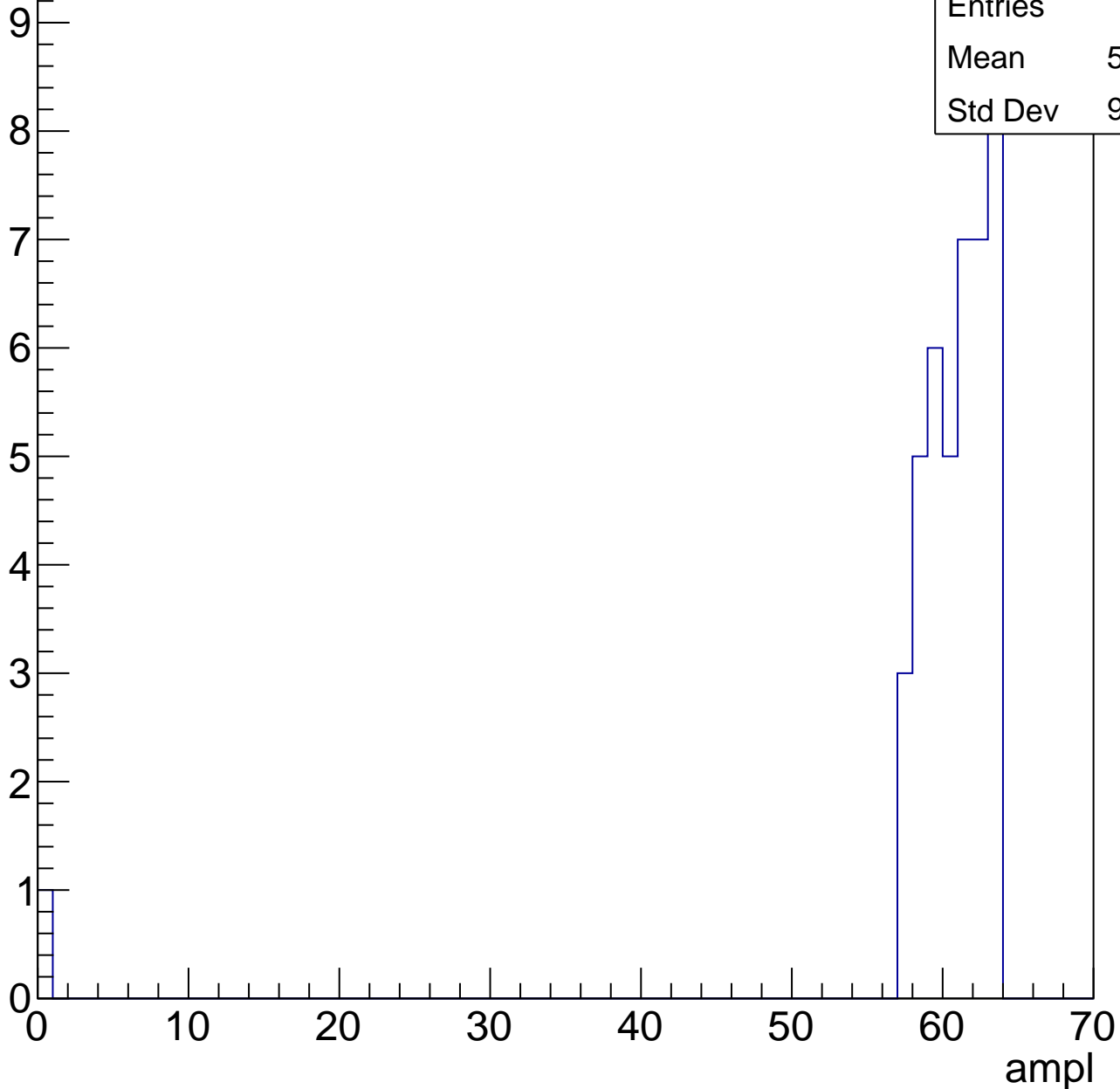


# B1L102S, U25-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.14
Std Dev	9.323



# B1L102S, U25-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.4
Std Dev	1.2

ampl



# B1L102S, U25-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch82, adc0

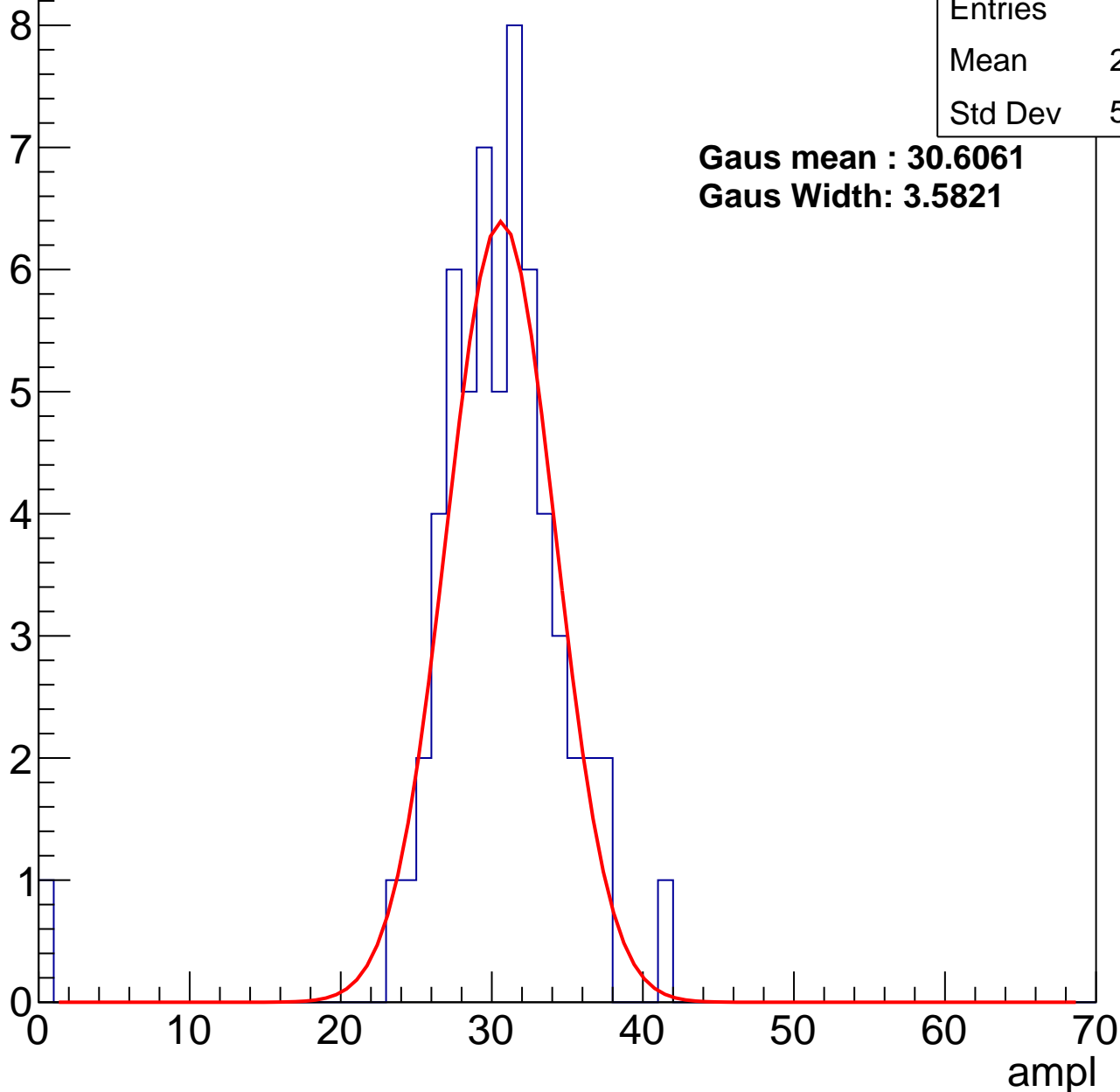
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.78
Std Dev	5.219

**Gaus mean : 30.6061**

**Gaus Width: 3.5821**



# B1L102S, U25-ch82, adc1

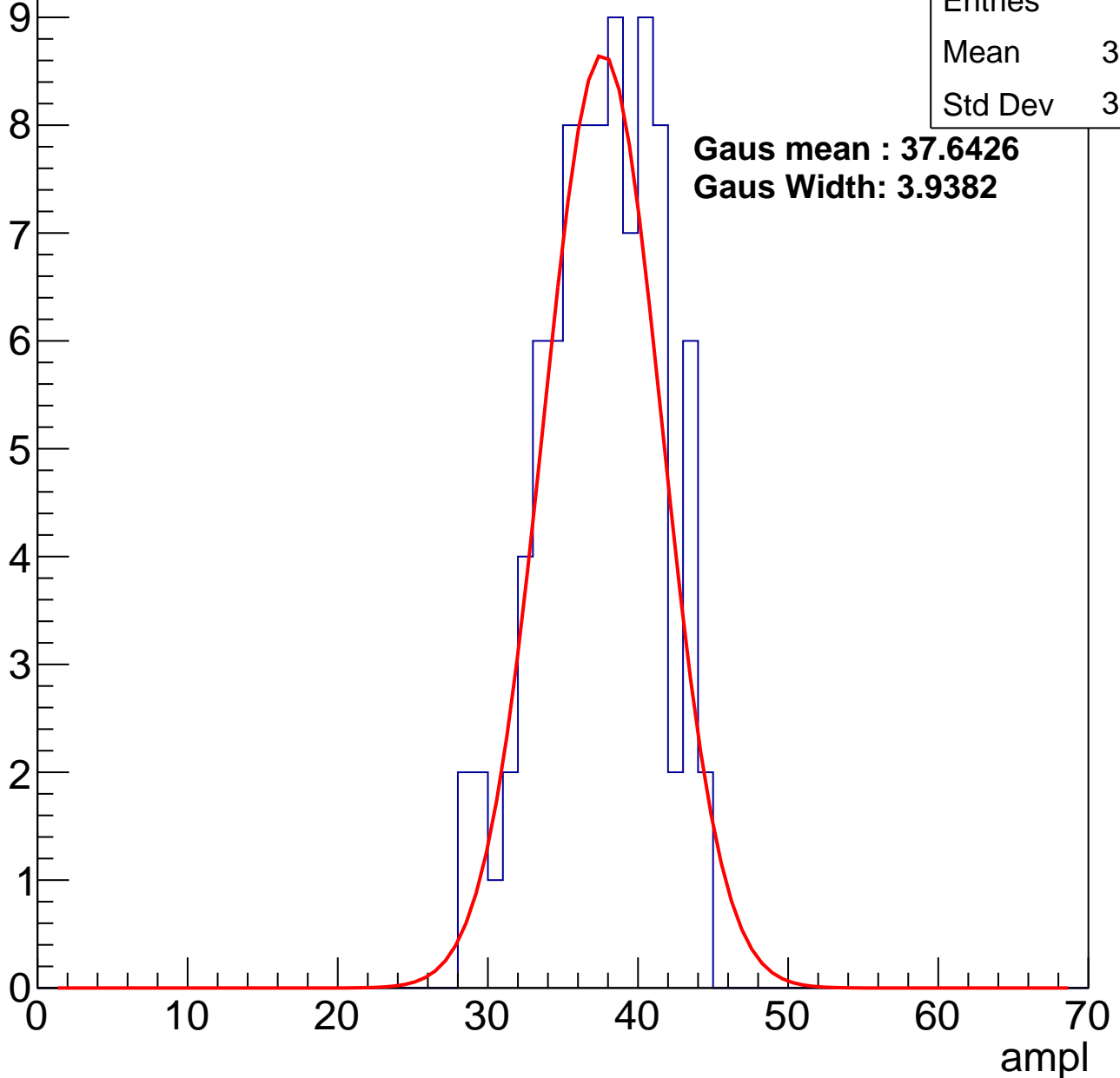
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	90
Mean	37.03
Std Dev	3.816

**Gaus mean : 37.6426**

**Gaus Width: 3.9382**



# B1L102S, U25-ch82, adc2

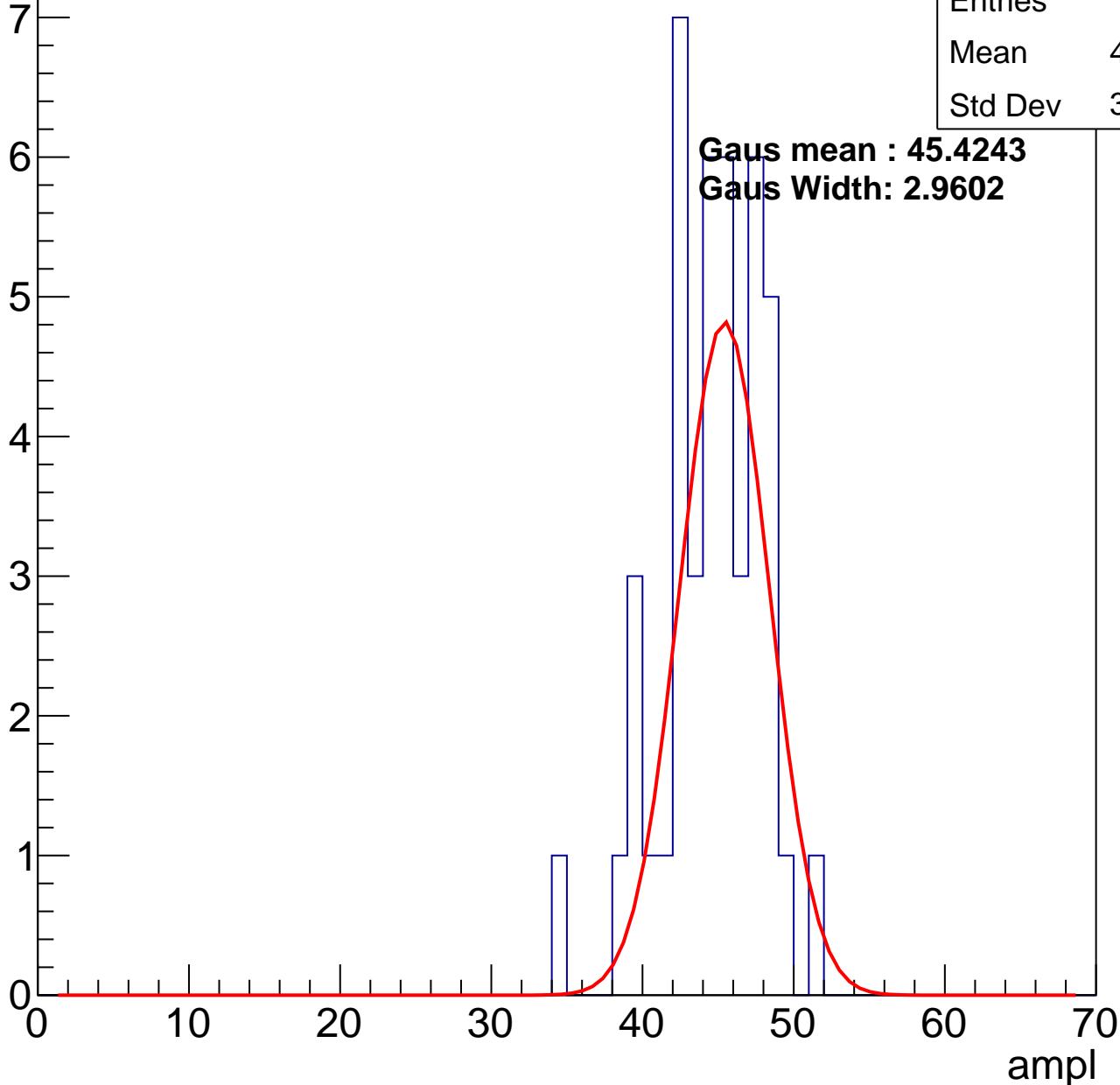
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	45
Mean	44.16
Std Dev	3.306

**Gaus mean : 45.4243**

**Gaus Width: 2.9602**

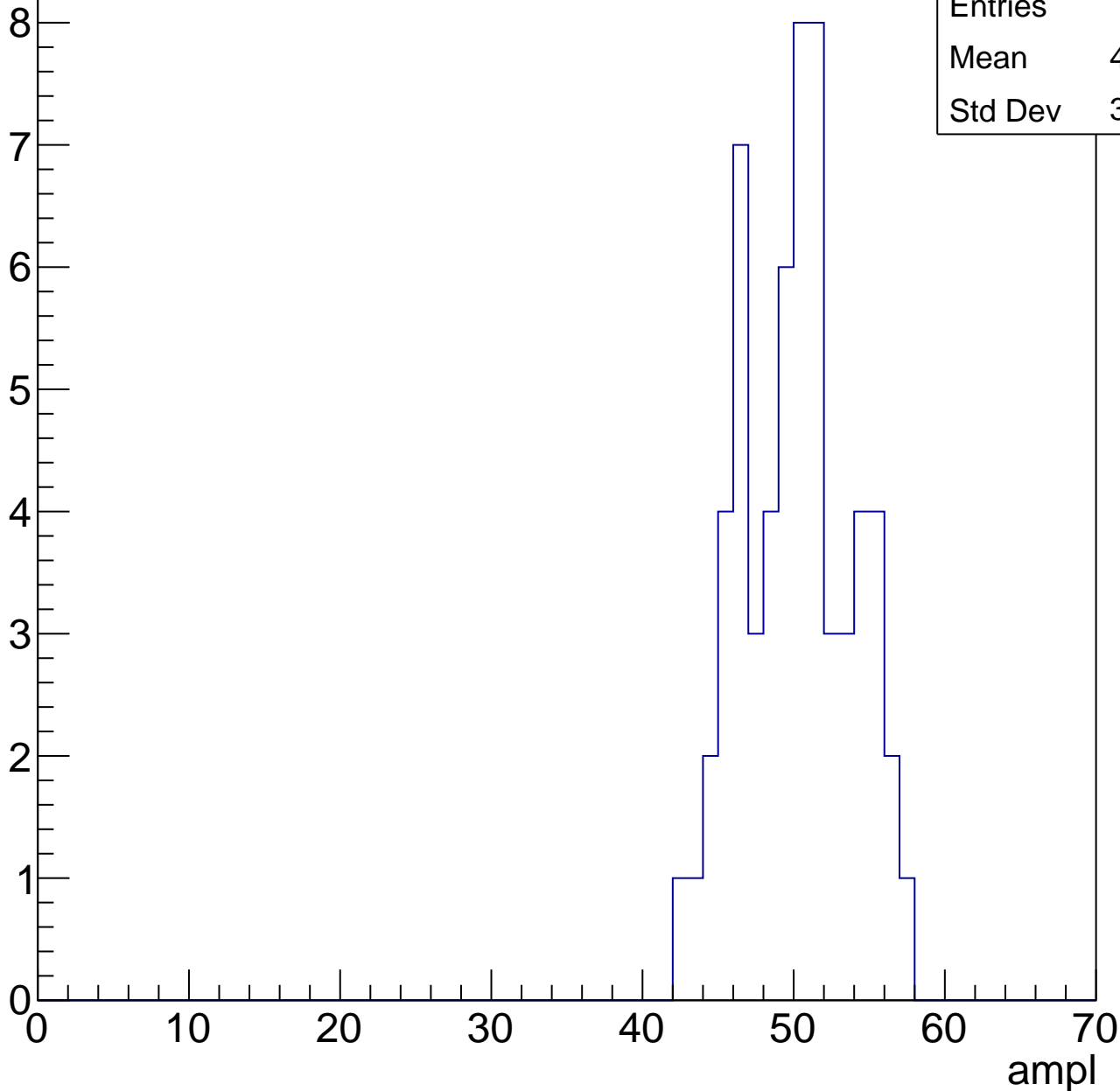


# B1L102S, U25-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.67
Std Dev	3.556

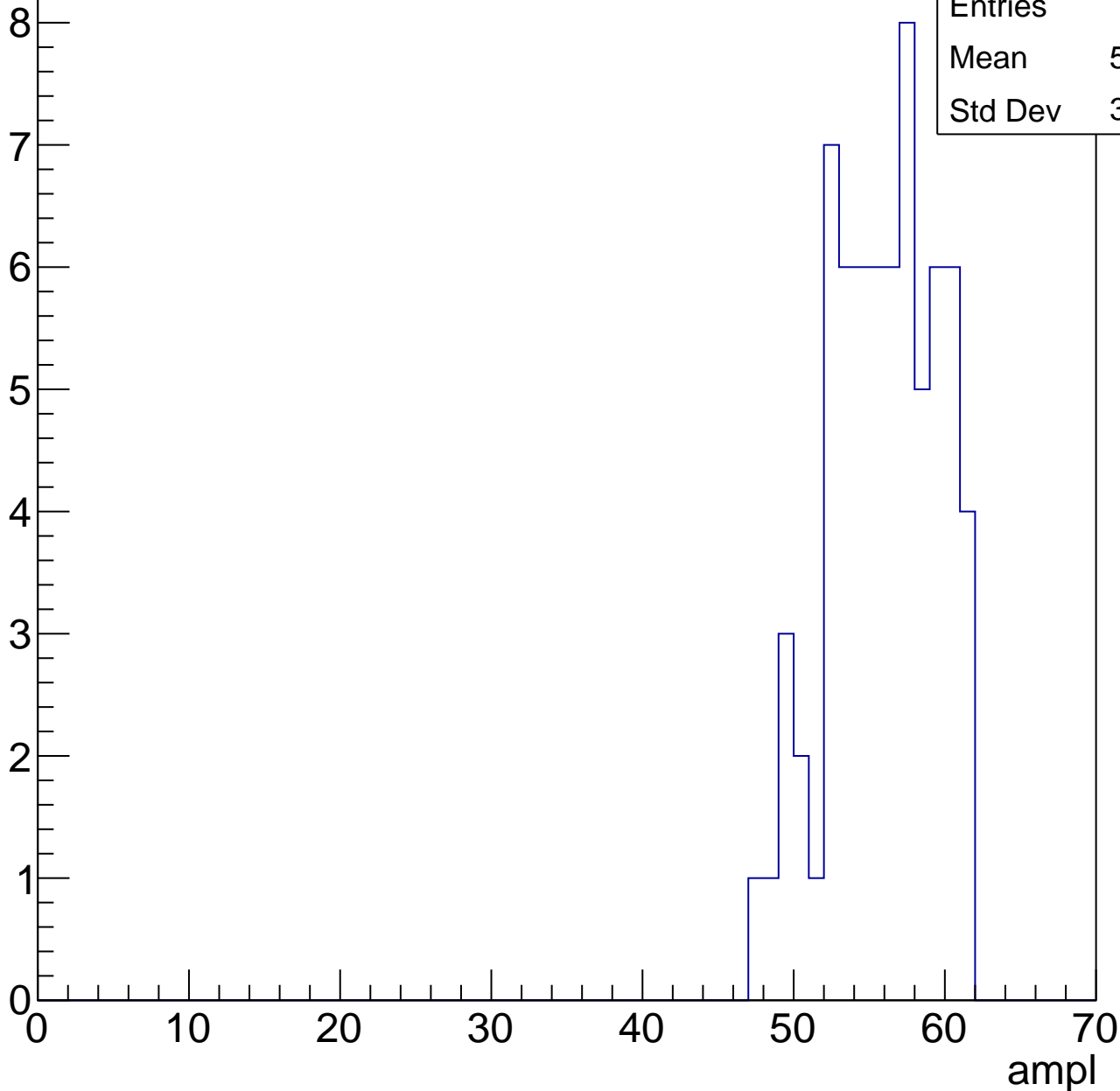


# B1L102S, U25-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.43
Std Dev	3.516



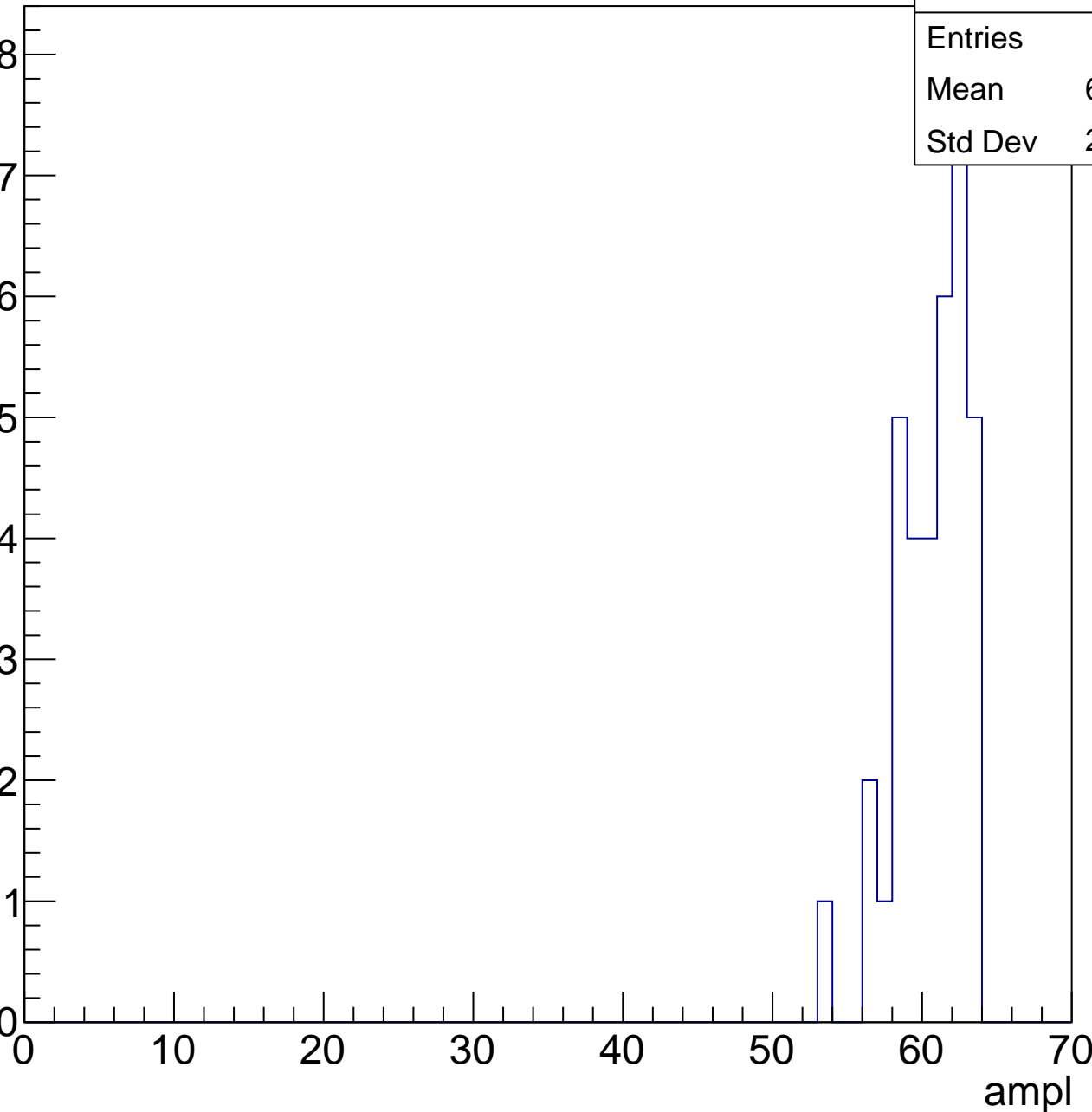
# B1L102S, U25-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	36
Mean	60.14
Std Dev	2.335

8  
7  
6  
5  
4  
3  
2  
1  
0

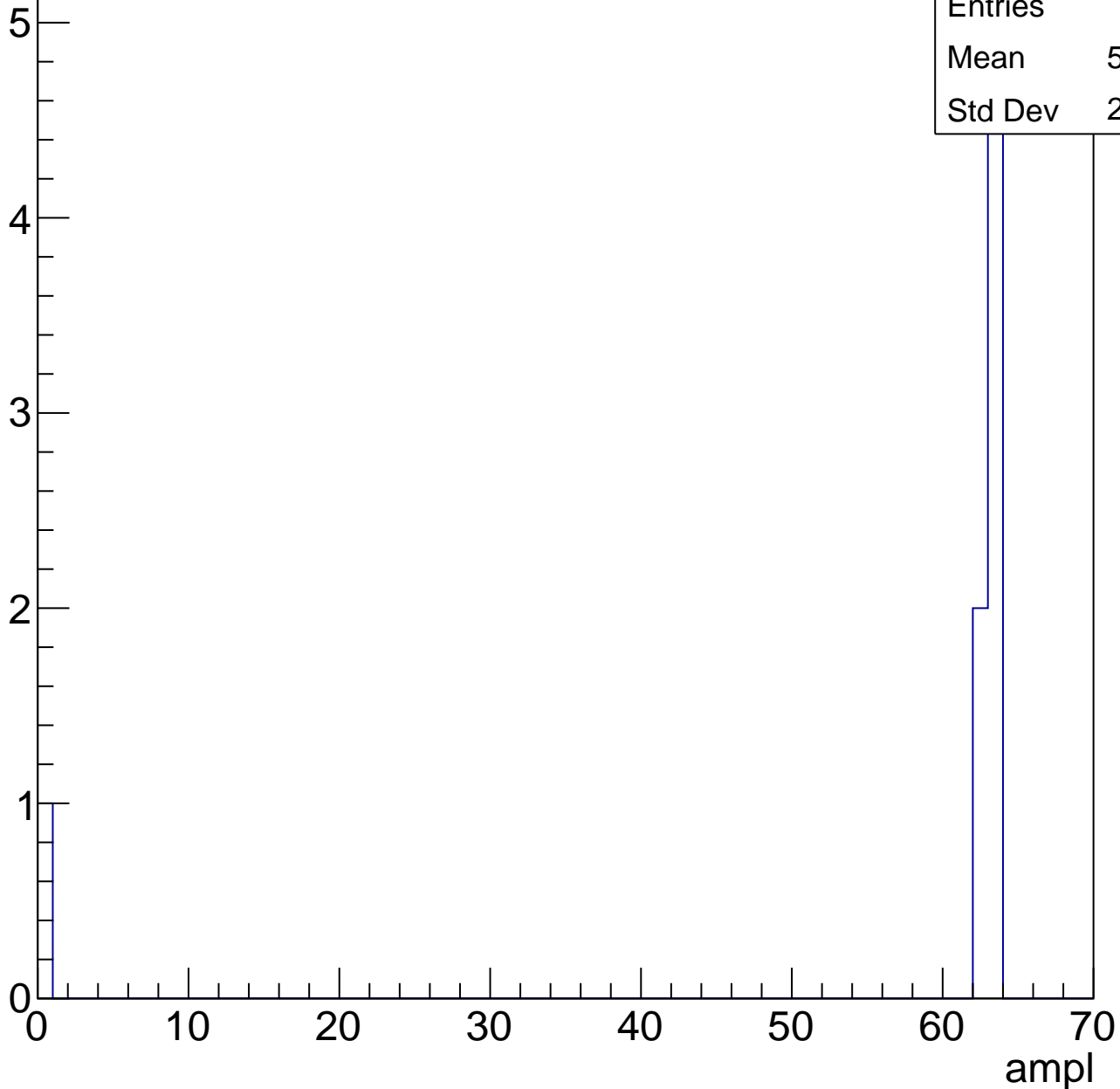


# B1L102S, U25-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	8
Mean	54.88
Std Dev	20.75





# B1L102S, U25-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch83, adc0

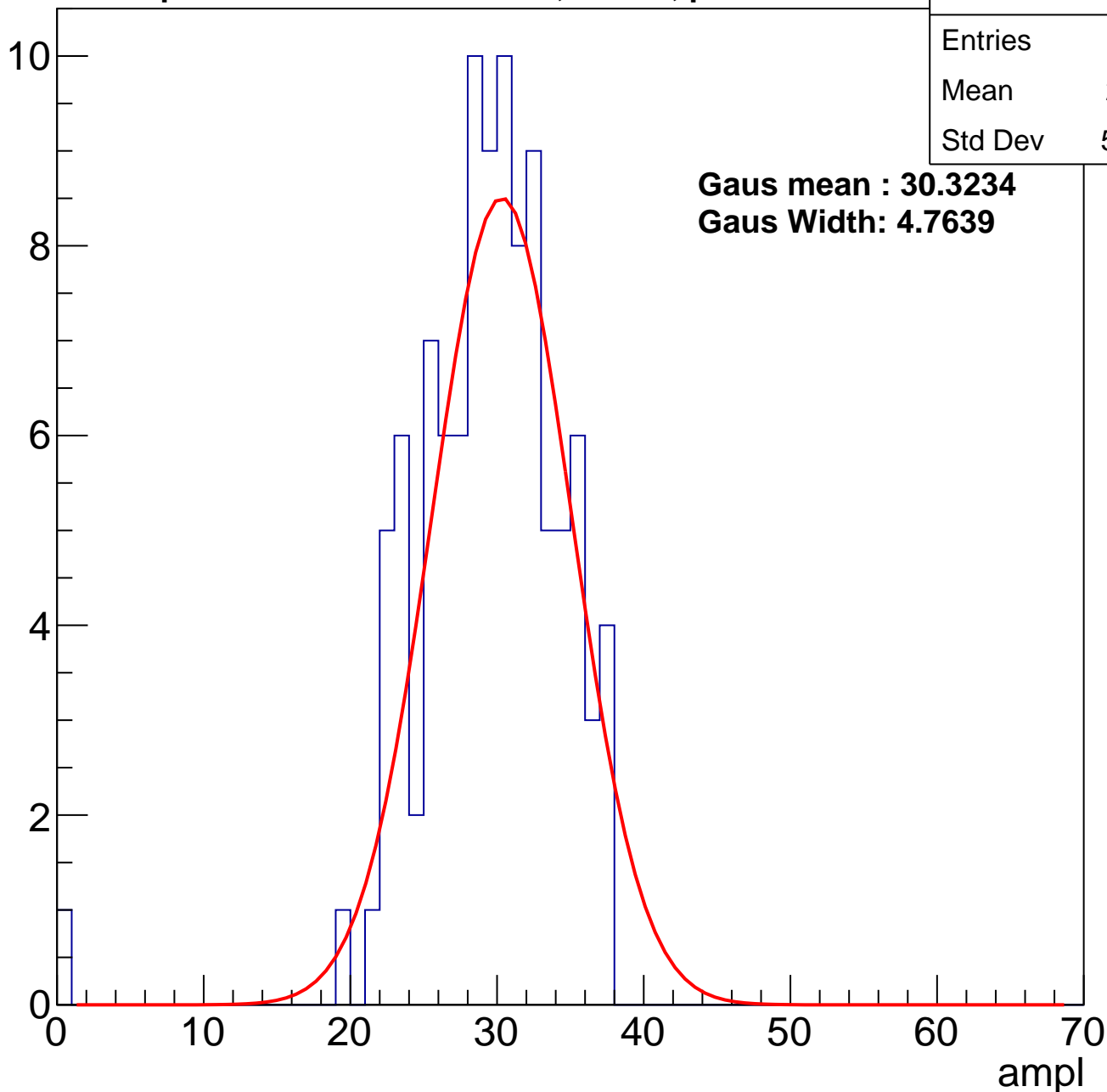
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	104
Mean	28.91
Std Dev	5.059

**Gaus mean : 30.3234**

**Gaus Width: 4.7639**

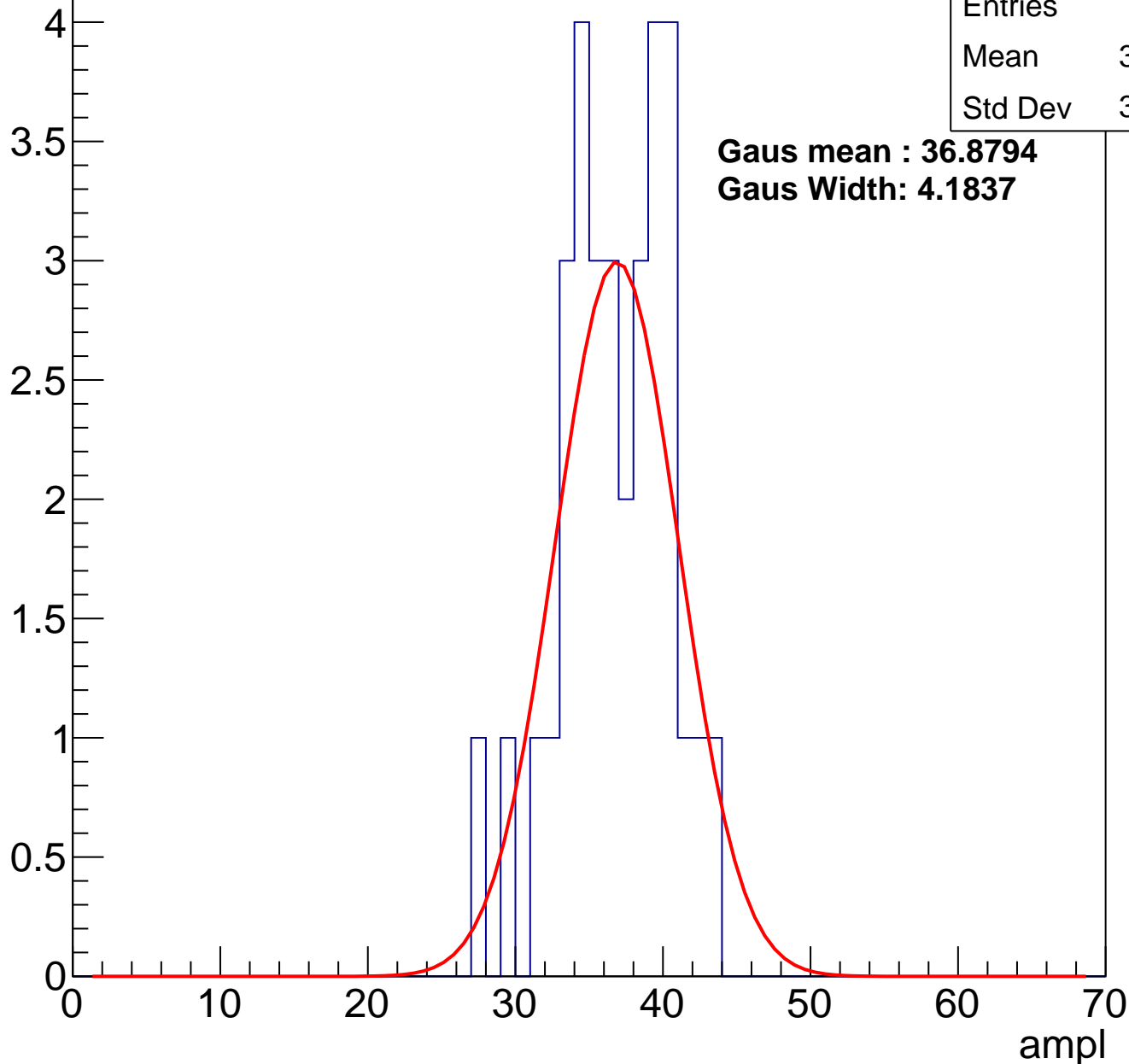
Entry



# B1L102S, U25-ch83, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch83, adc2

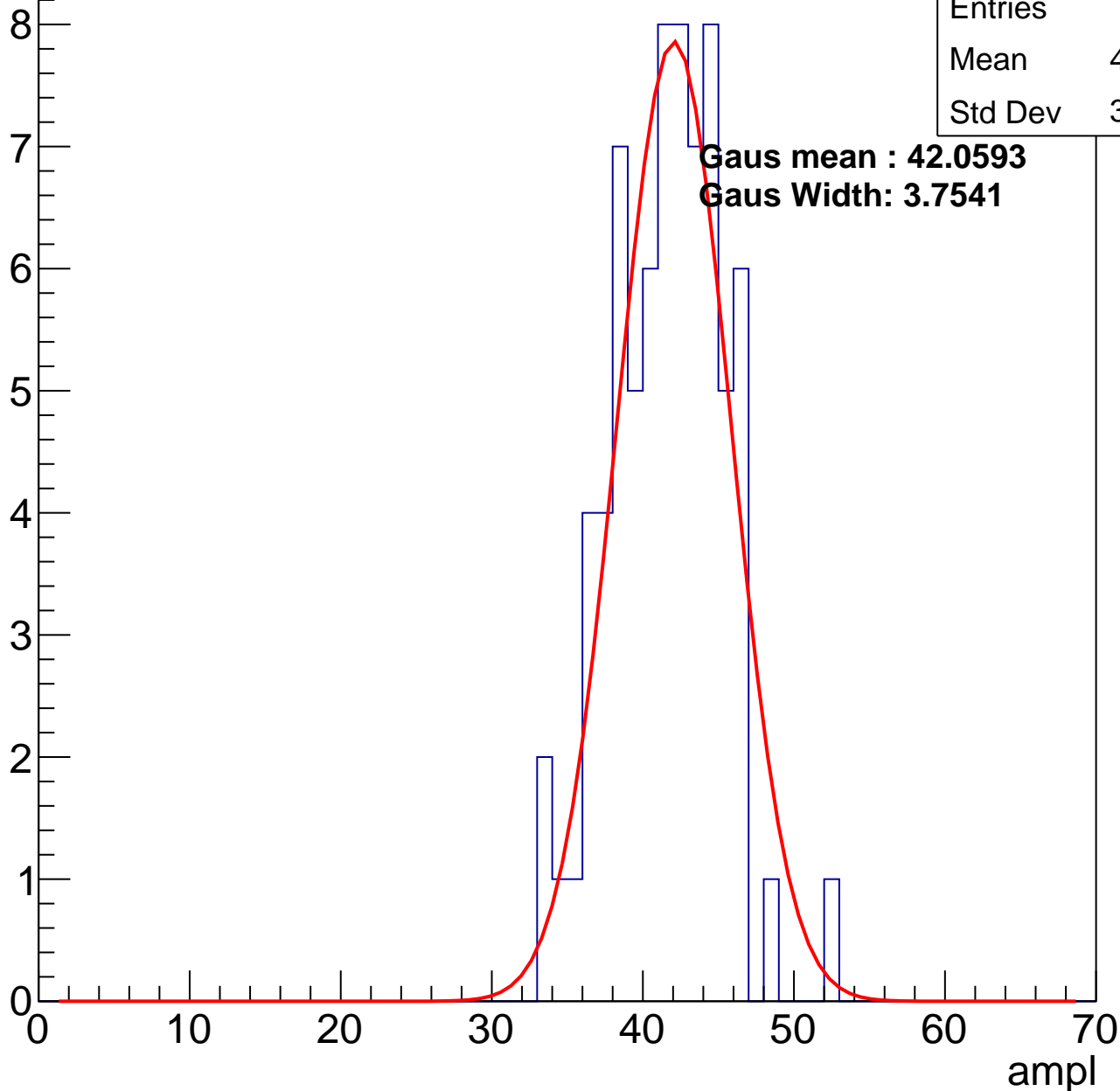
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.16
Std Dev	3.624

**Gaus mean : 42.0593**

**Gaus Width: 3.7541**

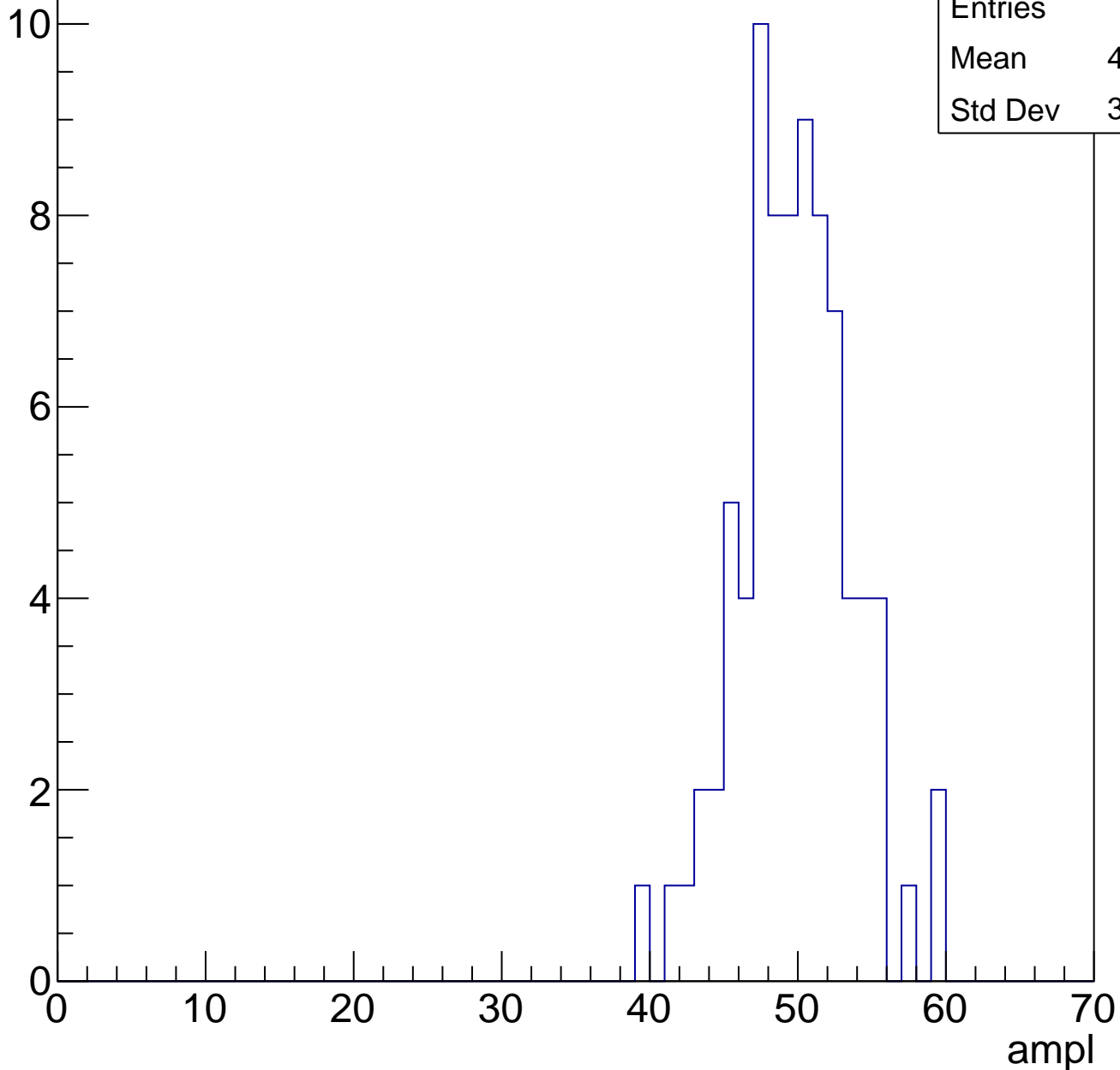


# B1L102S, U25-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	81
Mean	49.33
Std Dev	3.794

Entry



# B1L102S, U25-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

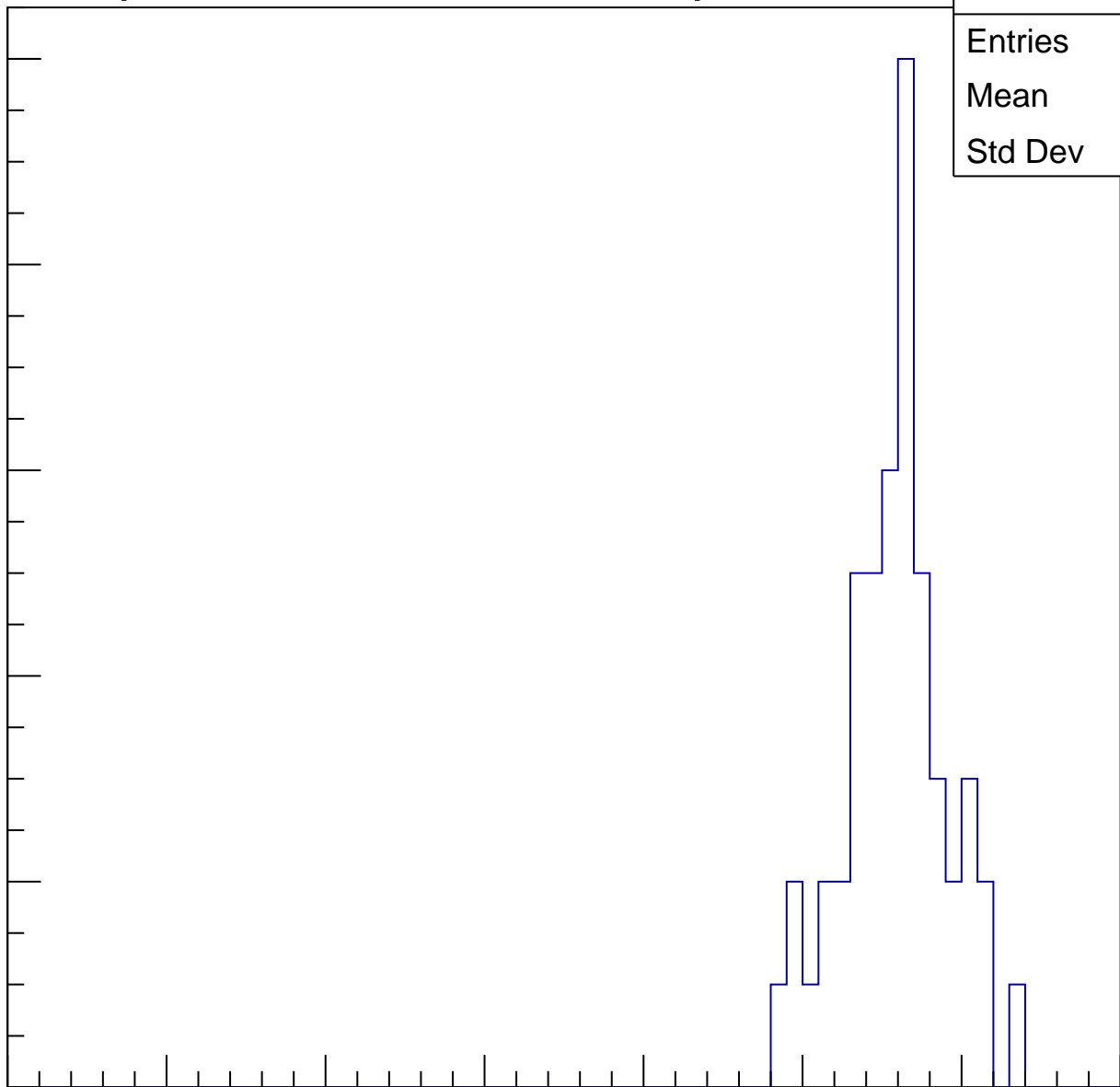
Entries	50
Mean	55.38
Std Dev	3.206

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

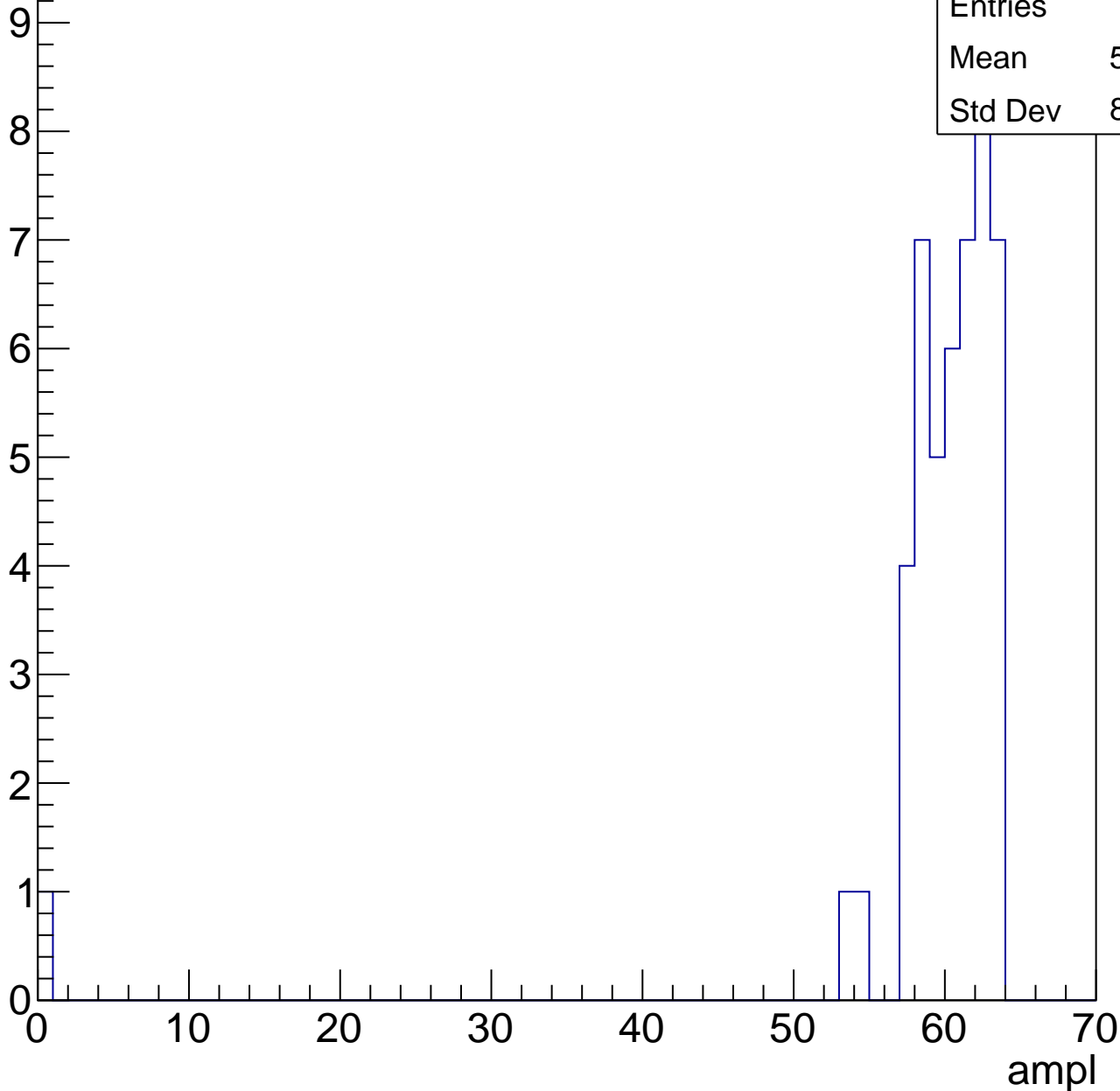


# B1L102S, U25-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.79
Std Dev	8.886



# B1L102S, U25-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

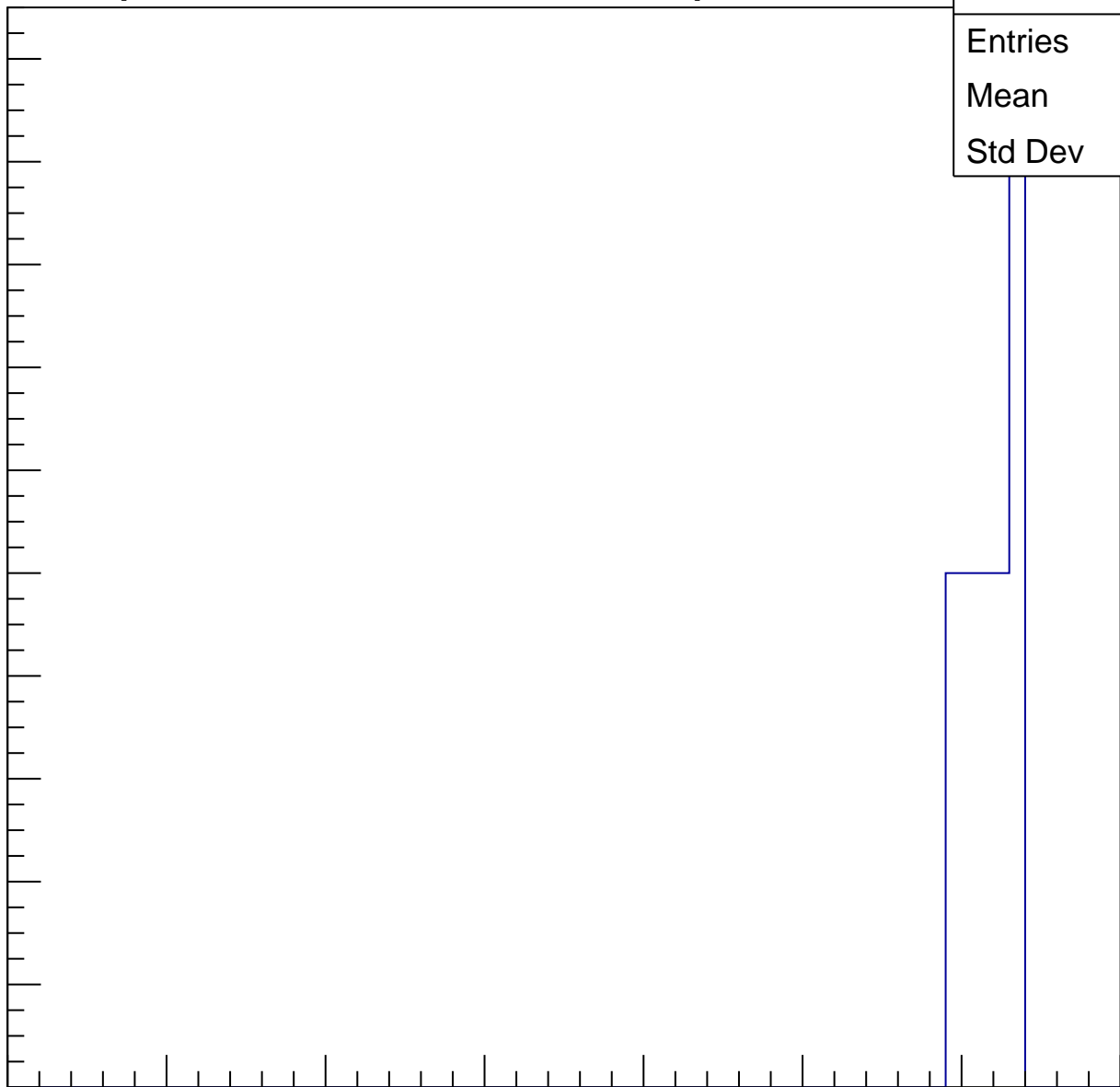
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl

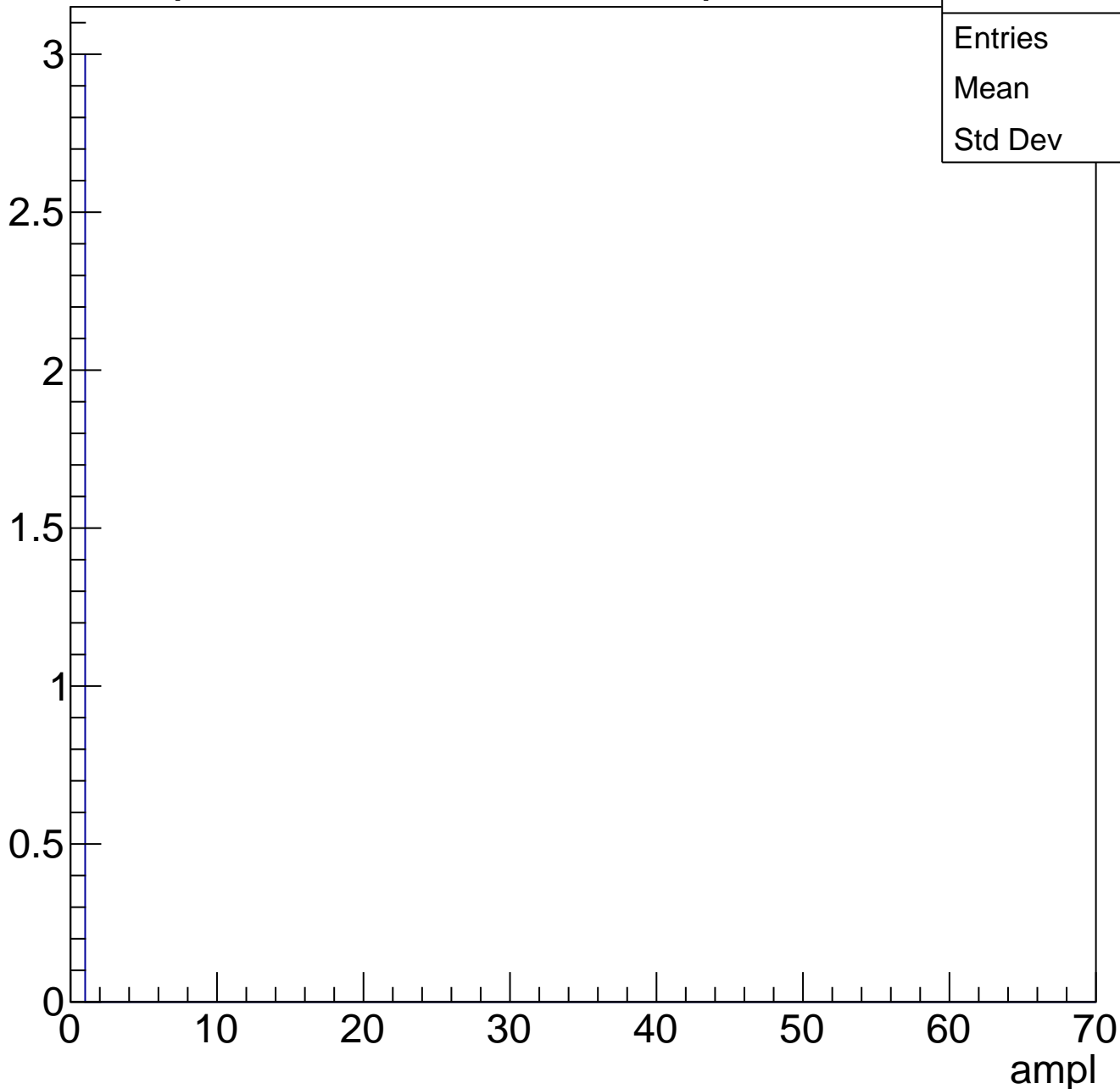




# B1L102S, U25-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L102S, U25-ch84, adc0

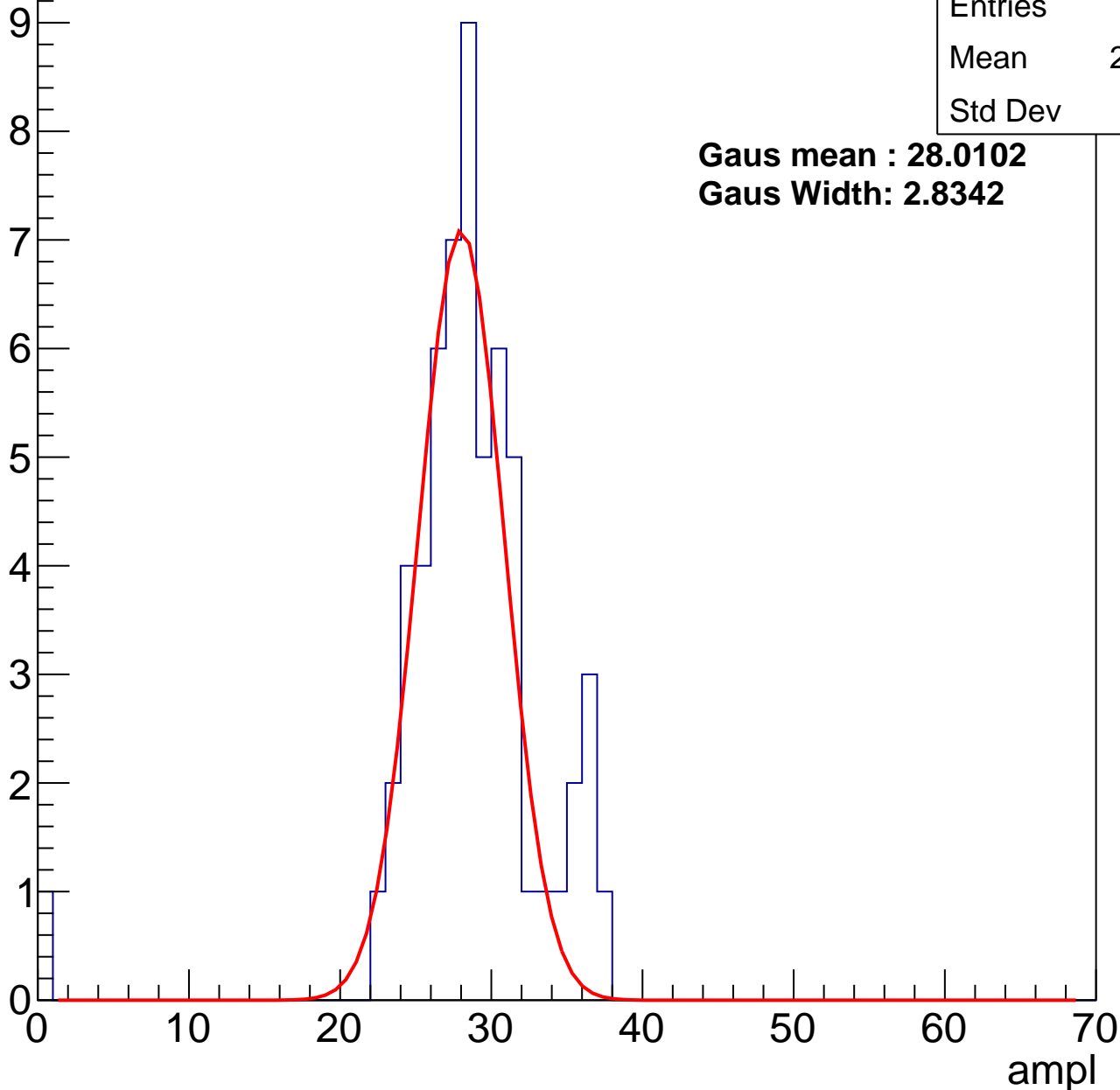
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.05
Std Dev	5.08

**Gaus mean : 28.0102**

**Gaus Width: 2.8342**



# B1L102S, U25-ch84, adc1

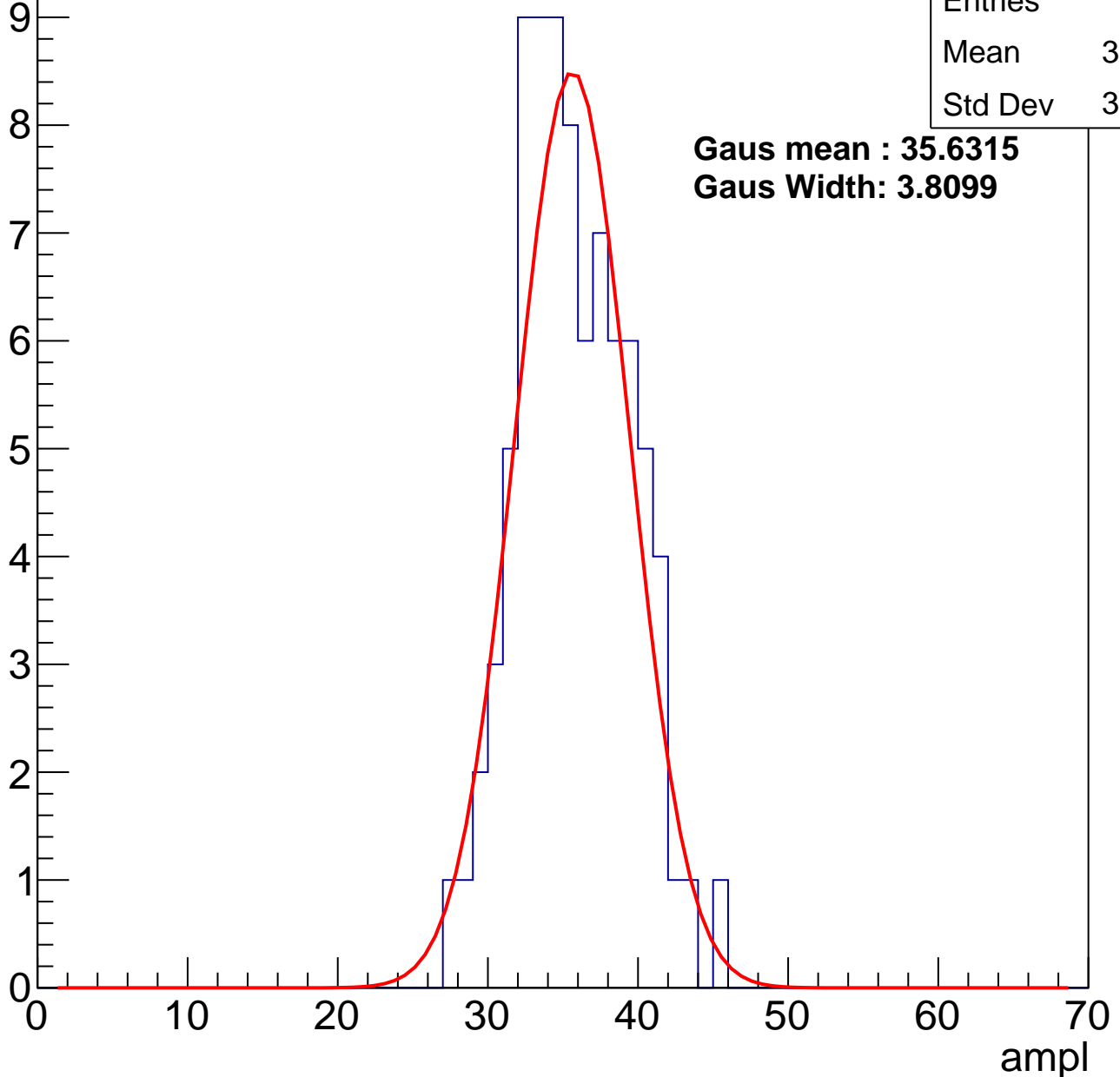
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	35.24
Std Dev	3.663

**Gaus mean : 35.6315**

**Gaus Width: 3.8099**



# B1L102S, U25-ch84, adc2

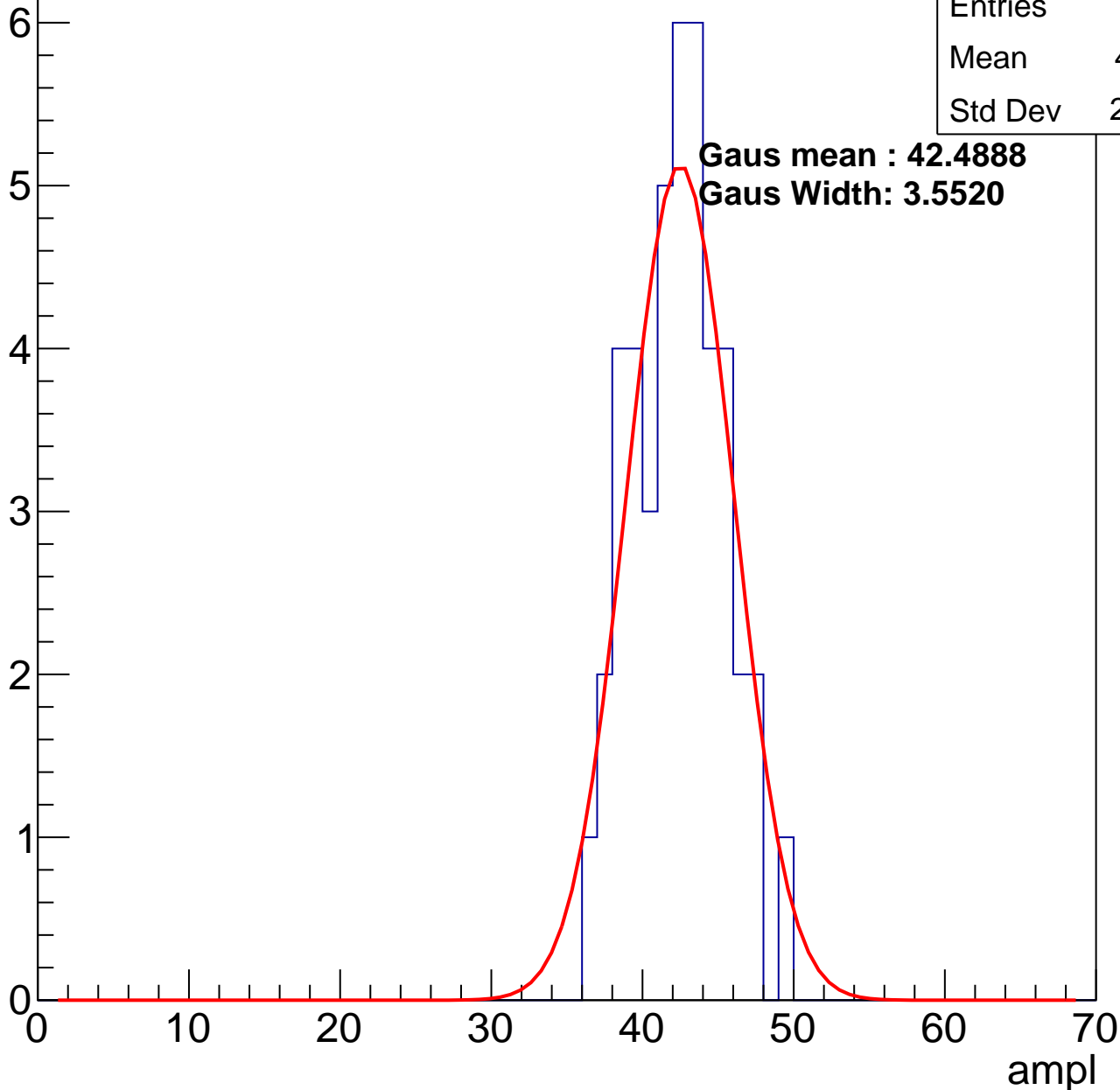
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	41.91
Std Dev	2.983

**Gaus mean : 42.4888**

**Gaus Width: 3.5520**



# B1L102S, U25-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

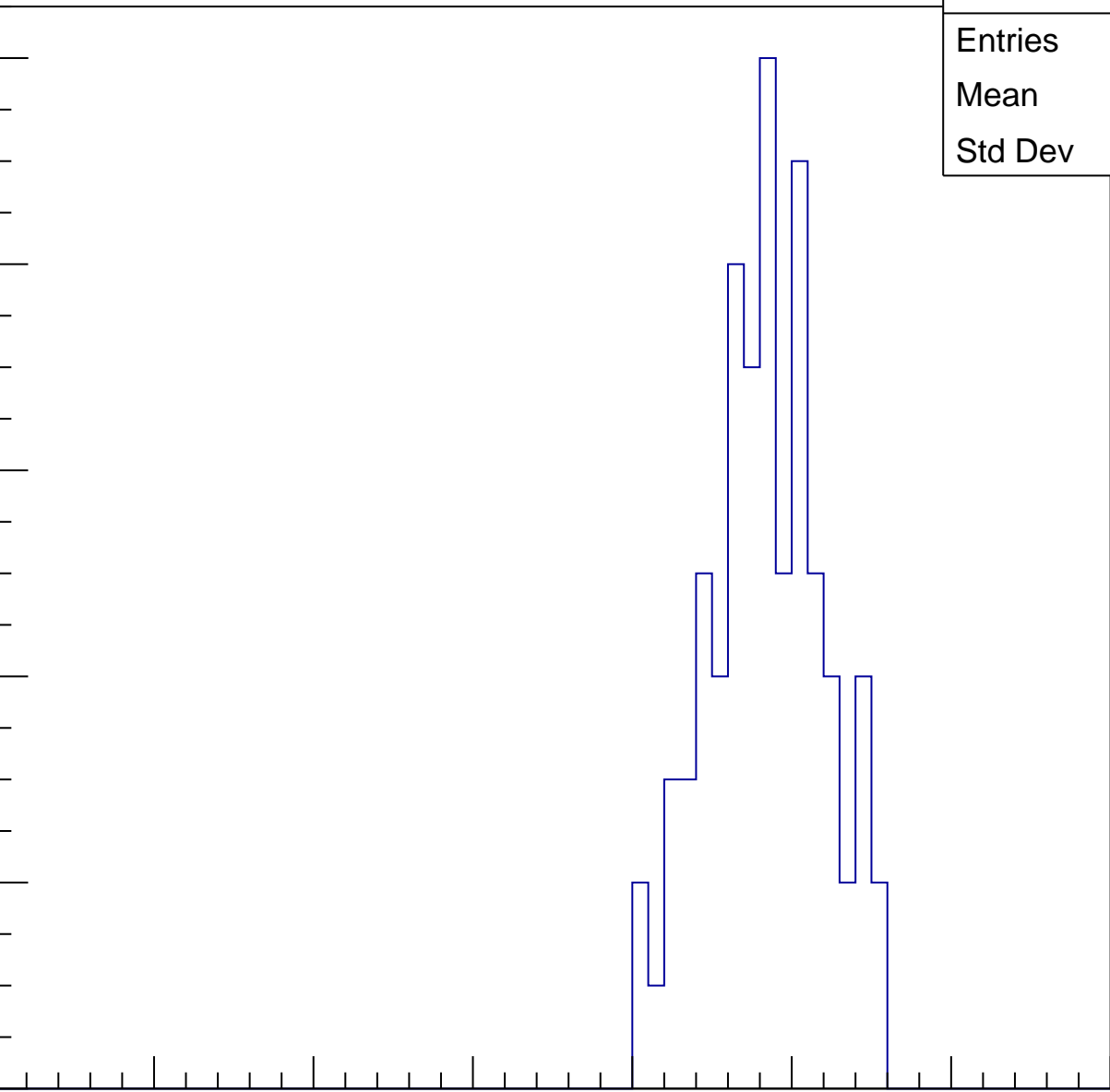
Entries	74
Mean	47.88
Std Dev	3.594

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

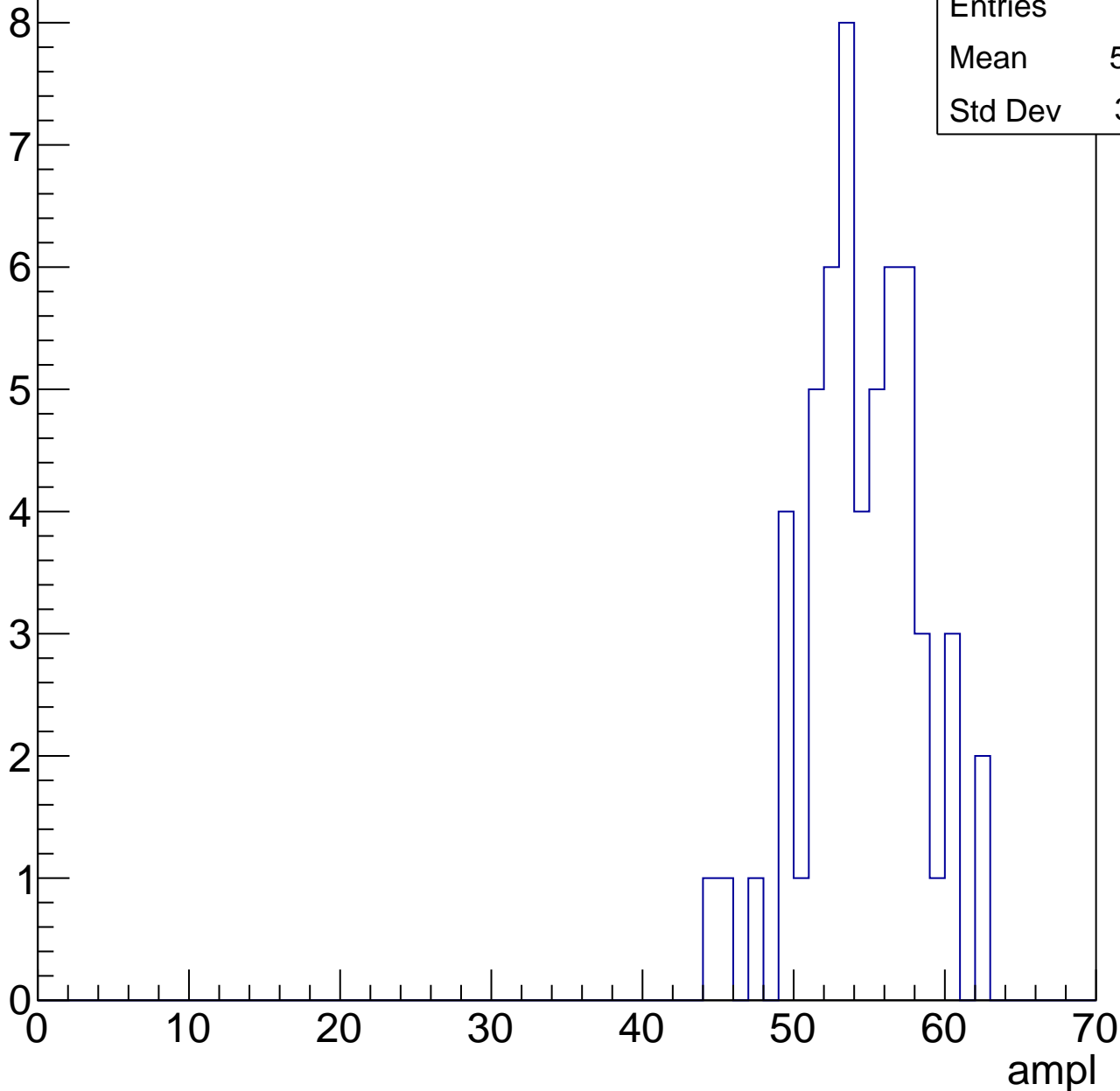


# B1L102S, U25-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

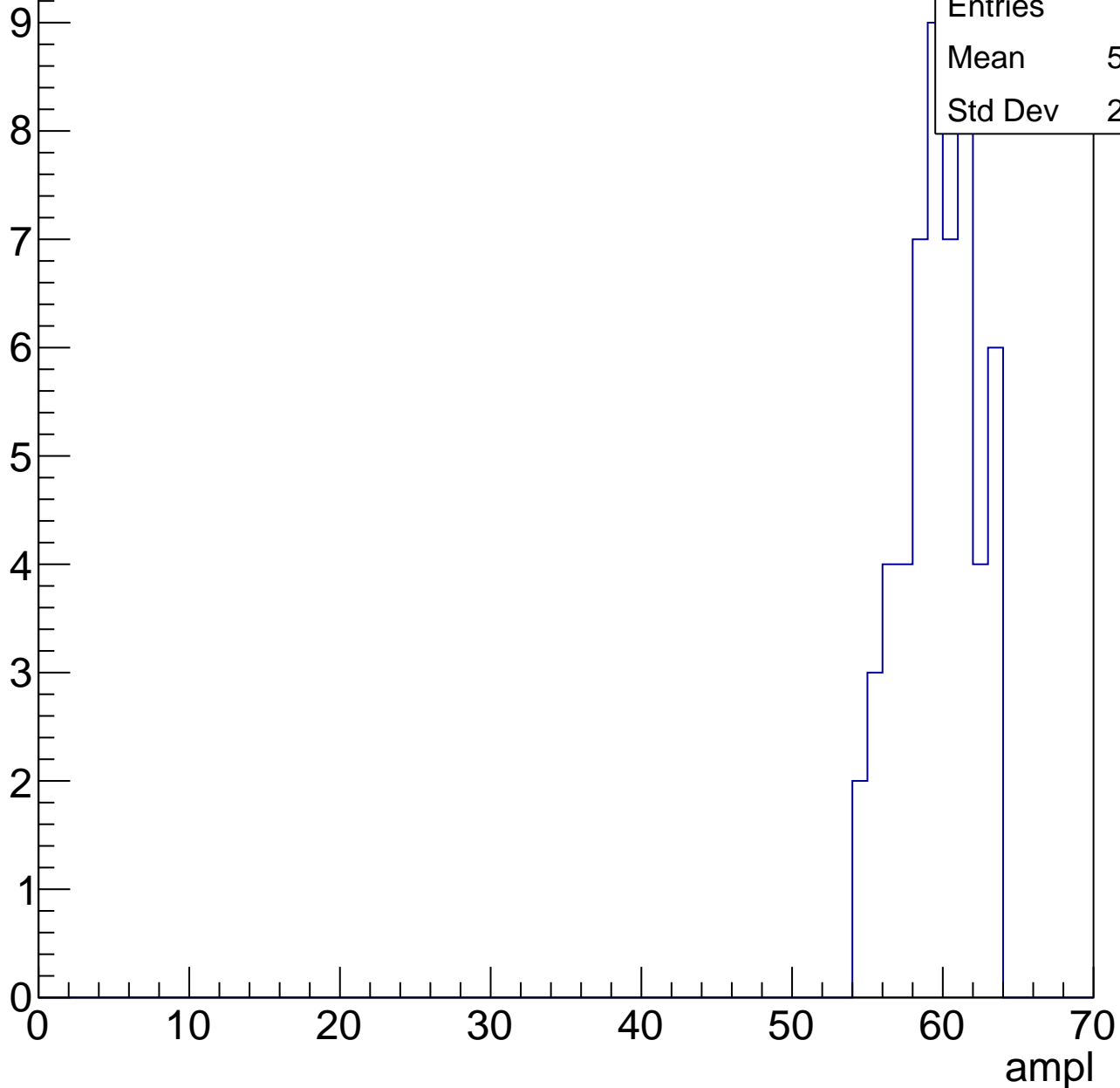
Entries	57
Mean	54.02
Std Dev	3.781



# B1L102S, U25-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

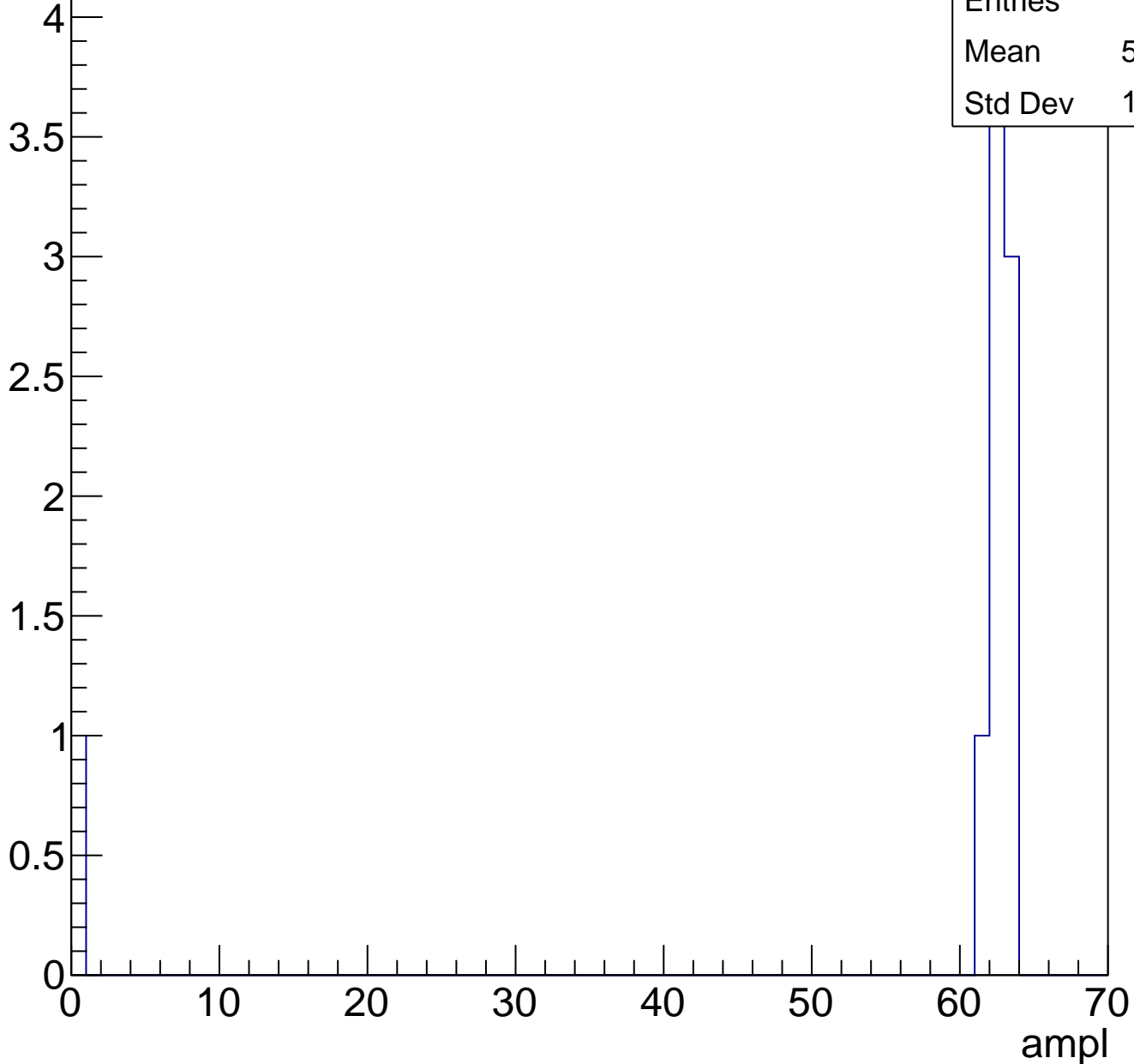


Entries	55
Mean	59.22
Std Dev	2.447

# B1L102S, U25-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch85, adc0

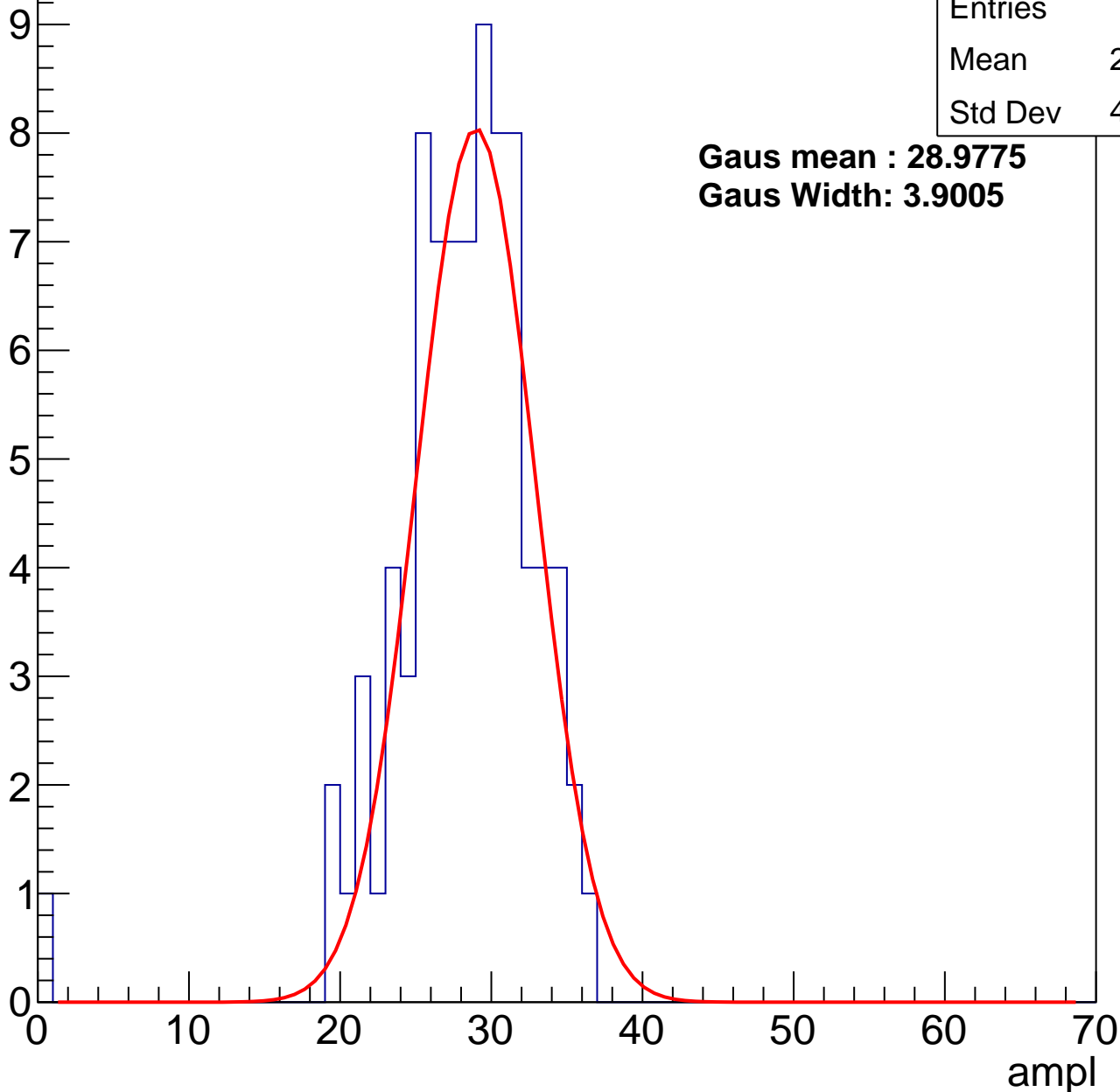
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	27.68
Std Dev	4.907

**Gaus mean : 28.9775**

**Gaus Width: 3.9005**



# B1L102S, U25-ch85, adc1

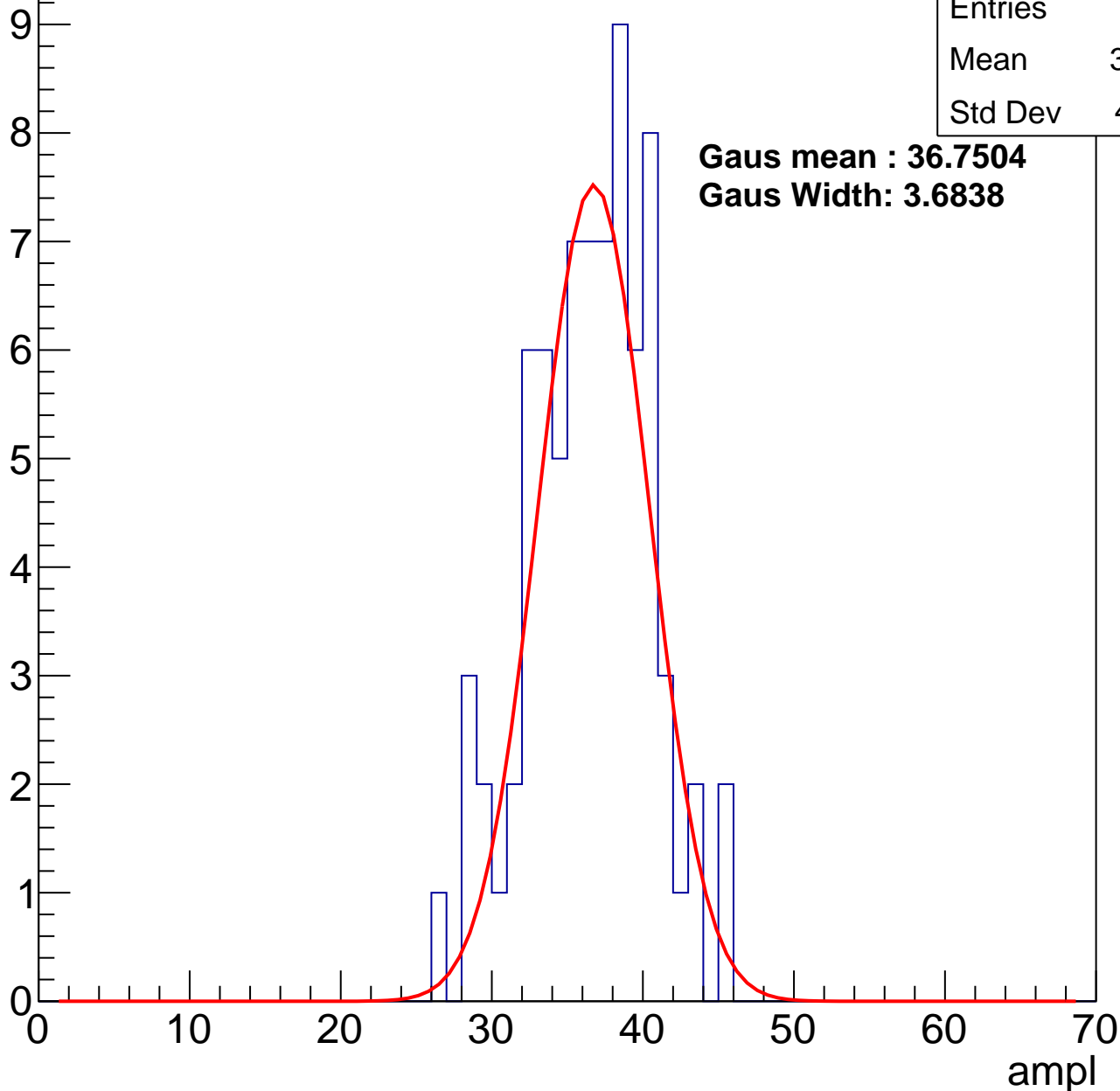
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	36.06
Std Dev	4.001

**Gaus mean : 36.7504**

**Gaus Width: 3.6838**



# B1L102S, U25-ch85, adc2

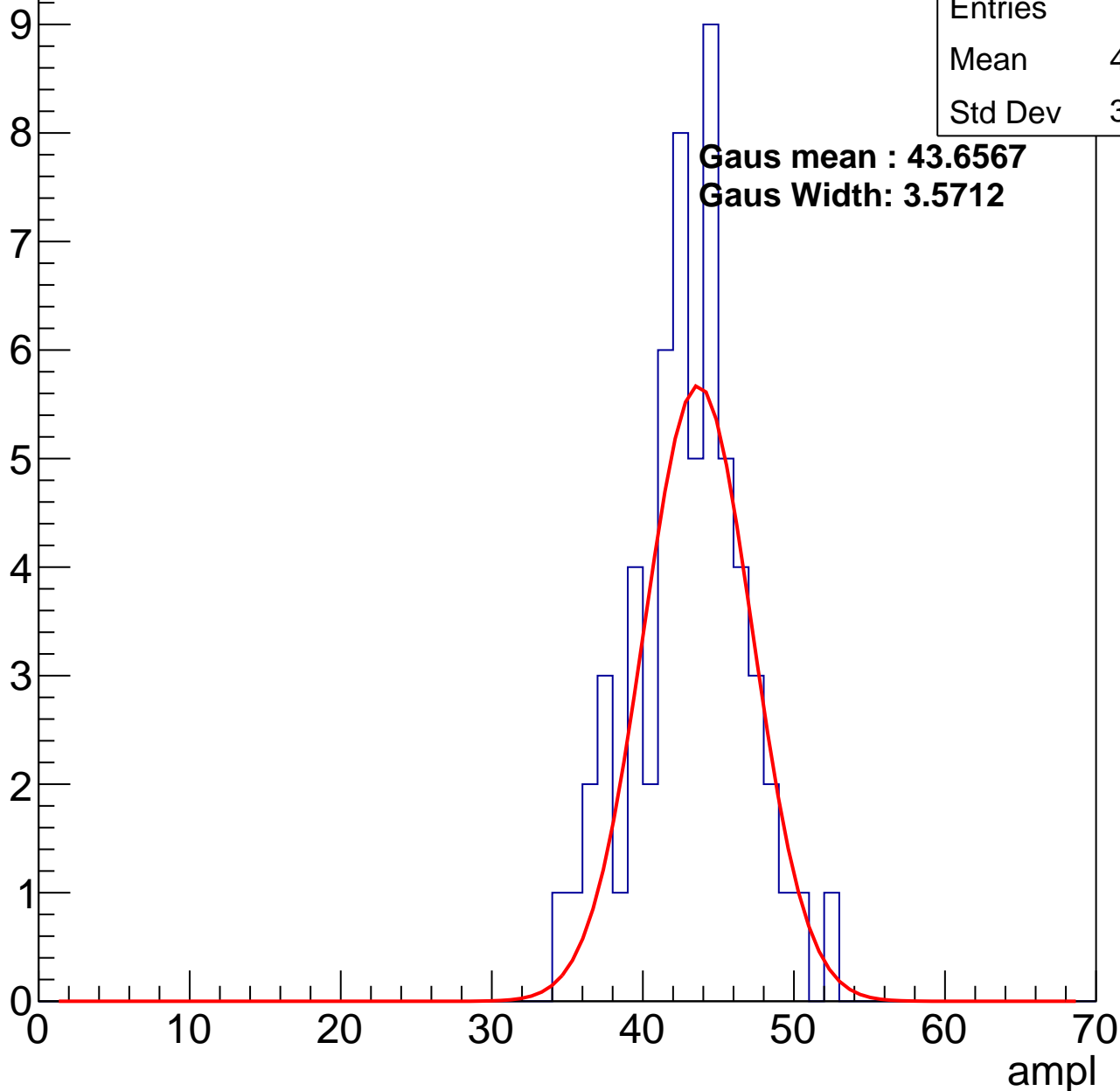
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.64
Std Dev	3.709

**Gaus mean : 43.6567**

**Gaus Width: 3.5712**

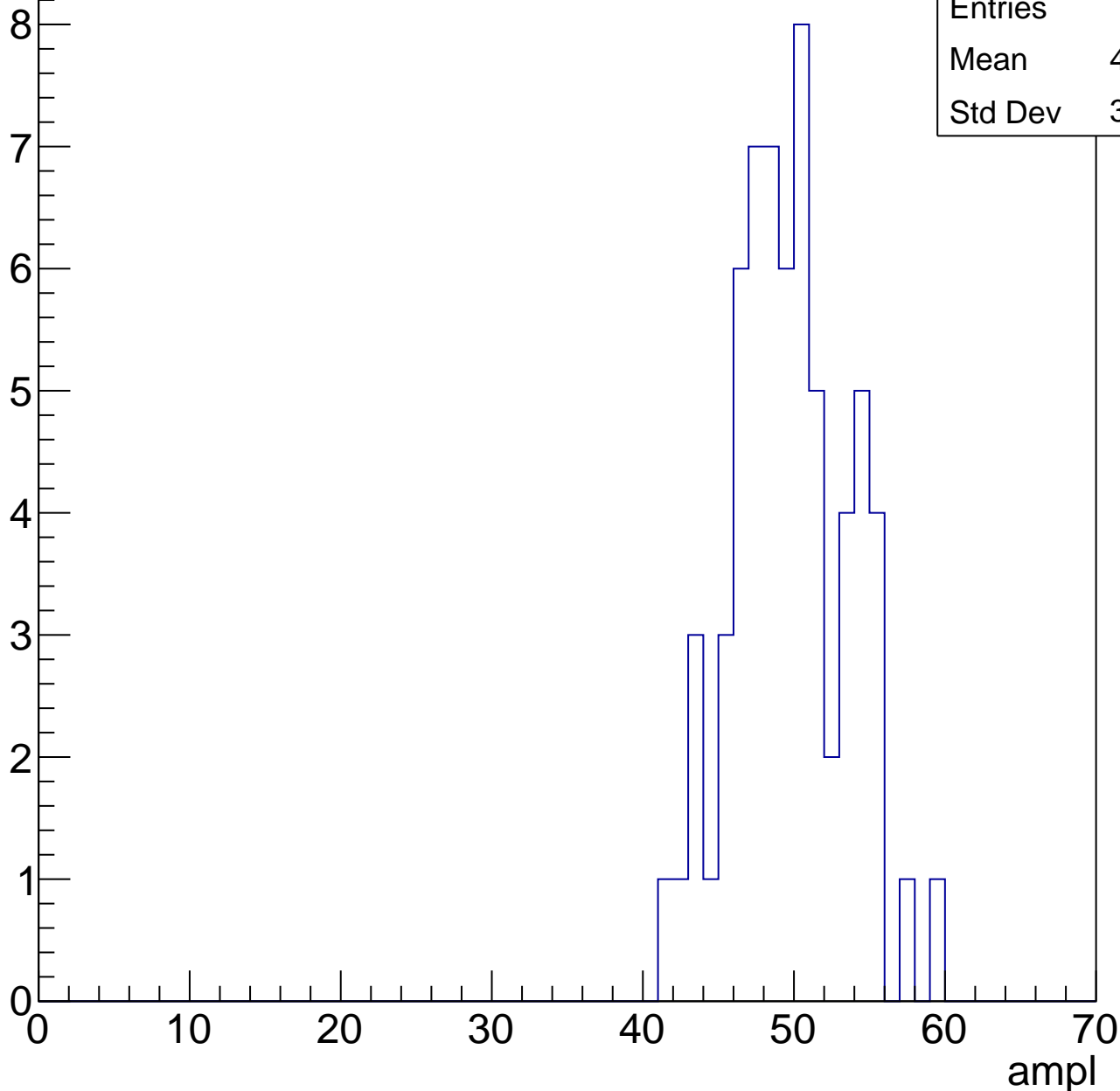


# B1L102S, U25-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.28
Std Dev	3.764

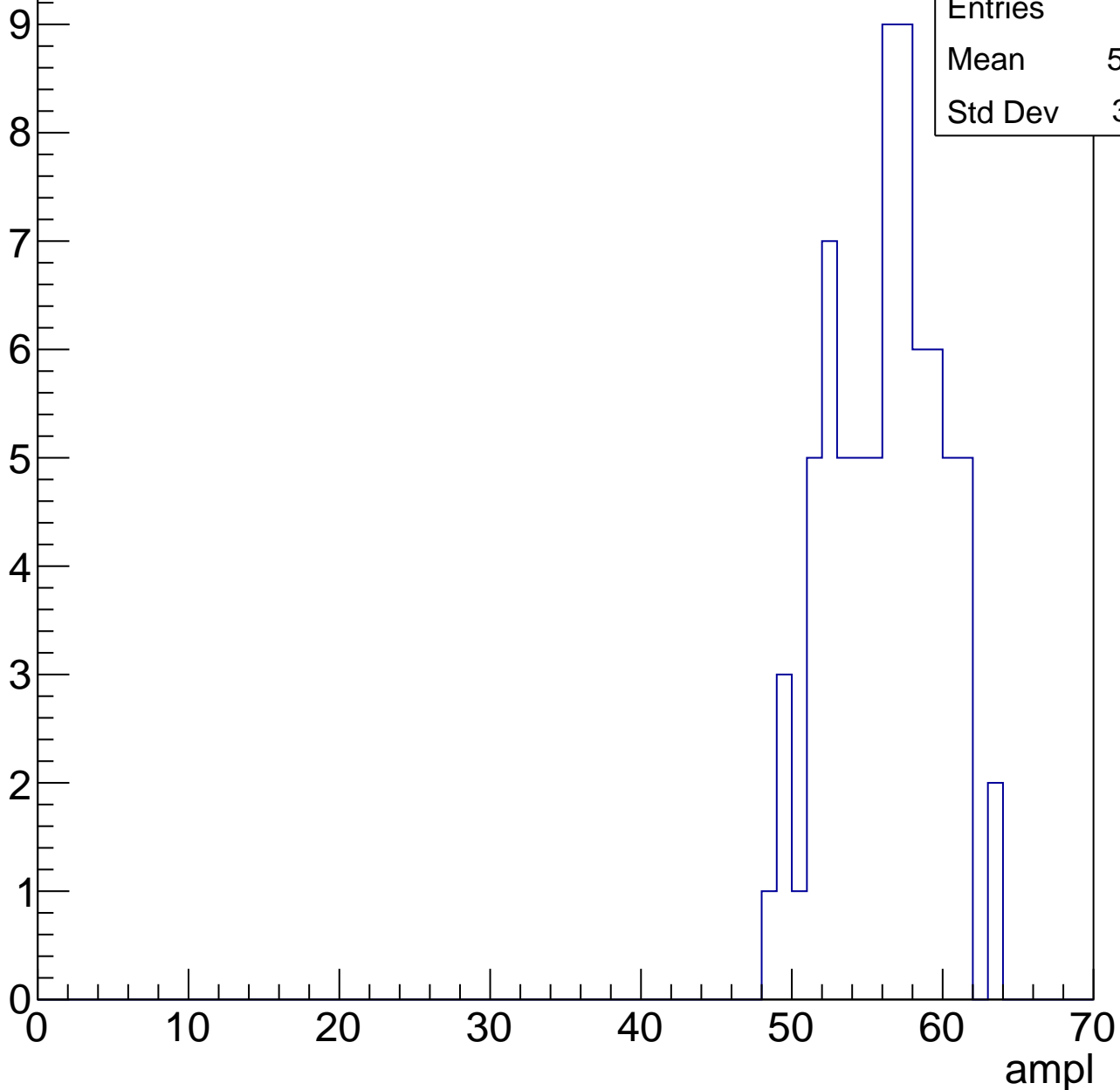


# B1L102S, U25-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	55.73
Std Dev	3.561

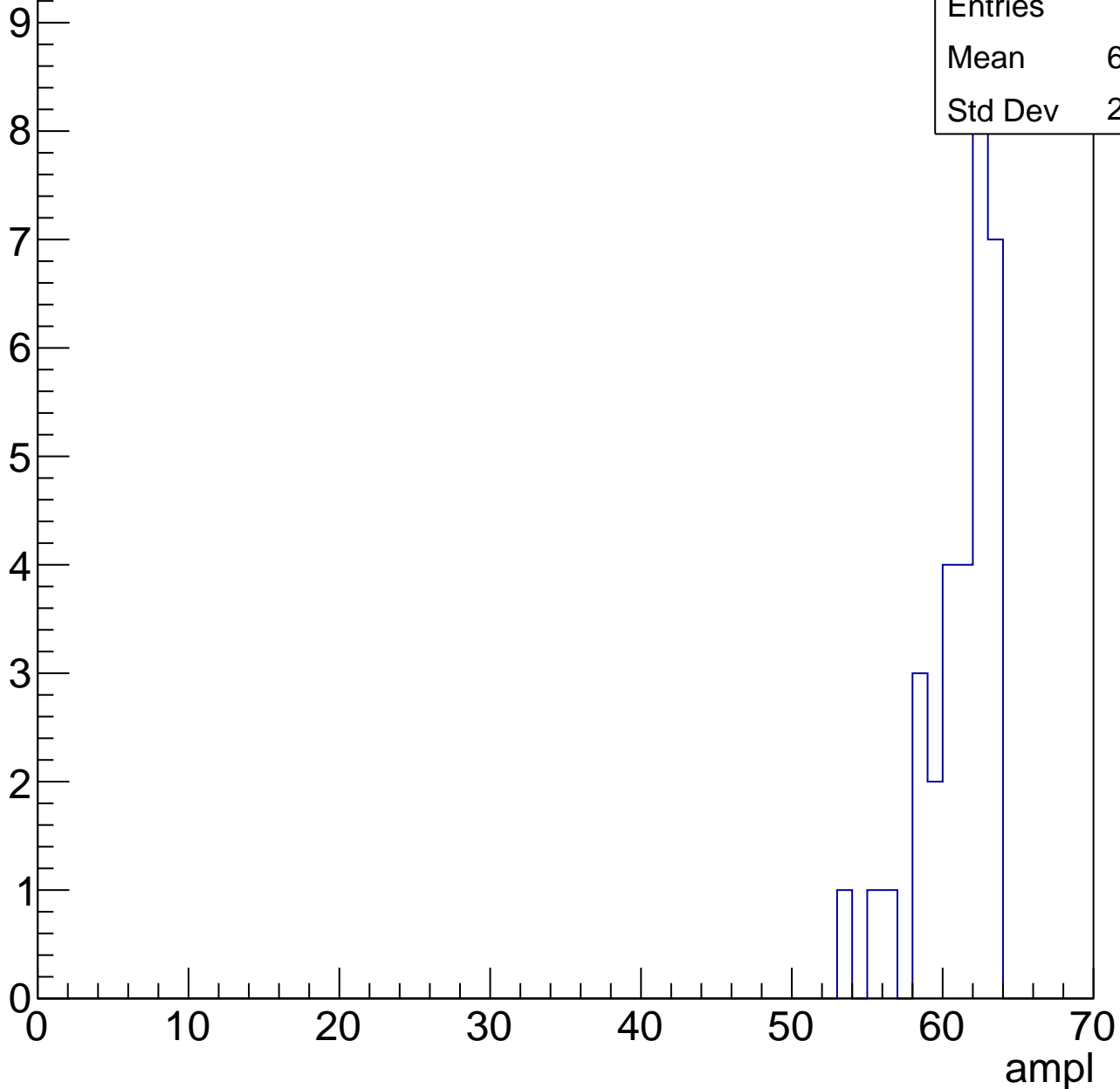


# B1L102S, U25-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

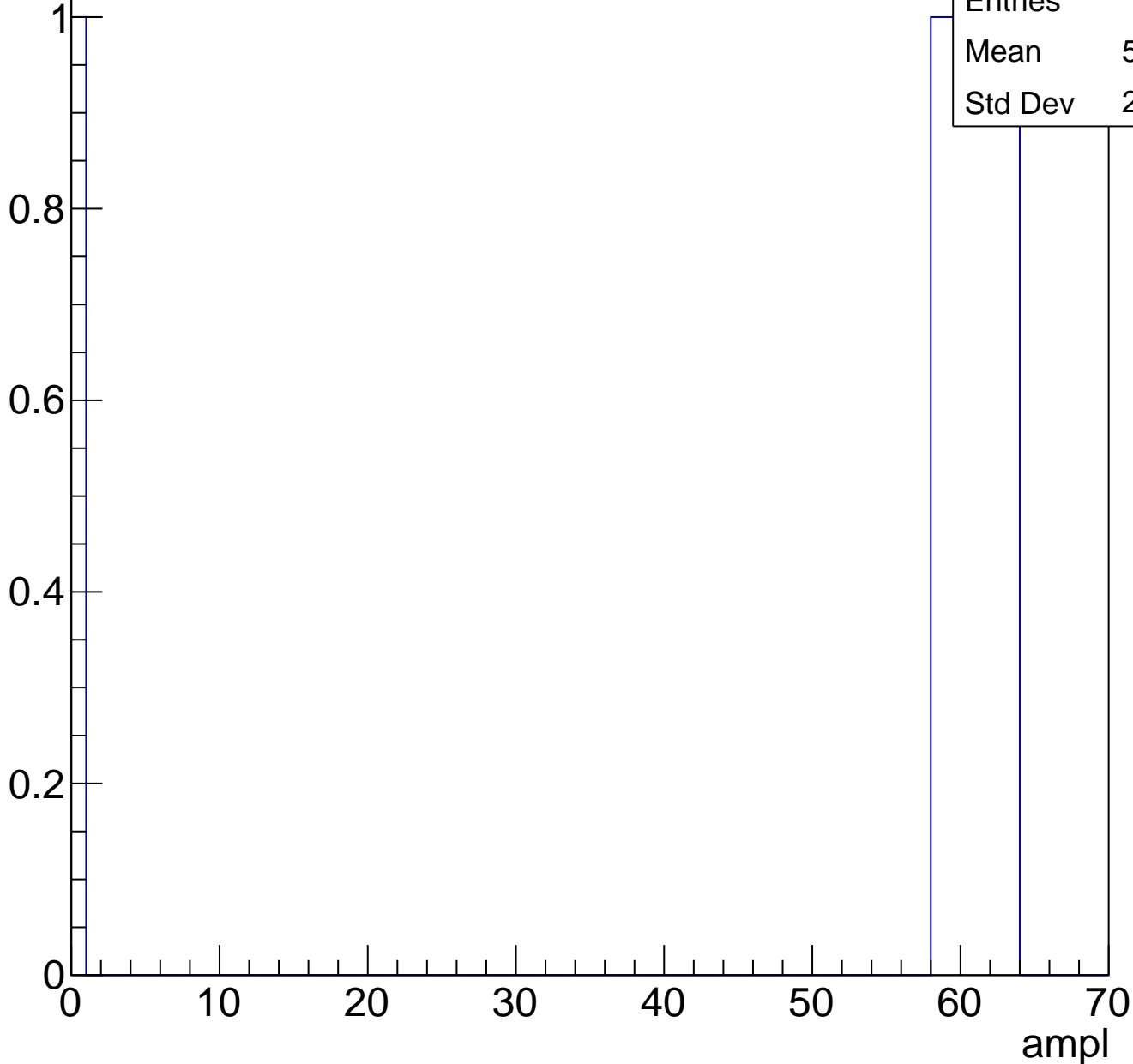
Entries	32
Mean	60.59
Std Dev	2.473



# B1L102S, U25-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	65
Mean	27.68
Std Dev	3.655

**Gaus mean : 28.9210**

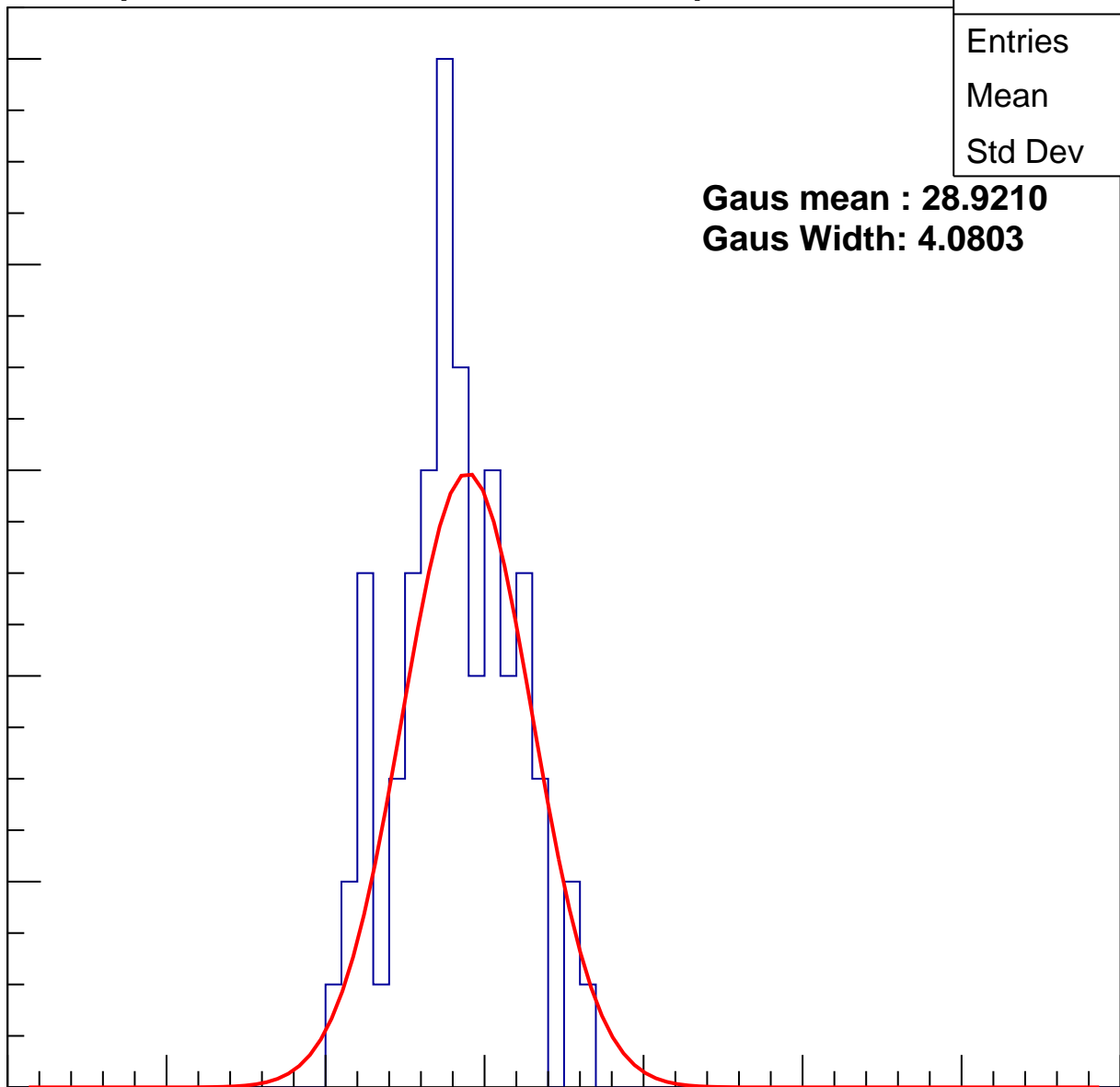
**Gaus Width: 4.0803**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch86, adc1

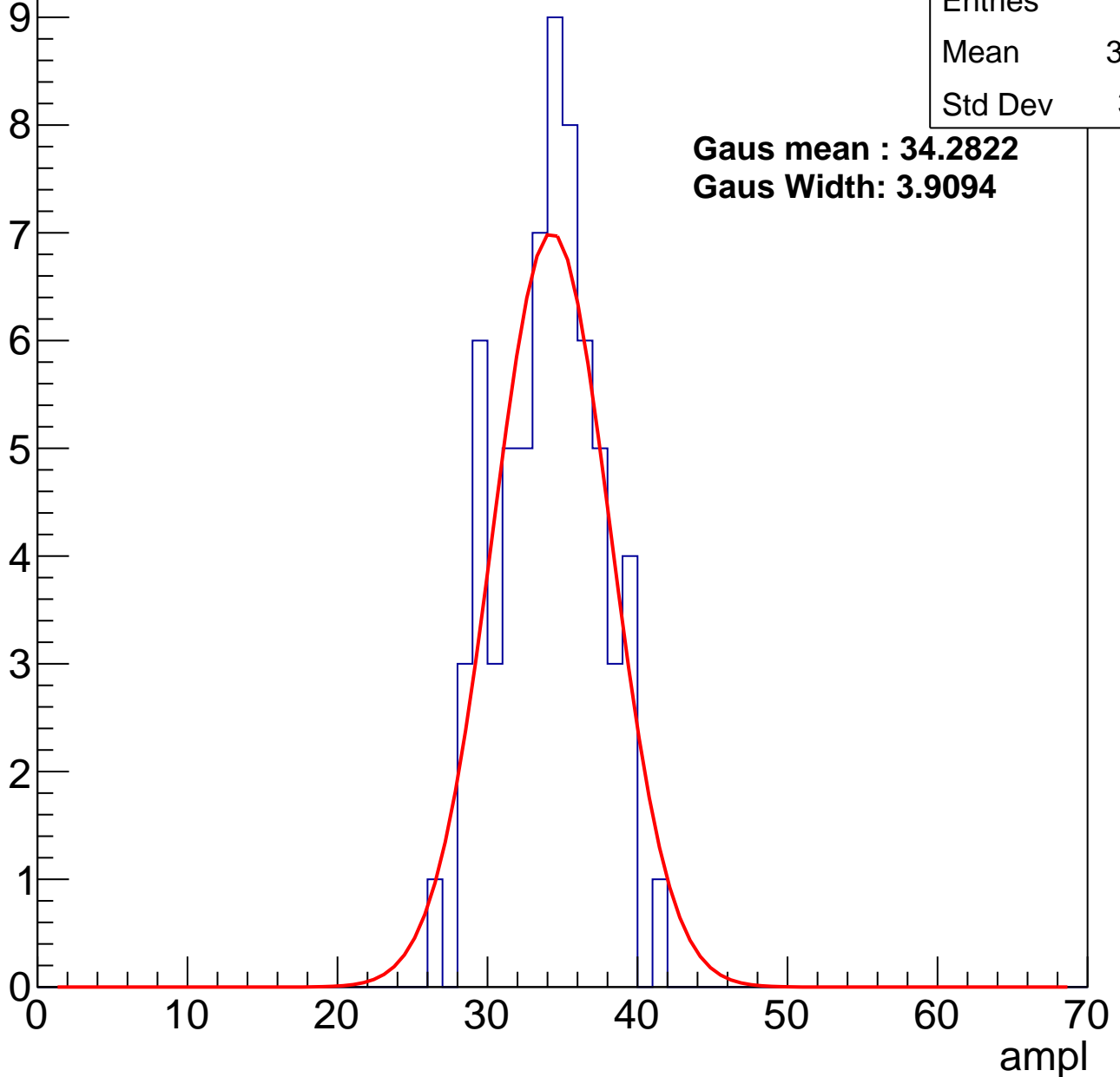
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	33.61
Std Dev	3.27

**Gaus mean : 34.2822**

**Gaus Width: 3.9094**



# B1L102S, U25-ch86, adc2

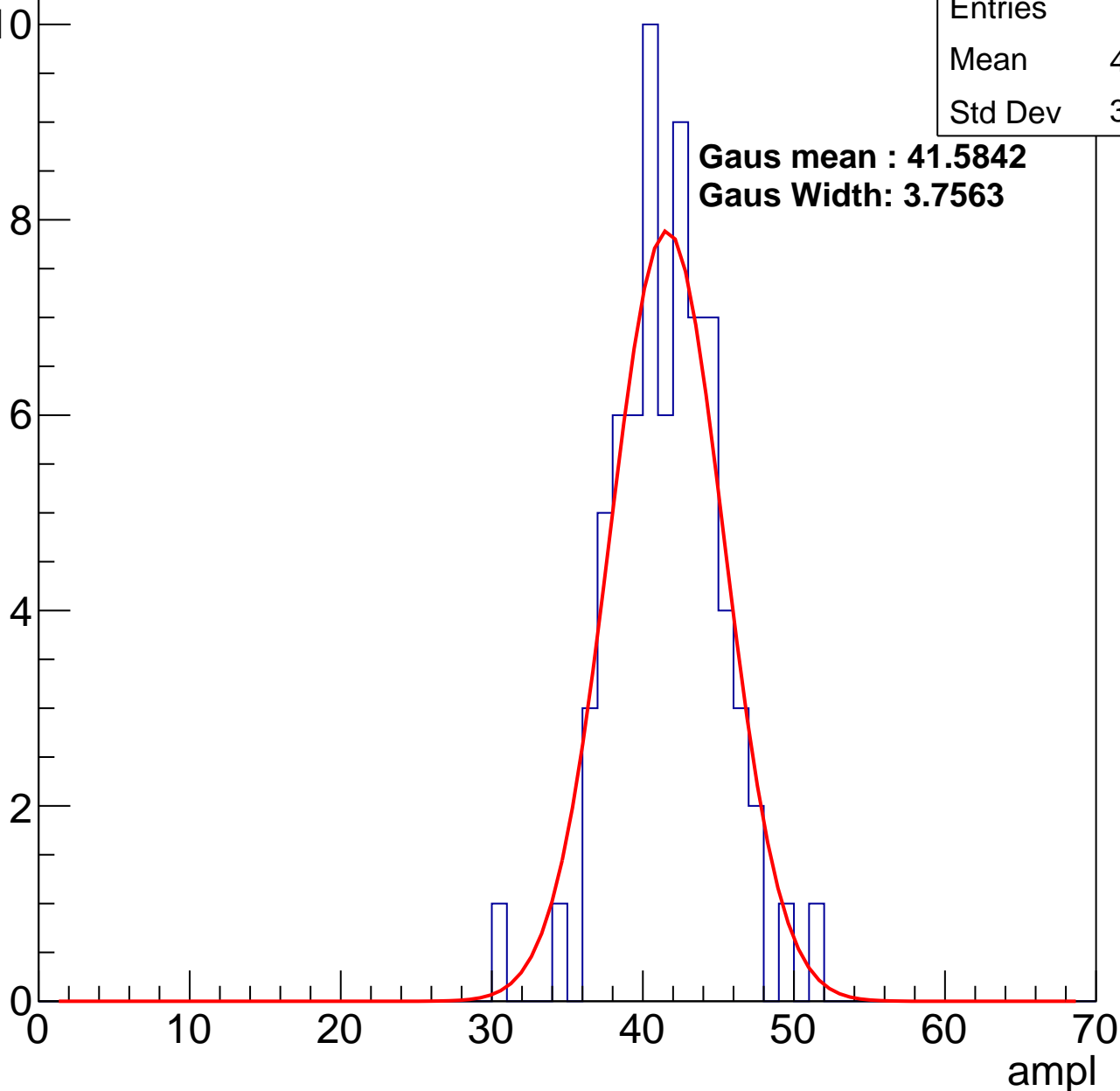
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.17
Std Dev	3.508

**Gaus mean : 41.5842**

**Gaus Width: 3.7563**

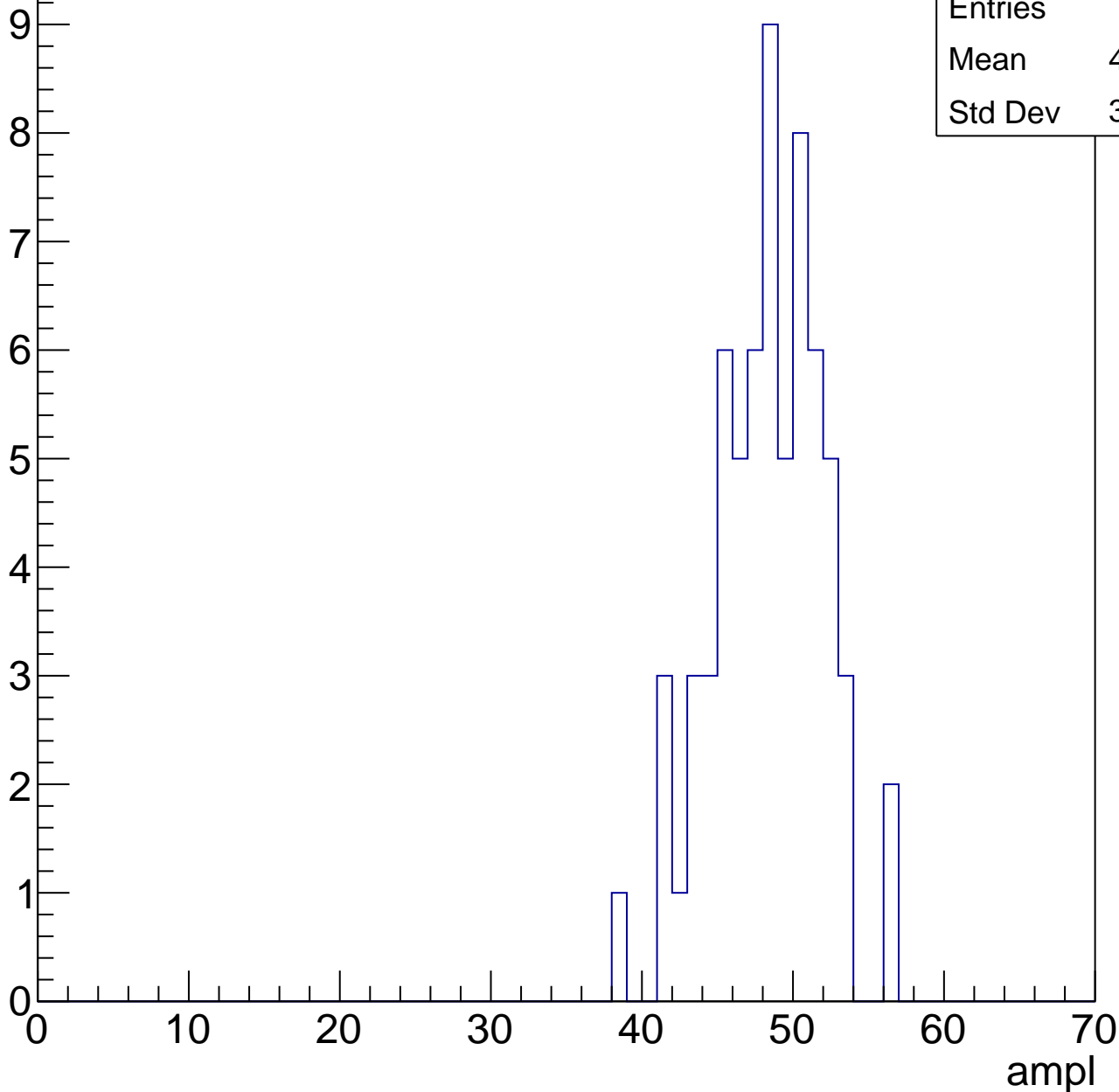


# B1L102S, U25-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.88
Std Dev	3.608

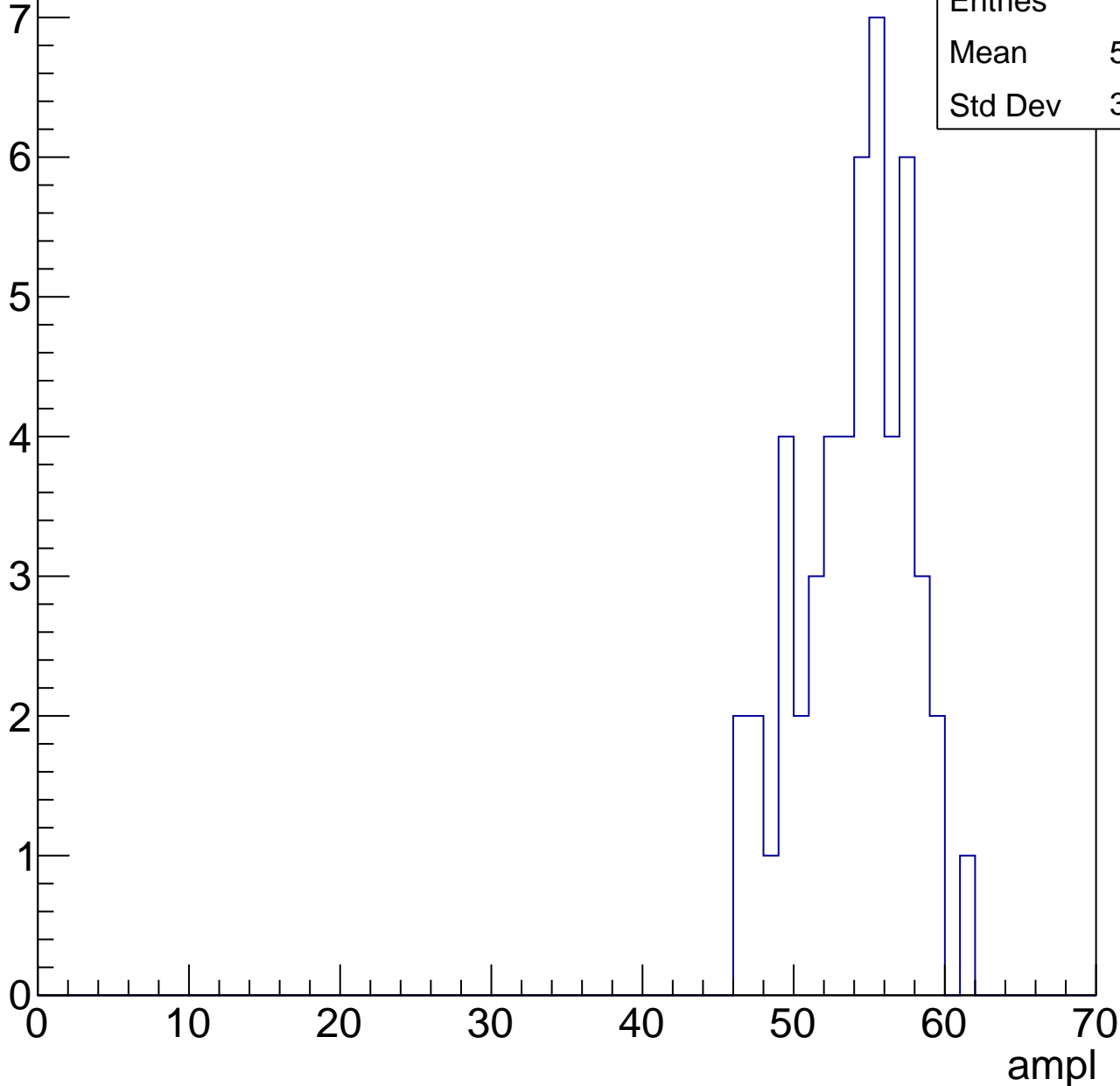


# B1L102S, U25-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	53.55
Std Dev	3.583

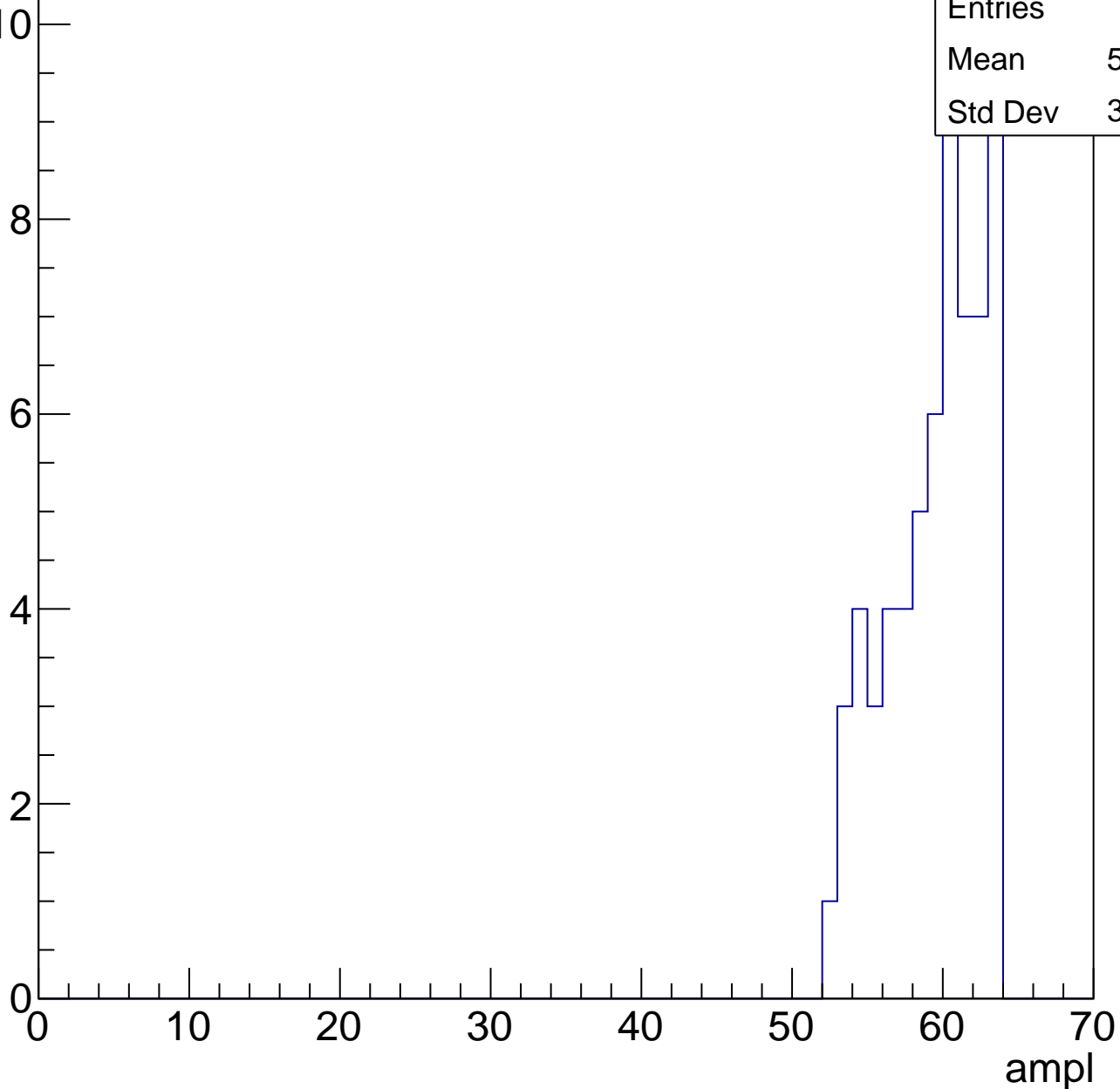


# B1L102S, U25-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

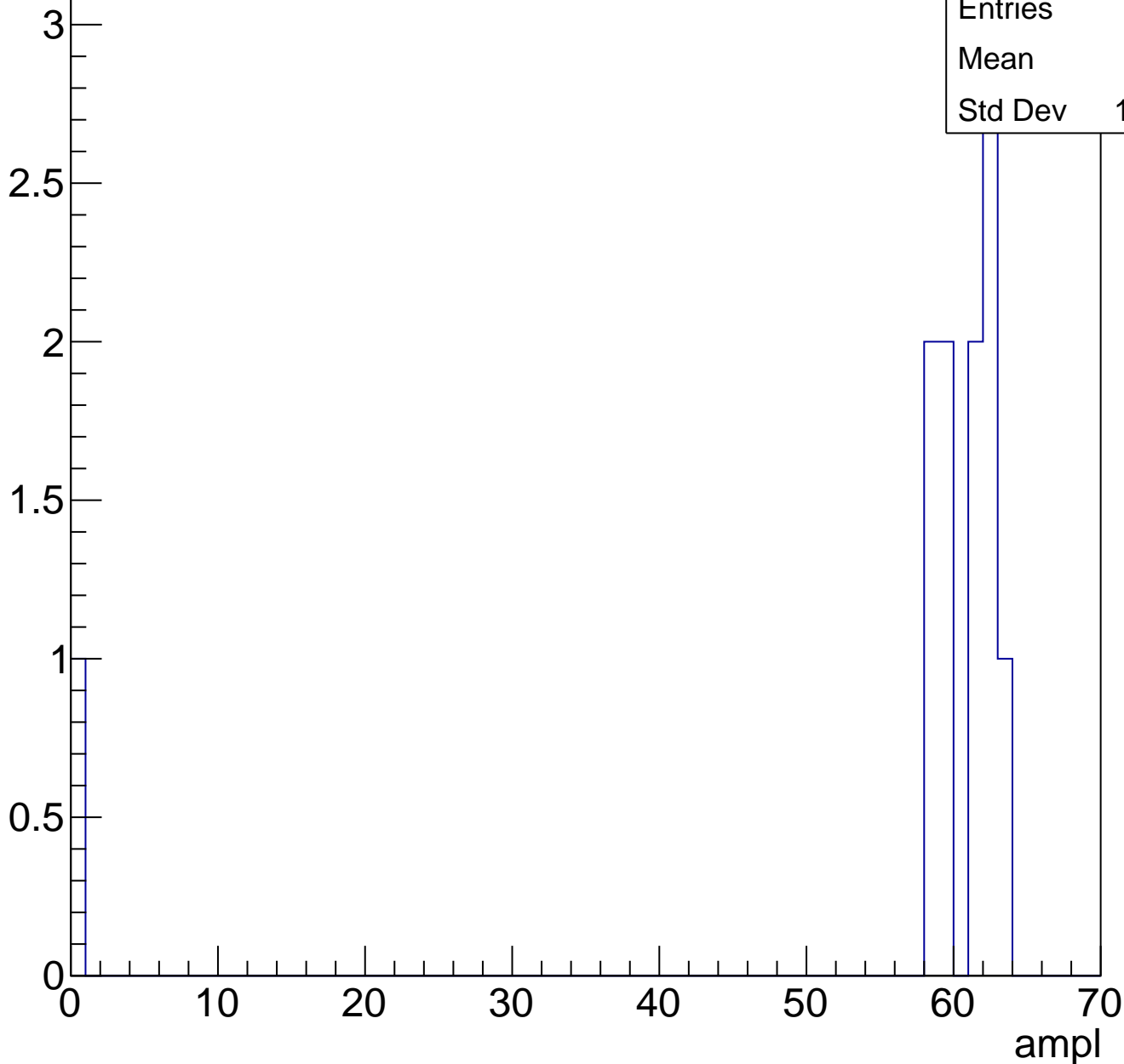
Entries	63
Mean	58.98
Std Dev	3.104



# B1L102S, U25-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

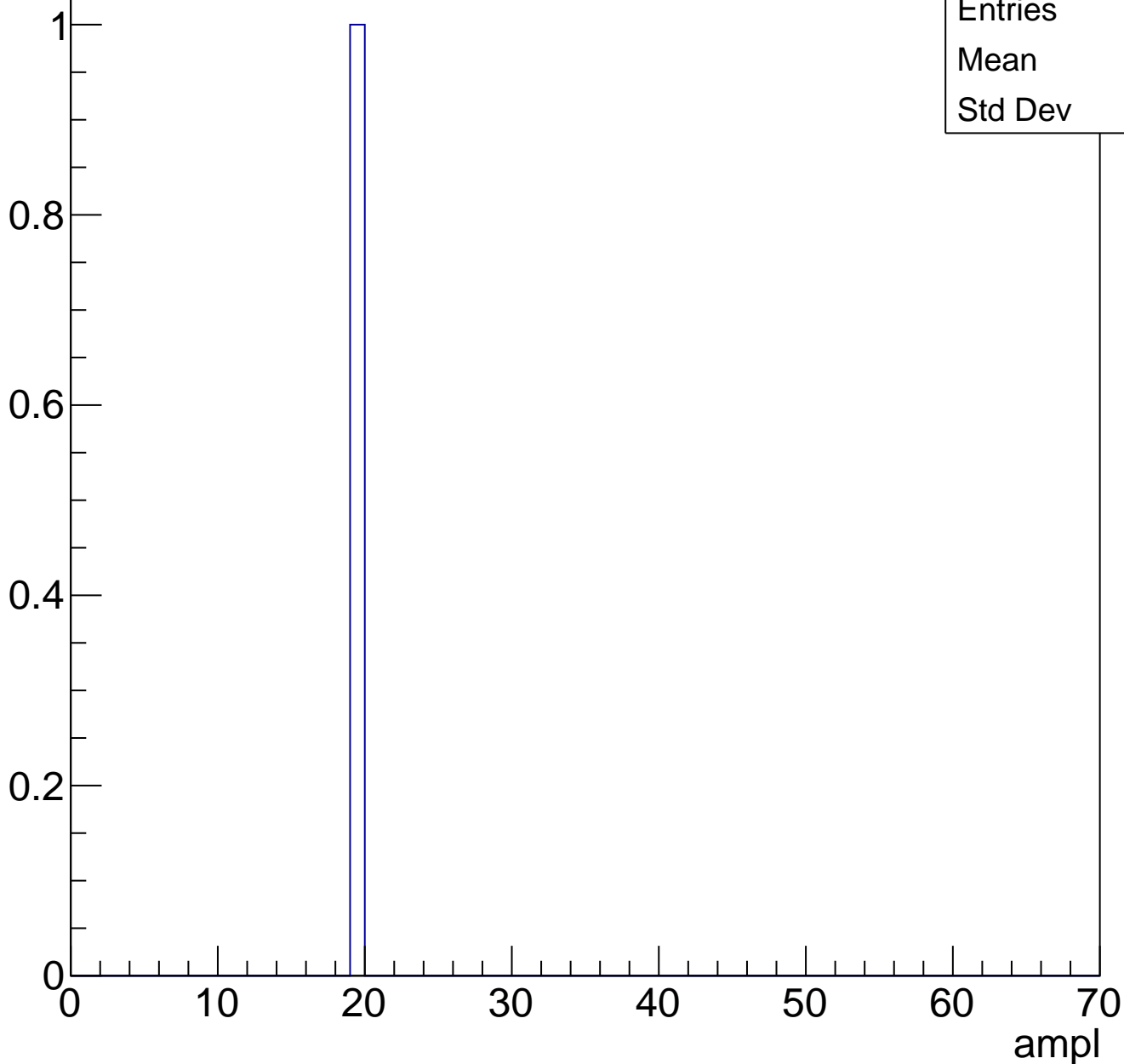




# B1L102S, U25-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch87, adc0

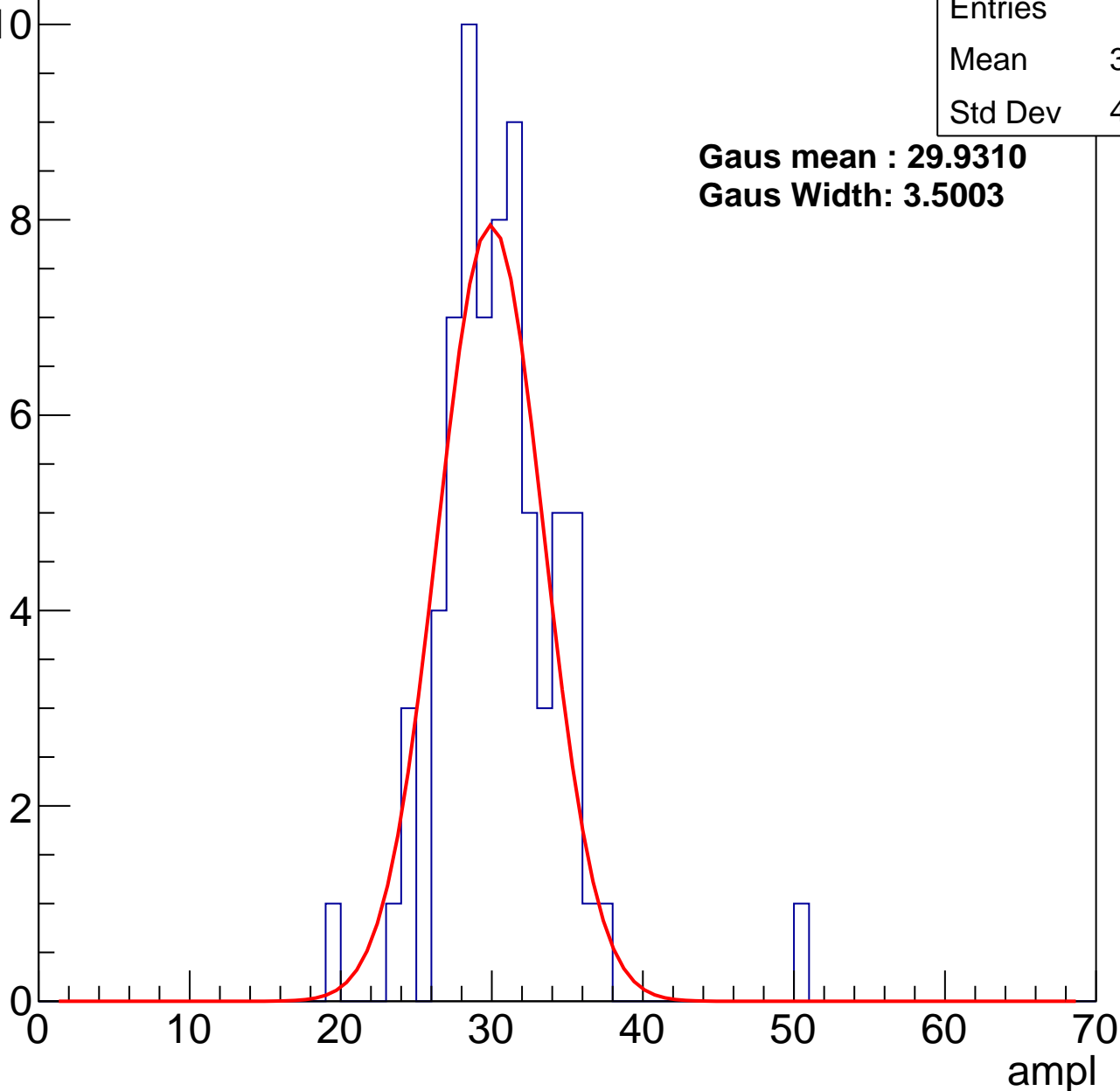
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	30.08
Std Dev	4.107

**Gaus mean : 29.9310**

**Gaus Width: 3.5003**



# B1L102S, U25-ch87, adc1

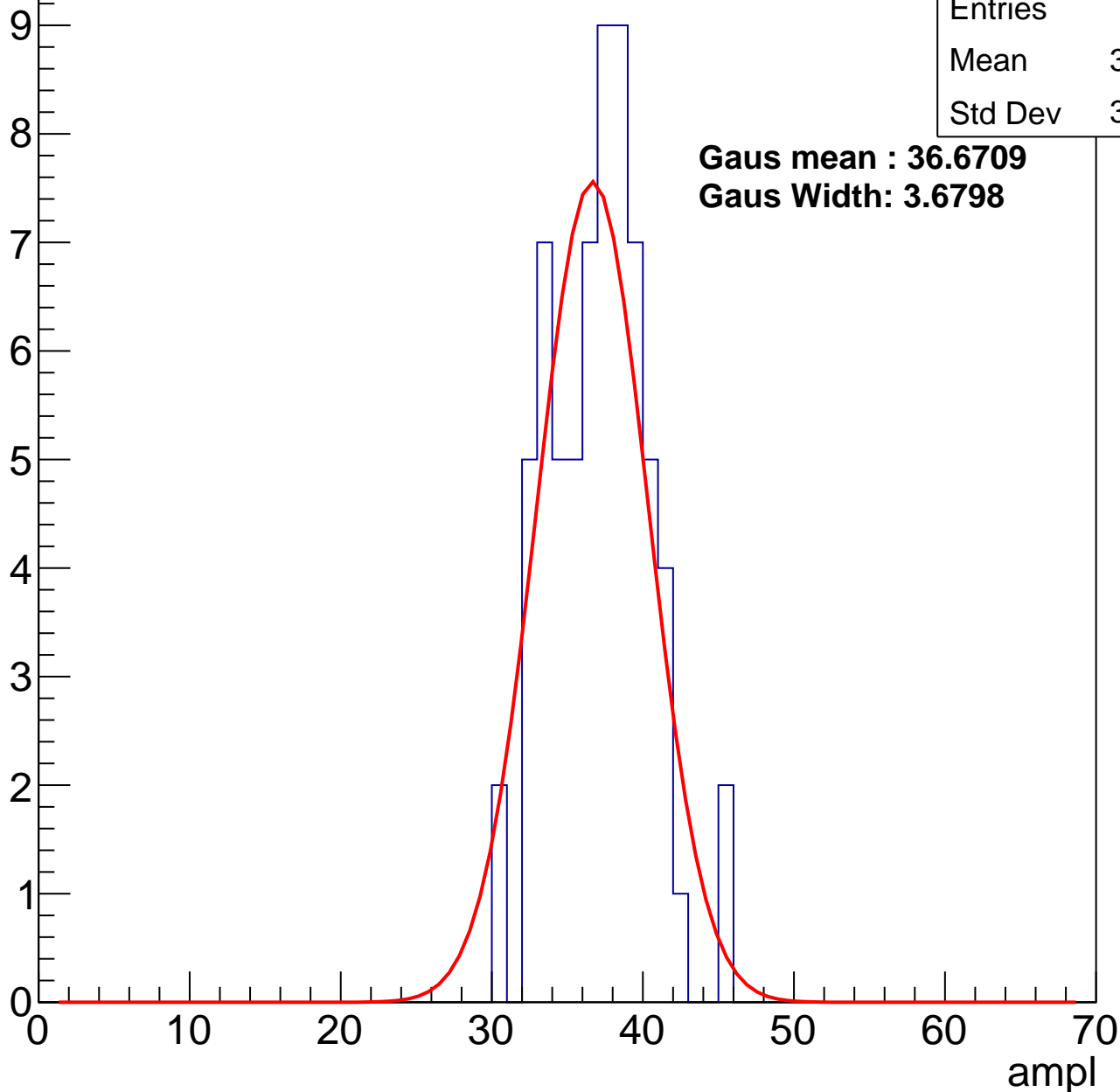
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.65
Std Dev	3.203

**Gaus mean : 36.6709**

**Gaus Width: 3.6798**



# B1L102S, U25-ch87, adc2

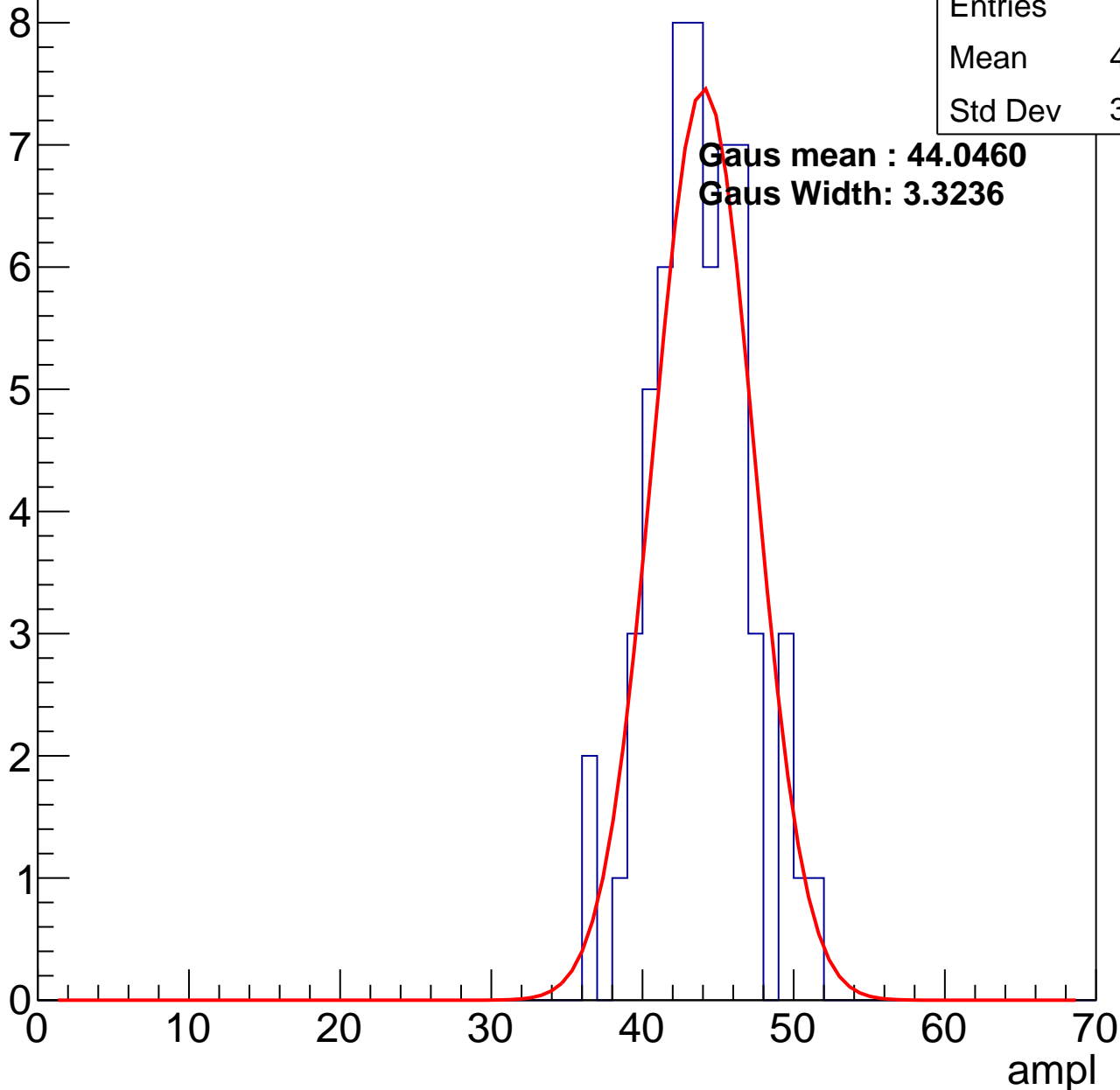
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.33
Std Dev	3.156

**Gaus mean : 44.0460**

**Gaus Width: 3.3236**

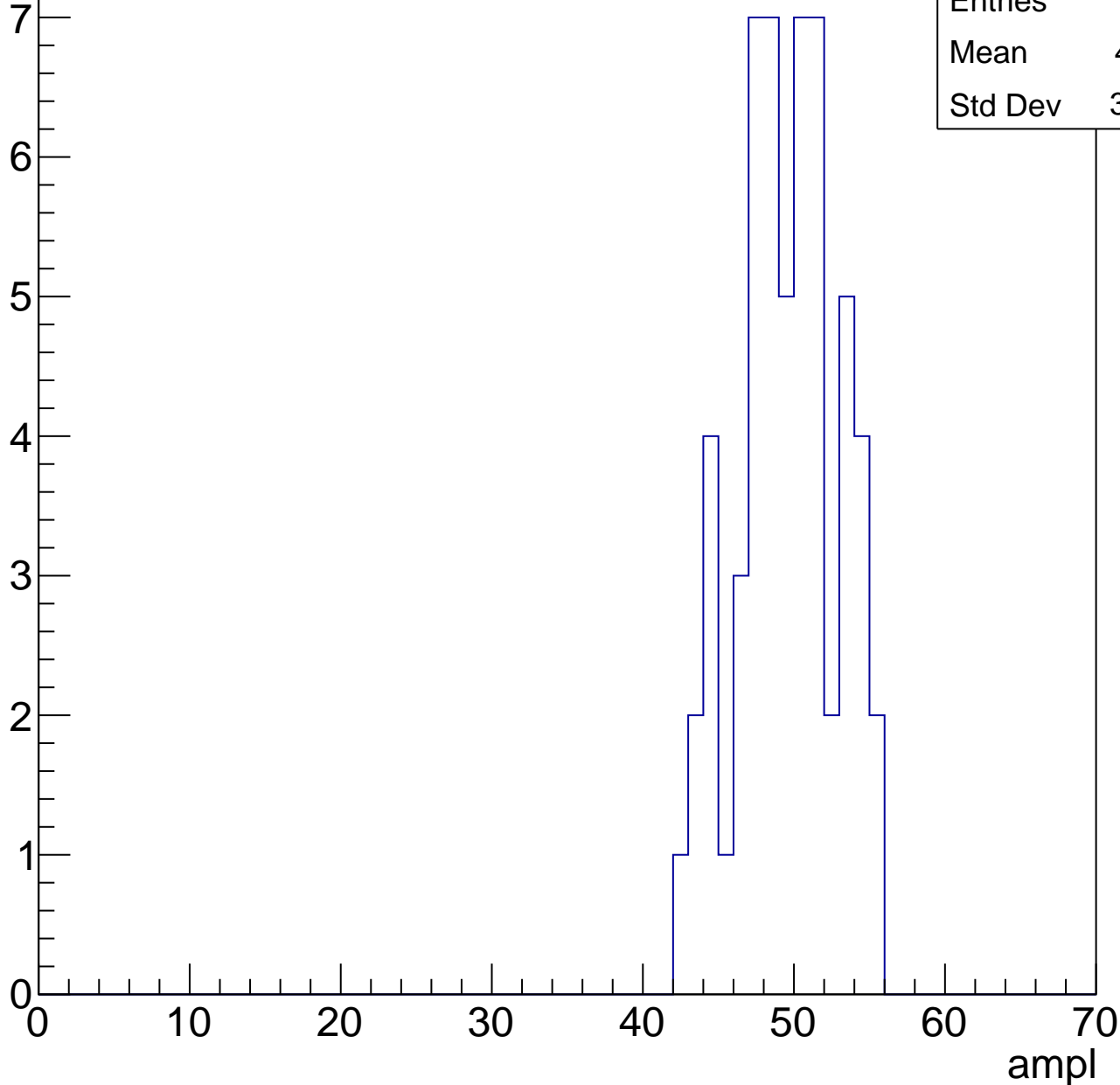


# B1L102S, U25-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.11
Std Dev	3.254



# B1L102S, U25-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

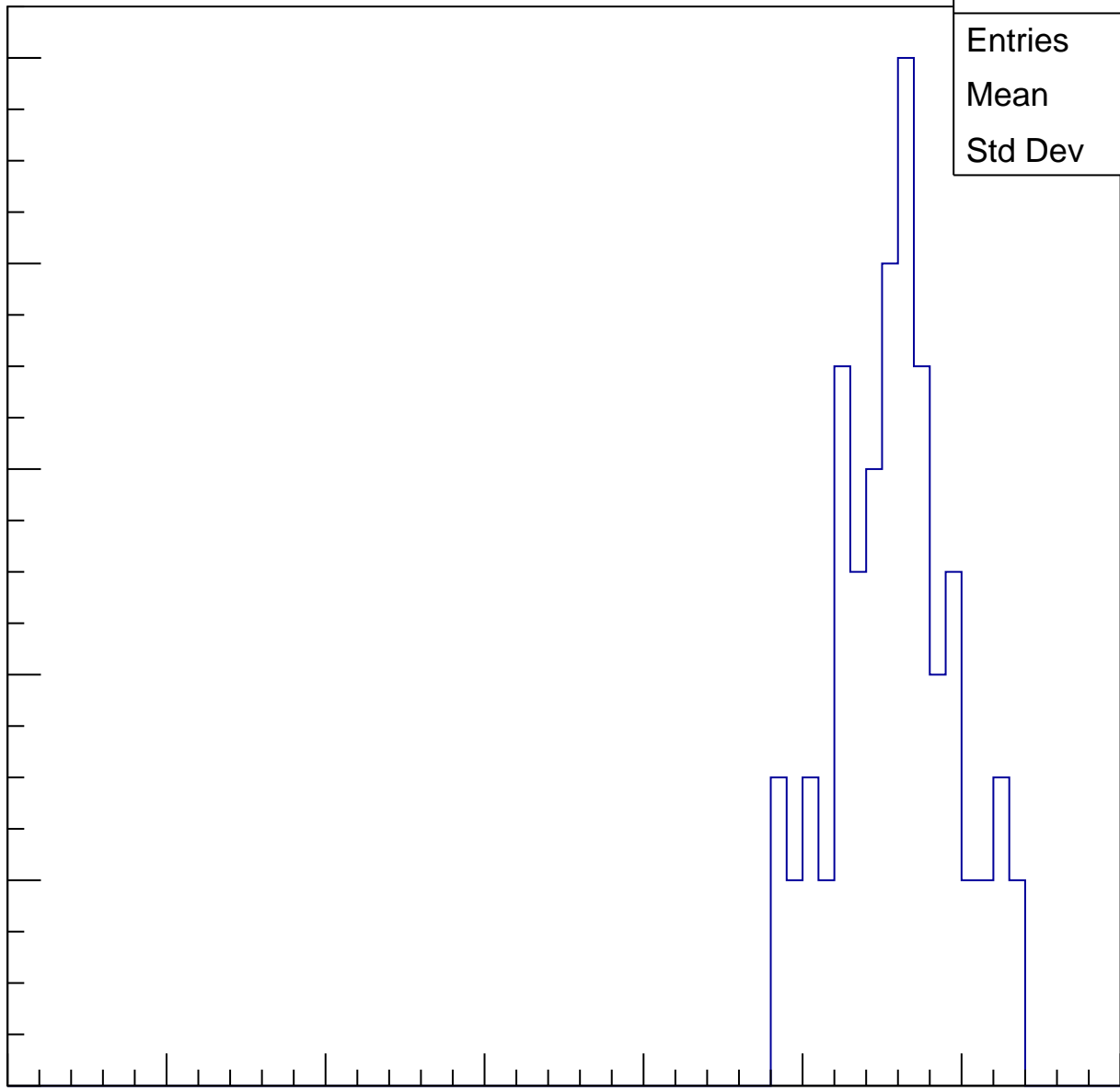
Entries	71
Mean	55.31
Std Dev	3.676

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

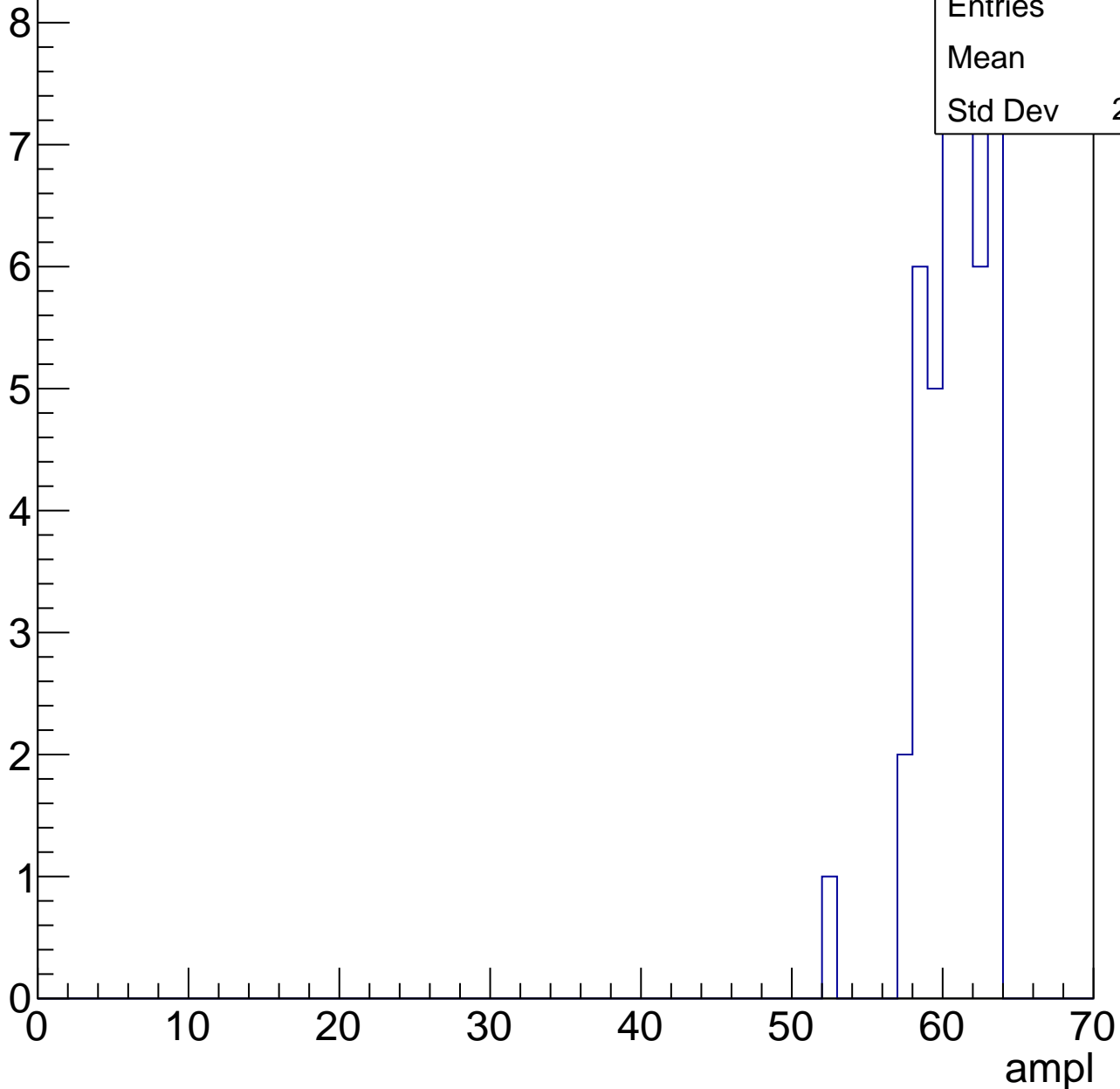


# B1L102S, U25-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

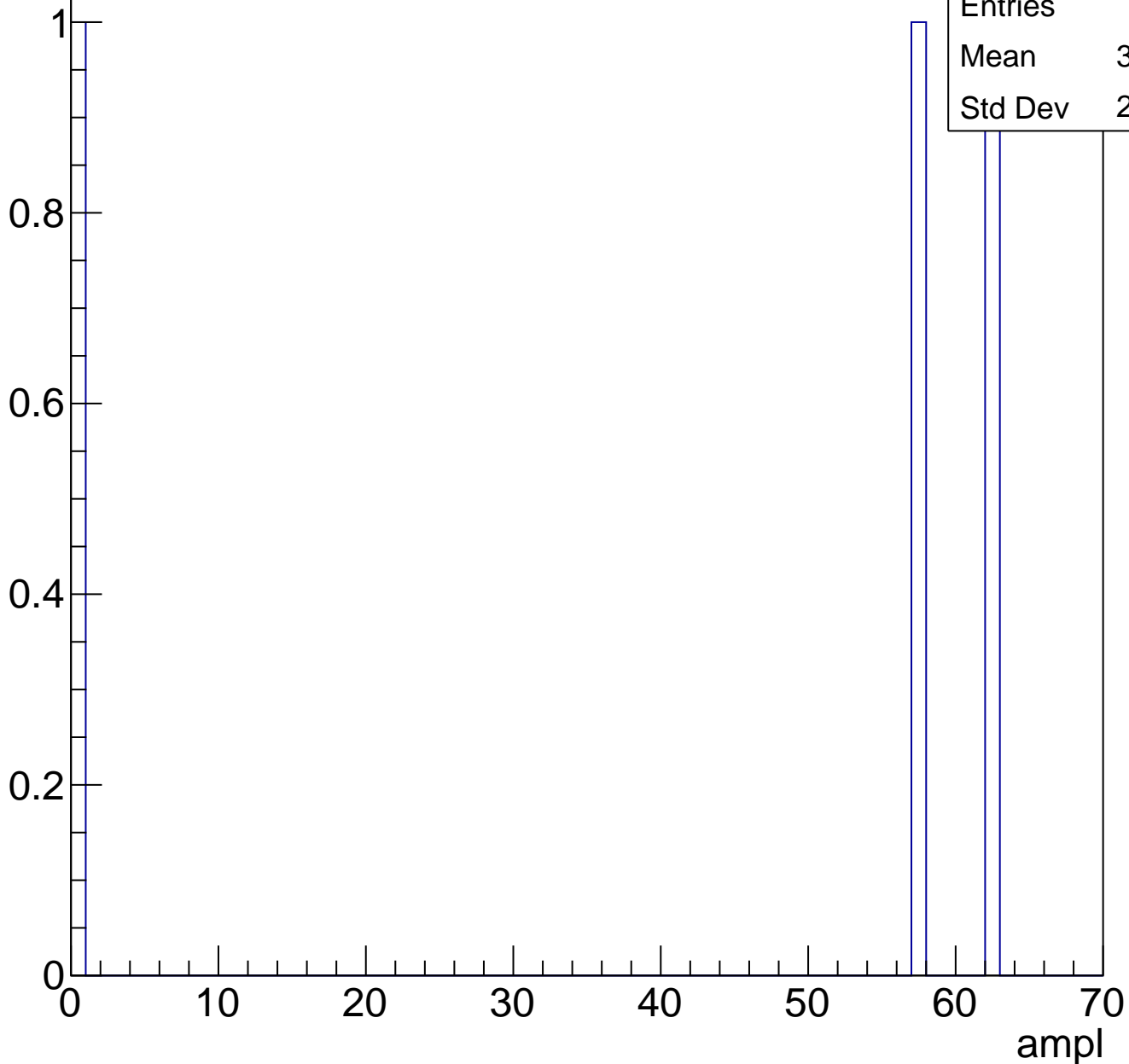
Entries	44
Mean	60.3
Std Dev	2.191



# B1L102S, U25-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch88, adc0

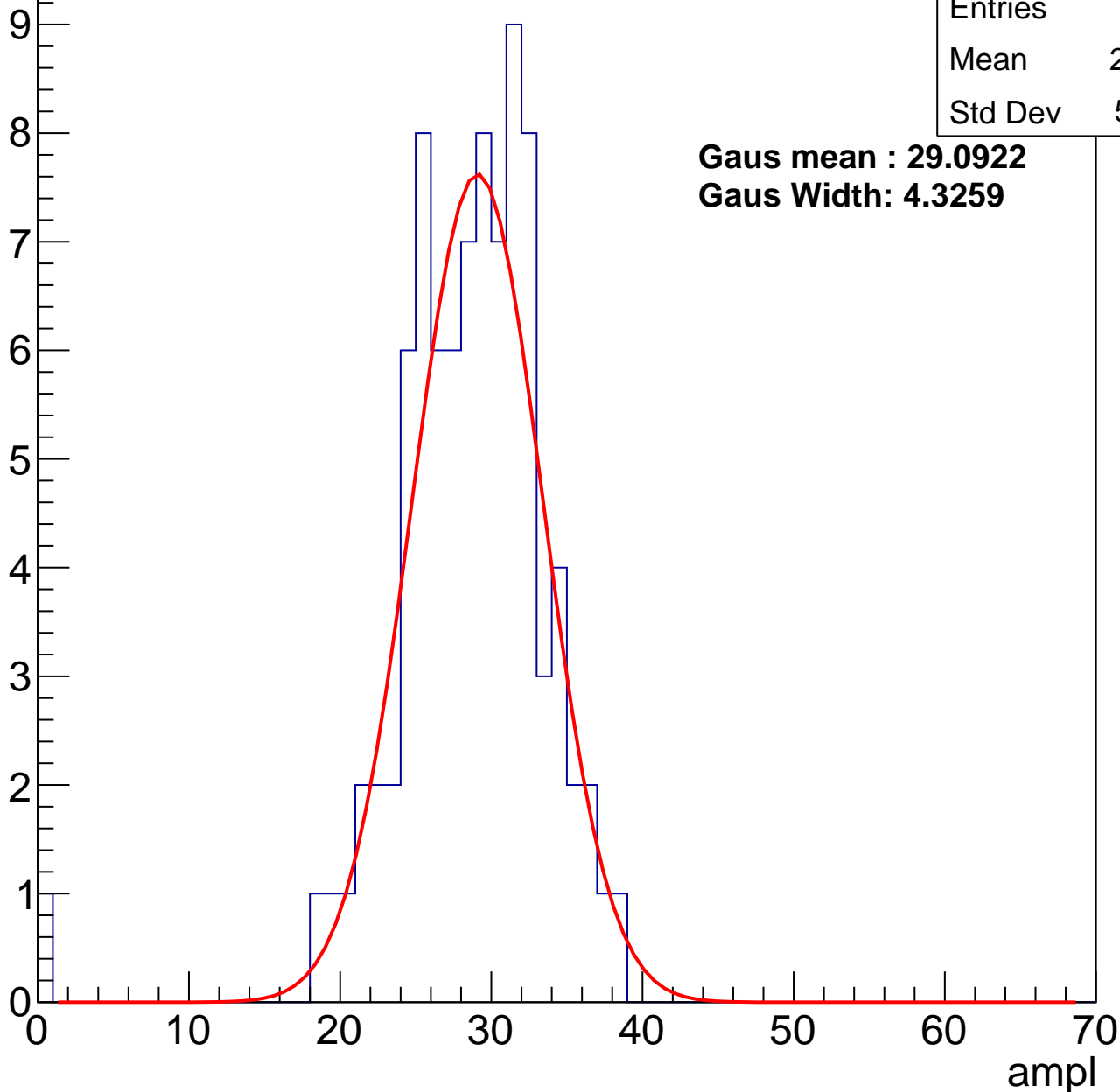
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.14
Std Dev	5.131

**Gaus mean : 29.0922**

**Gaus Width: 4.3259**



# B1L102S, U25-ch88, adc1

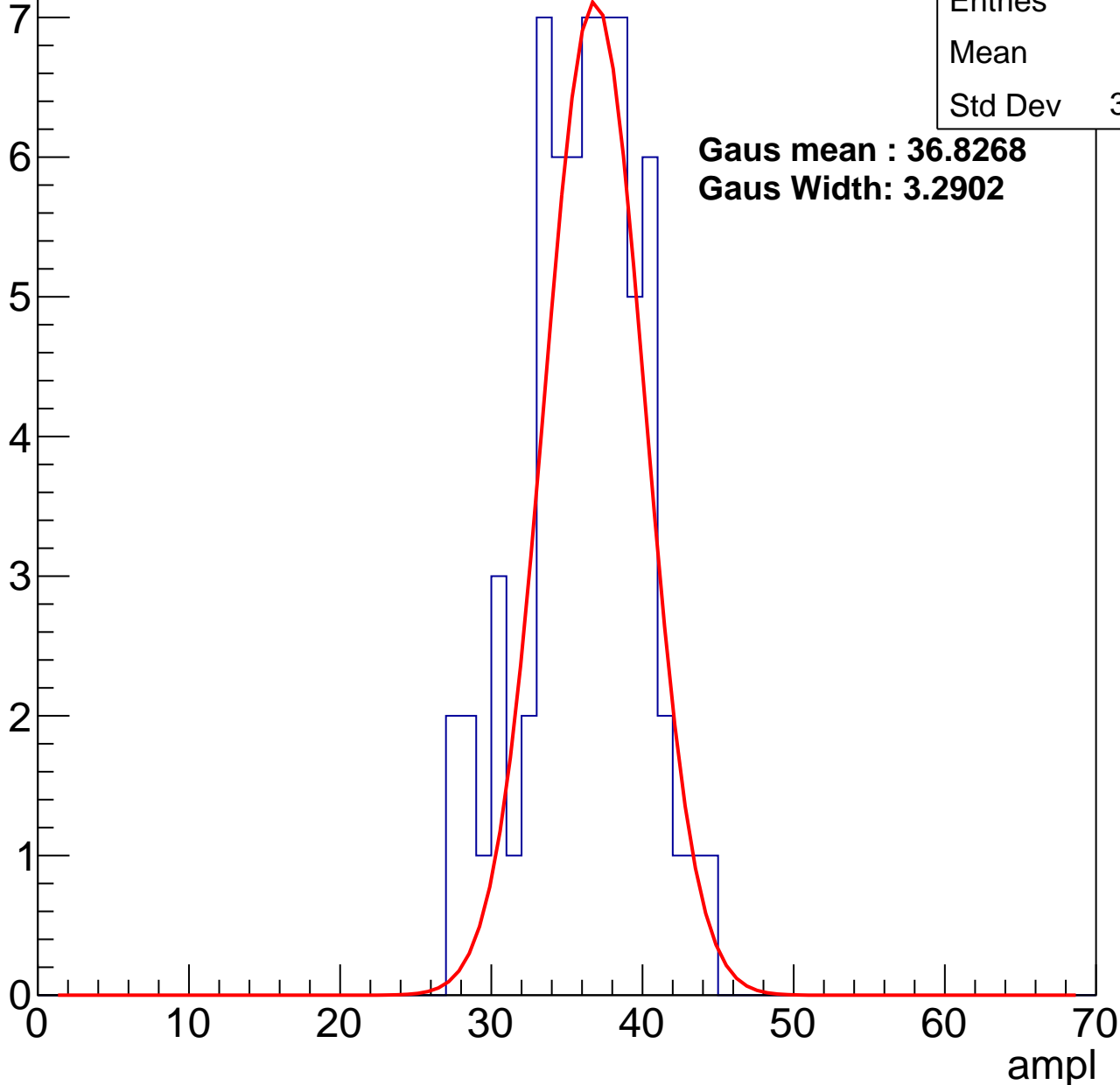
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.7
Std Dev	3.797

**Gaus mean : 36.8268**

**Gaus Width: 3.2902**



# B1L102S, U25-ch88, adc2

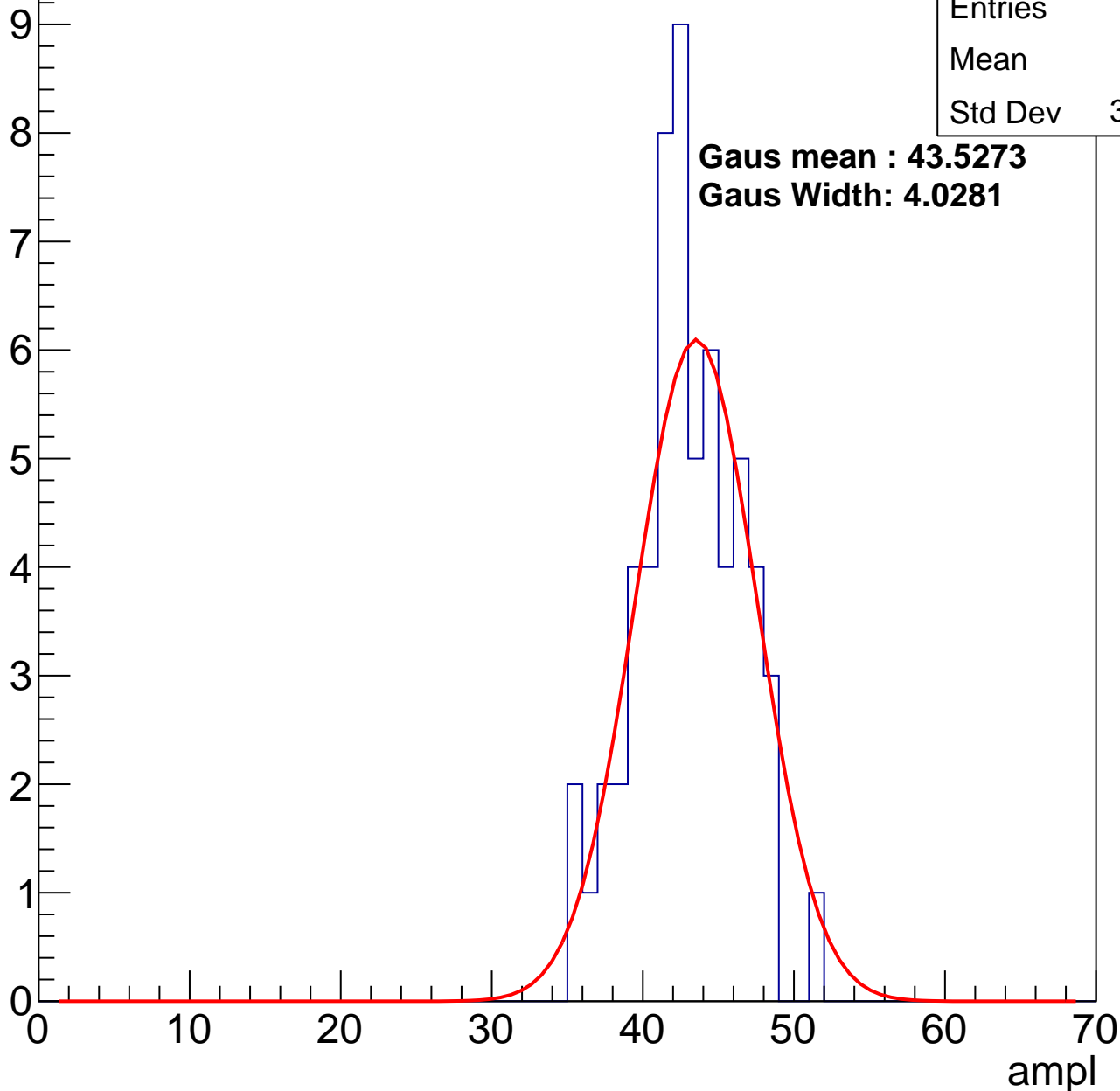
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.5
Std Dev	3.403

**Gaus mean : 43.5273**

**Gaus Width: 4.0281**

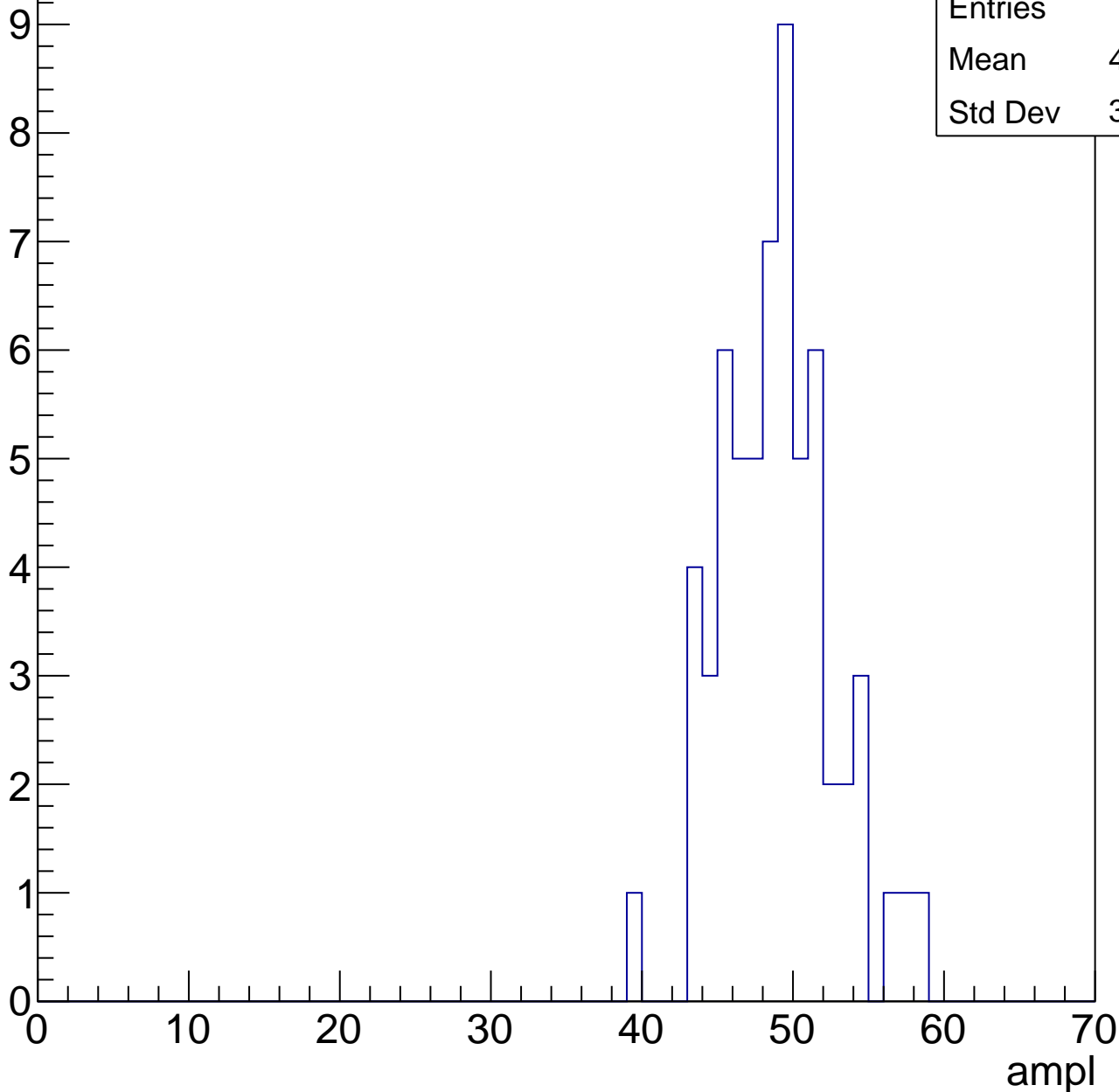


# B1L102S, U25-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

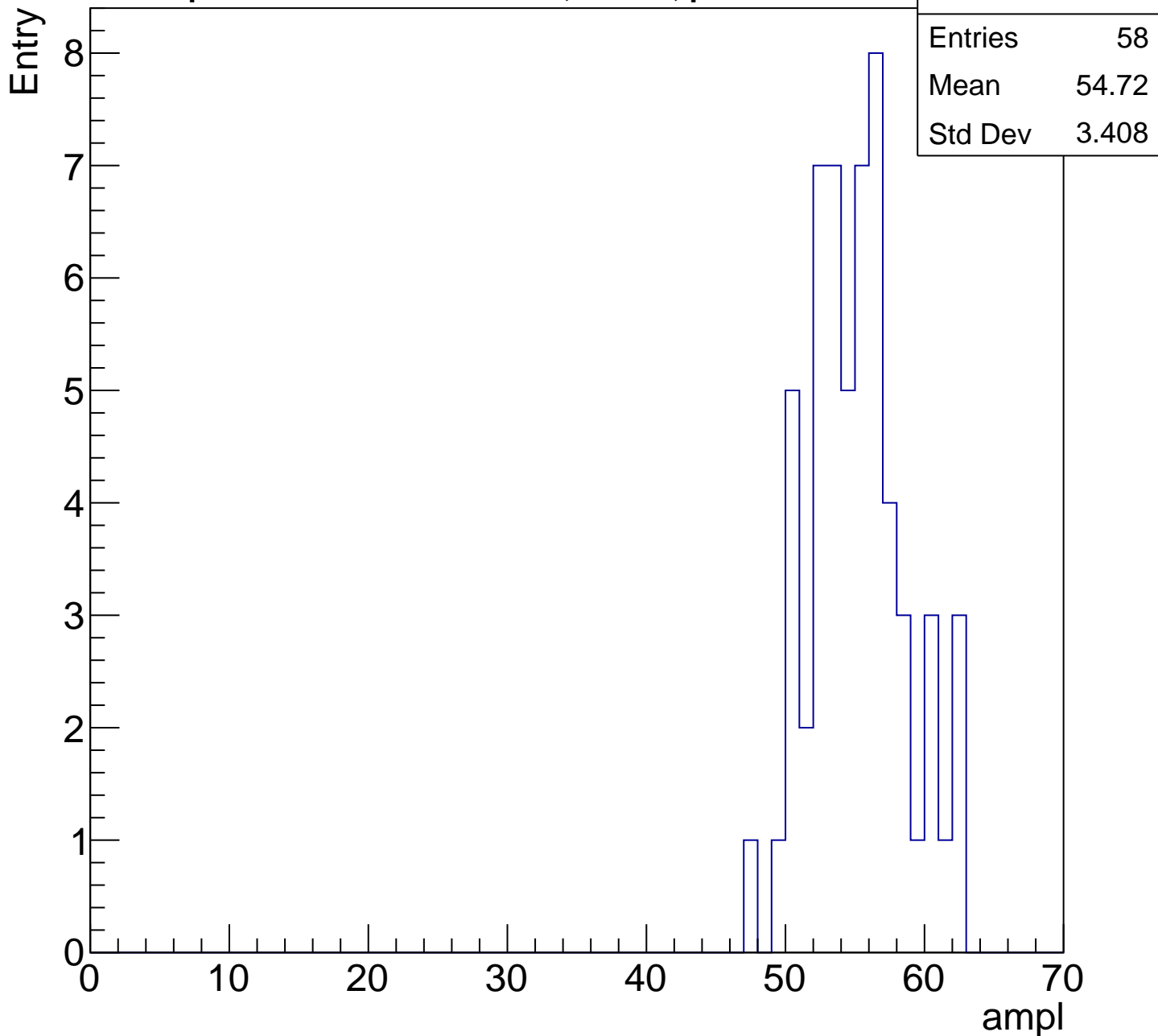
Entry

Entries	61
Mean	48.43
Std Dev	3.655



# B1L102S, U25-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

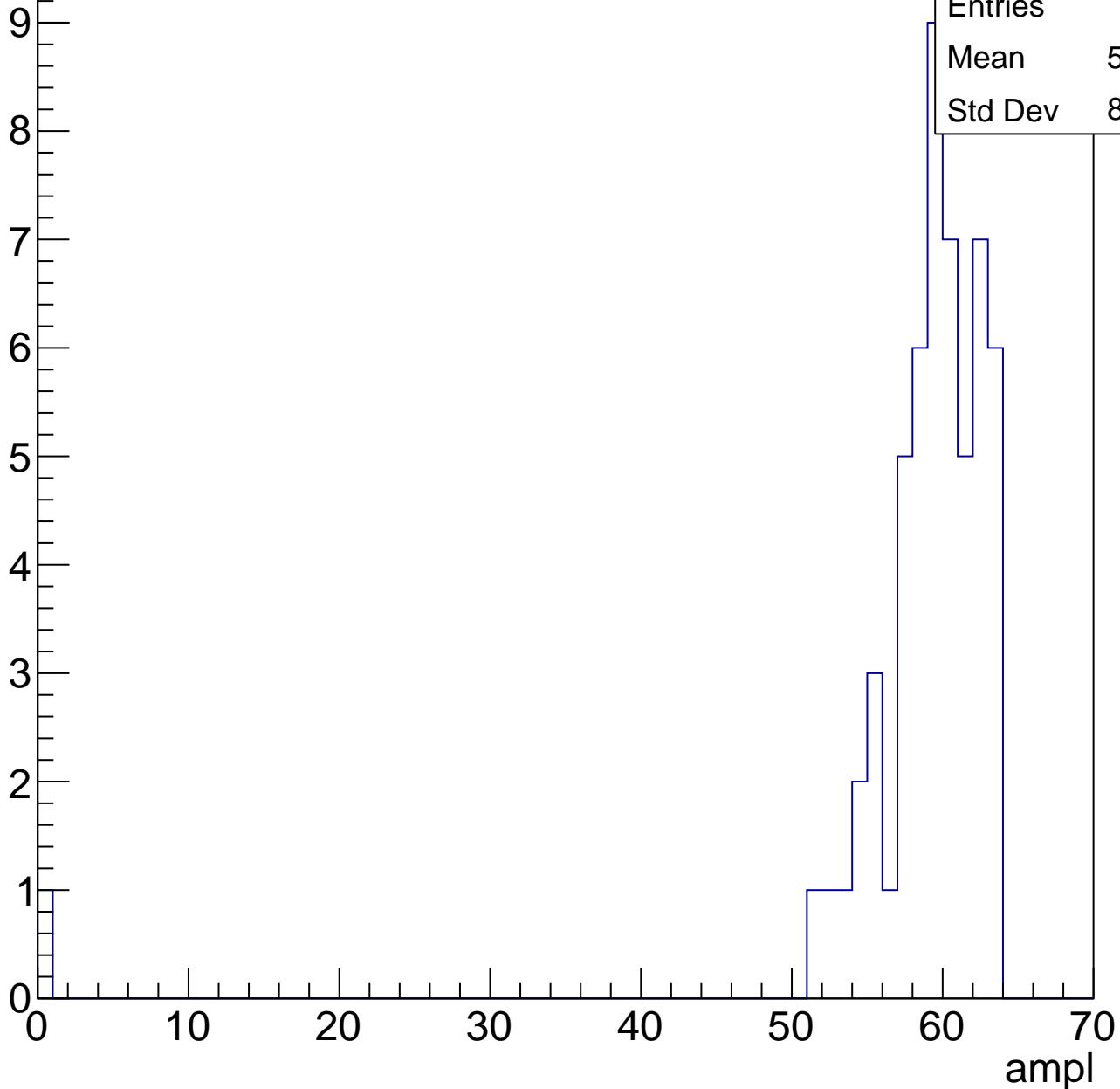


# B1L102S, U25-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

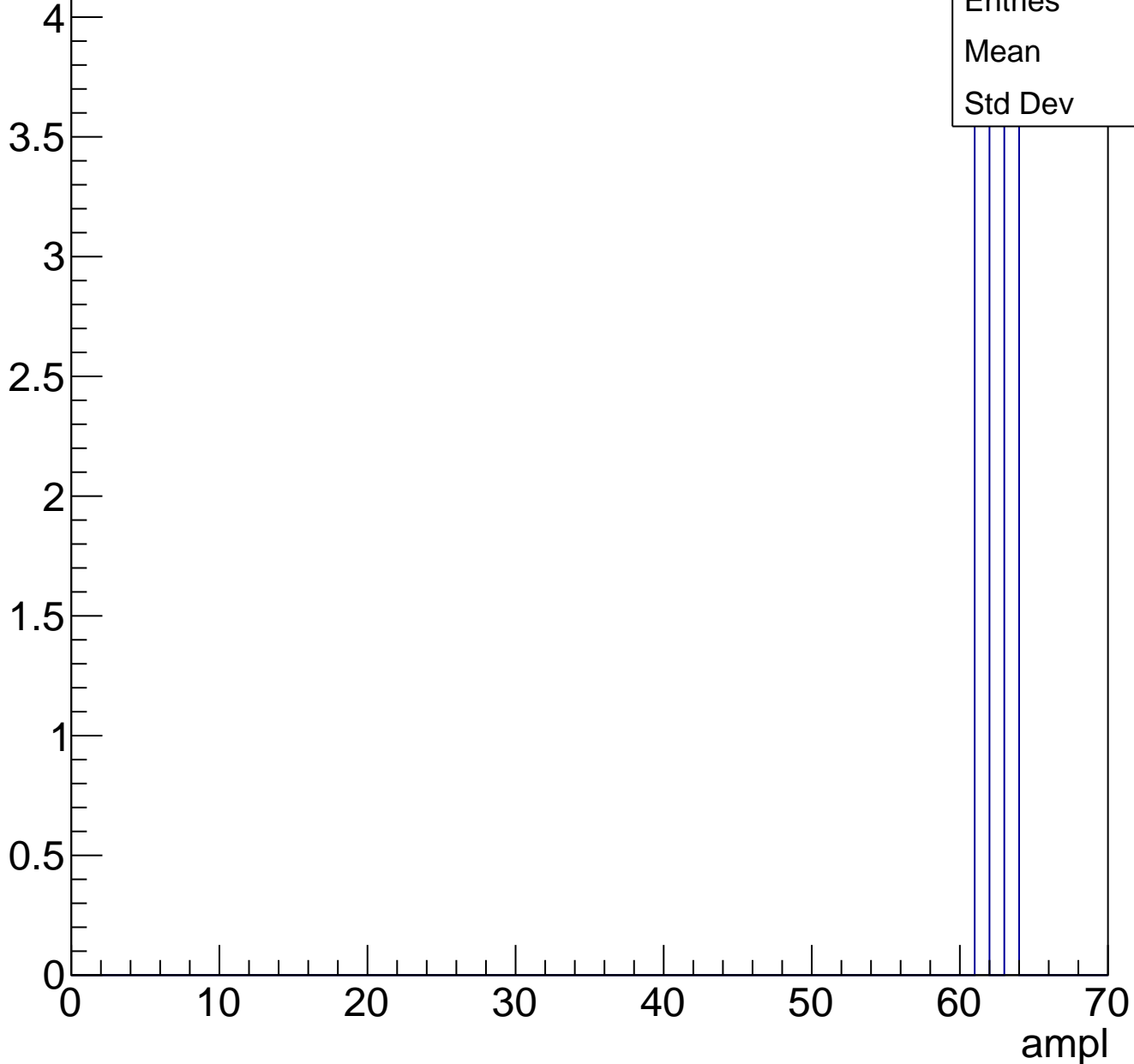
Entries	55
Mean	57.93
Std Dev	8.405



# B1L102S, U25-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch89, adc0

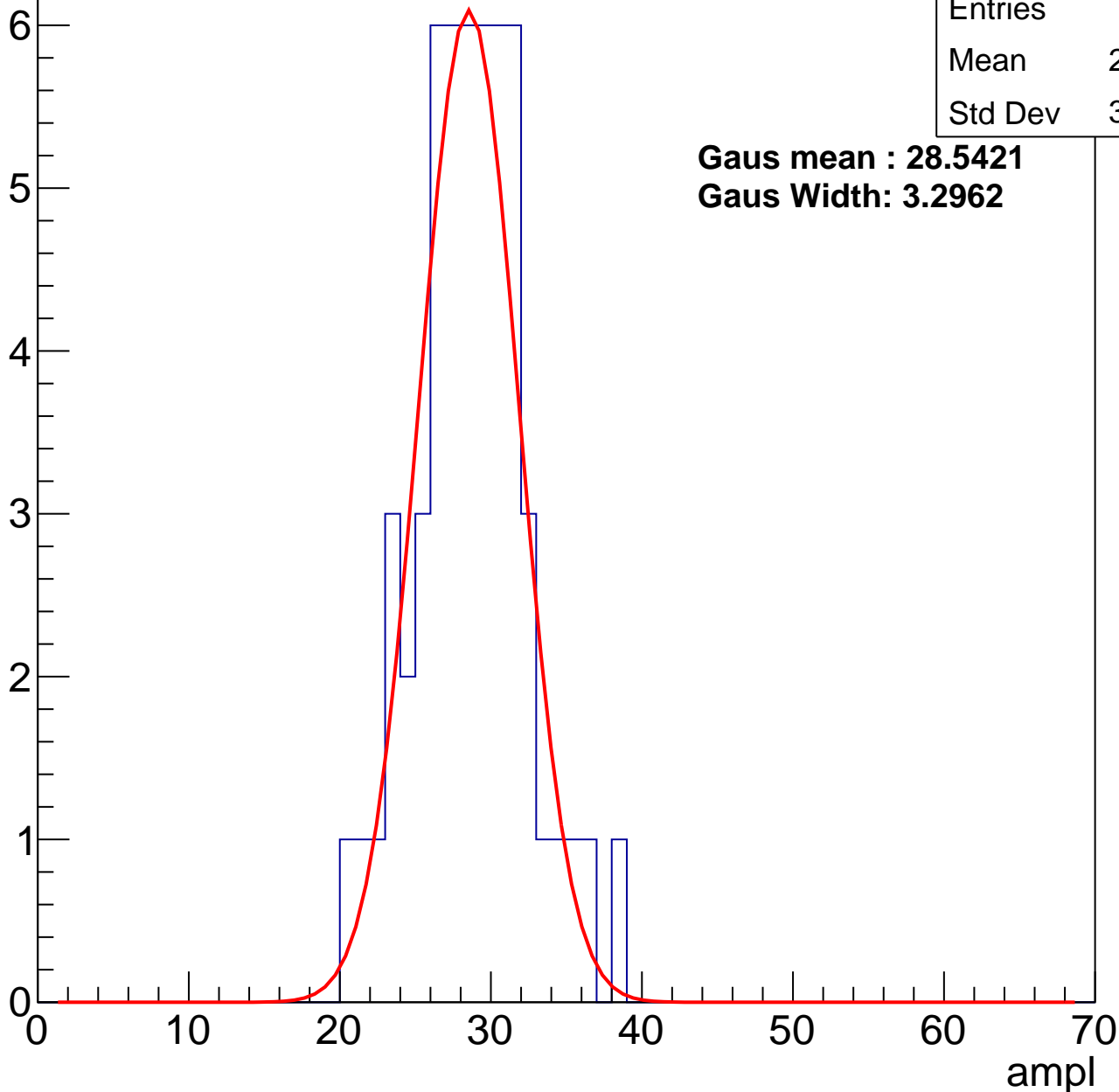
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.24
Std Dev	3.608

**Gaus mean : 28.5421**

**Gaus Width: 3.2962**



# B1L102S, U25-ch89, adc1

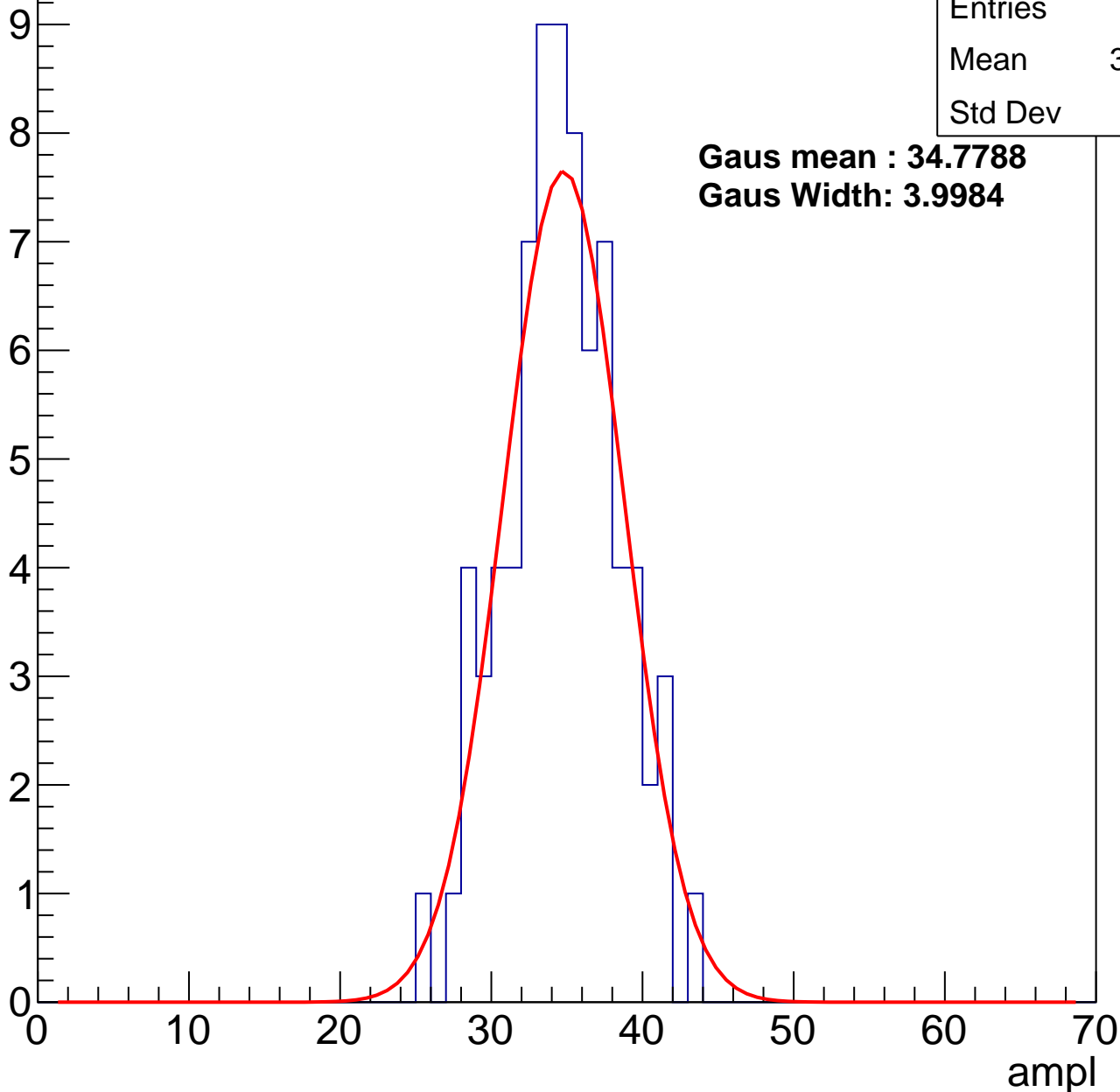
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.17
Std Dev	3.68

**Gaus mean : 34.7788**

**Gaus Width: 3.9984**



# B1L102S, U25-ch89, adc2

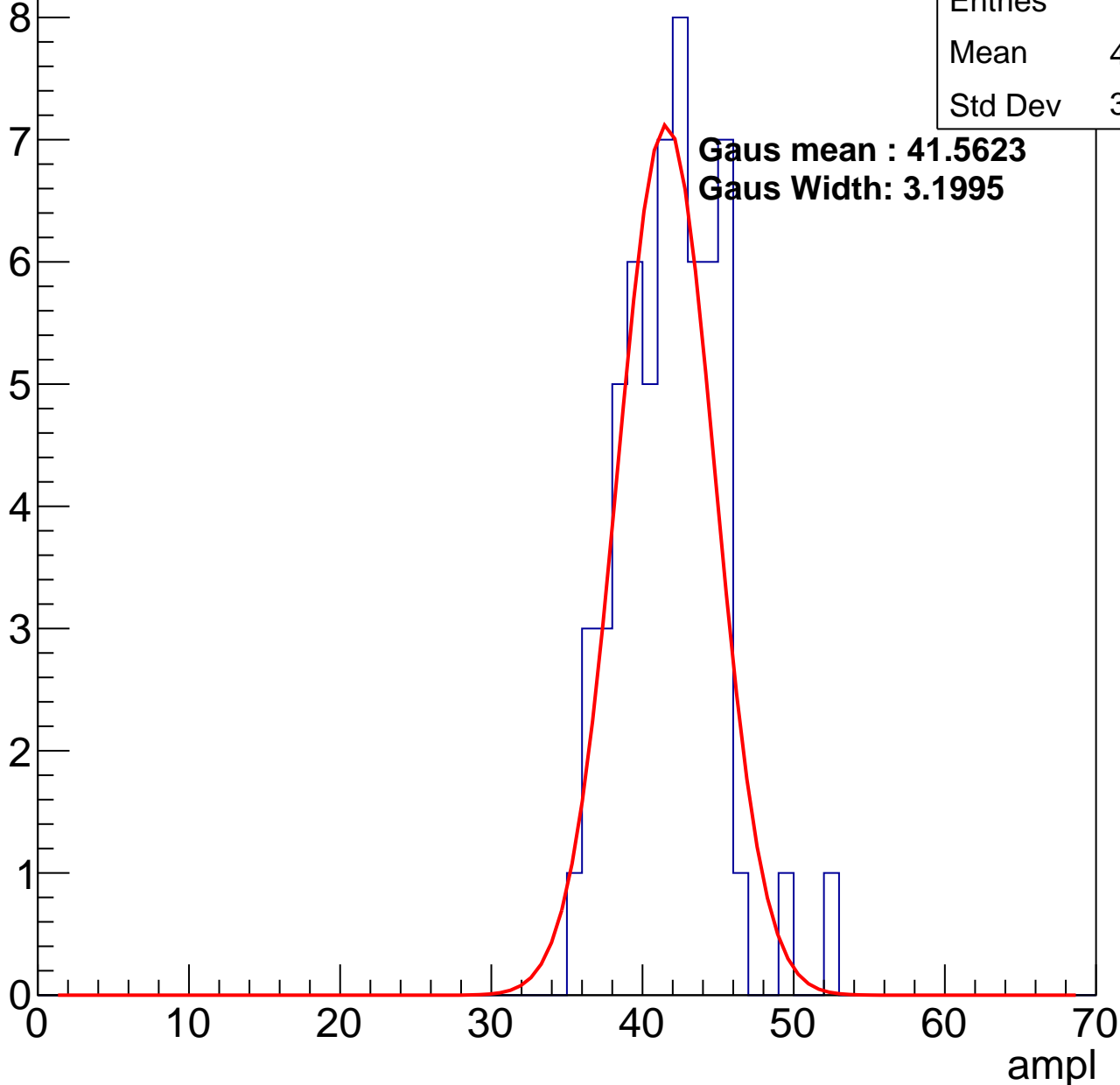
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.42
Std Dev	3.242

**Gaus mean : 41.5623**

**Gaus Width: 3.1995**

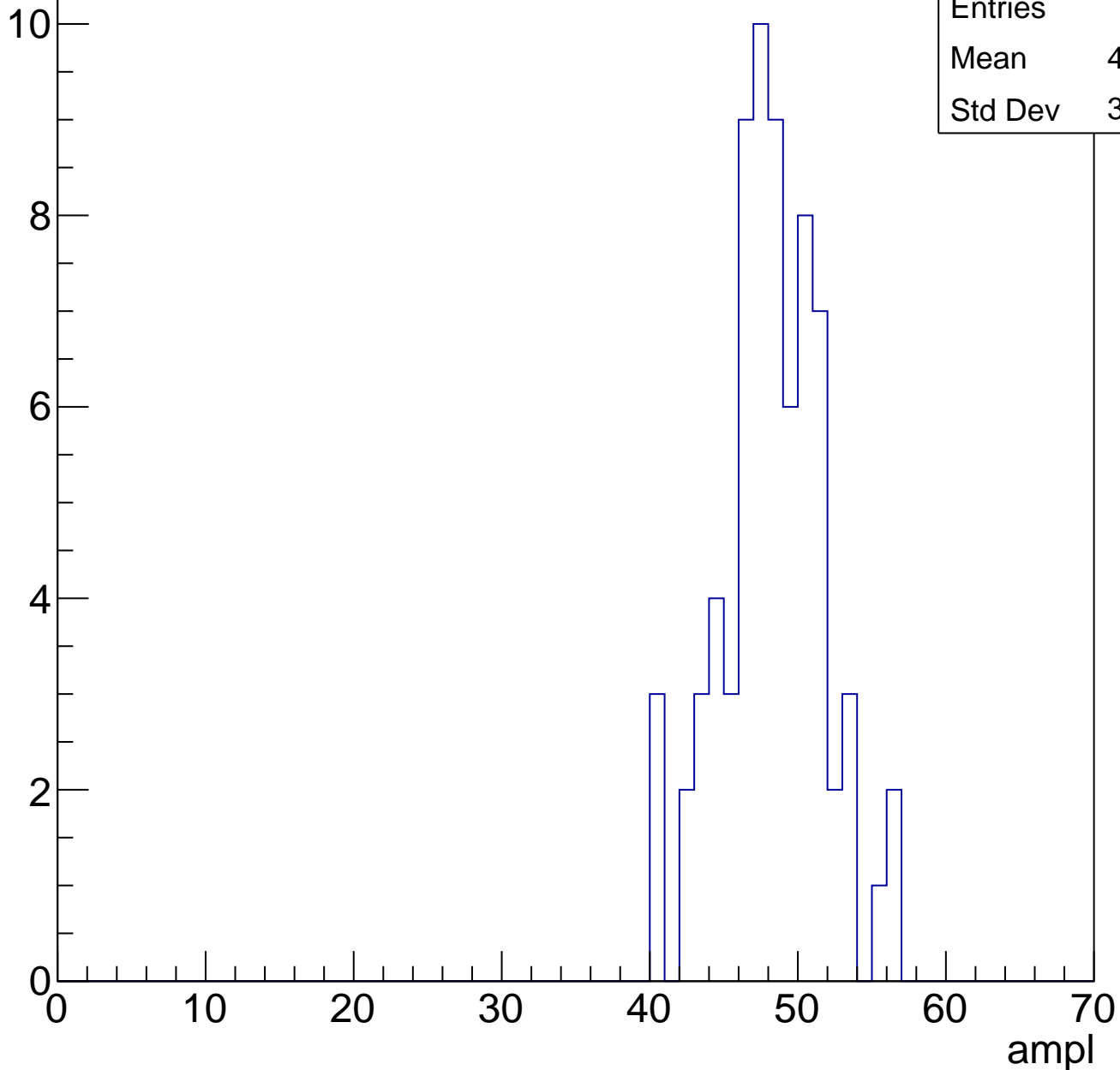


# B1L102S, U25-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	72
Mean	47.79
Std Dev	3.452

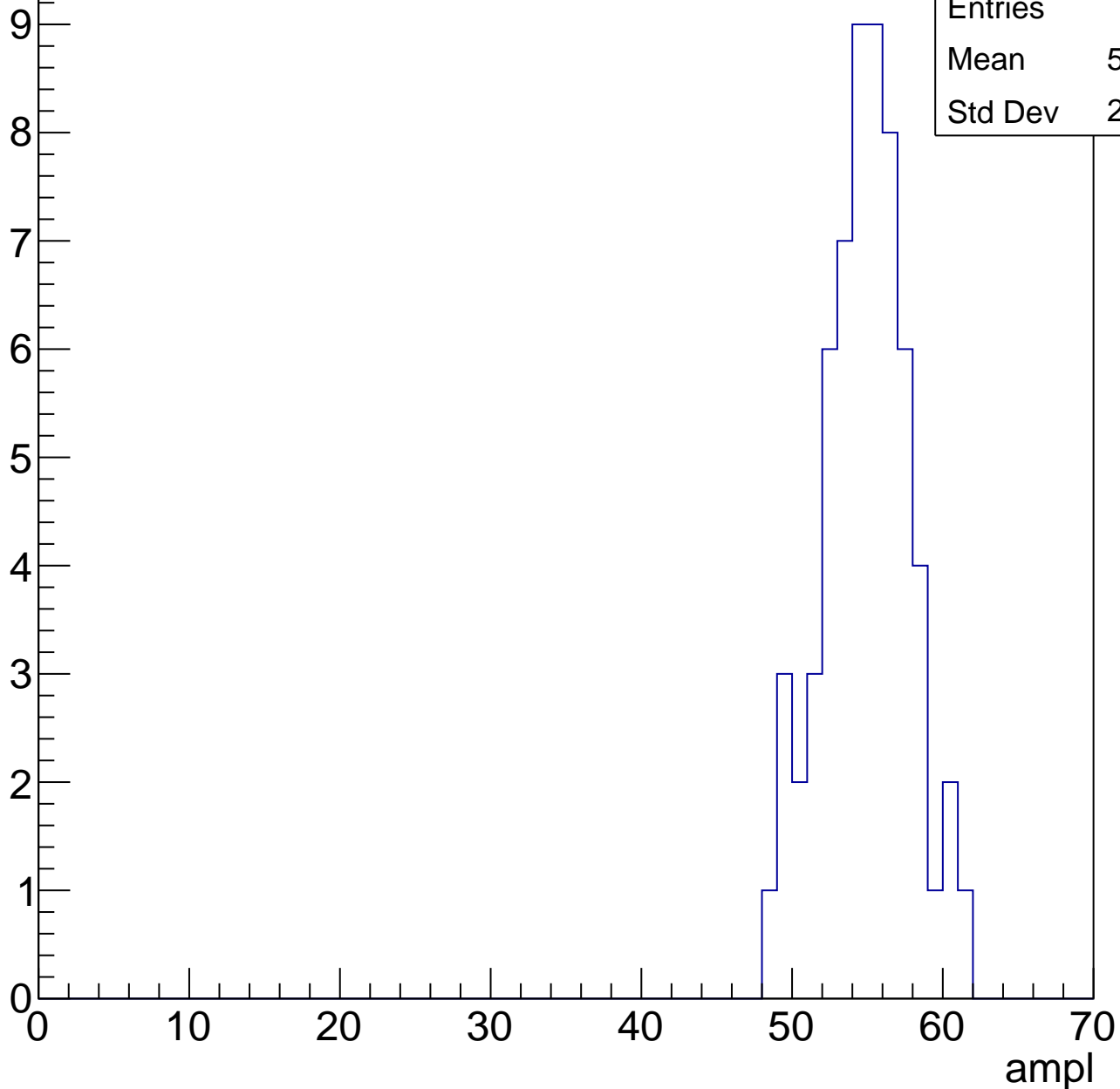
Entry



# B1L102S, U25-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



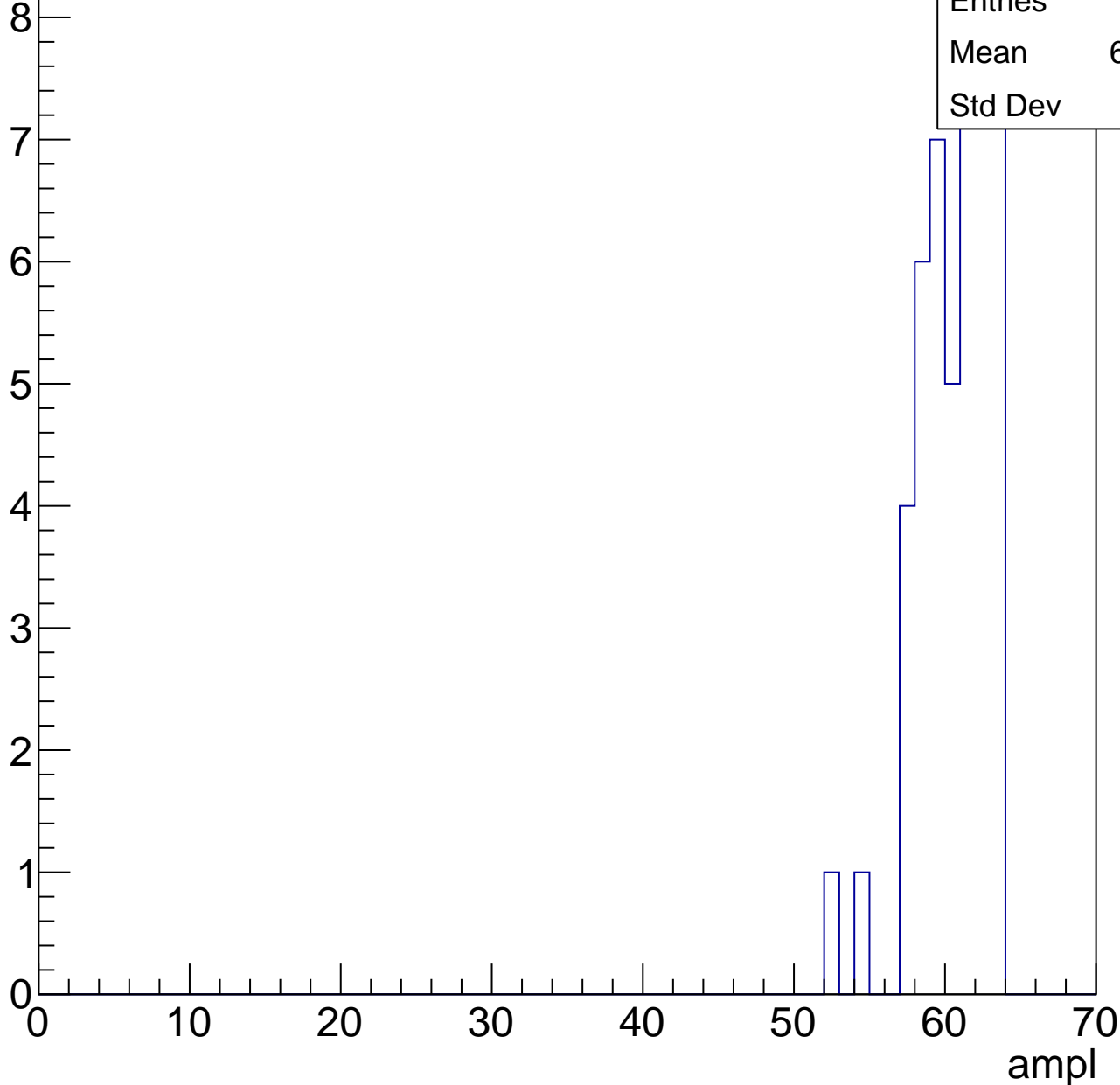
Entries	62
Mean	54.42
Std Dev	2.826

# B1L102S, U25-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	60.06
Std Dev	2.41



# B1L102S, U25-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

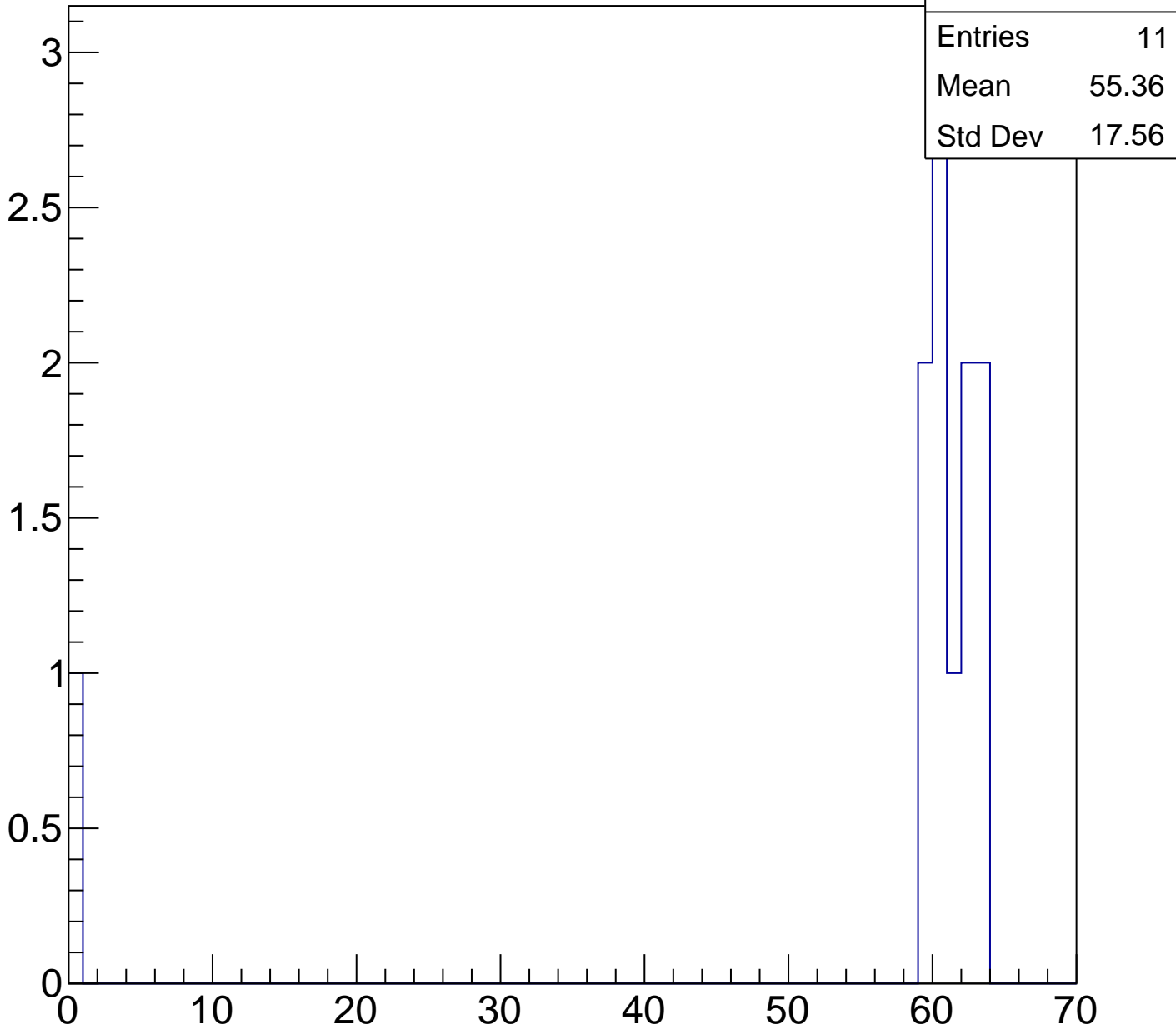
1

0.5

0

ampl

Entries	11
Mean	55.36
Std Dev	17.56





# B1L102S, U25-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch90, adc0

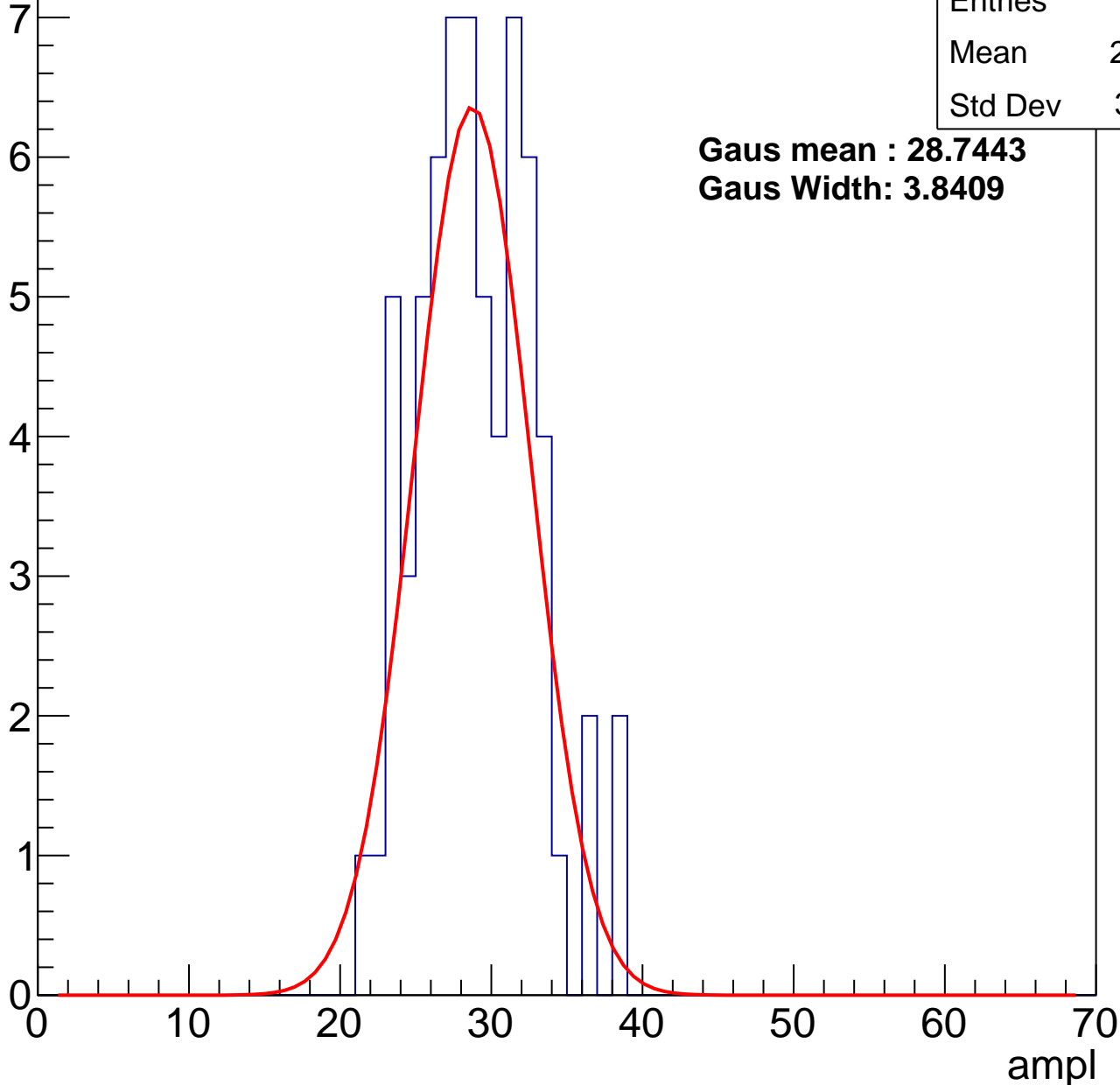
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.55
Std Dev	3.811

**Gaus mean : 28.7443**

**Gaus Width: 3.8409**



# B1L102S, U25-ch90, adc1

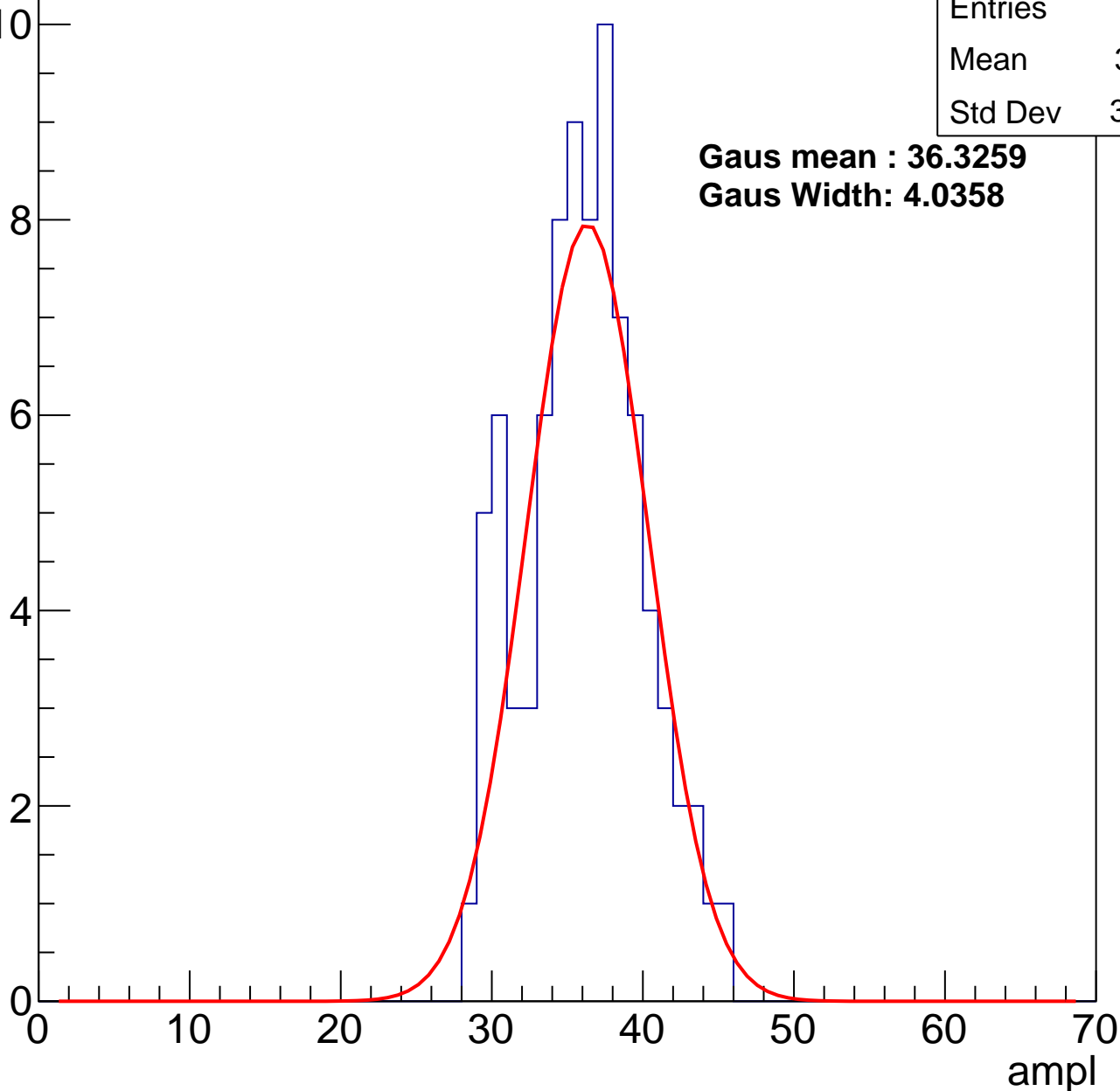
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	85
Mean	35.61
Std Dev	3.875

**Gaus mean : 36.3259**

**Gaus Width: 4.0358**



# B1L102S, U25-ch90, adc2

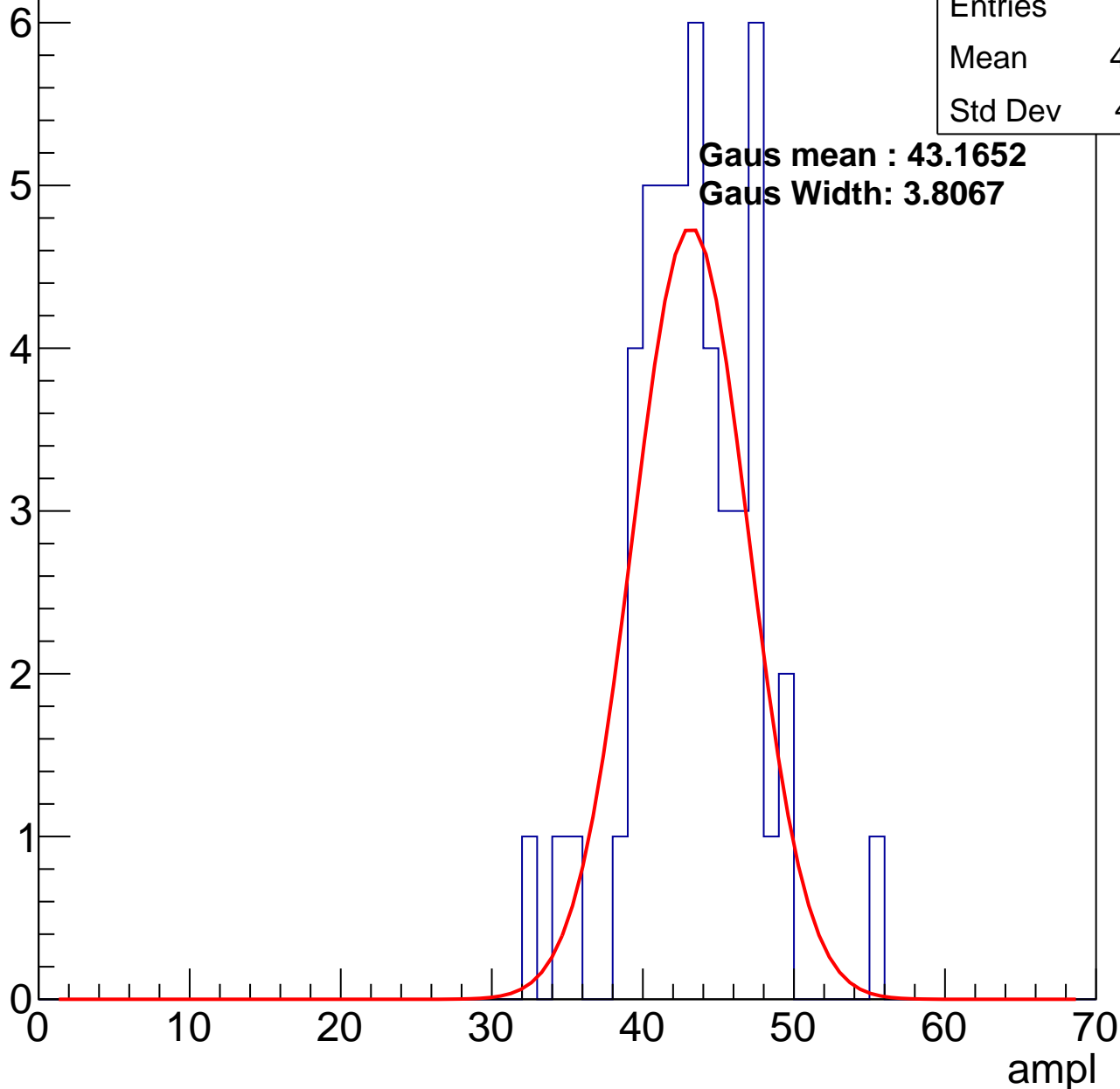
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.86
Std Dev	4.051

**Gaus mean : 43.1652**

**Gaus Width: 3.8067**

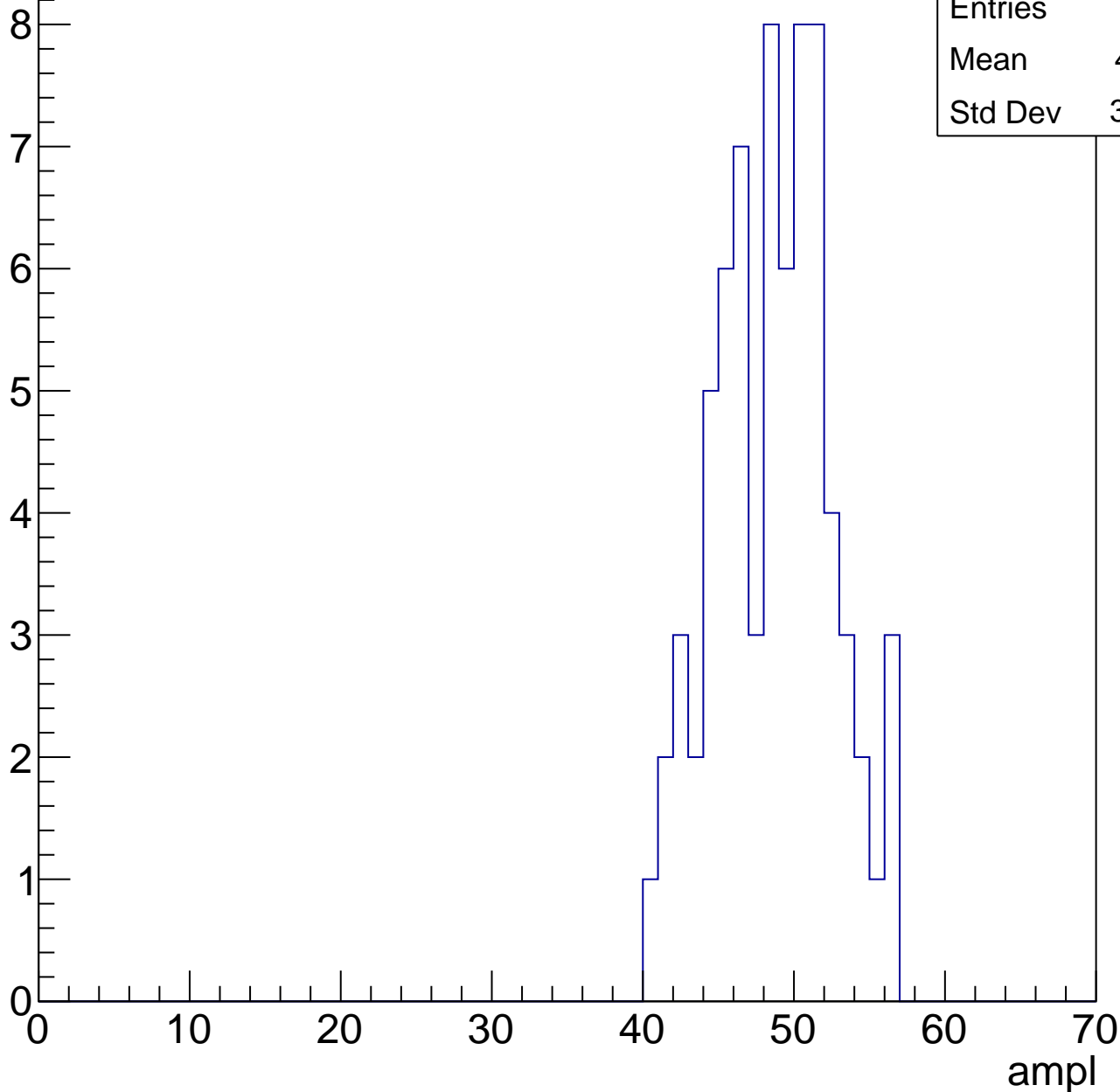


# B1L102S, U25-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.21
Std Dev	3.808

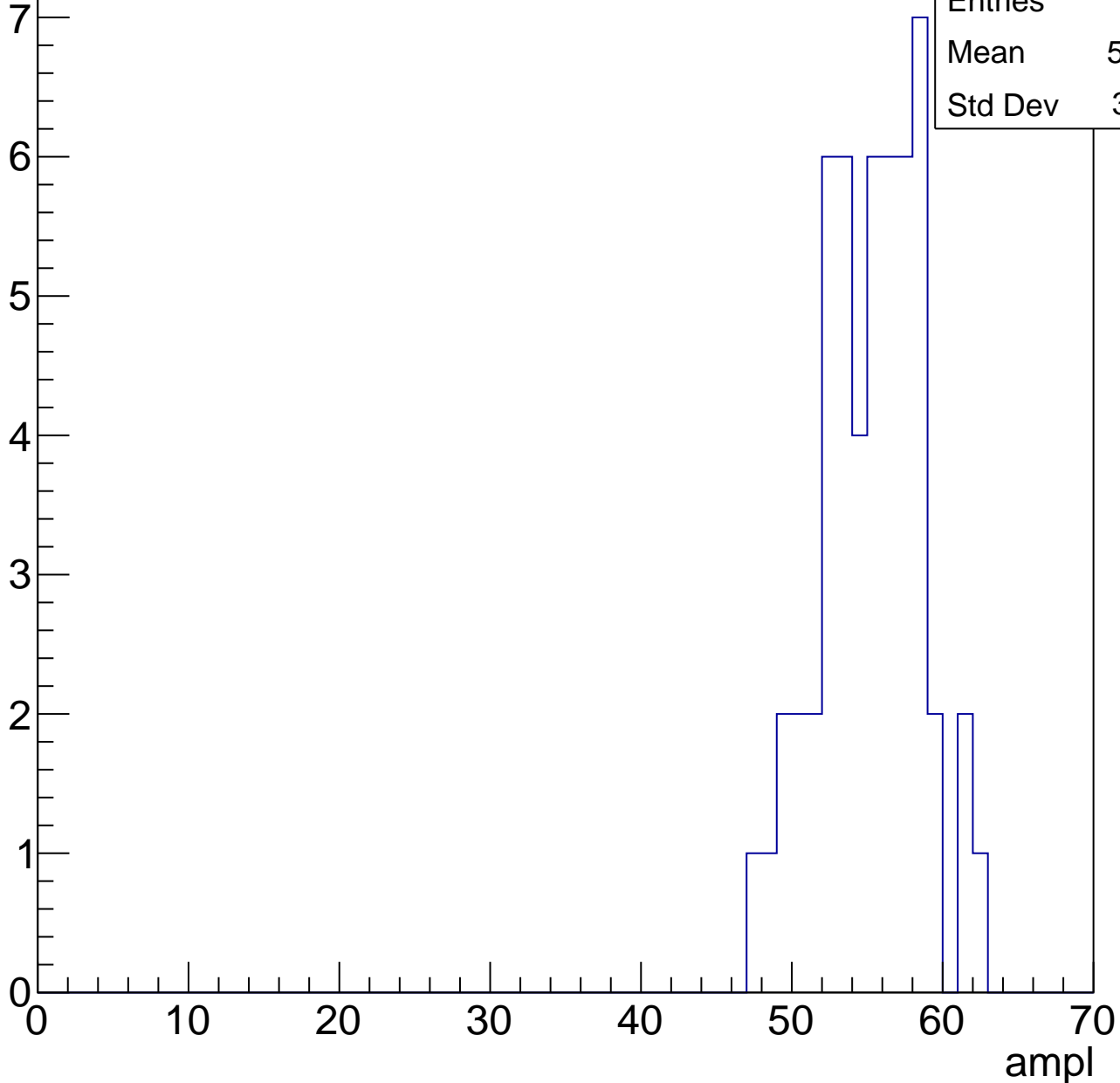


# B1L102S, U25-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.76
Std Dev	3.311

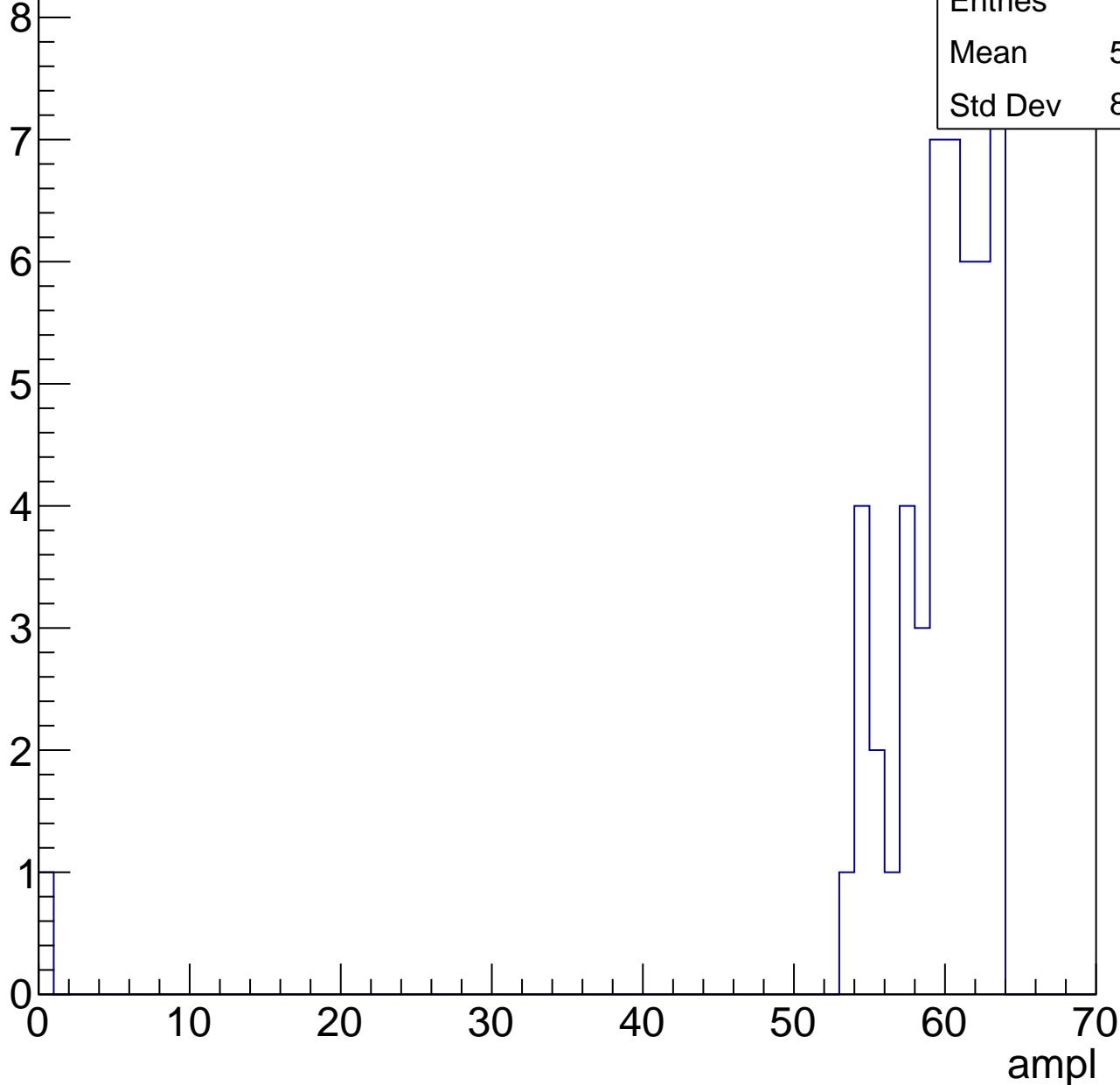


# B1L102S, U25-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.24
Std Dev	8.788



# B1L102S, U25-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.18
Std Dev	1.266



# B1L102S, U25-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch91, adc0

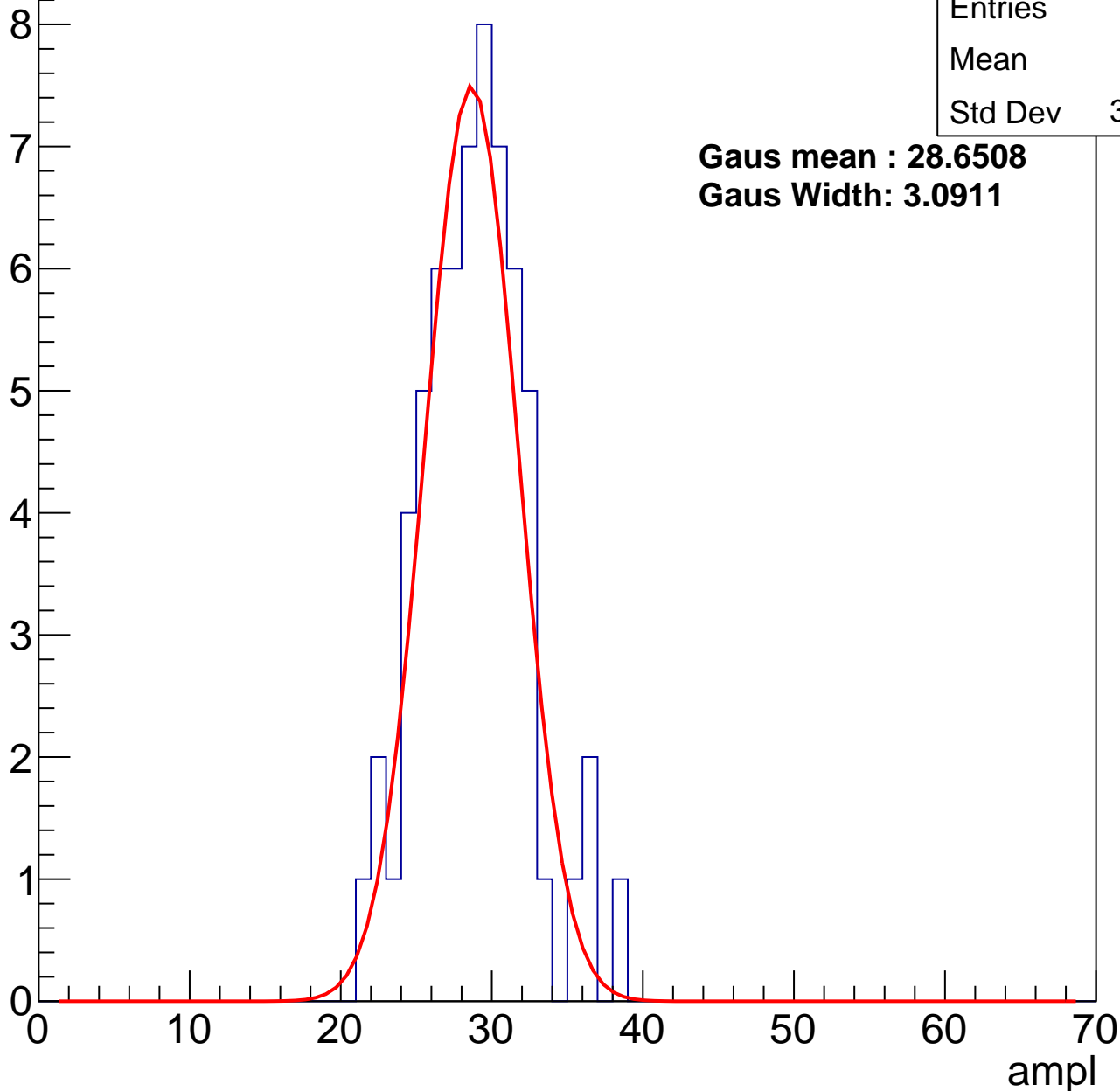
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.4
Std Dev	3.444

**Gaus mean : 28.6508**

**Gaus Width: 3.0911**



# B1L102S, U25-ch91, adc1

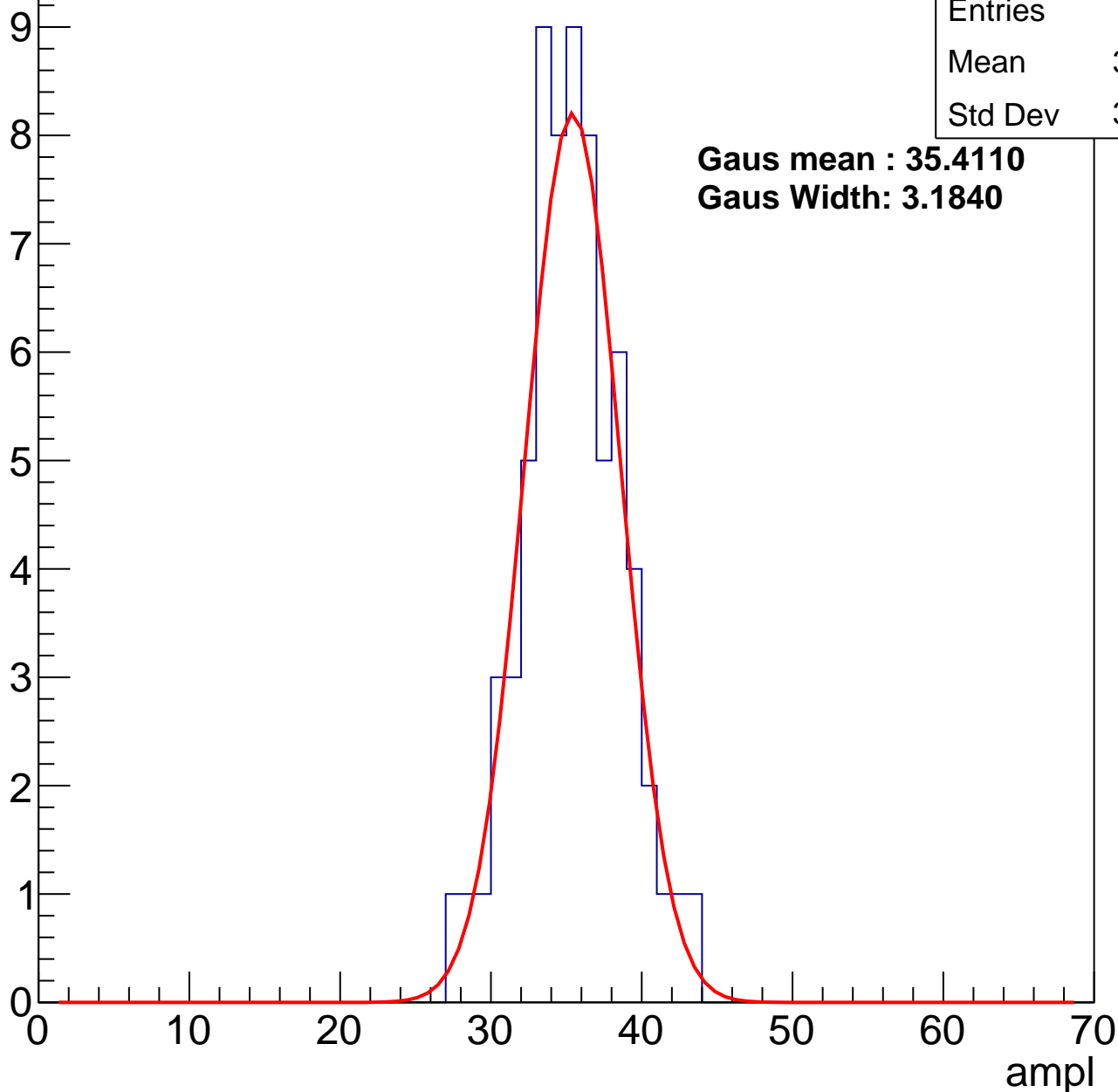
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.91
Std Dev	3.221

**Gaus mean : 35.4110**

**Gaus Width: 3.1840**



# B1L102S, U25-ch91, adc2

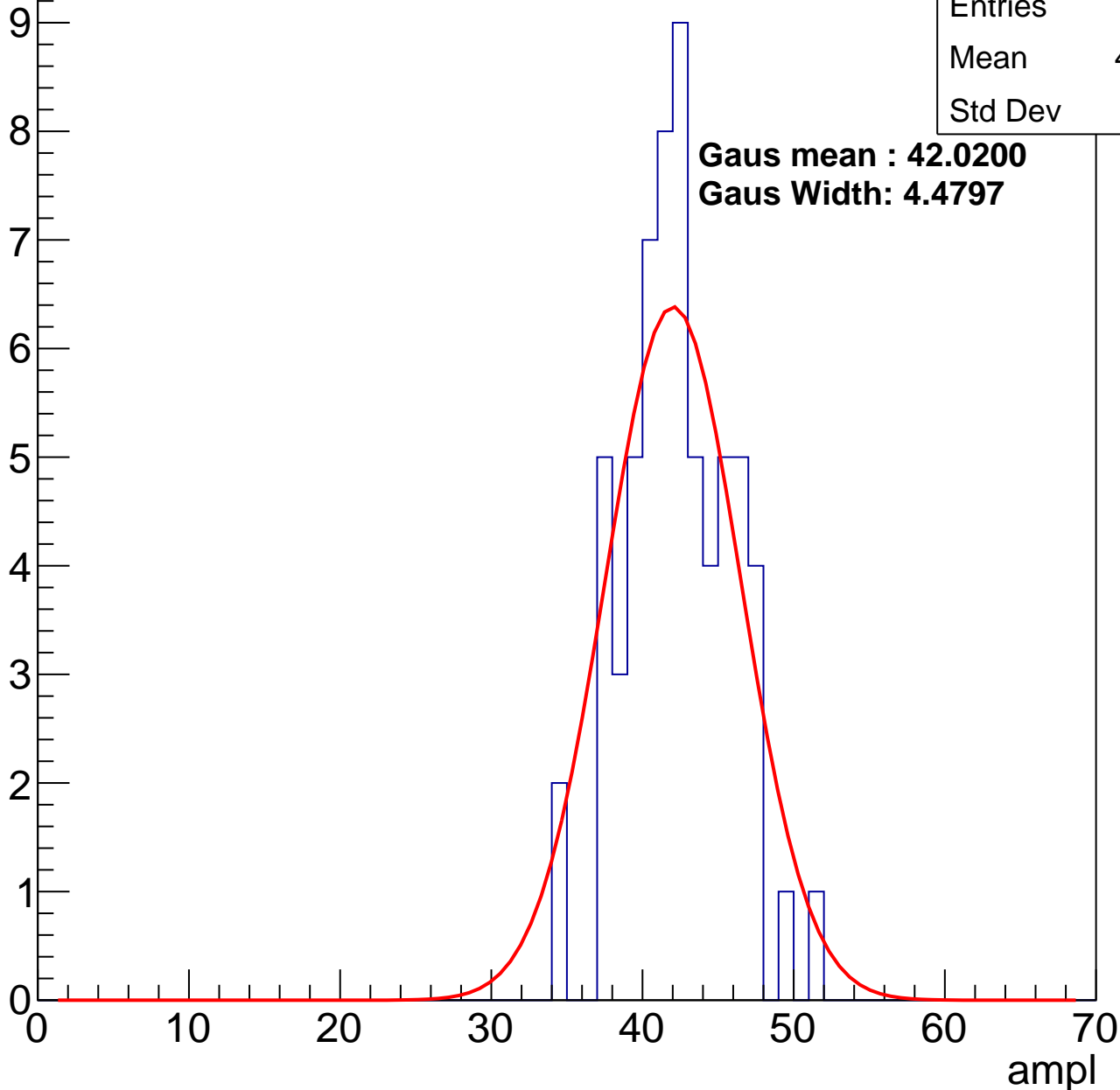
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.91
Std Dev	3.44

**Gaus mean : 42.0200**

**Gaus Width: 4.4797**

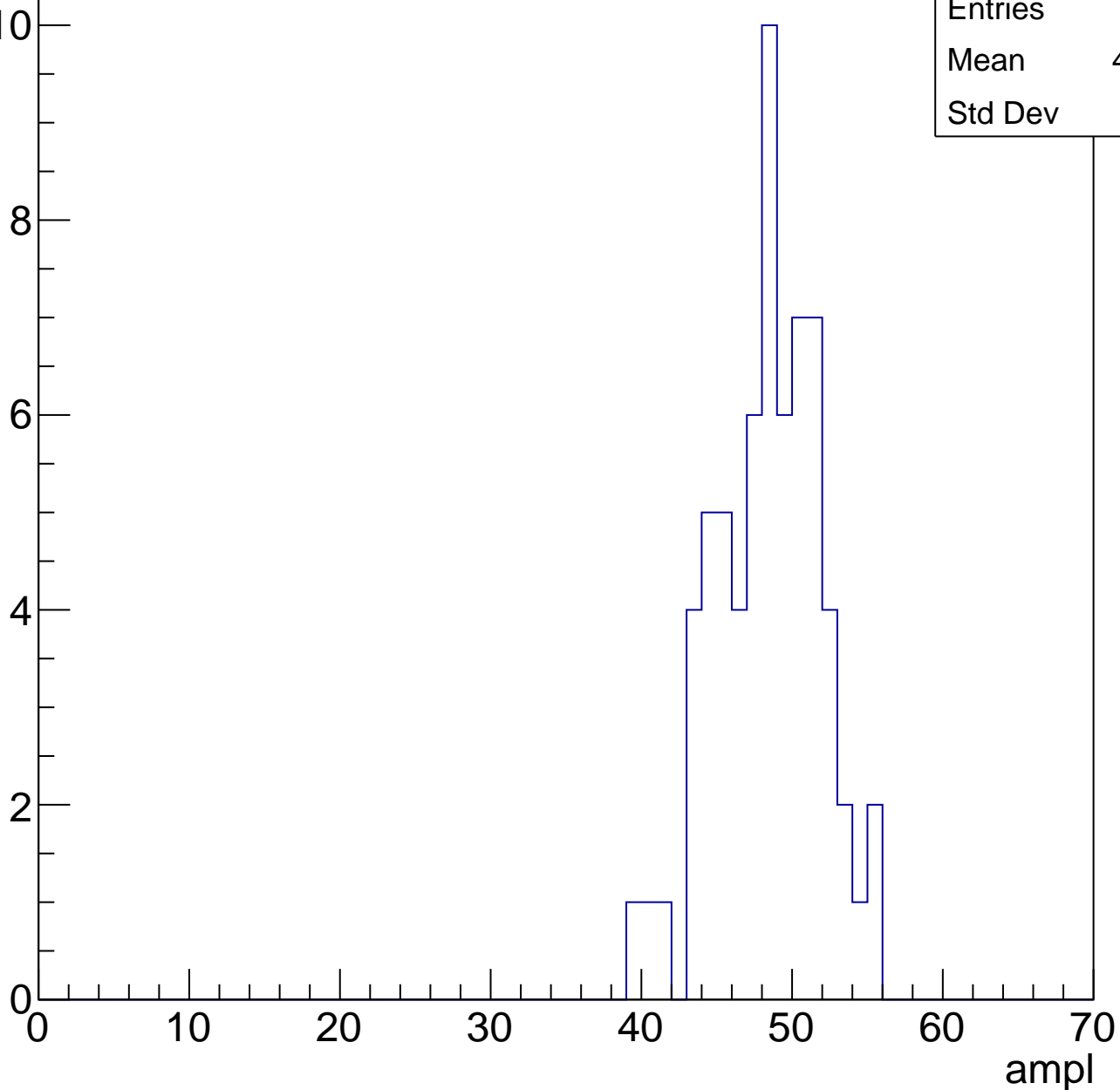


# B1L102S, U25-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.91
Std Dev	3.45

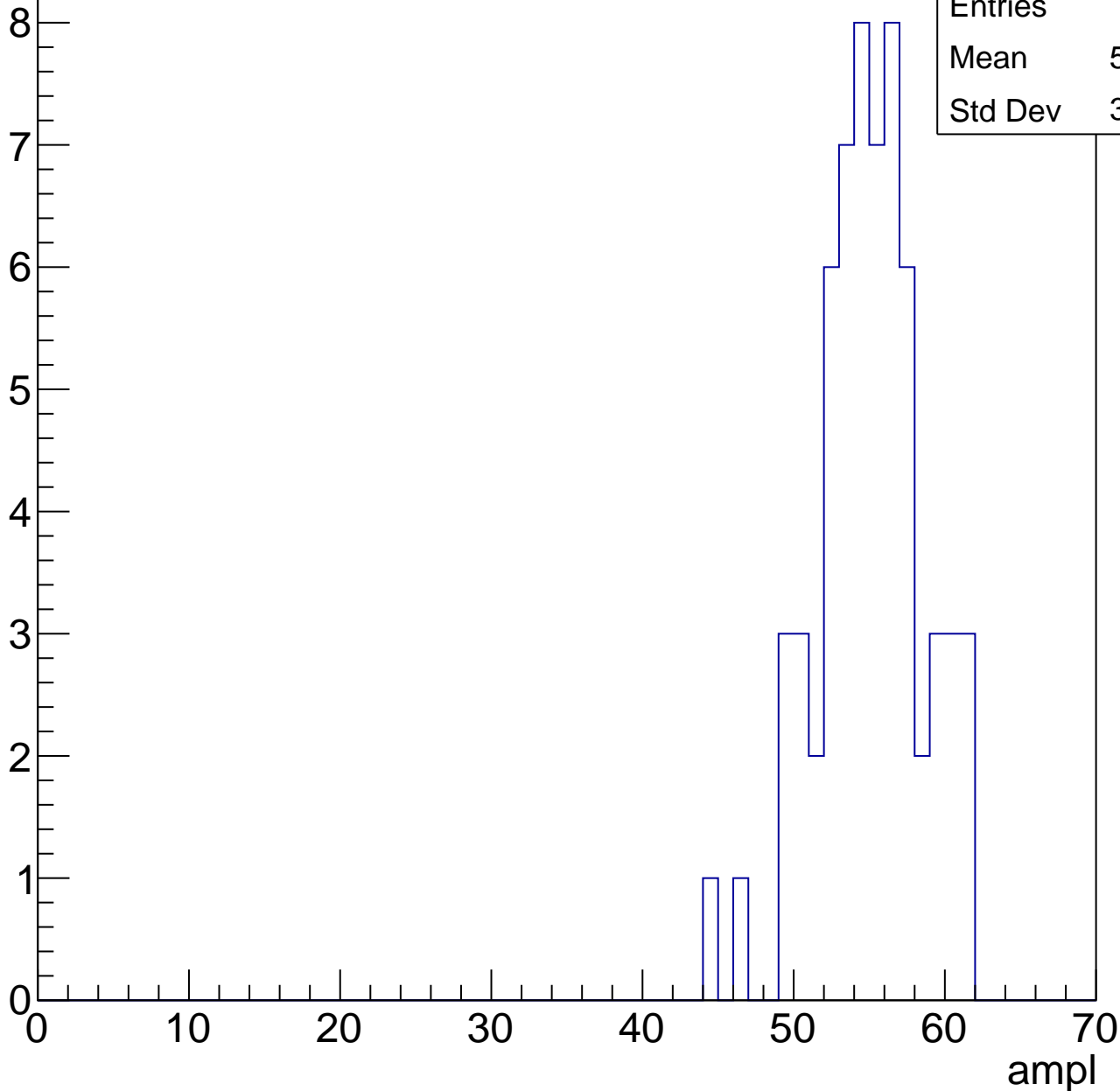


# B1L102S, U25-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.52
Std Dev	3.504

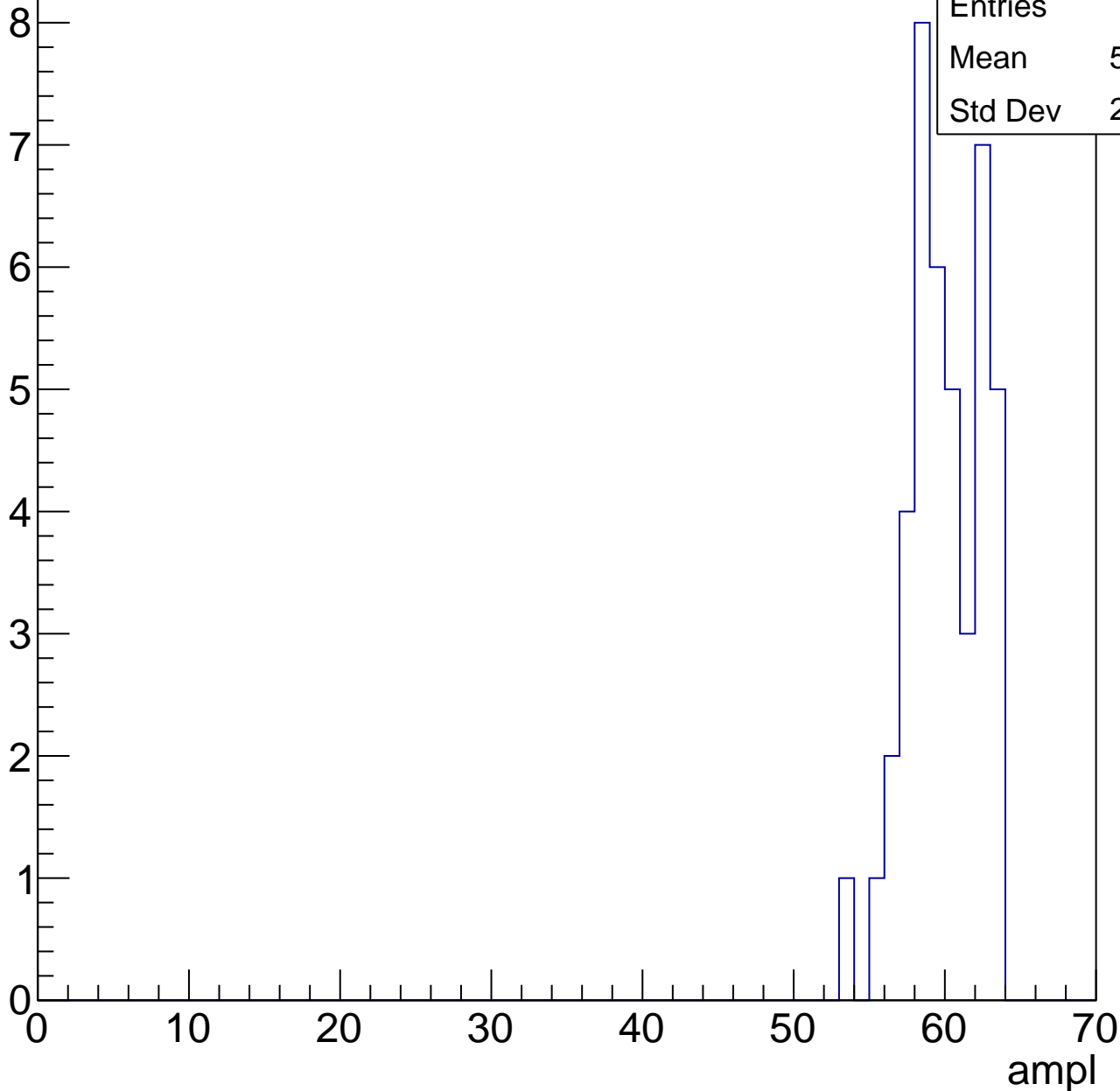


# B1L102S, U25-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	42
Mean	59.48
Std Dev	2.413

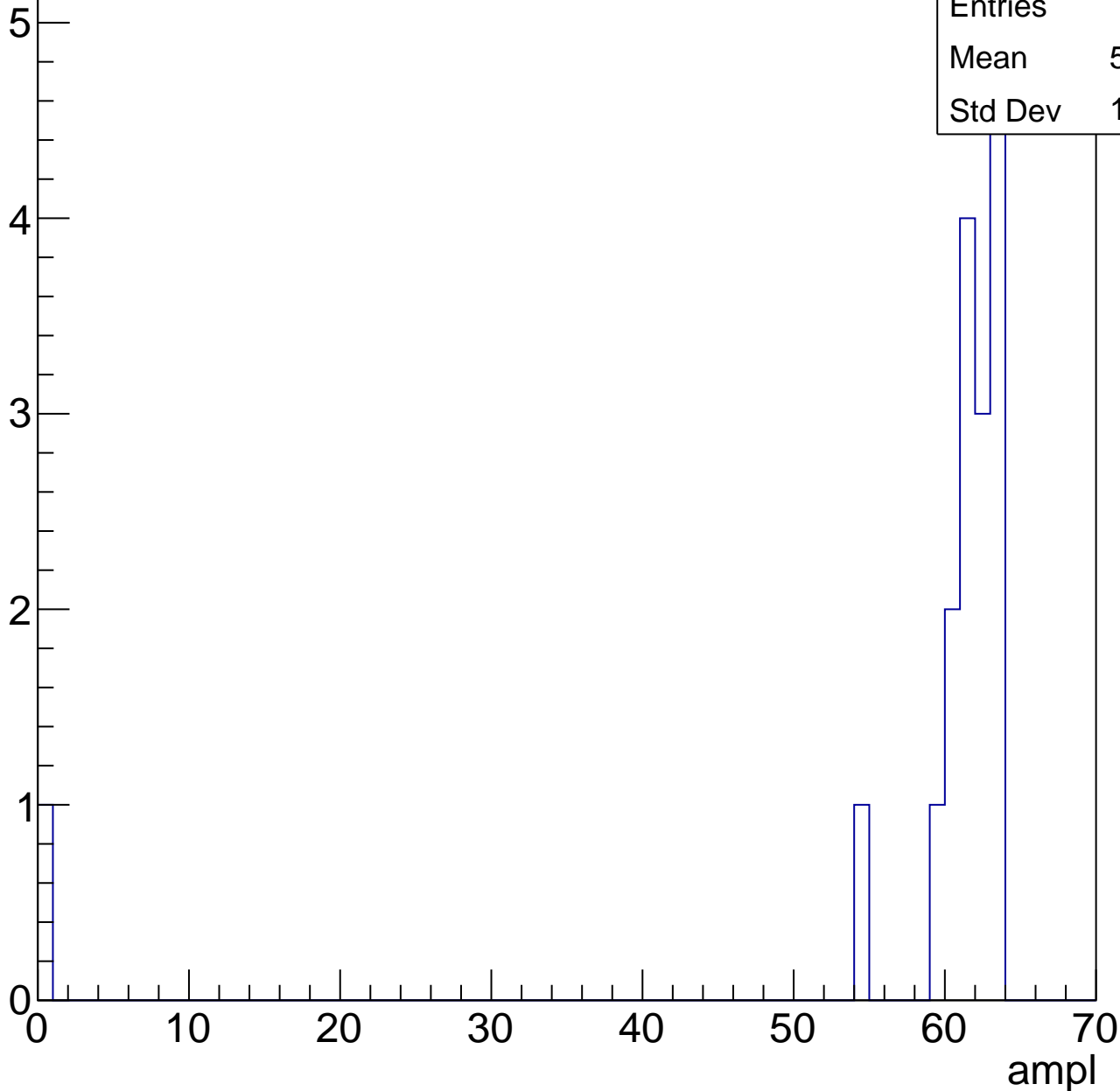


# B1L102S, U25-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.53
Std Dev	14.54





# B1L102S, U25-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch92, adc0

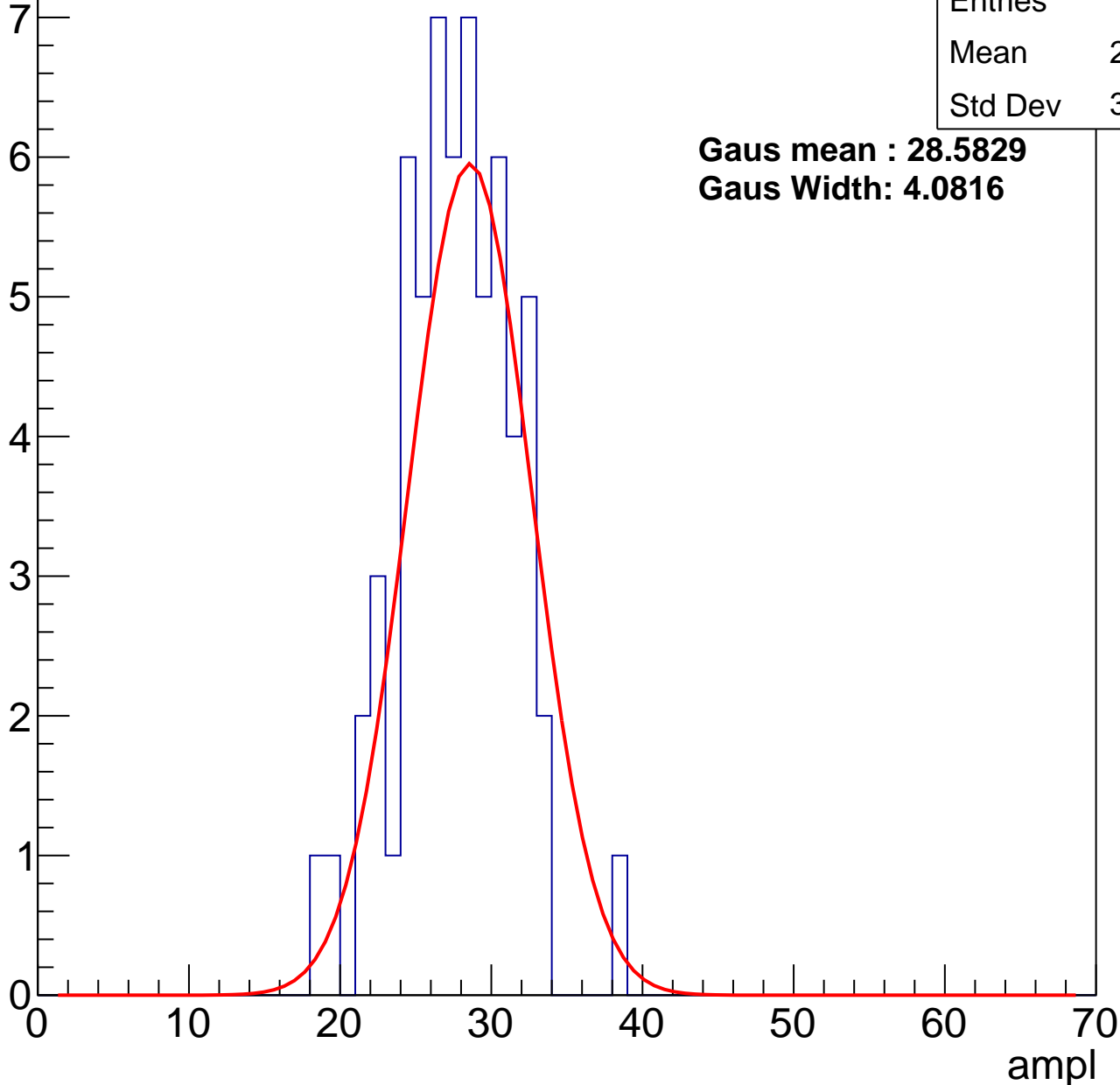
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.26
Std Dev	3.698

**Gaus mean : 28.5829**

**Gaus Width: 4.0816**



# B1L102S, U25-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	87
Mean	34.48
Std Dev	4.085

**Gaus mean : 34.9103**

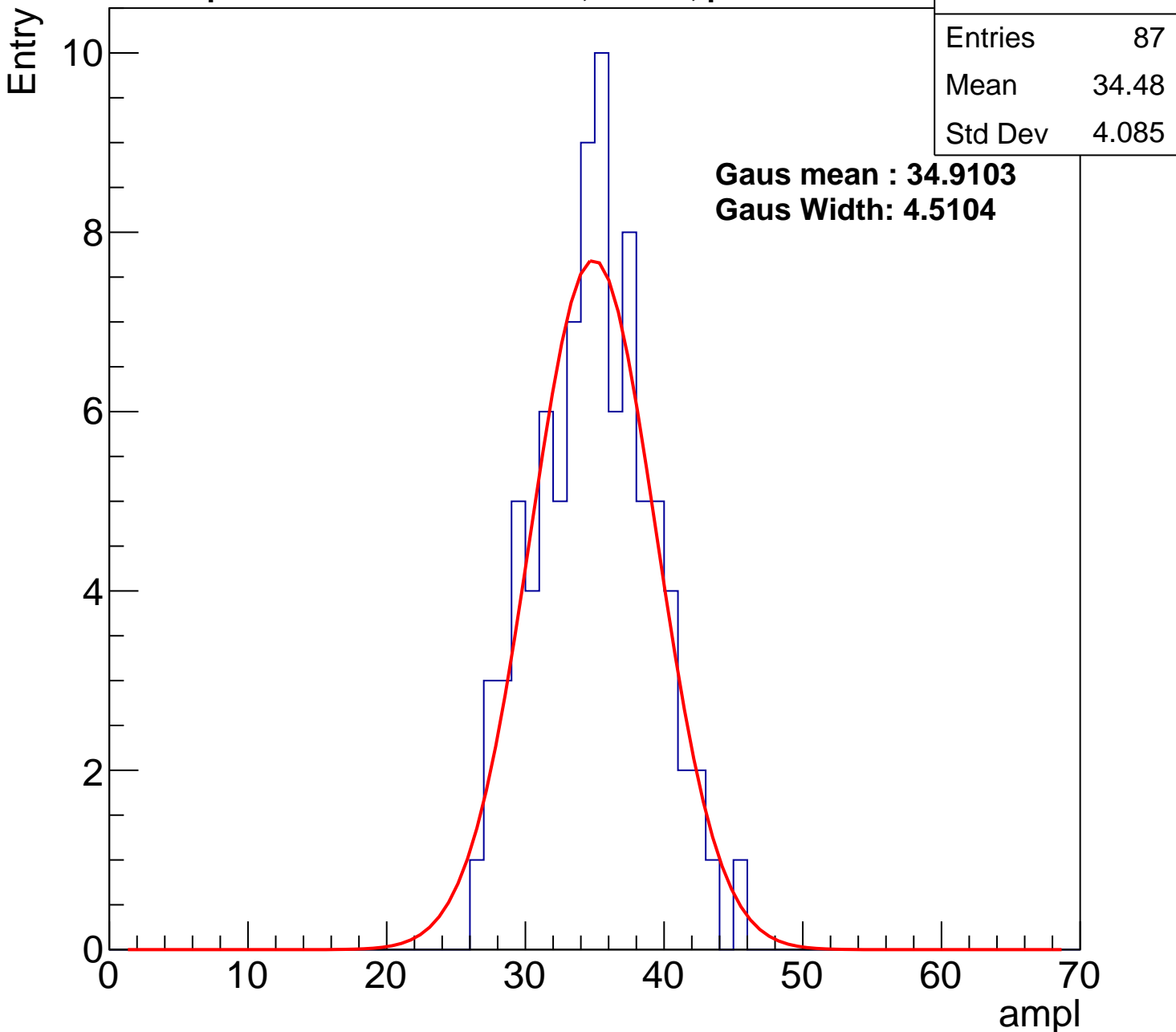
**Gaus Width: 4.5104**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch92, adc2

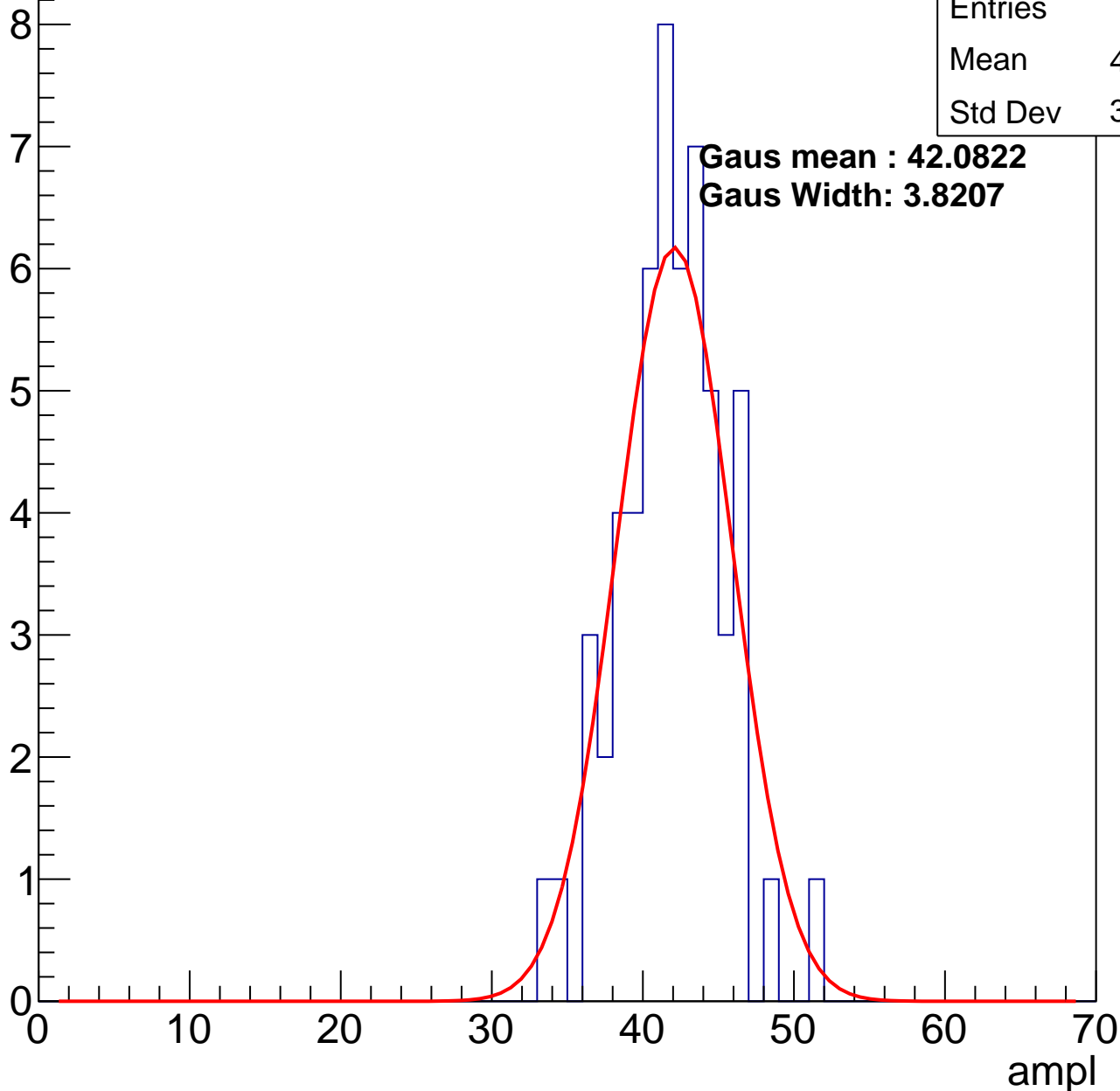
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.44
Std Dev	3.423

**Gaus mean : 42.0822**

**Gaus Width: 3.8207**

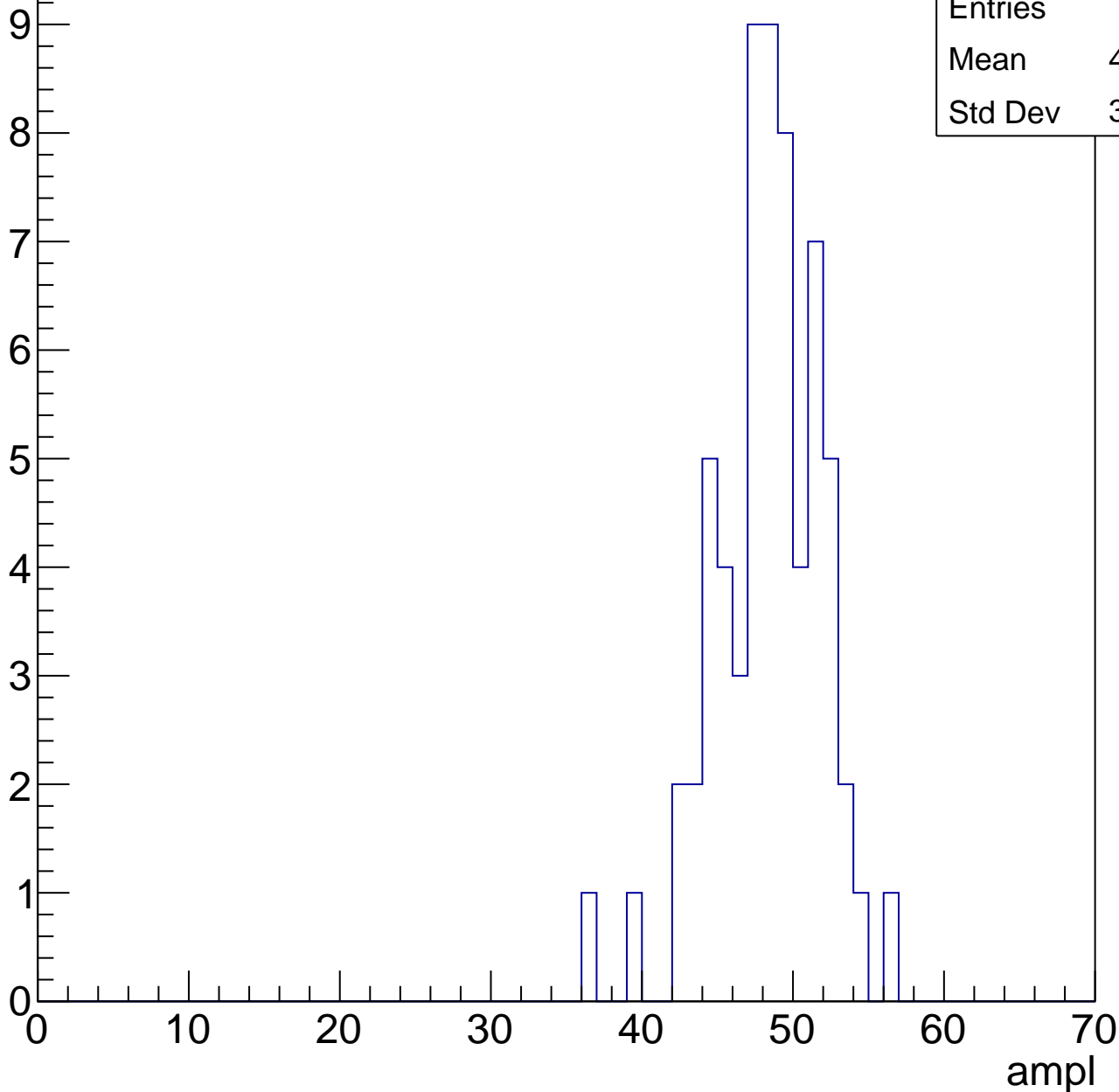


# B1L102S, U25-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.86
Std Dev	3.535

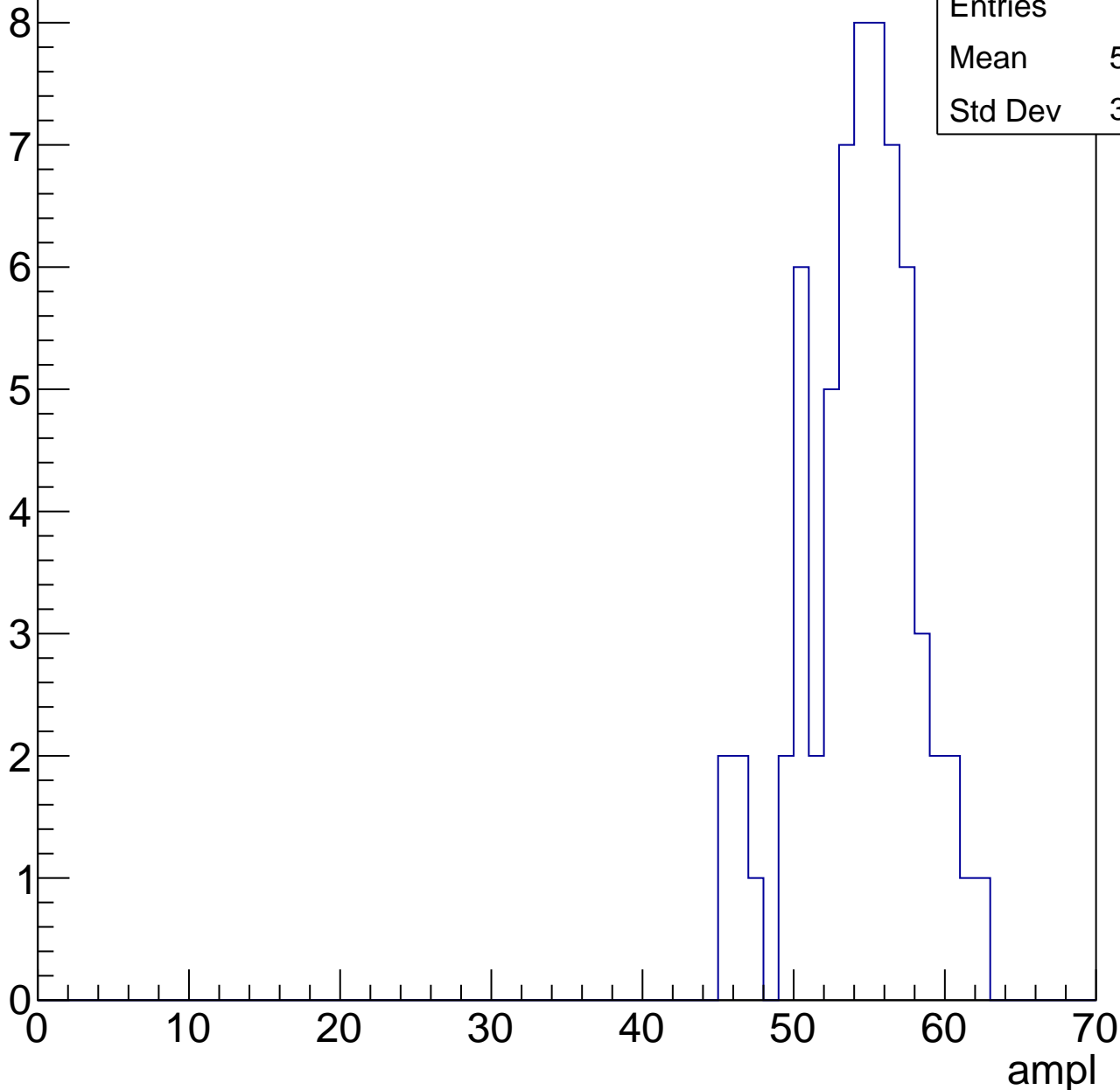


# B1L102S, U25-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

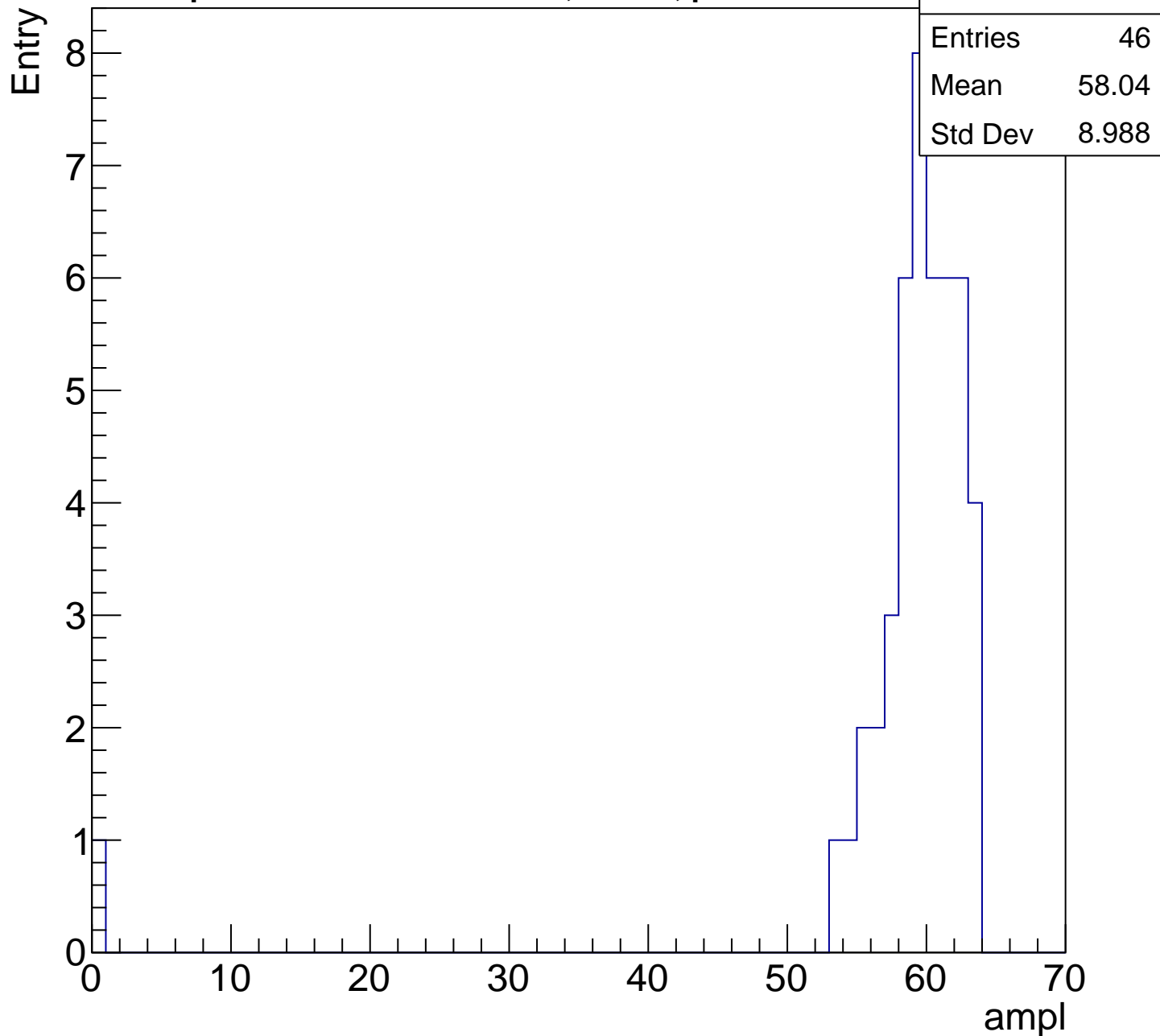
Entry

Entries	65
Mean	53.86
Std Dev	3.725



# B1L102S, U25-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

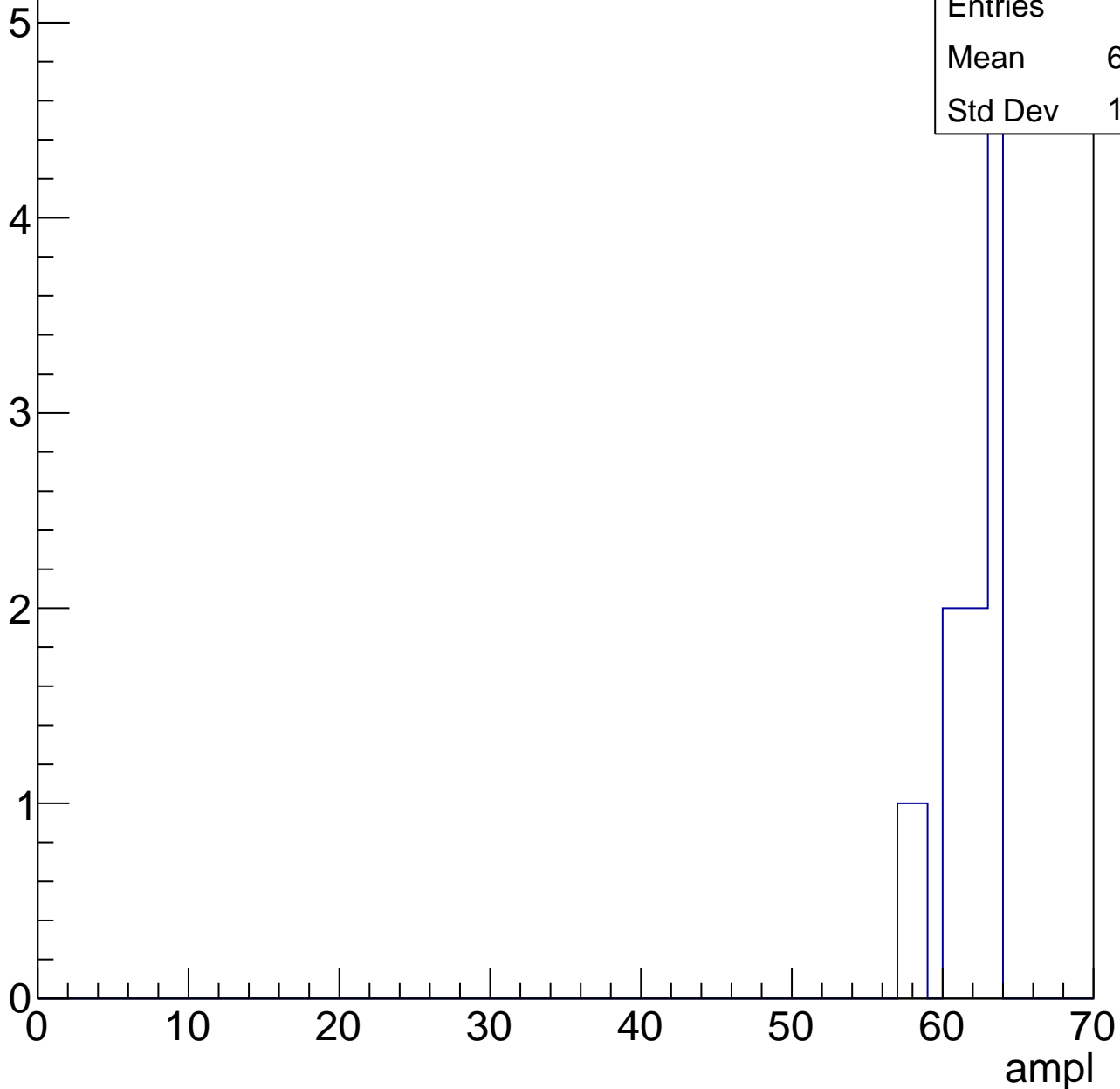


# B1L102S, U25-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.23
Std Dev	1.928

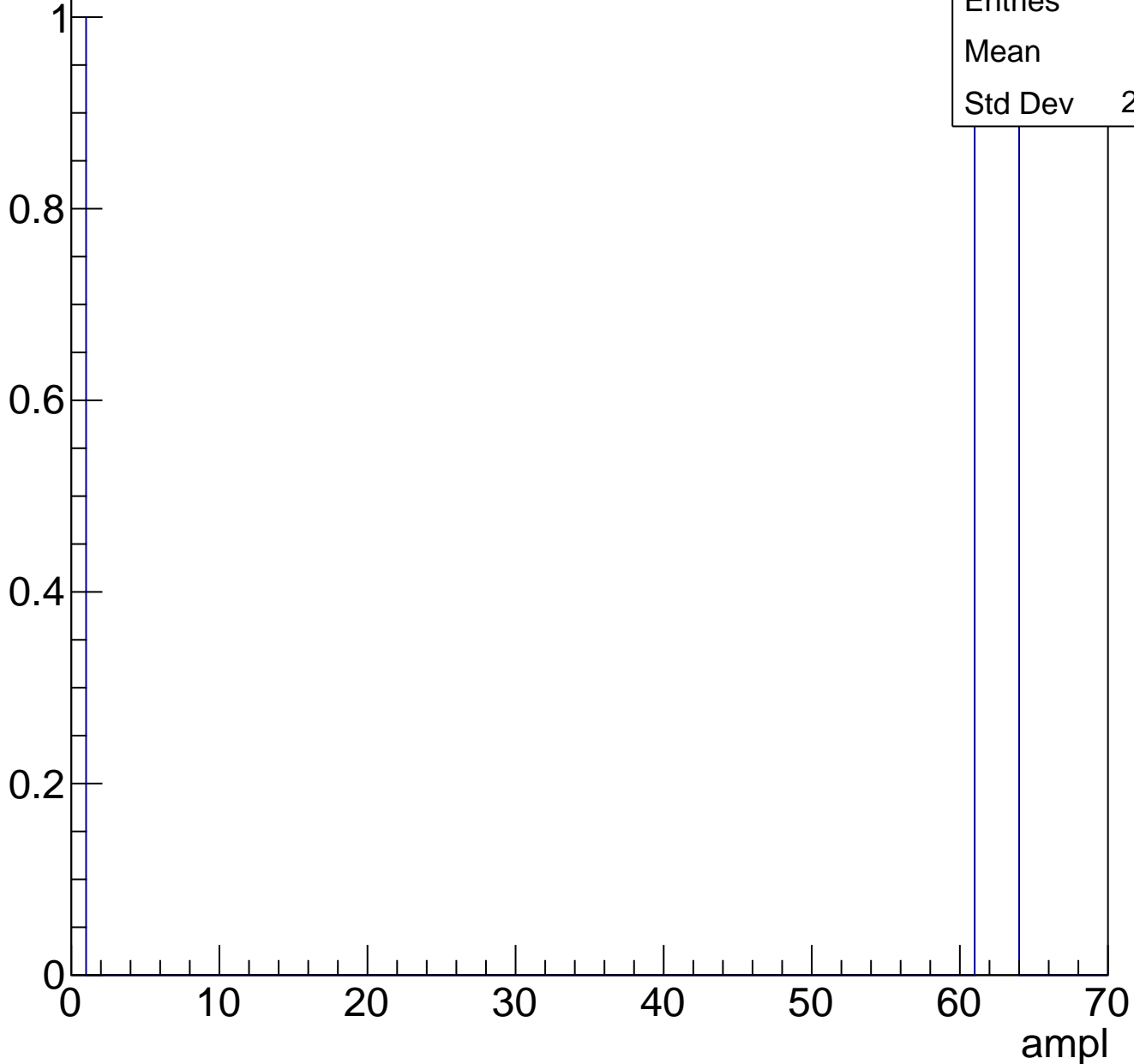




# B1L102S, U25-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch93, adc0

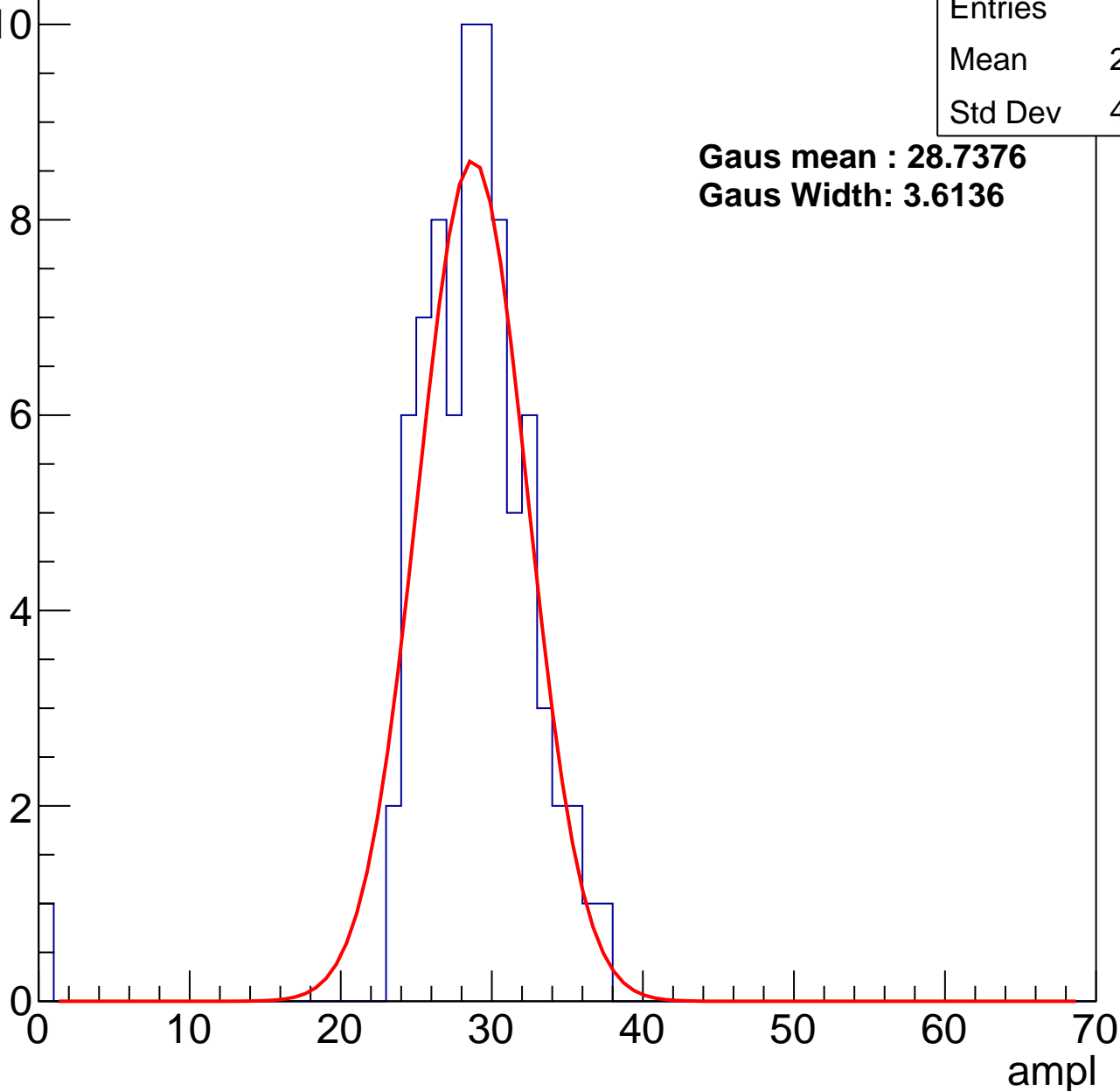
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.23
Std Dev	4.529

**Gaus mean : 28.7376**

**Gaus Width: 3.6136**



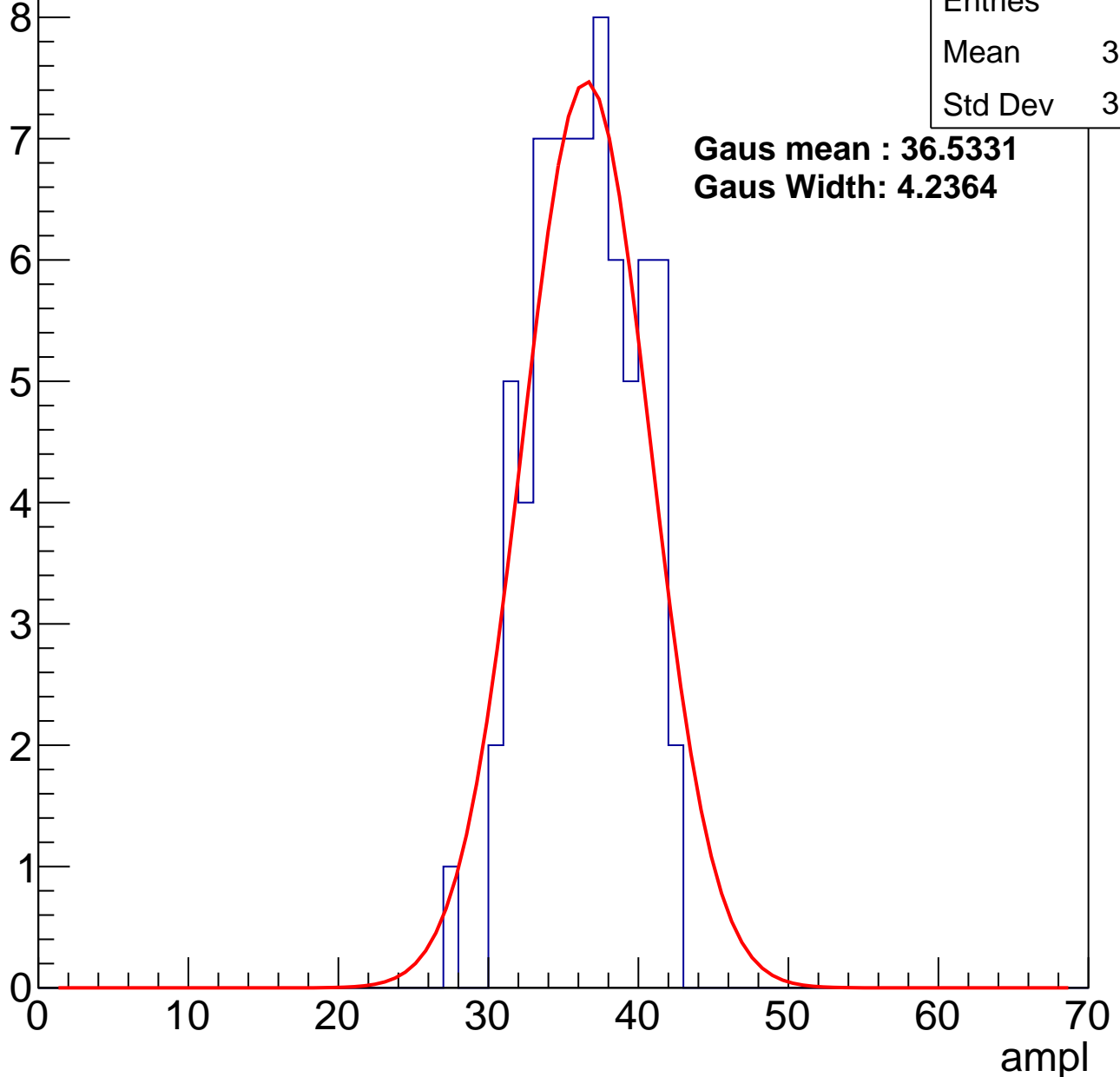
# B1L102S, U25-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.96
Std Dev	3.382

**Gaus mean : 36.5331**  
**Gaus Width: 4.2364**



# B1L102S, U25-ch93, adc2

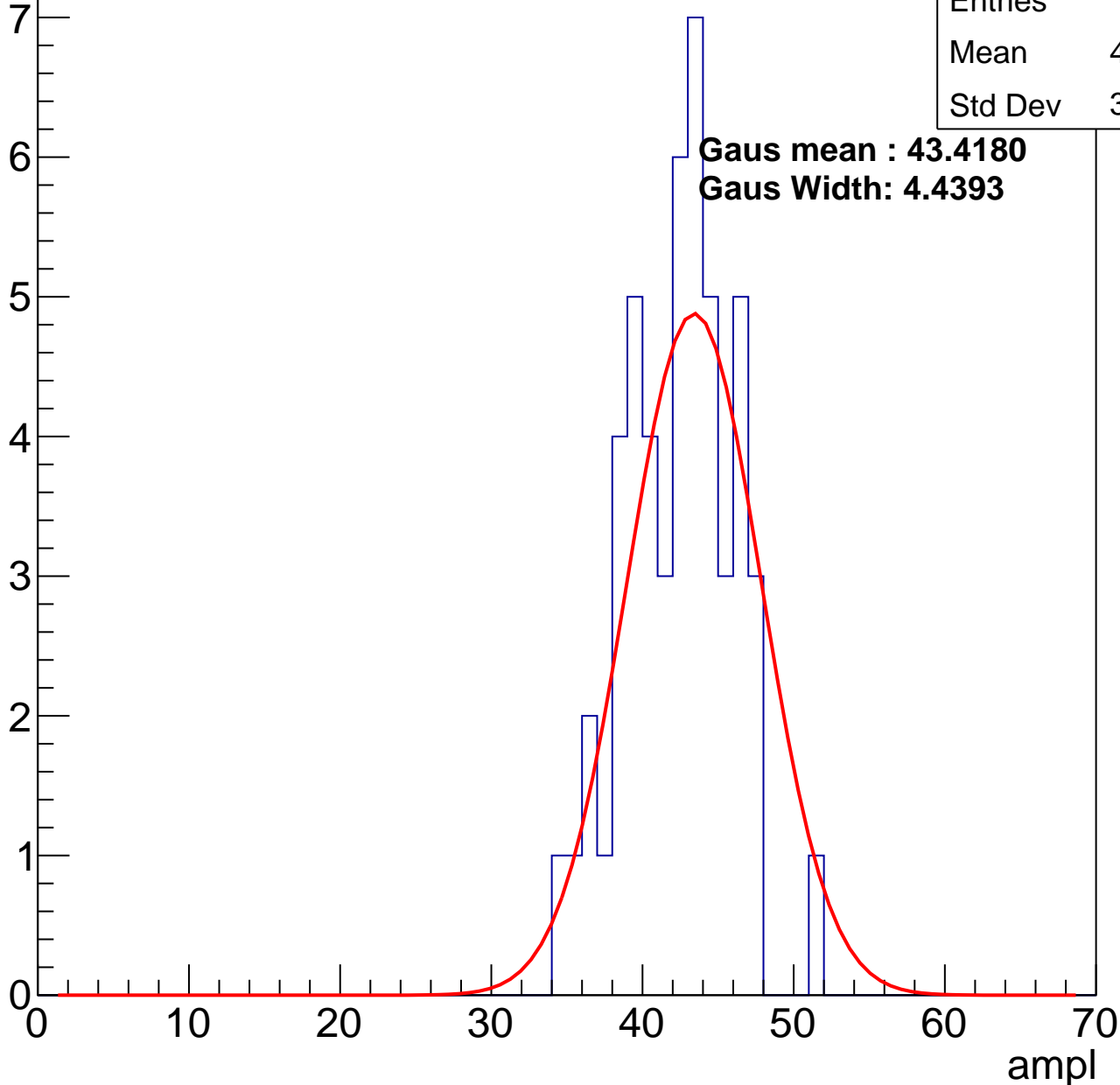
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.92
Std Dev	3.508

**Gaus mean : 43.4180**

**Gaus Width: 4.4393**

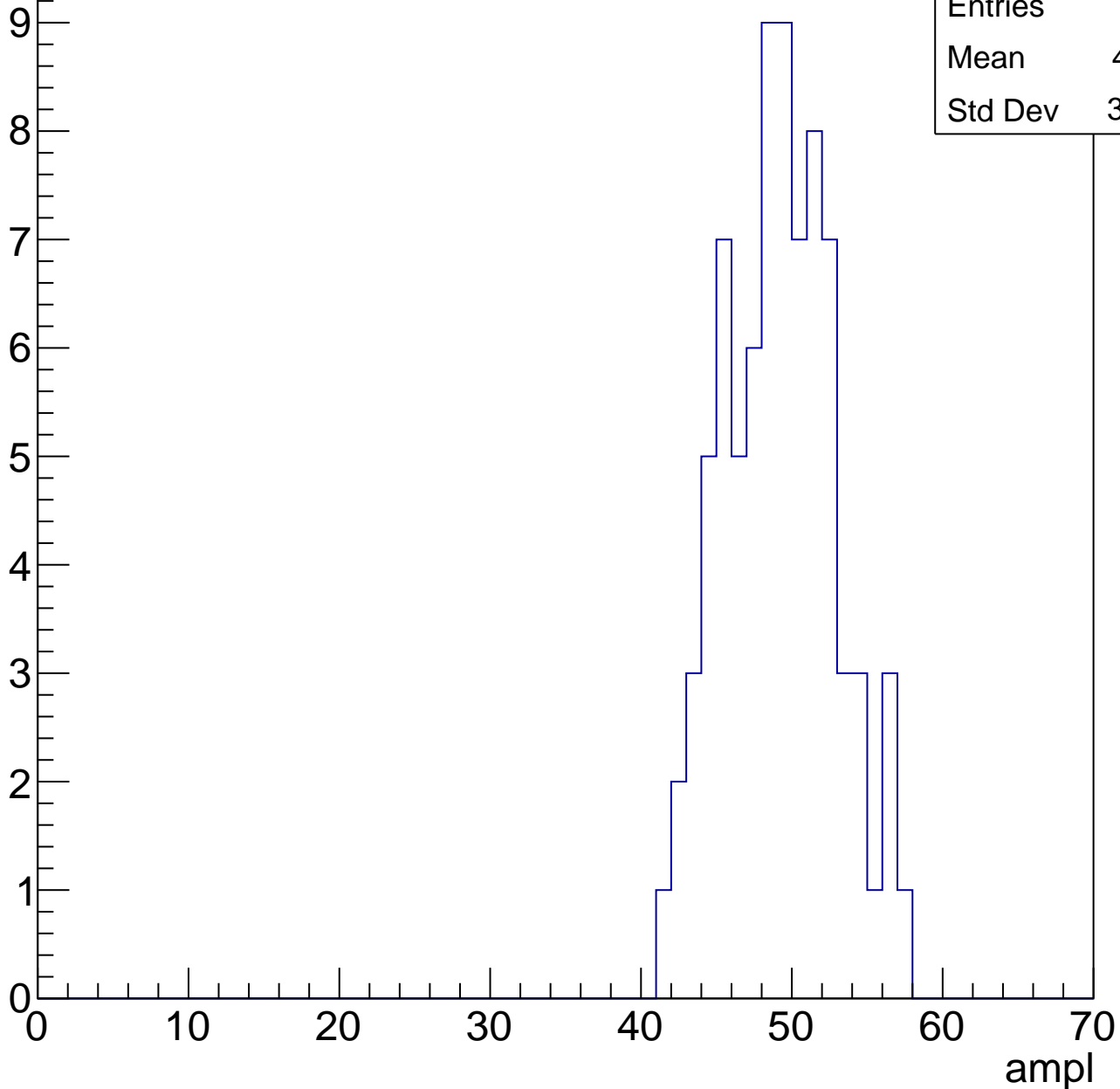


# B1L102S, U25-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	48.71
Std Dev	3.624

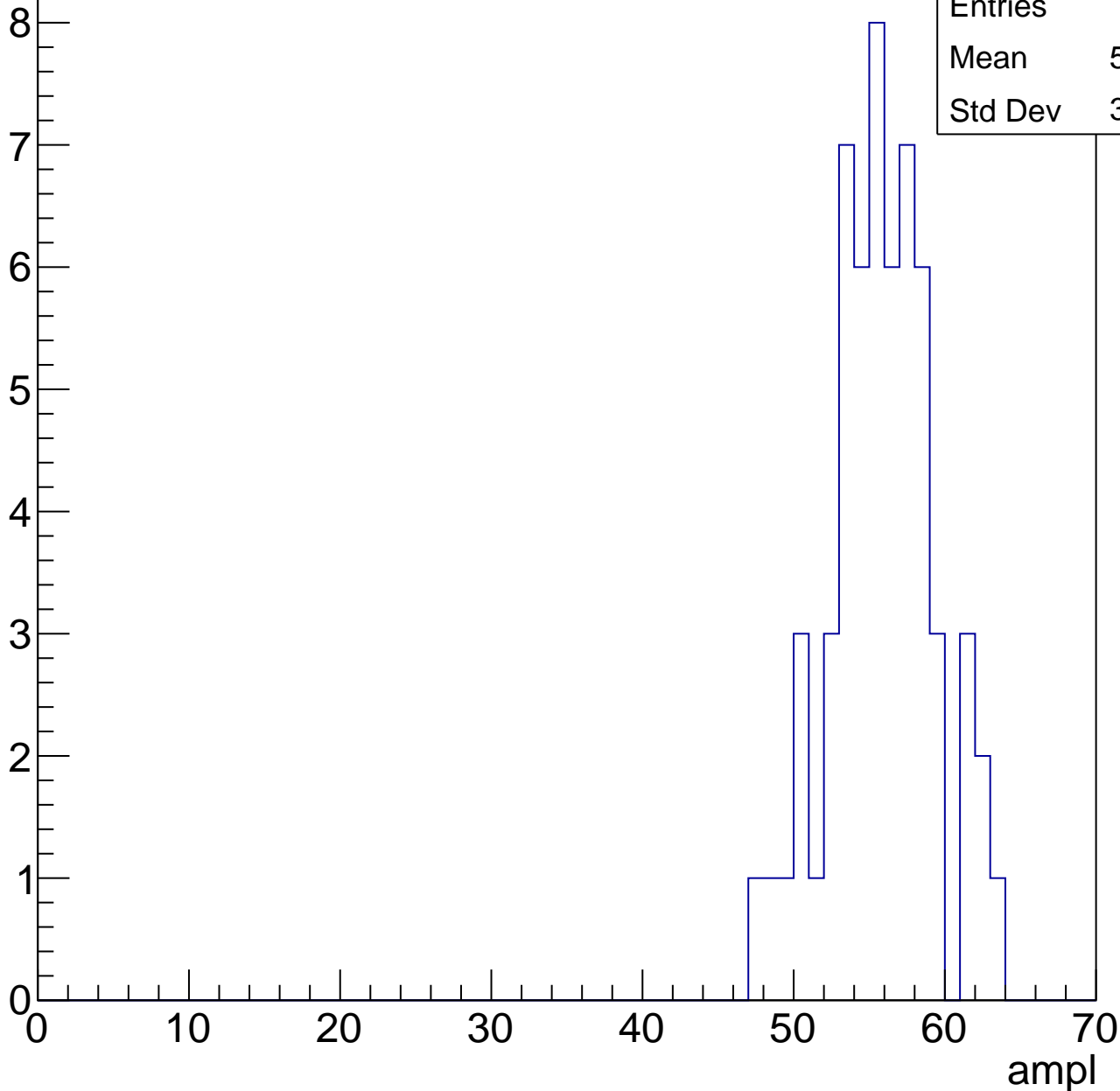


# B1L102S, U25-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.36
Std Dev	3.443

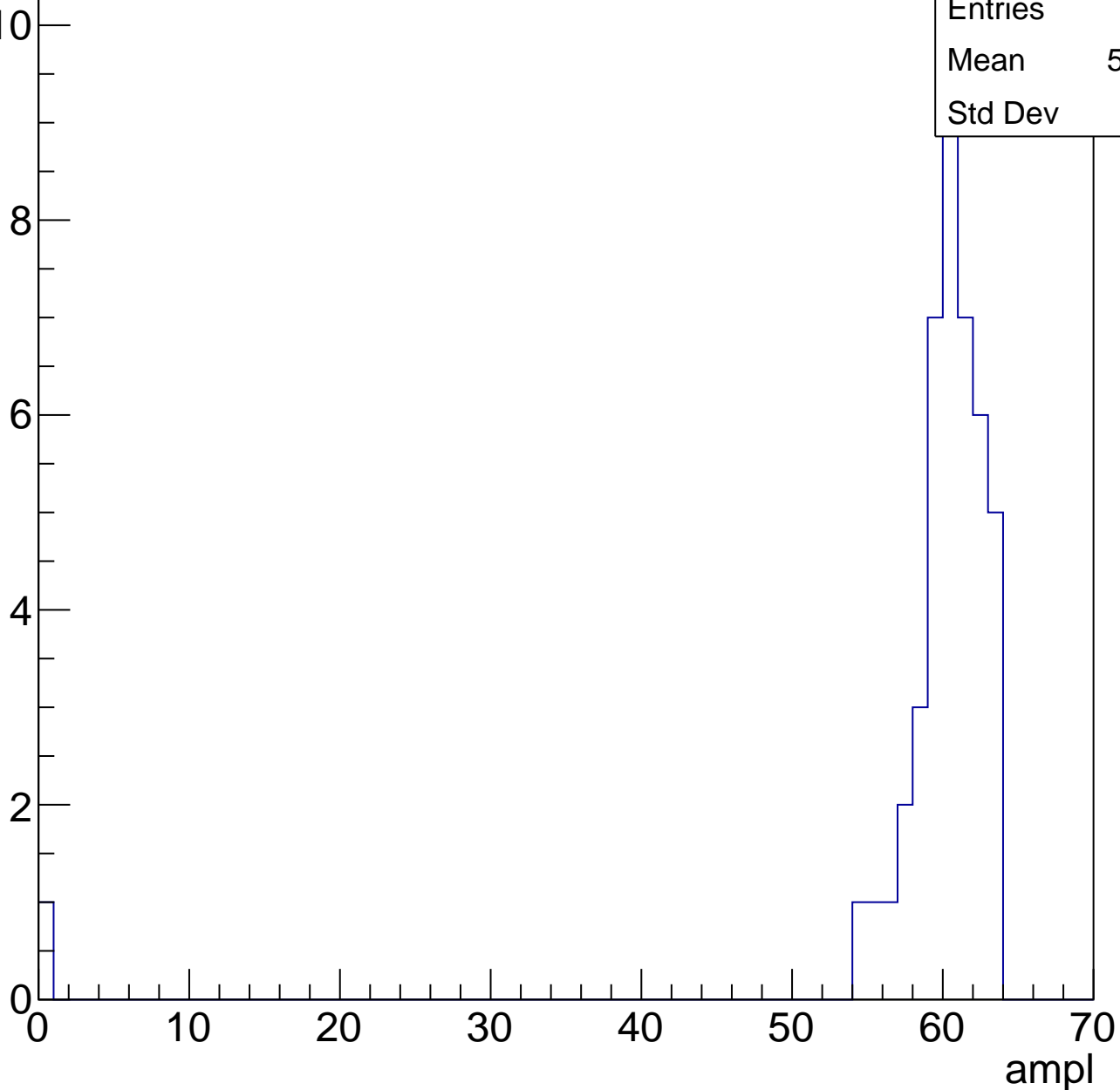


# B1L102S, U25-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

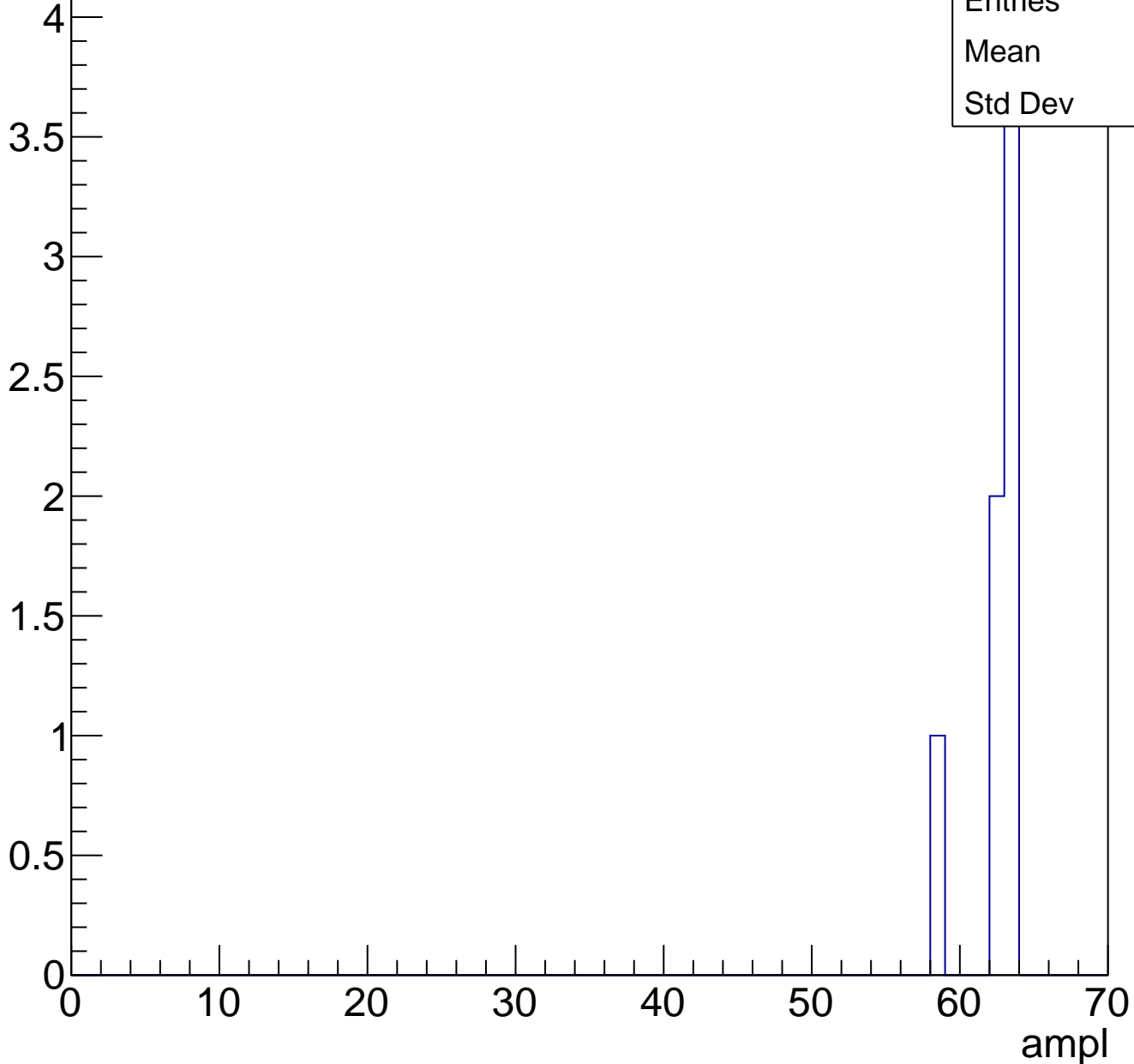
Entries	44
Mean	58.64
Std Dev	9.18



# B1L102S, U25-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

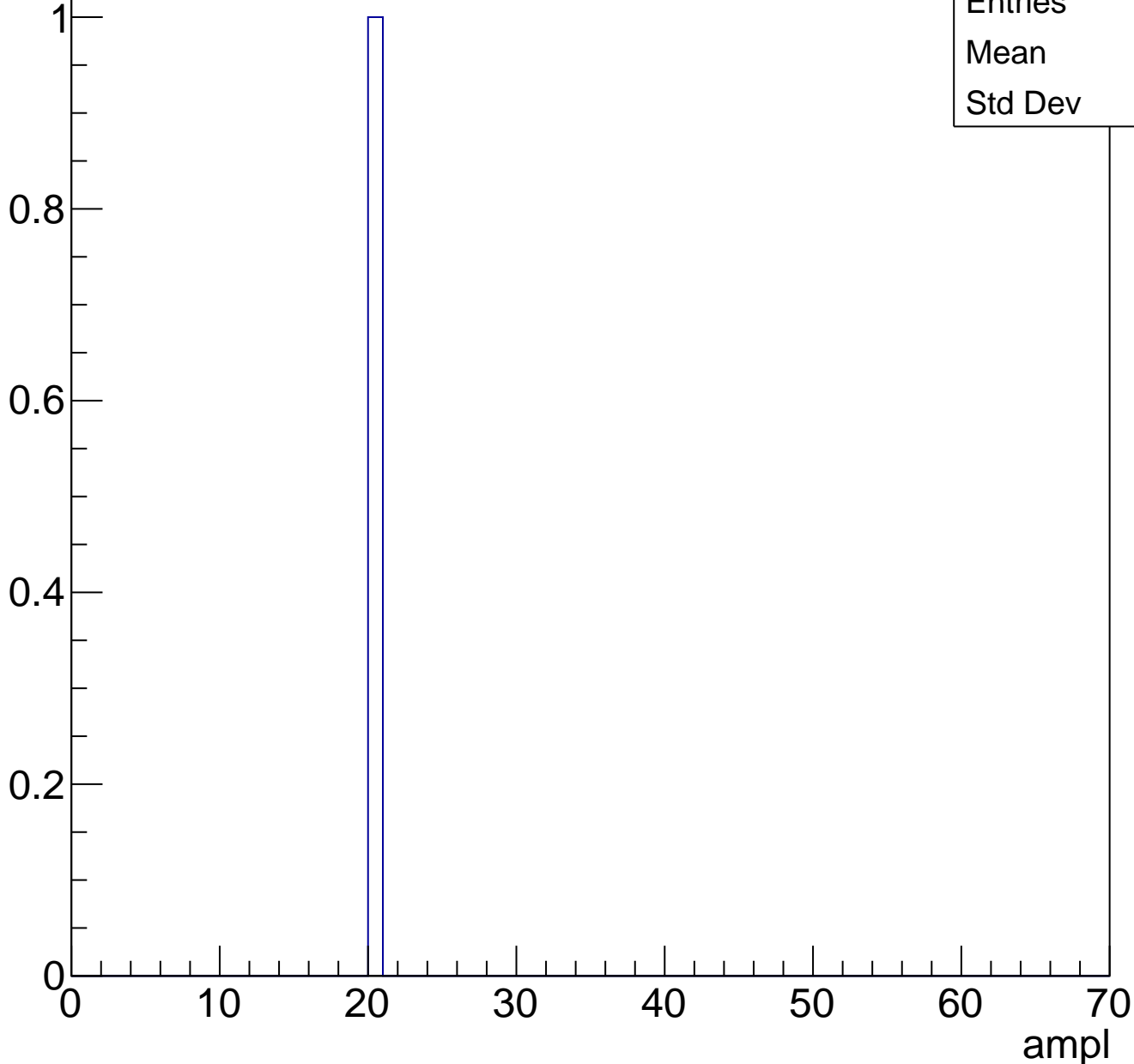




# B1L102S, U25-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	20
Std Dev	0

# B1L102S, U25-ch94, adc0

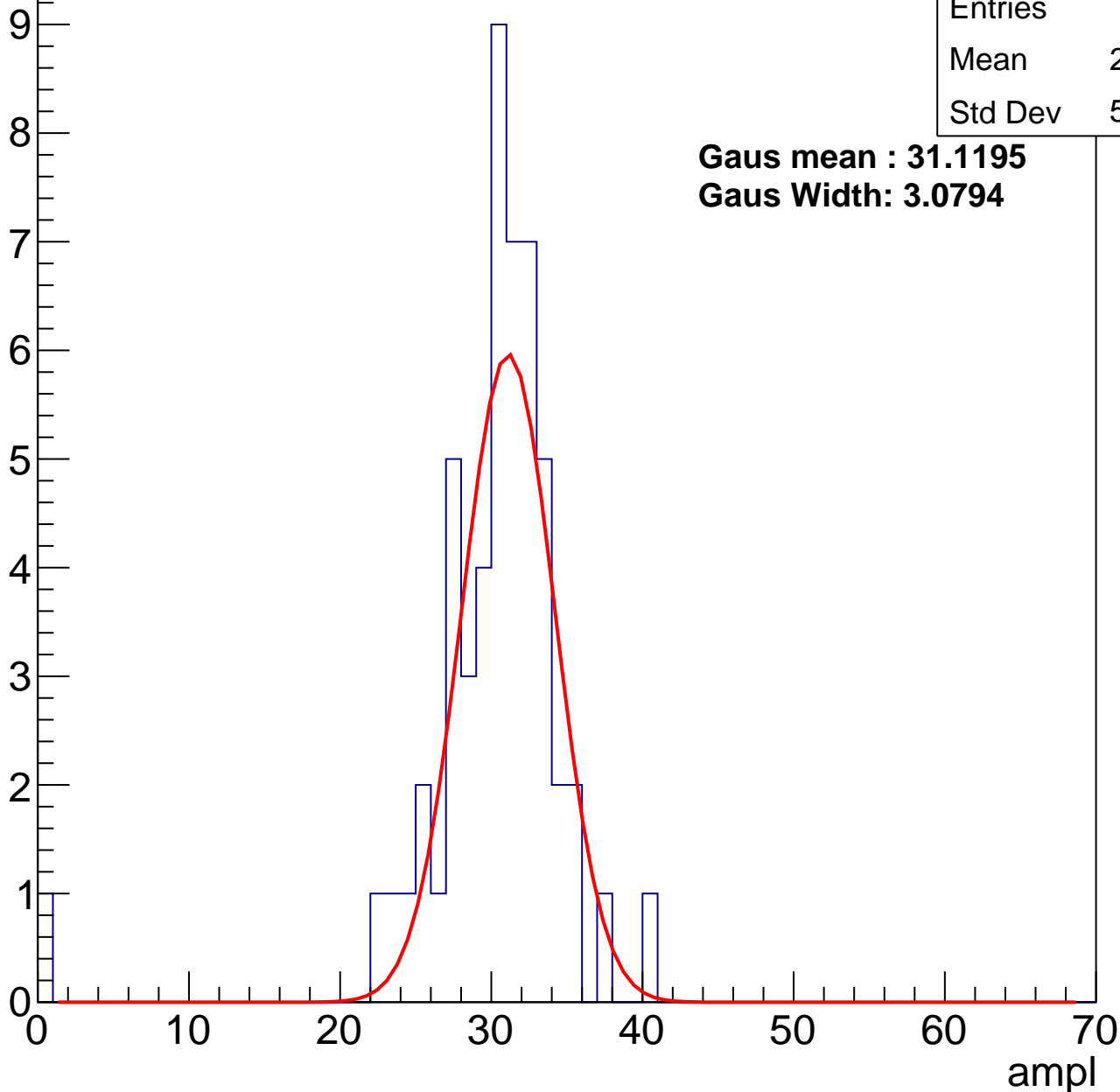
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	29.64
Std Dev	5.285

**Gaus mean : 31.1195**

**Gaus Width: 3.0794**



# B1L102S, U25-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	77
Mean	36.55
Std Dev	3.754

**Gaus mean : 37.1144**

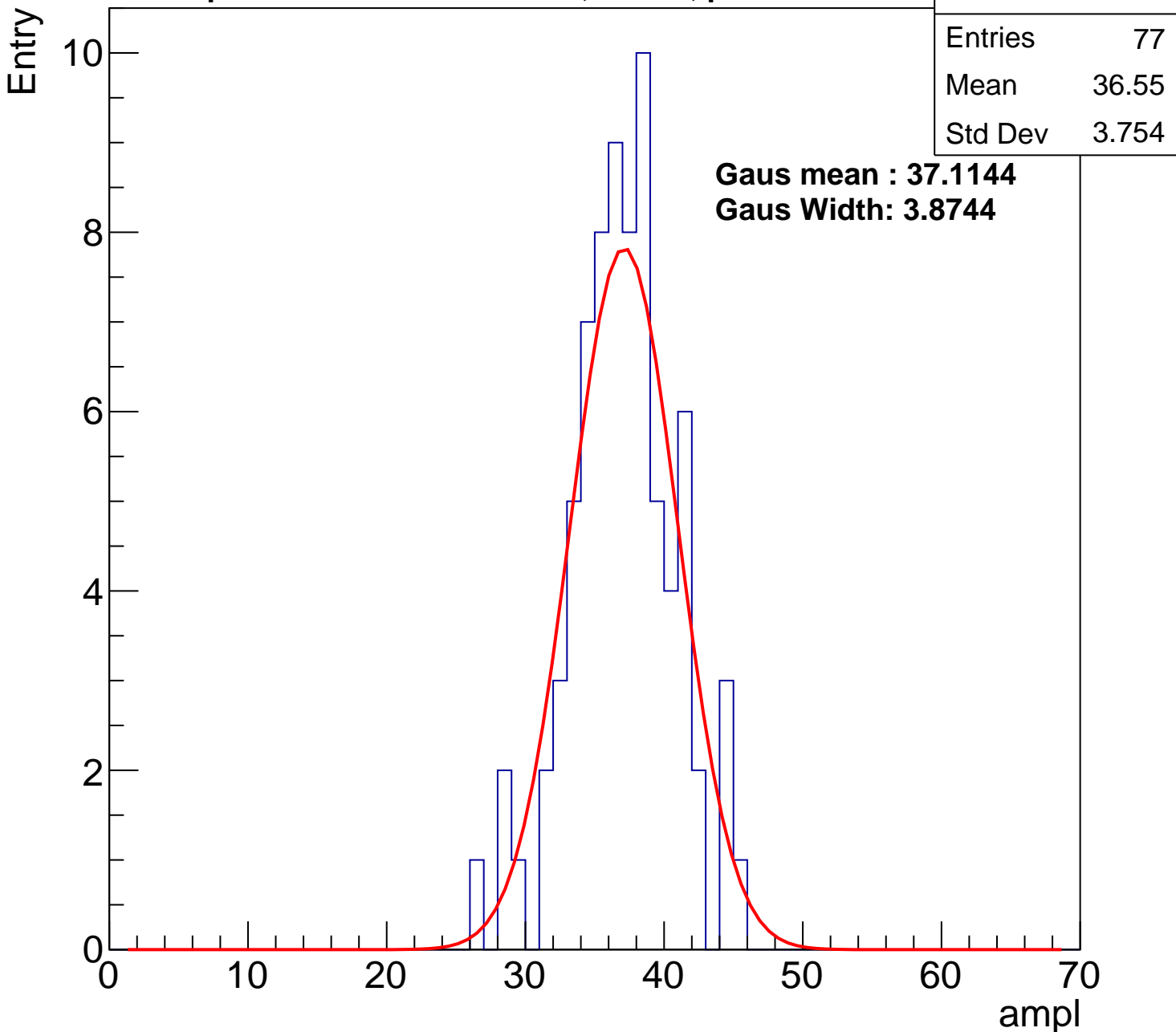
**Gaus Width: 3.8744**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch94, adc2

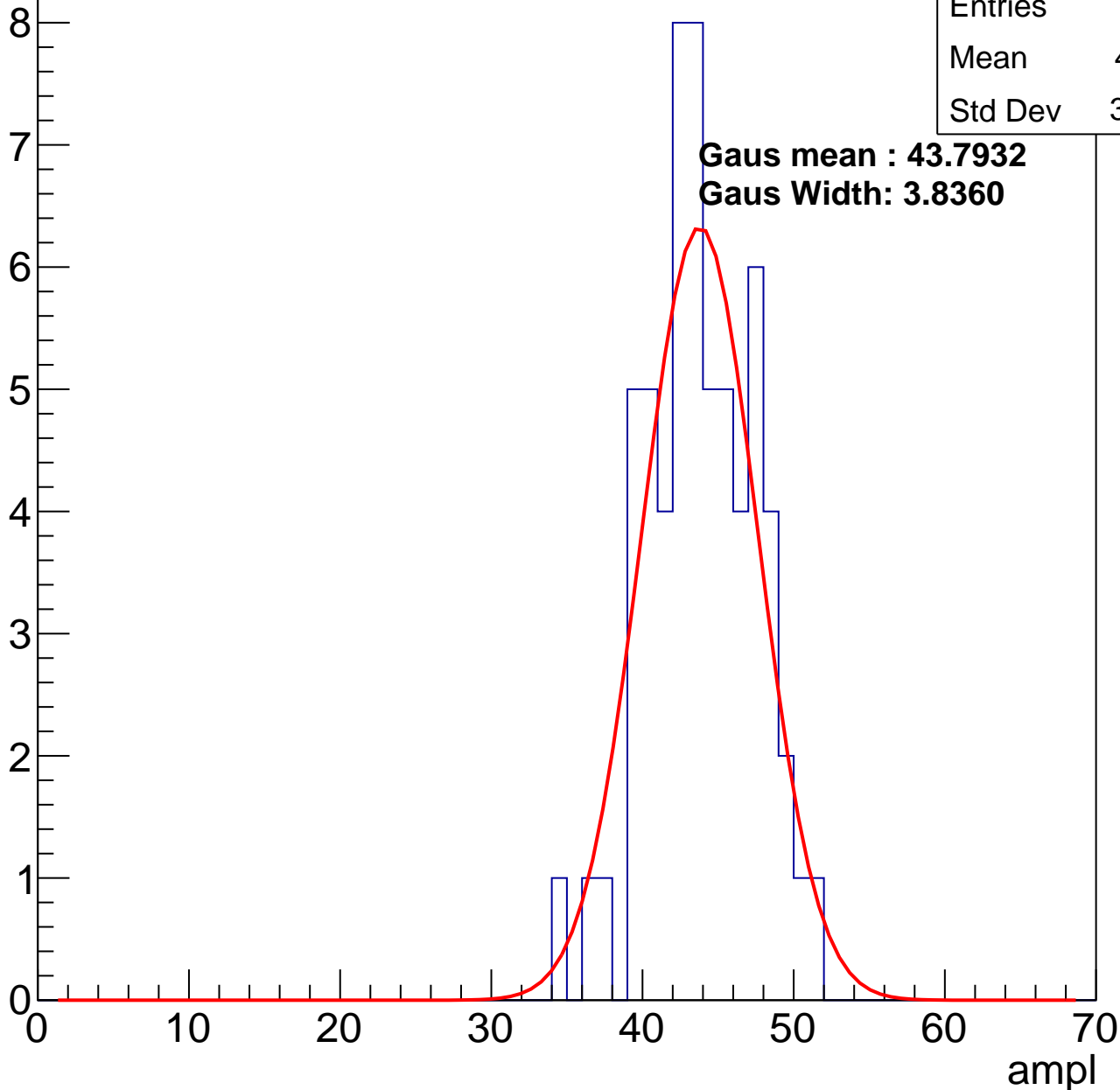
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.41
Std Dev	3.499

**Gaus mean : 43.7932**

**Gaus Width: 3.8360**

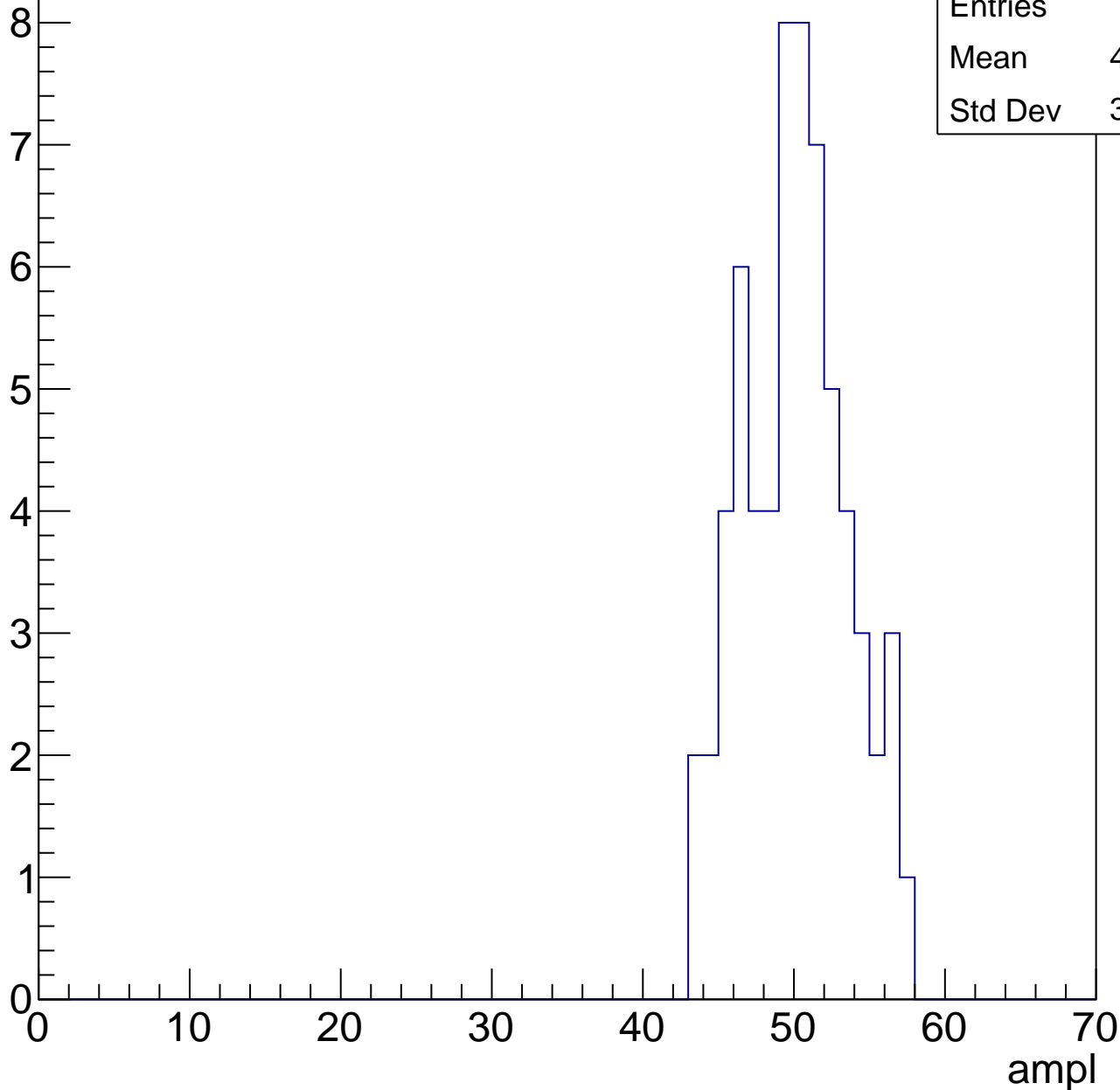


# B1L102S, U25-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.65
Std Dev	3.419

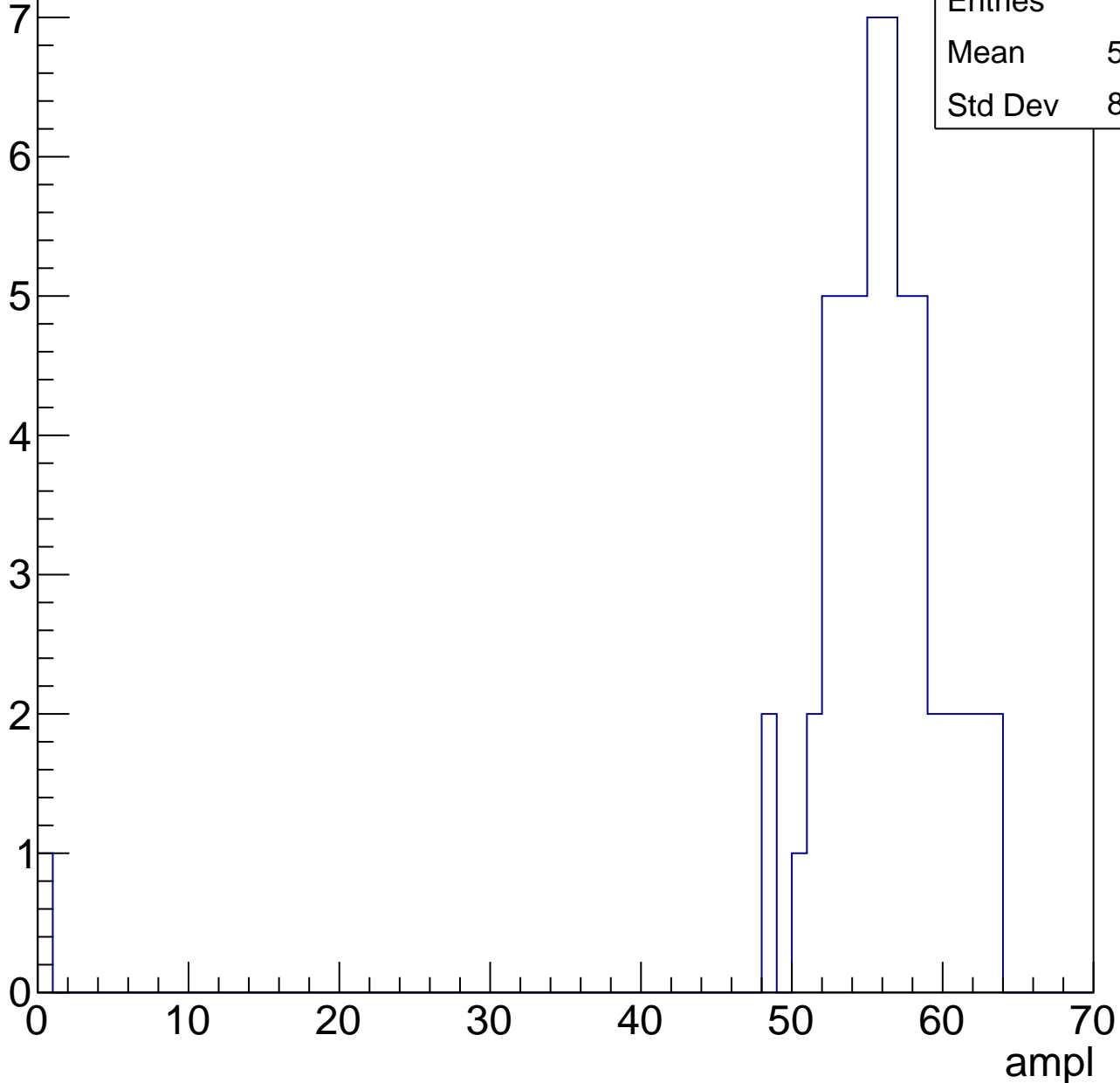


# B1L102S, U25-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.64
Std Dev	8.196

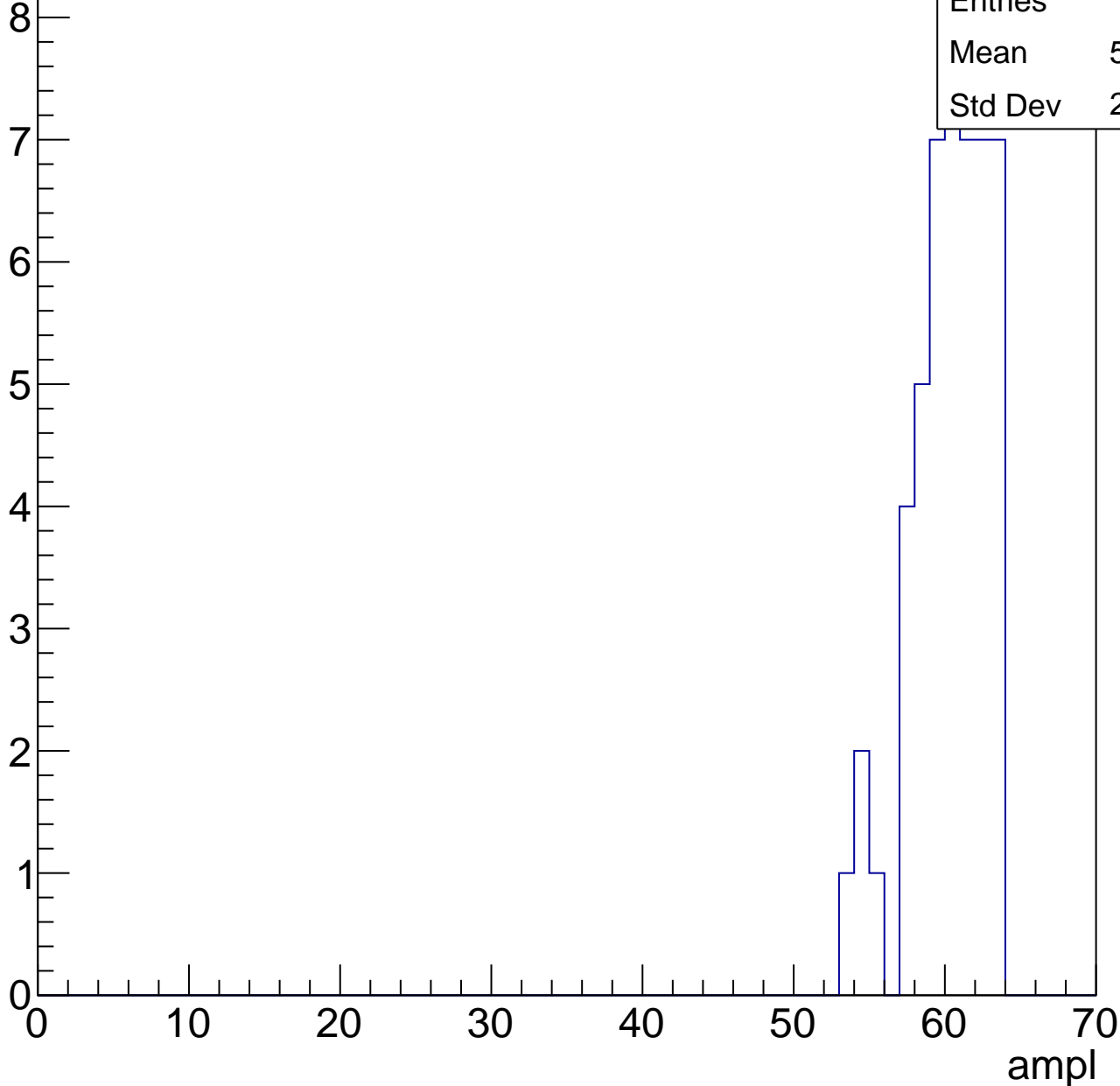


# B1L102S, U25-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

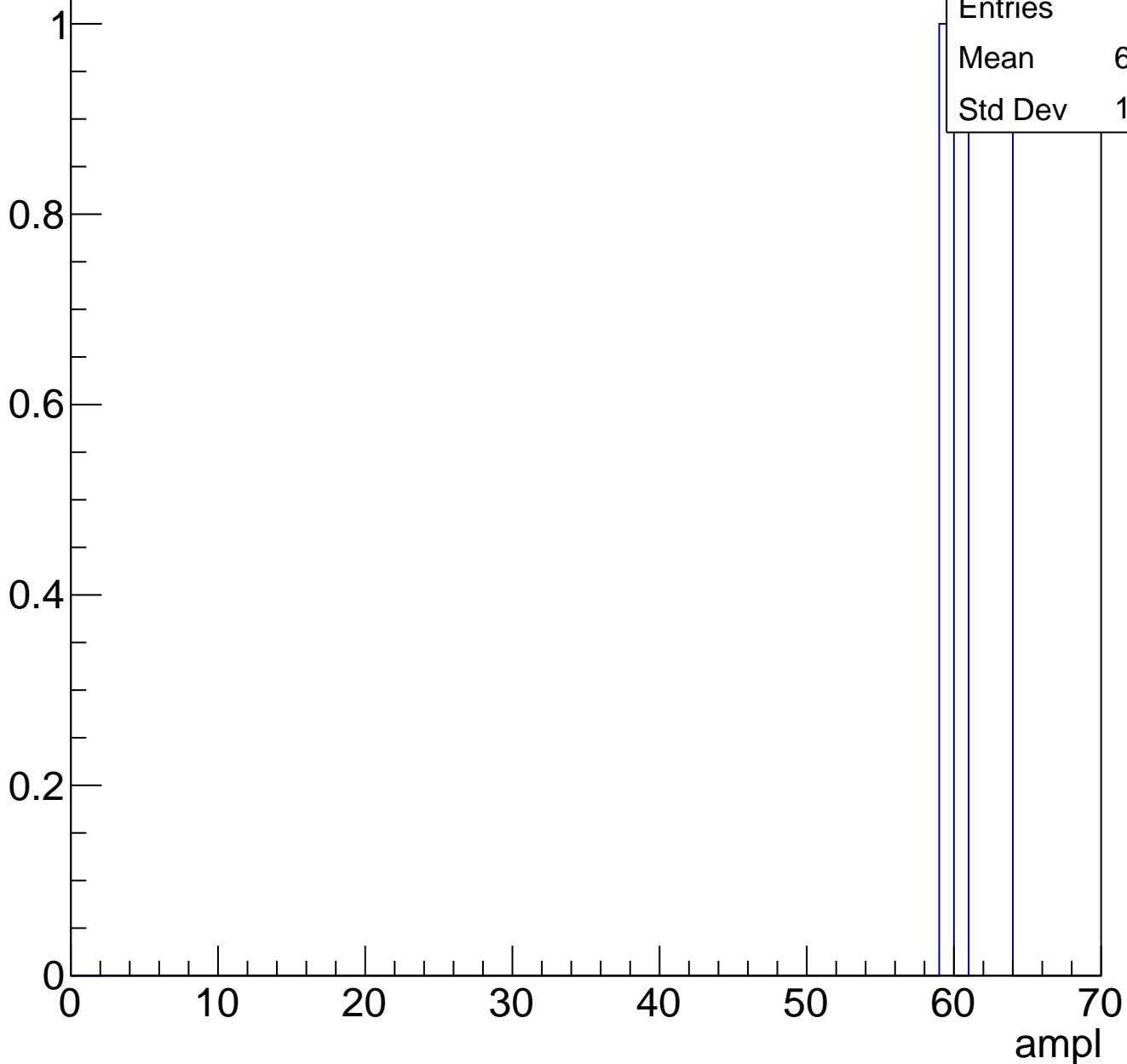
Entries	49
Mean	59.78
Std Dev	2.493



# B1L102S, U25-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B1L102S, U25-ch95, adc0

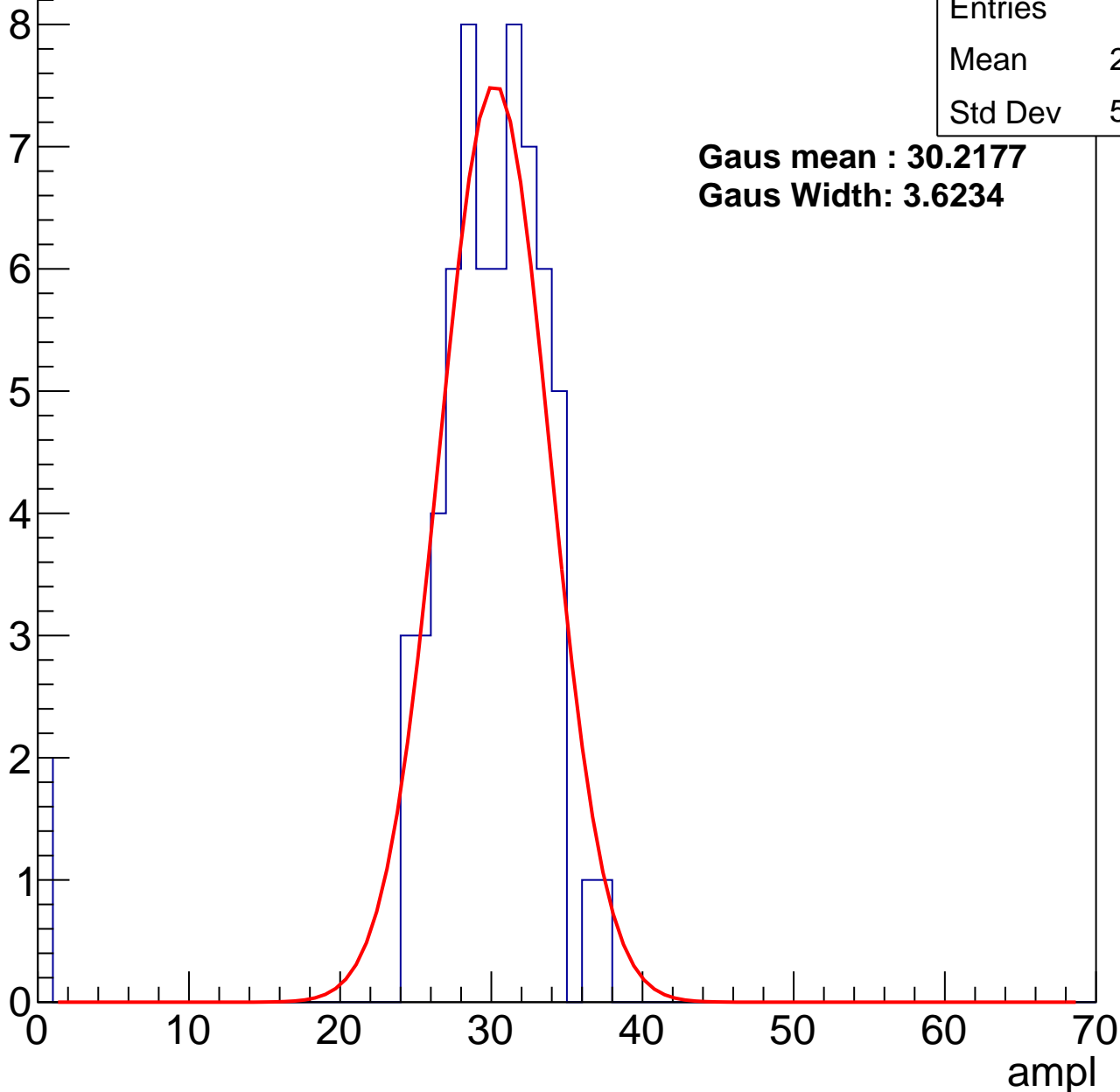
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.85
Std Dev	5.912

**Gaus mean : 30.2177**

**Gaus Width: 3.6234**



# B1L102S, U25-ch95, adc1

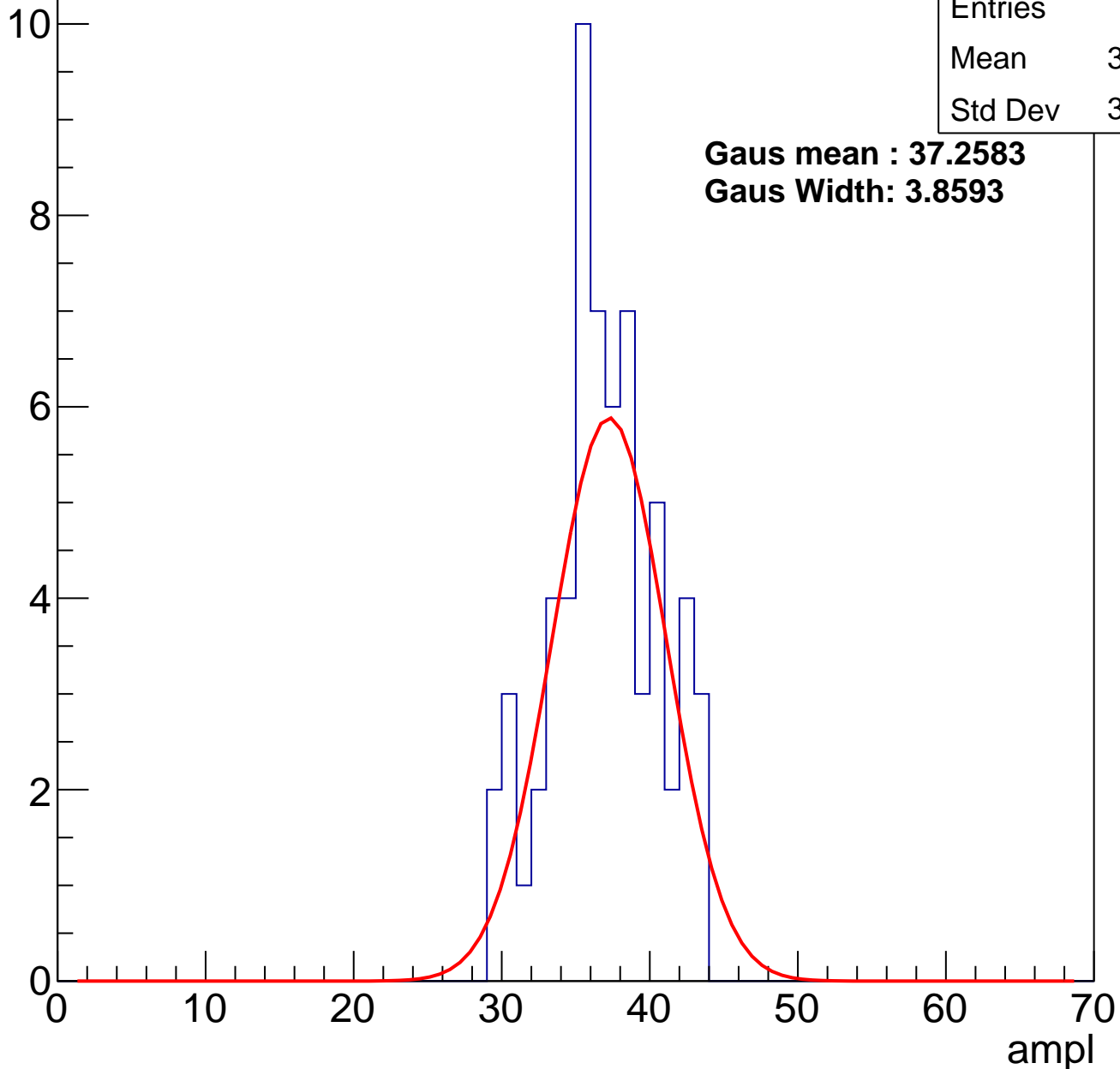
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	63
Mean	36.46
Std Dev	3.549

**Gaus mean : 37.2583**

**Gaus Width: 3.8593**

Entry

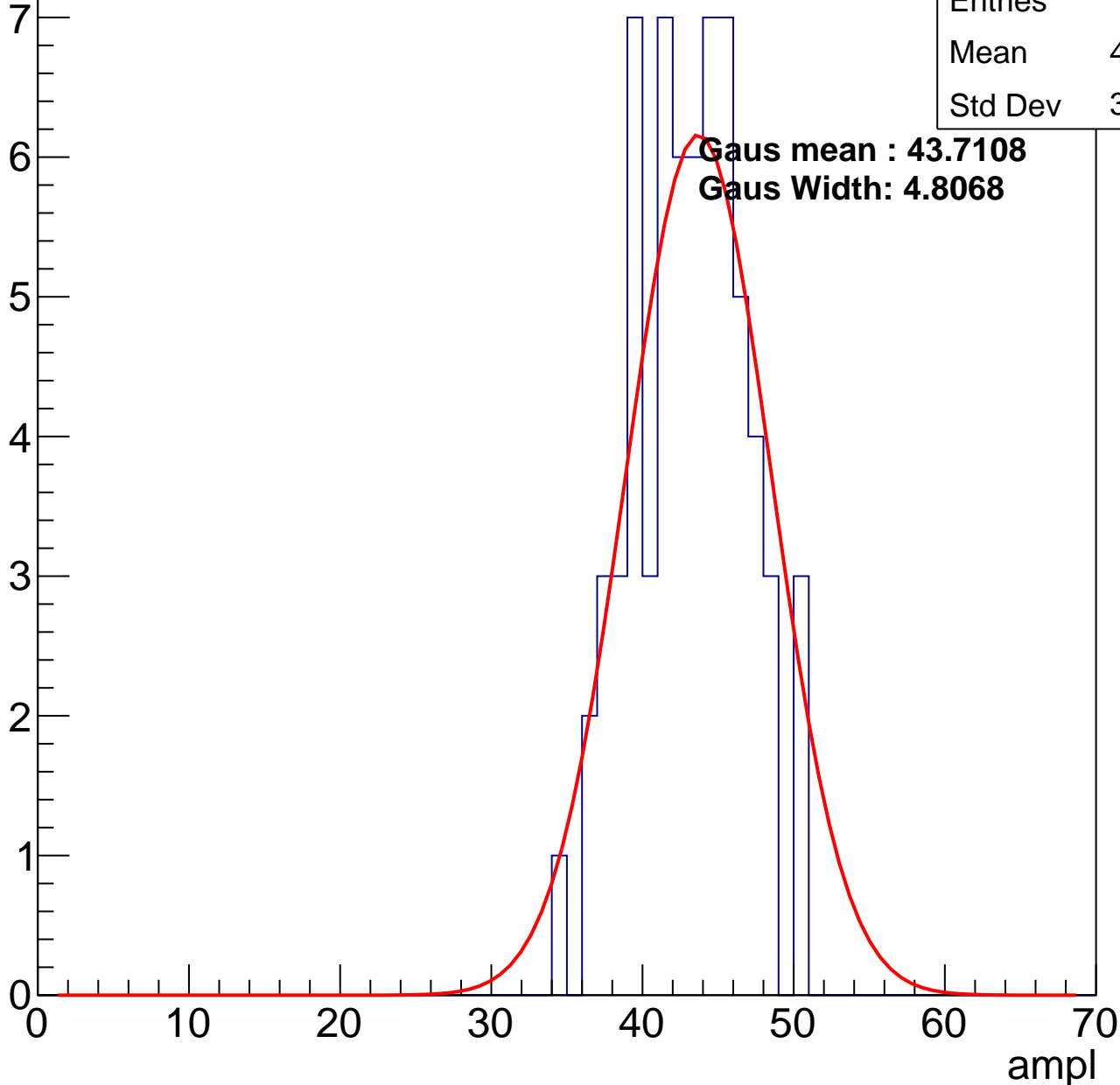


# B1L102S, U25-ch95, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.63
Std Dev	3.648



# B1L102S, U25-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	62
Mean	49.03
Std Dev	3.551

Entry

10

8

6

4

2

0

0

10

20

30

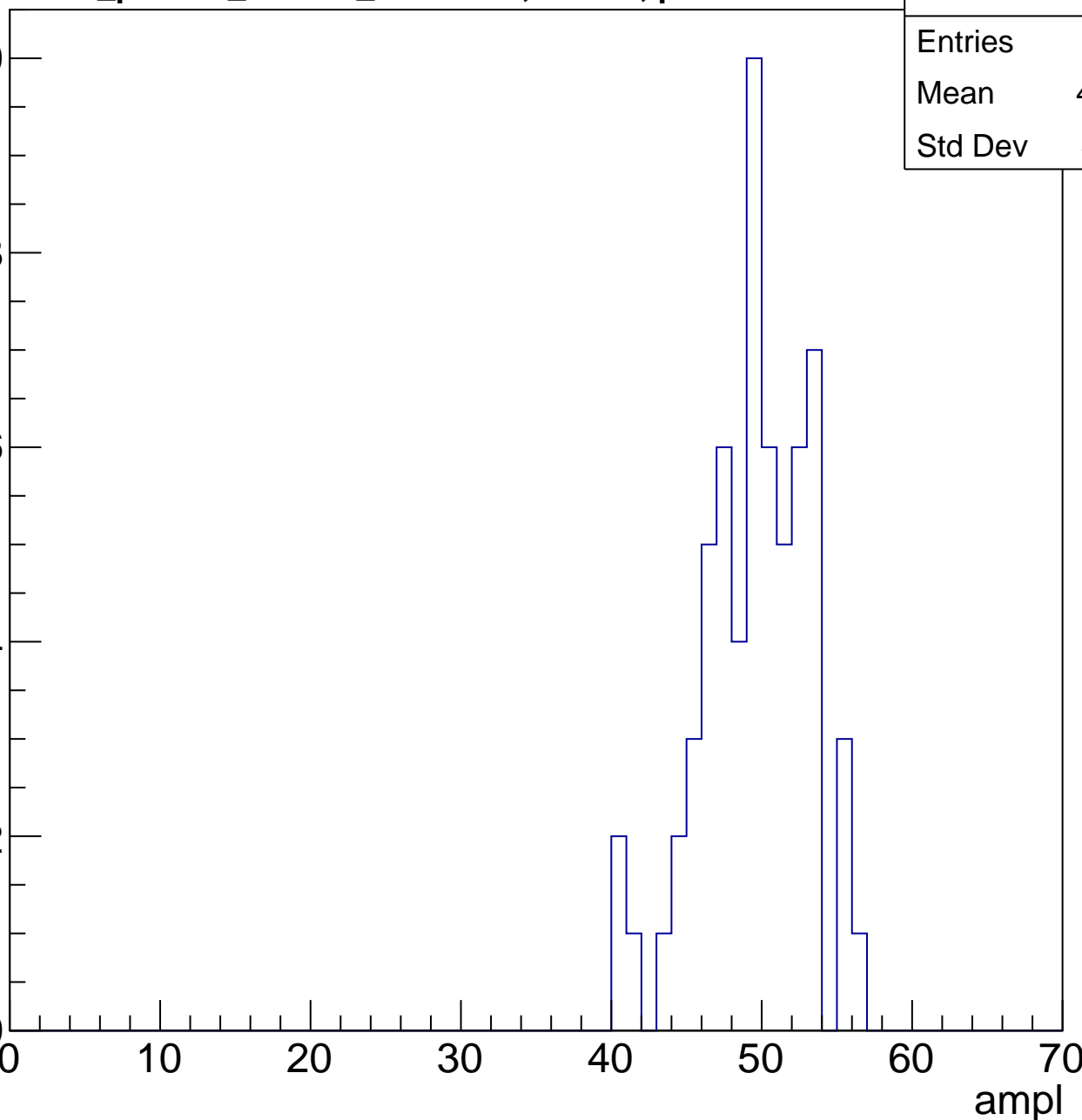
40

50

60

70

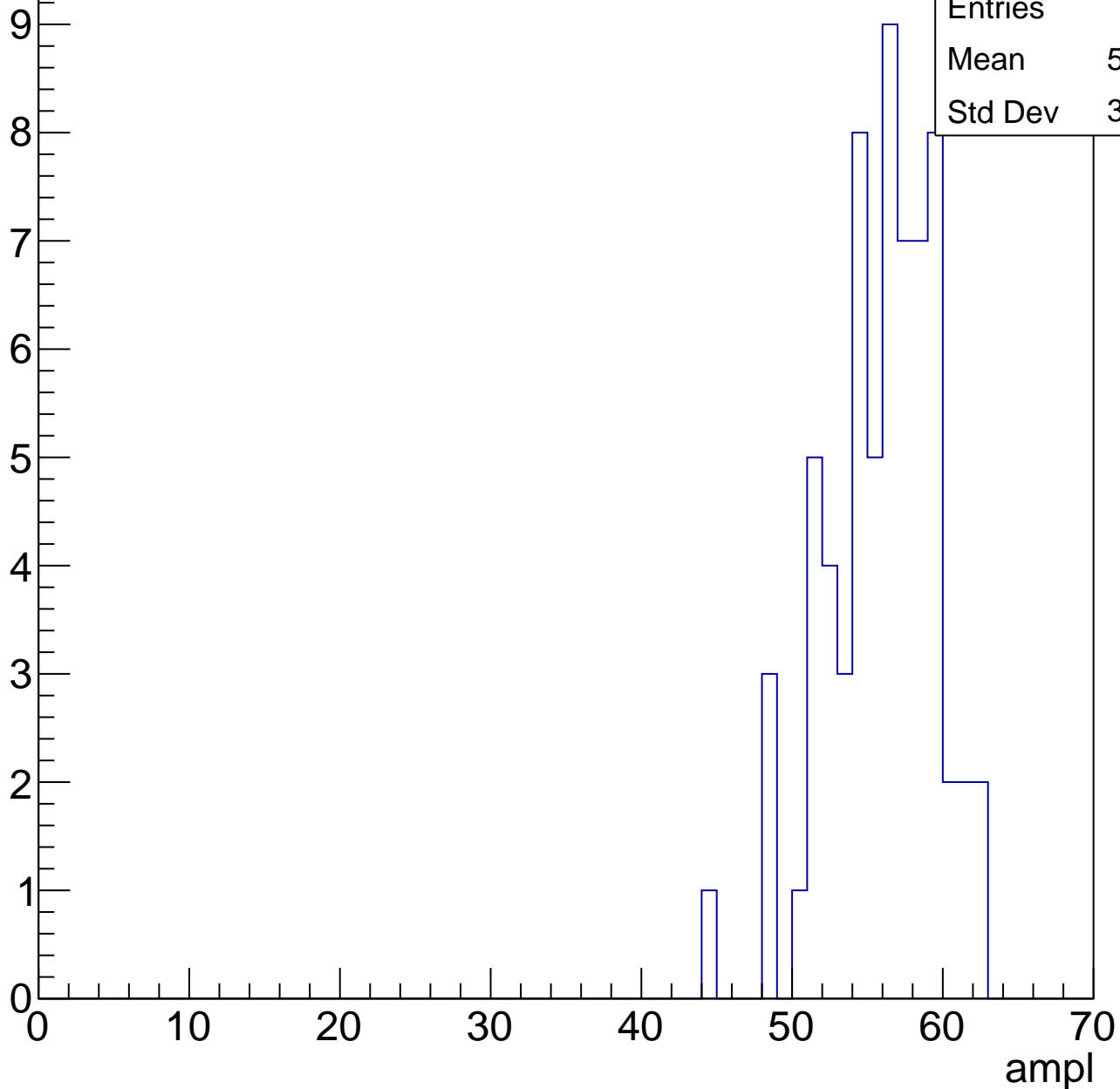
ampl



# B1L102S, U25-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	67
Mean	55.43
Std Dev	3.592

# B1L102S, U25-ch95, adc5

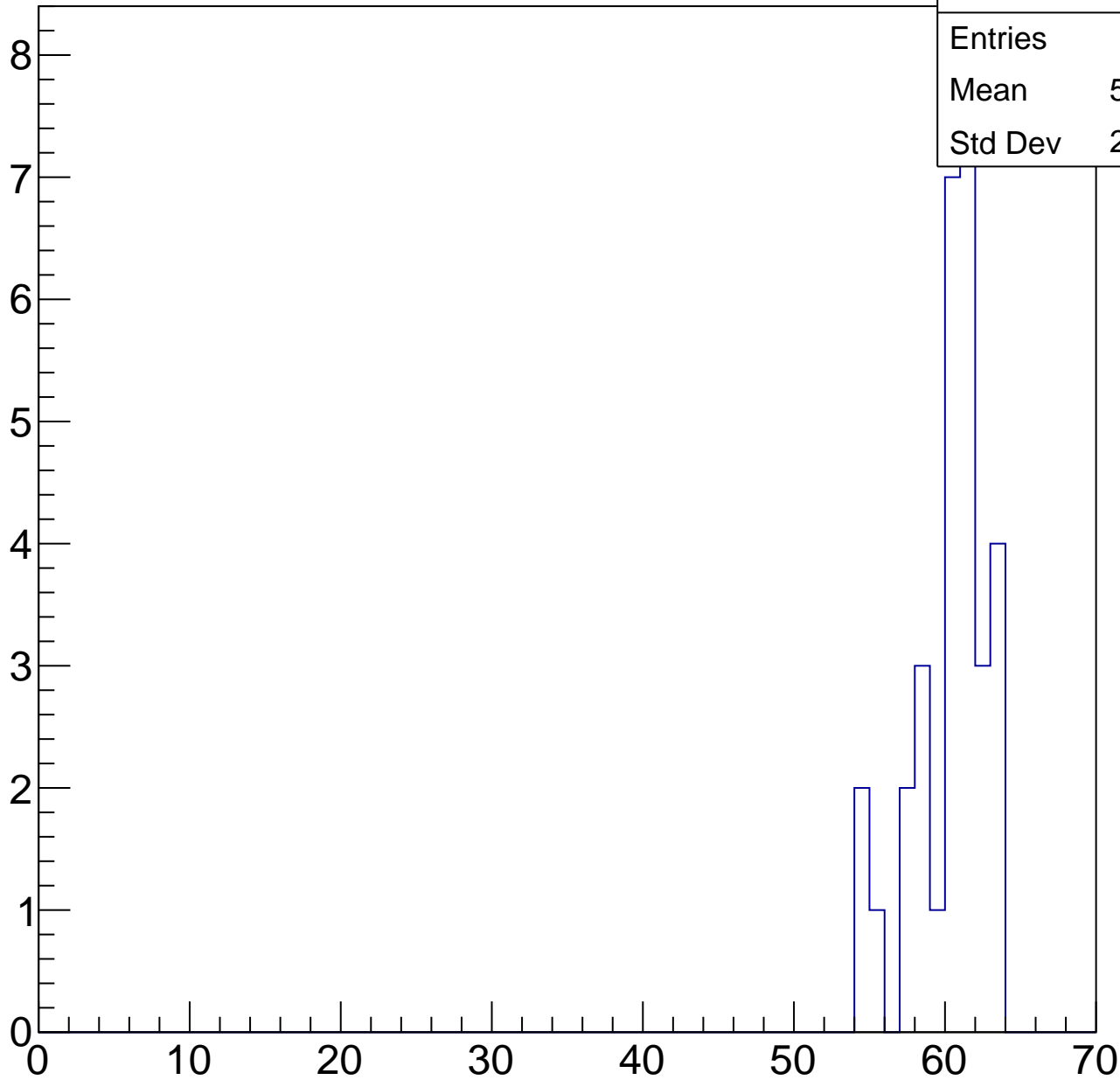
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	31
Mean	59.87
Std Dev	2.433

ampl

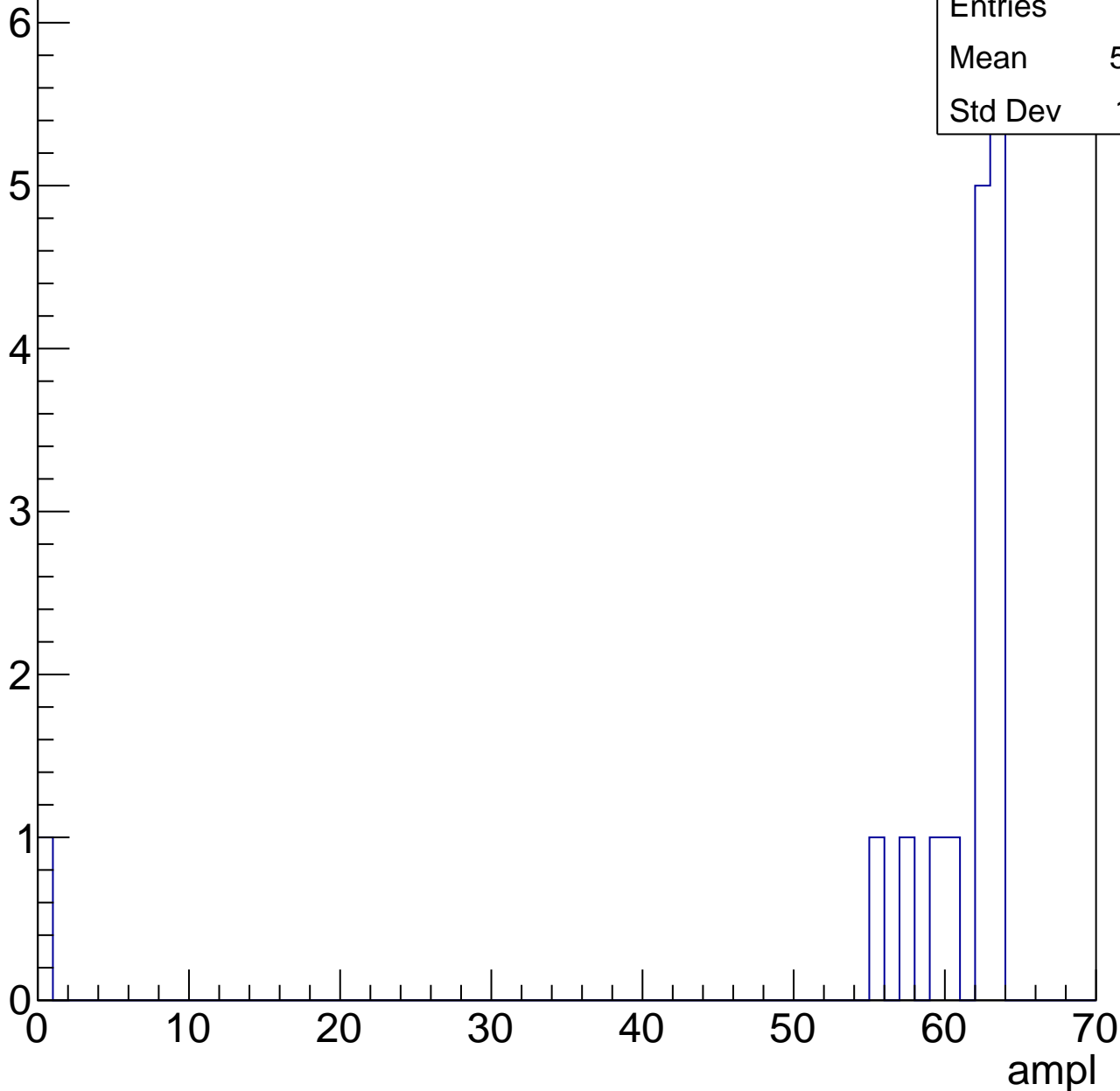


# B1L102S, U25-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.44
Std Dev	15.01





# B1L102S, U25-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch96, adc0

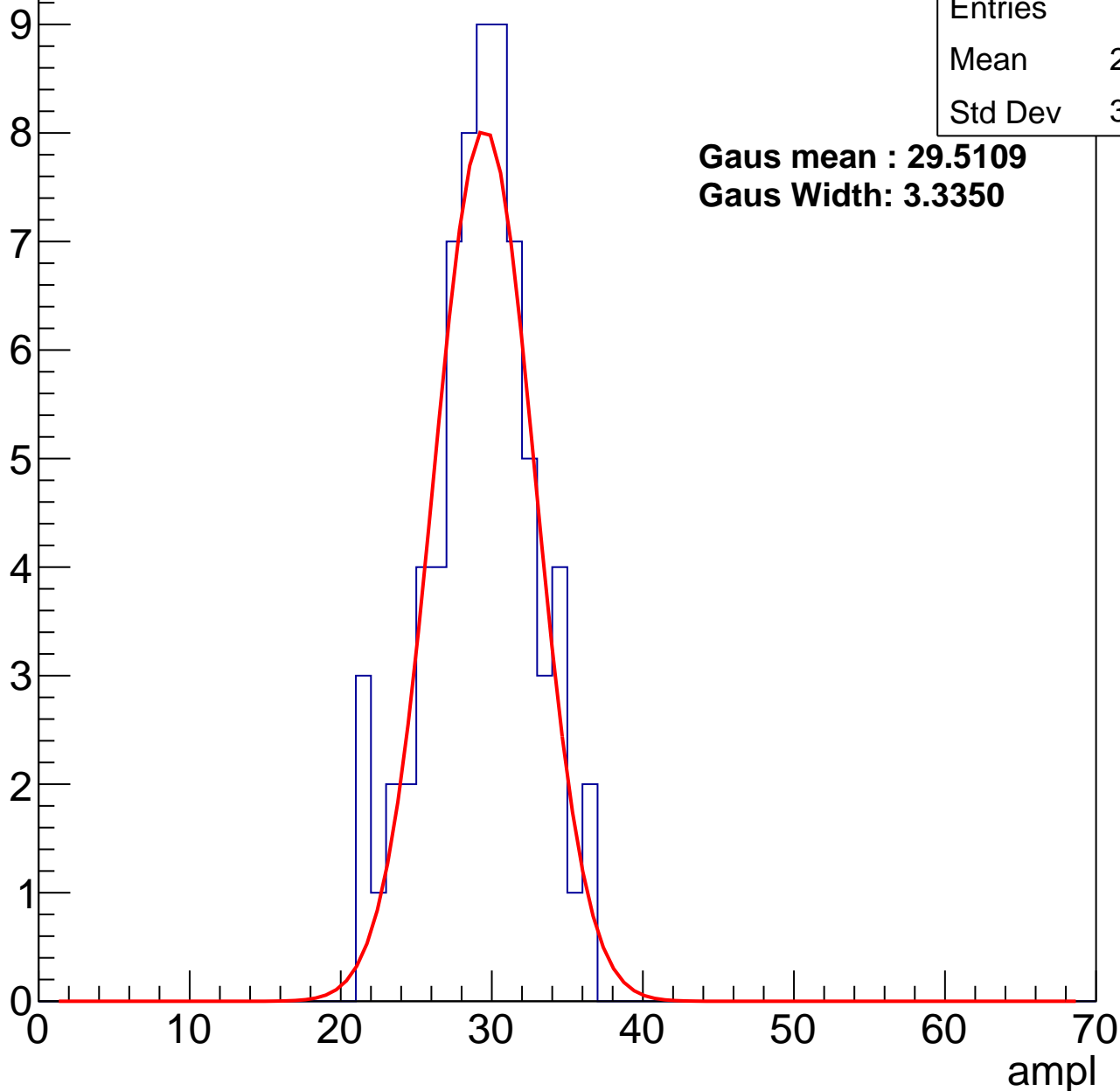
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.82
Std Dev	3.482

**Gaus mean : 29.5109**

**Gaus Width: 3.3350**

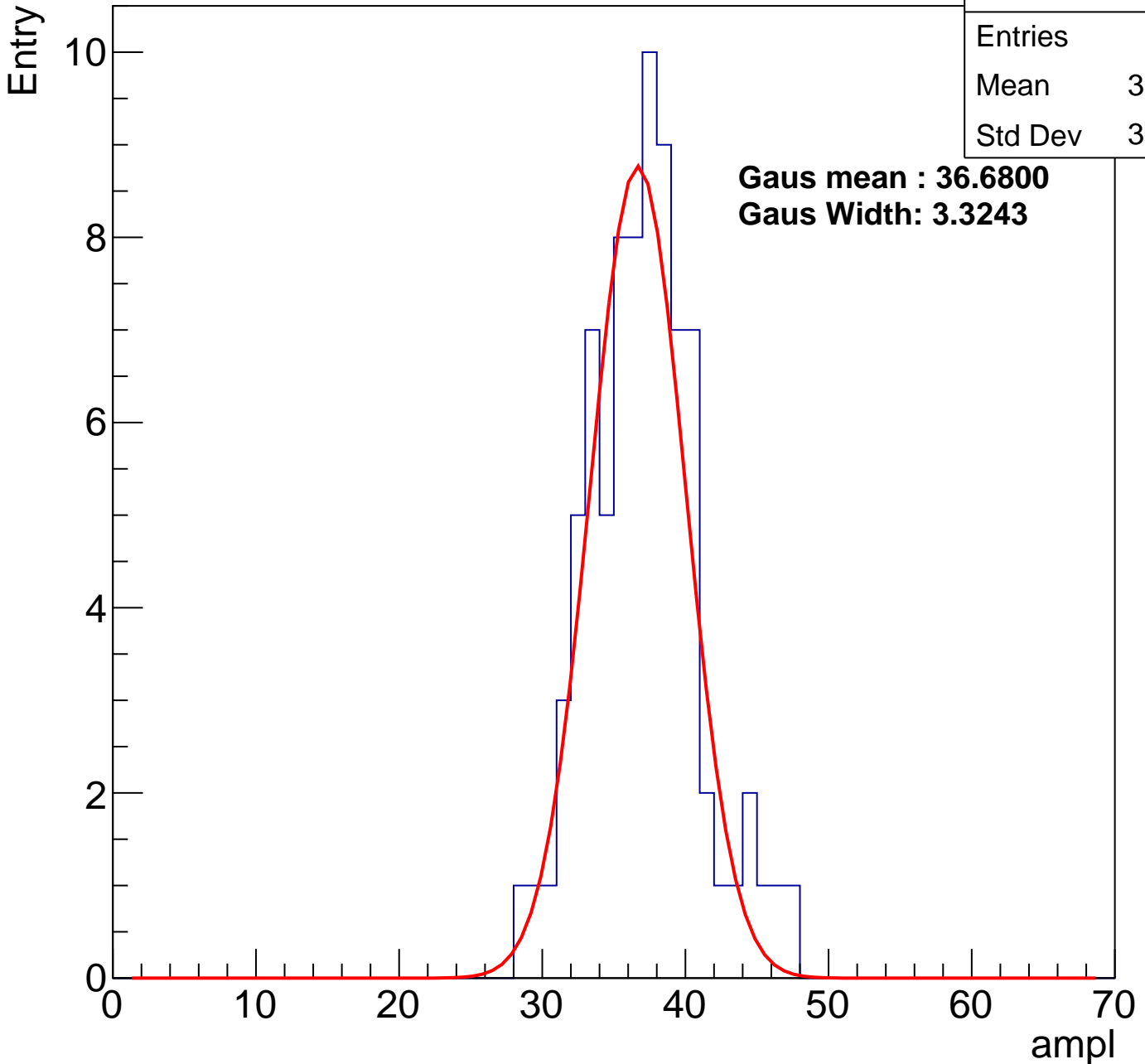


# B1L102S, U25-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	81
Mean	36.63
Std Dev	3.766

**Gaus mean : 36.6800**  
**Gaus Width: 3.3243**



# B1L102S, U25-ch96, adc2

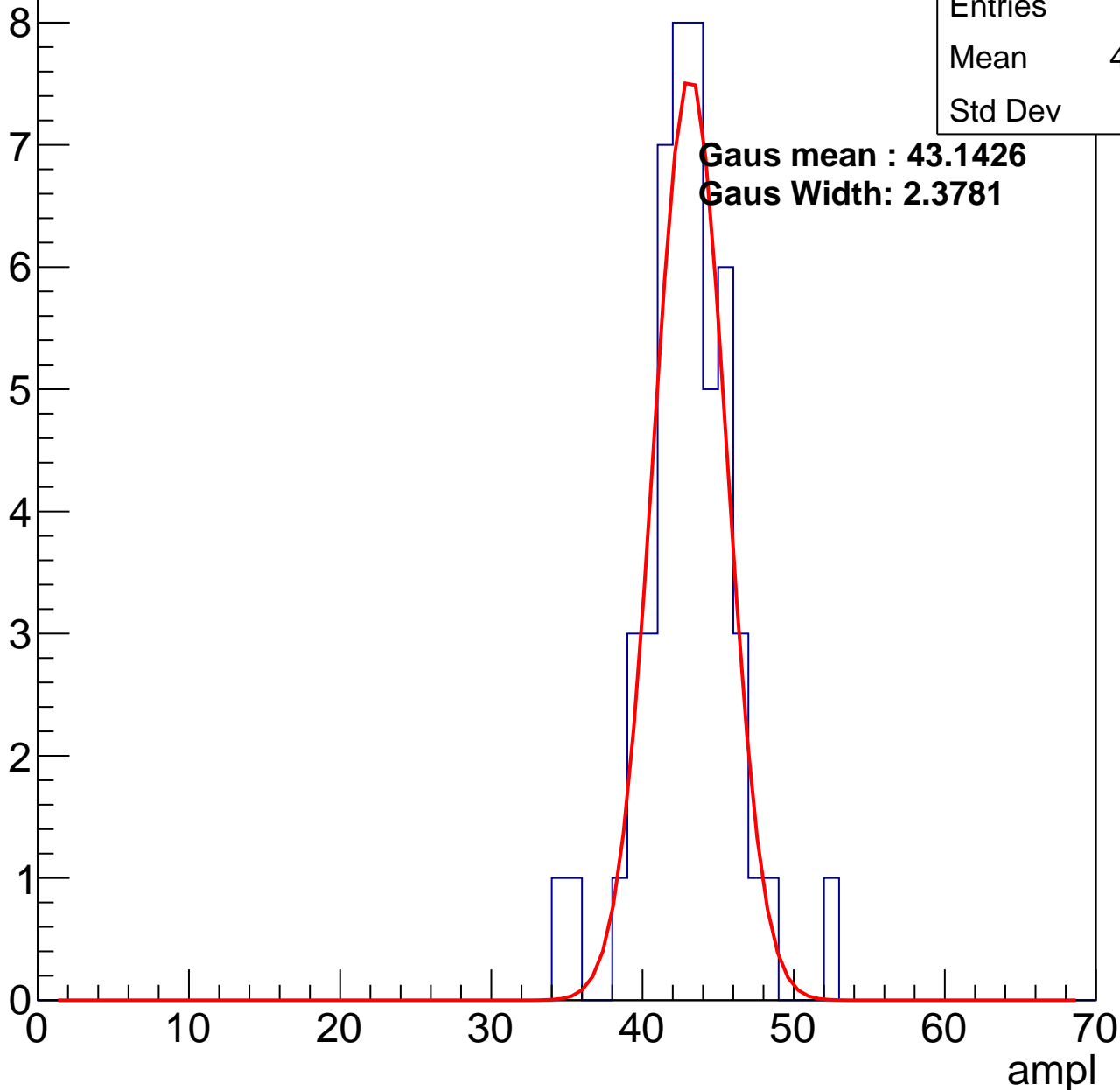
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.57
Std Dev	3.03

**Gaus mean : 43.1426**

**Gaus Width: 2.3781**

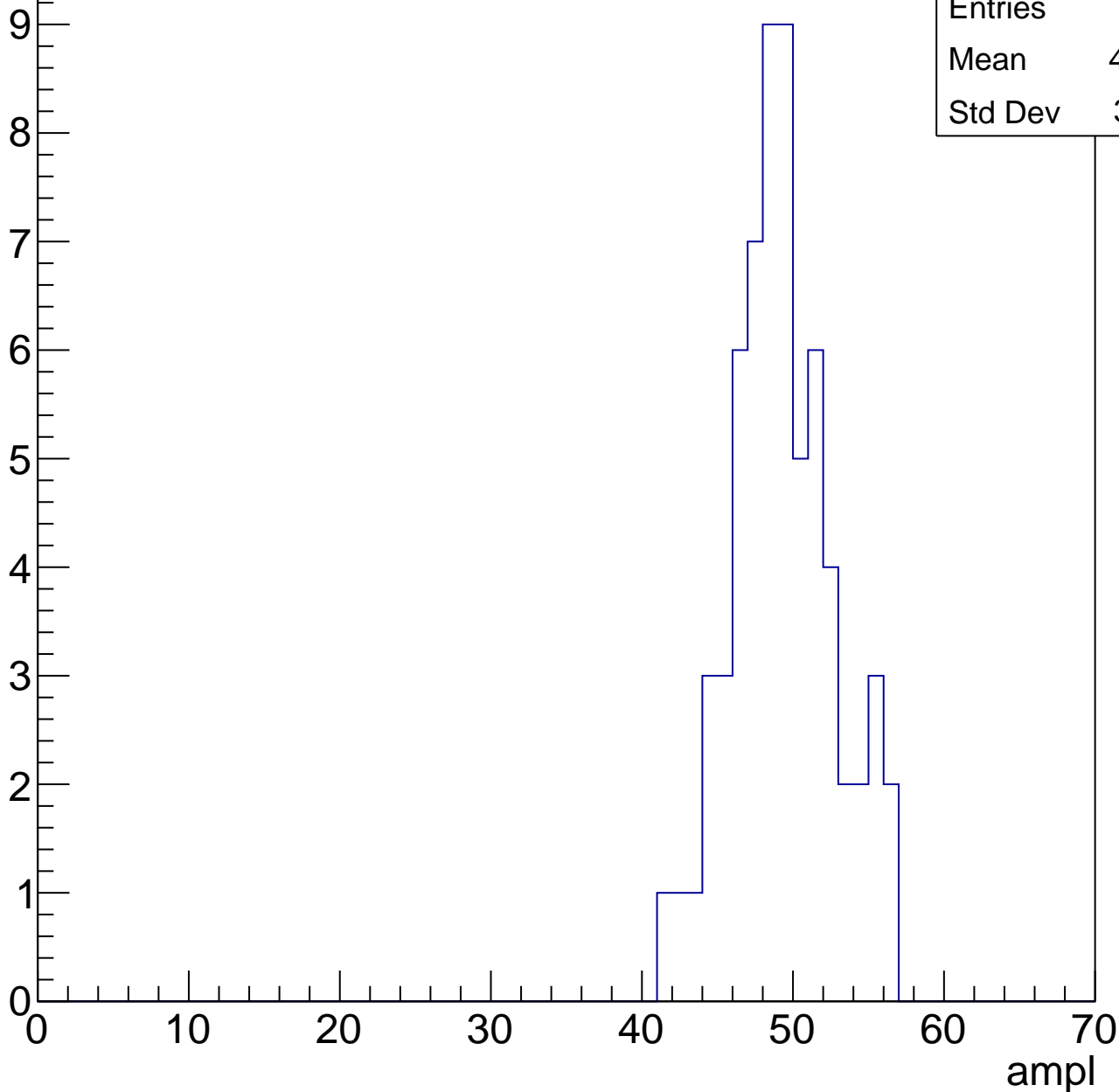


# B1L102S, U25-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.84
Std Dev	3.341

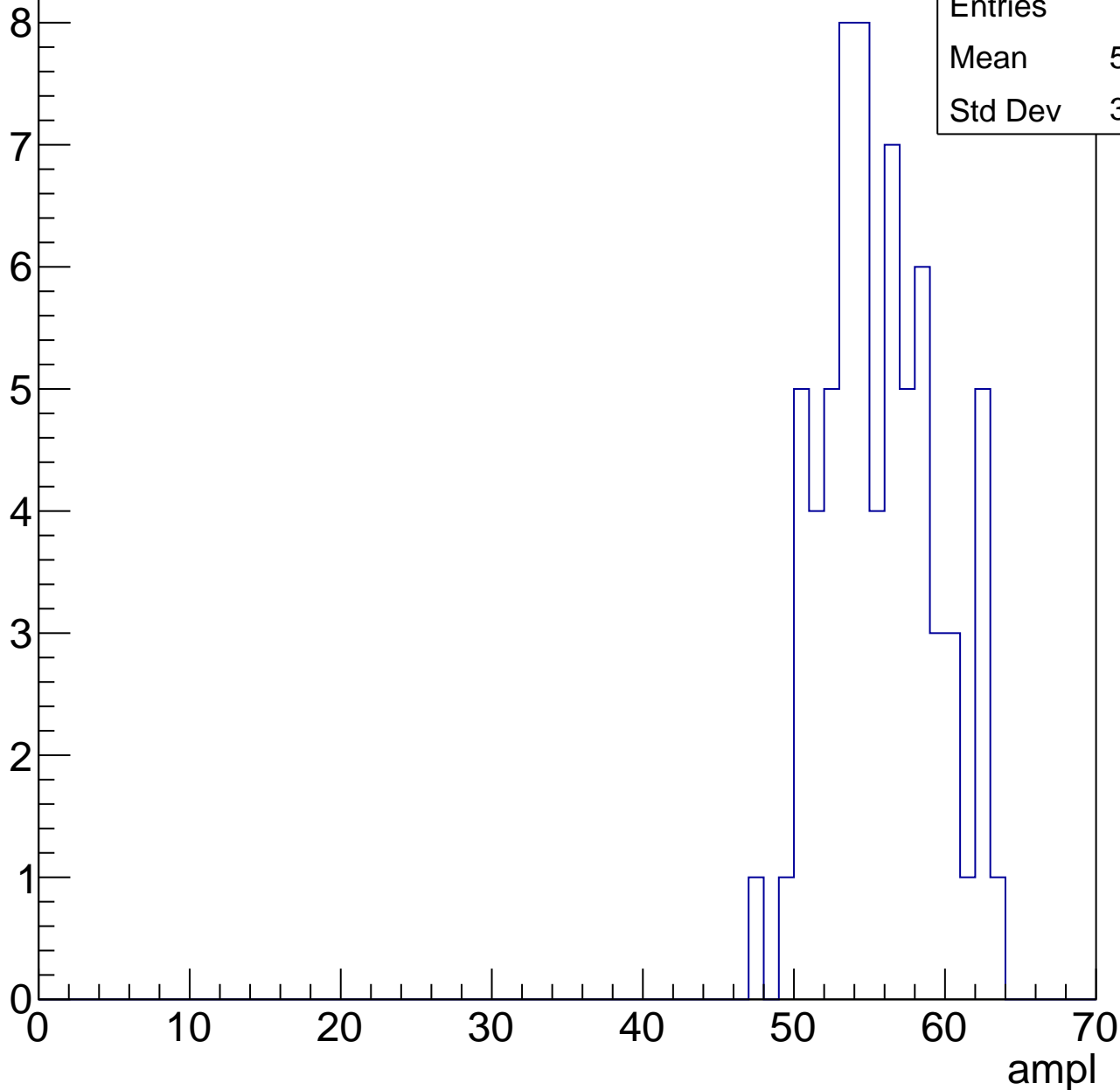


# B1L102S, U25-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

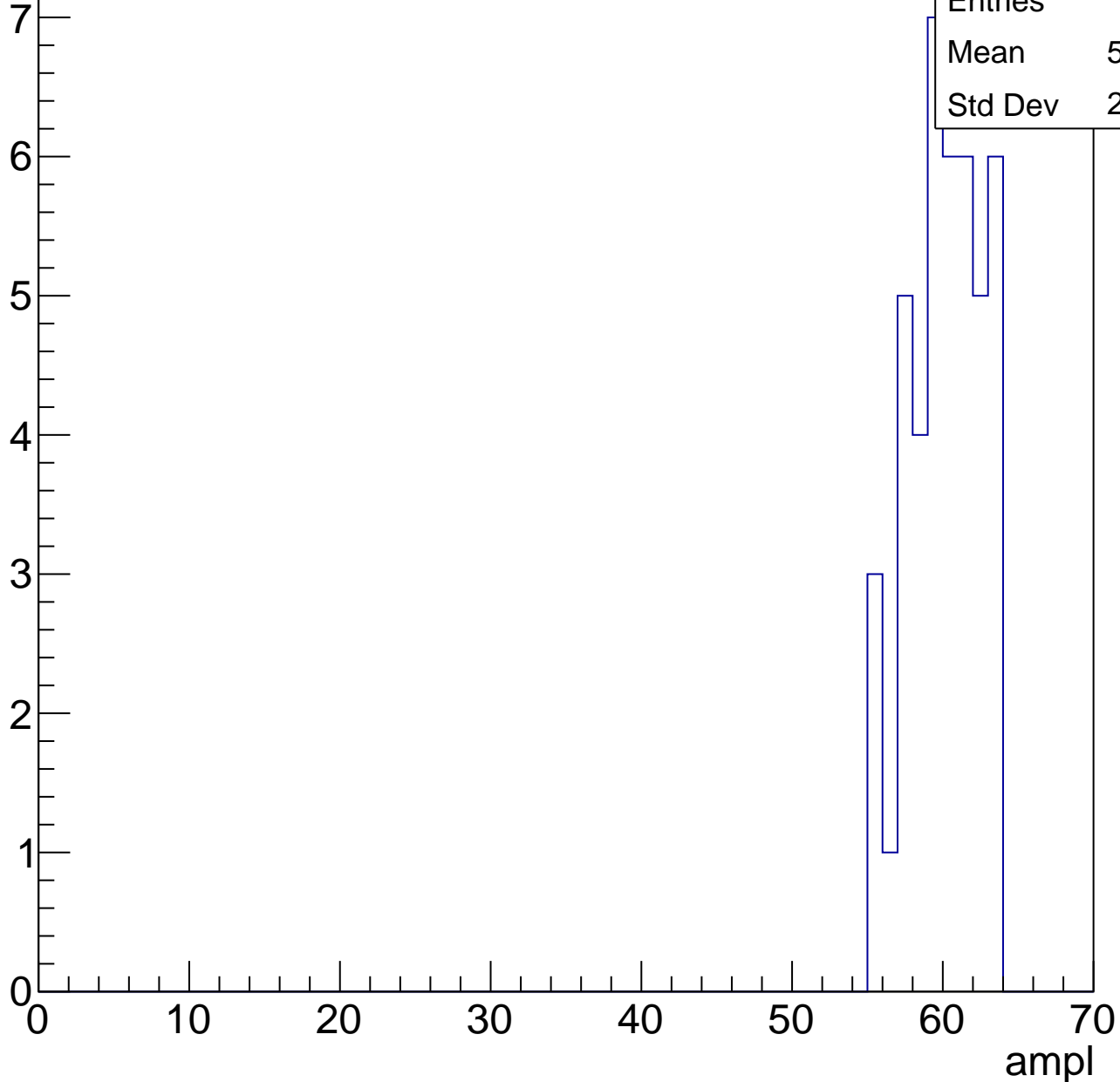
Entries	67
Mean	55.25
Std Dev	3.703



# B1L102S, U25-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

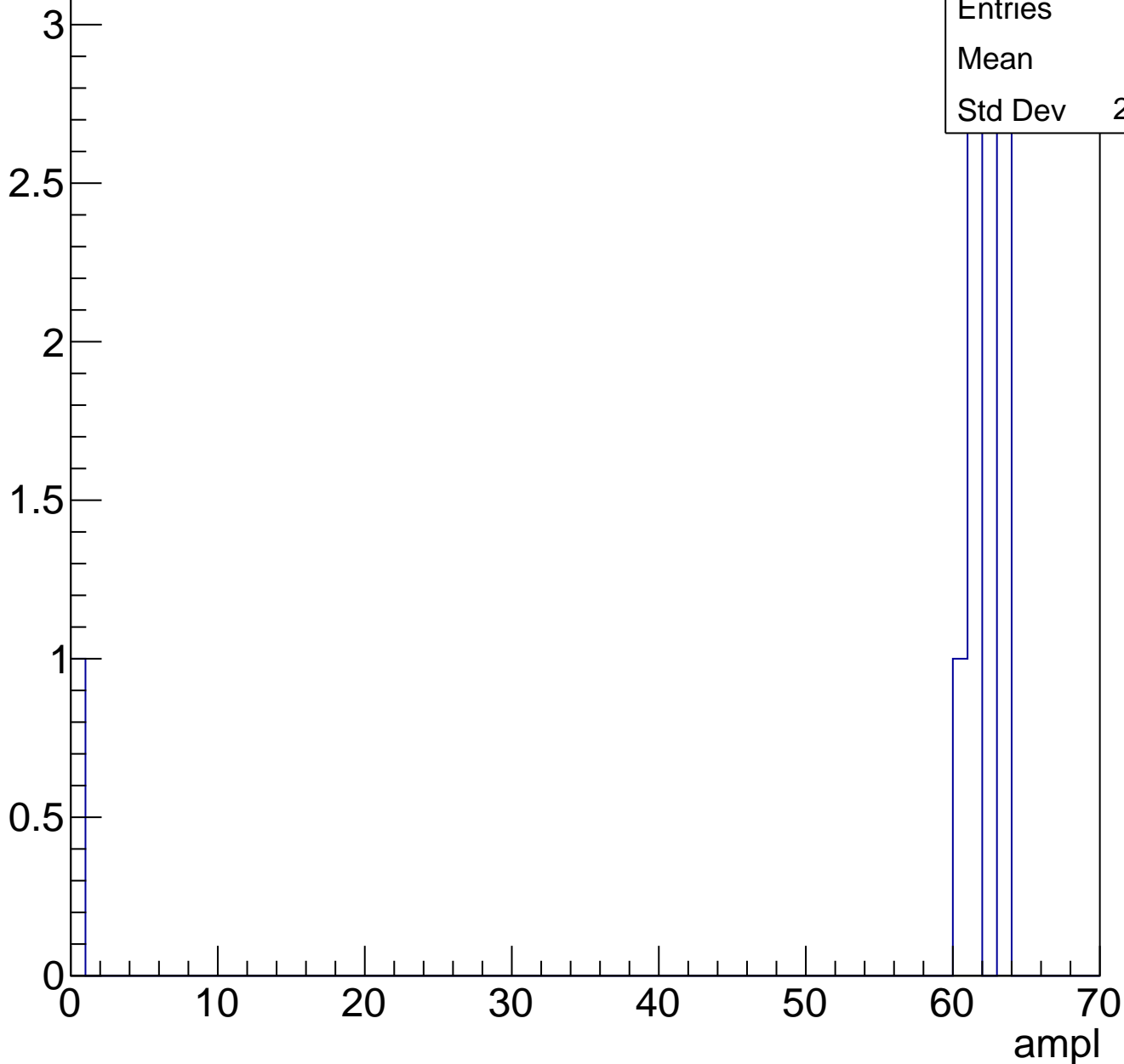
Entry



# B1L102S, U25-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch97, adc0

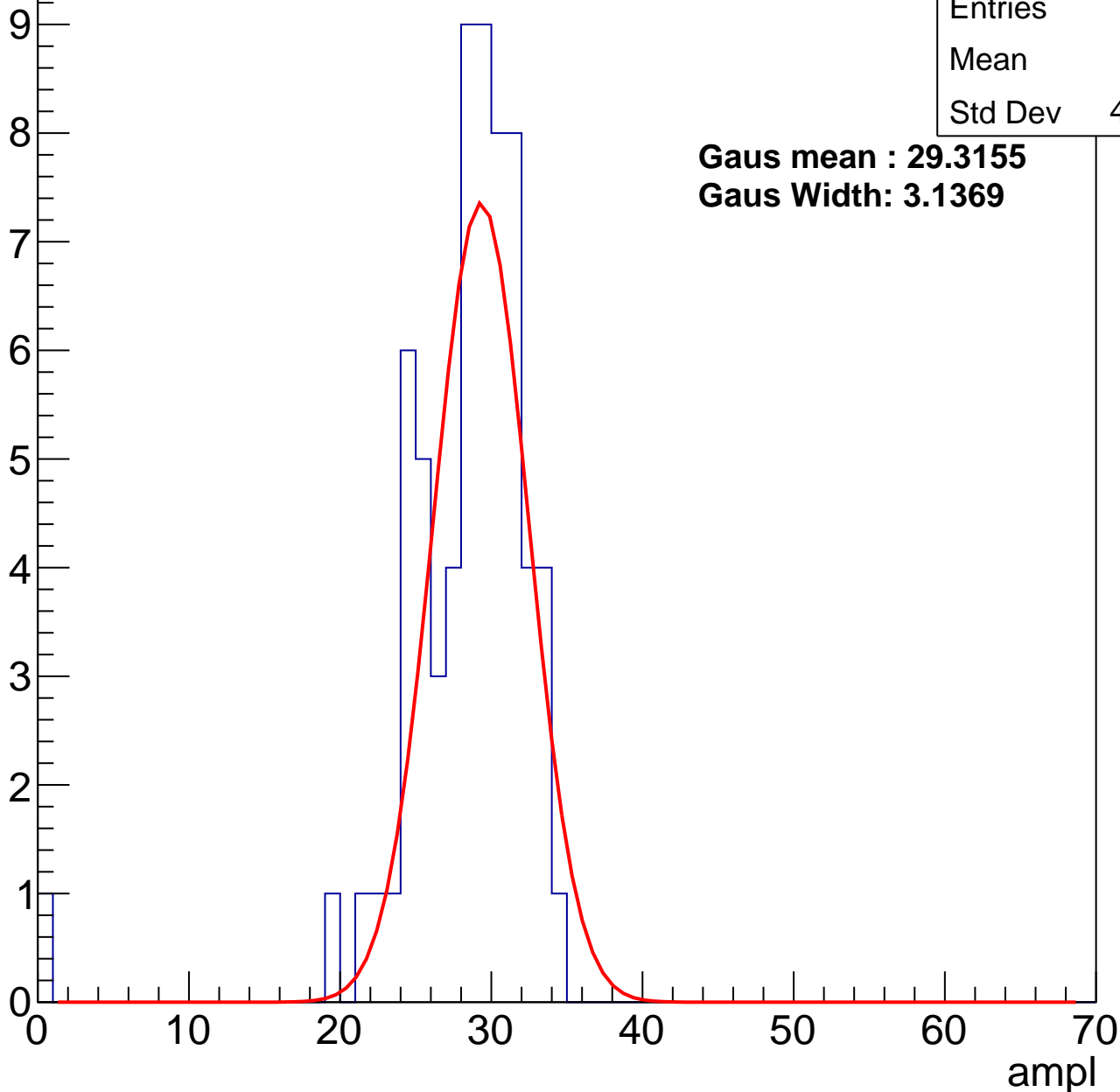
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.8
Std Dev	4.675

**Gaus mean : 29.3155**

**Gaus Width: 3.1369**



# B1L102S, U25-ch97, adc1

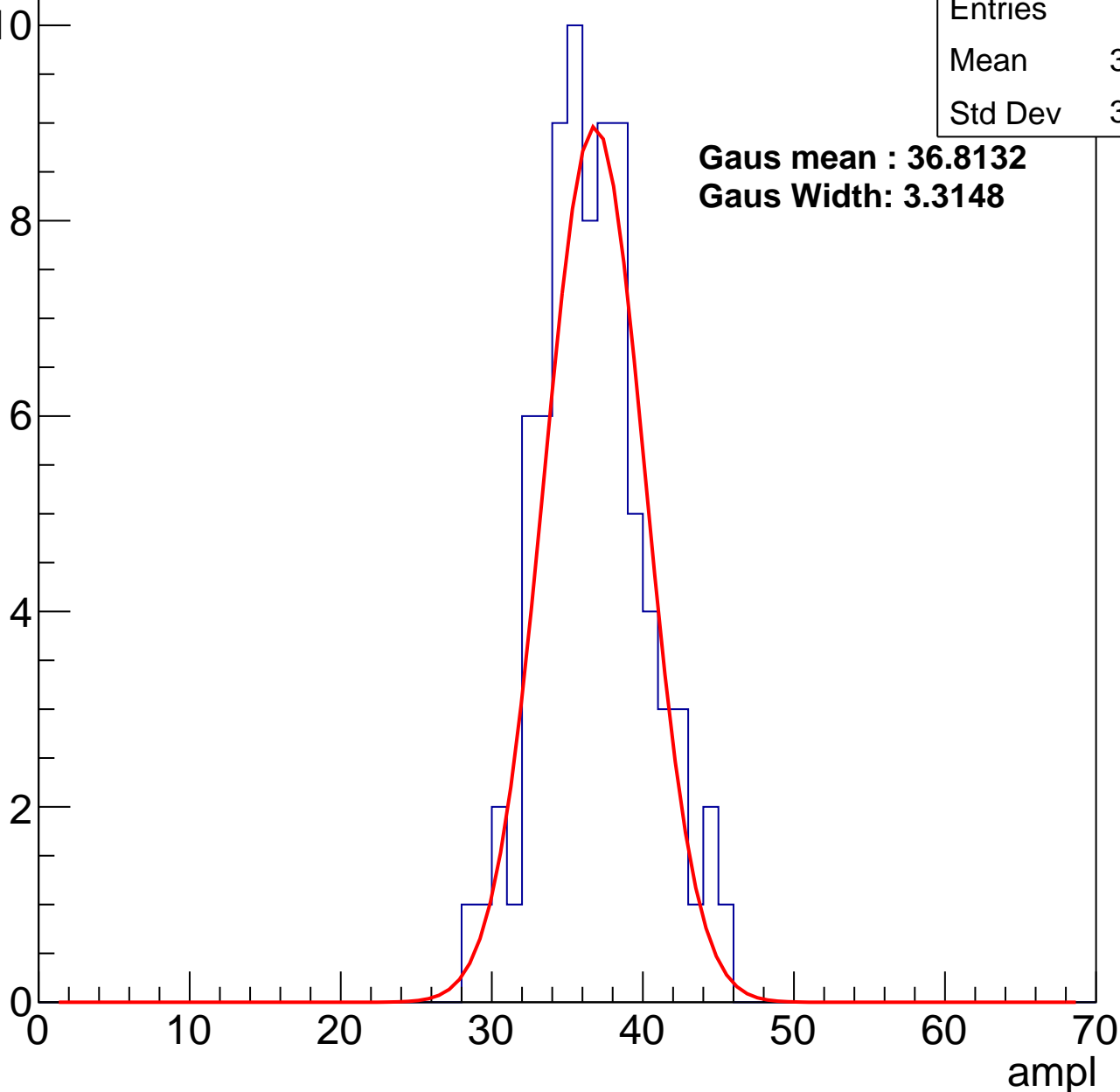
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.26
Std Dev	3.506

**Gaus mean : 36.8132**

**Gaus Width: 3.3148**



# B1L102S, U25-ch97, adc2

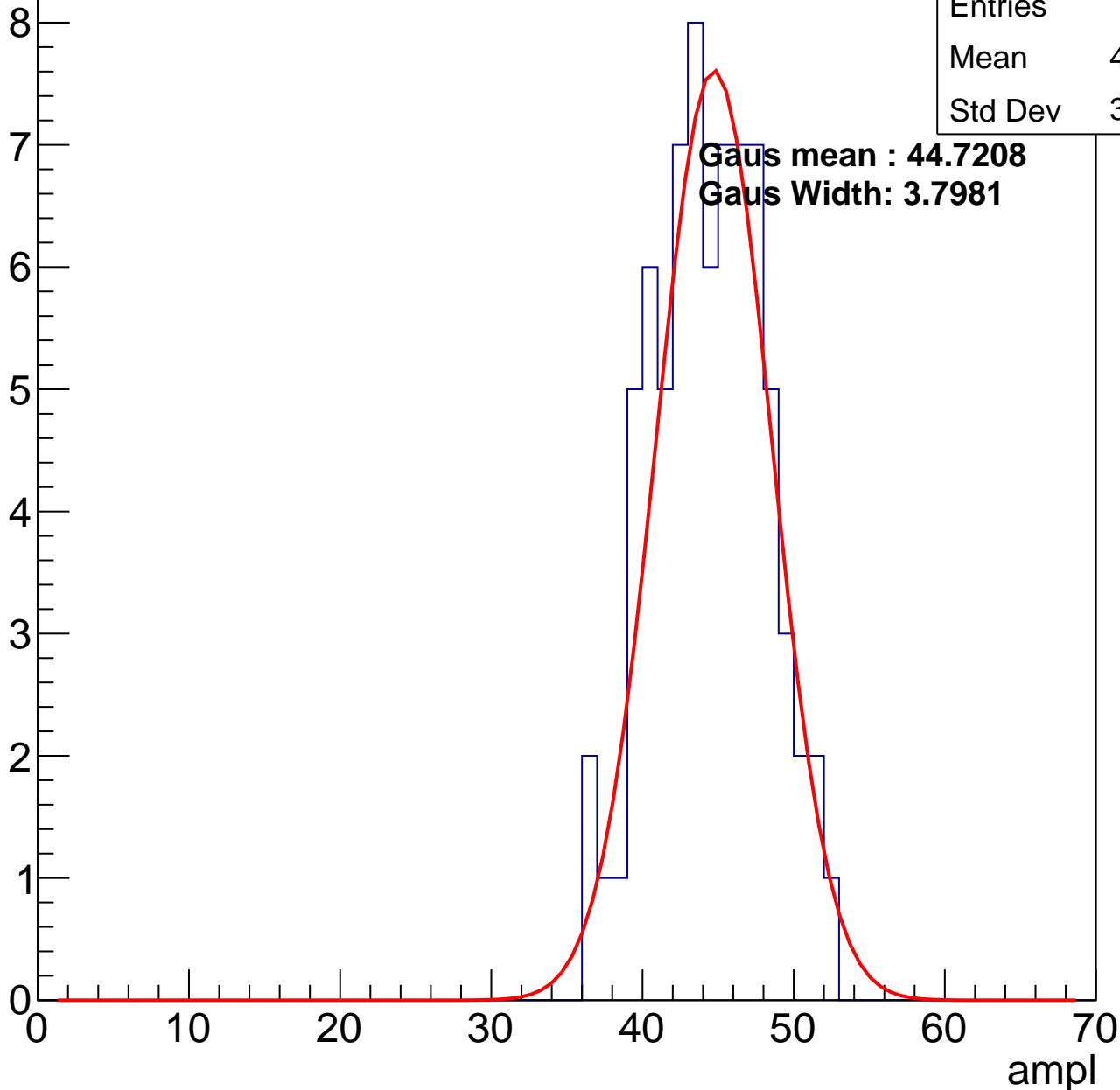
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	43.95
Std Dev	3.655

**Gaus mean : 44.7208**

**Gaus Width: 3.7981**

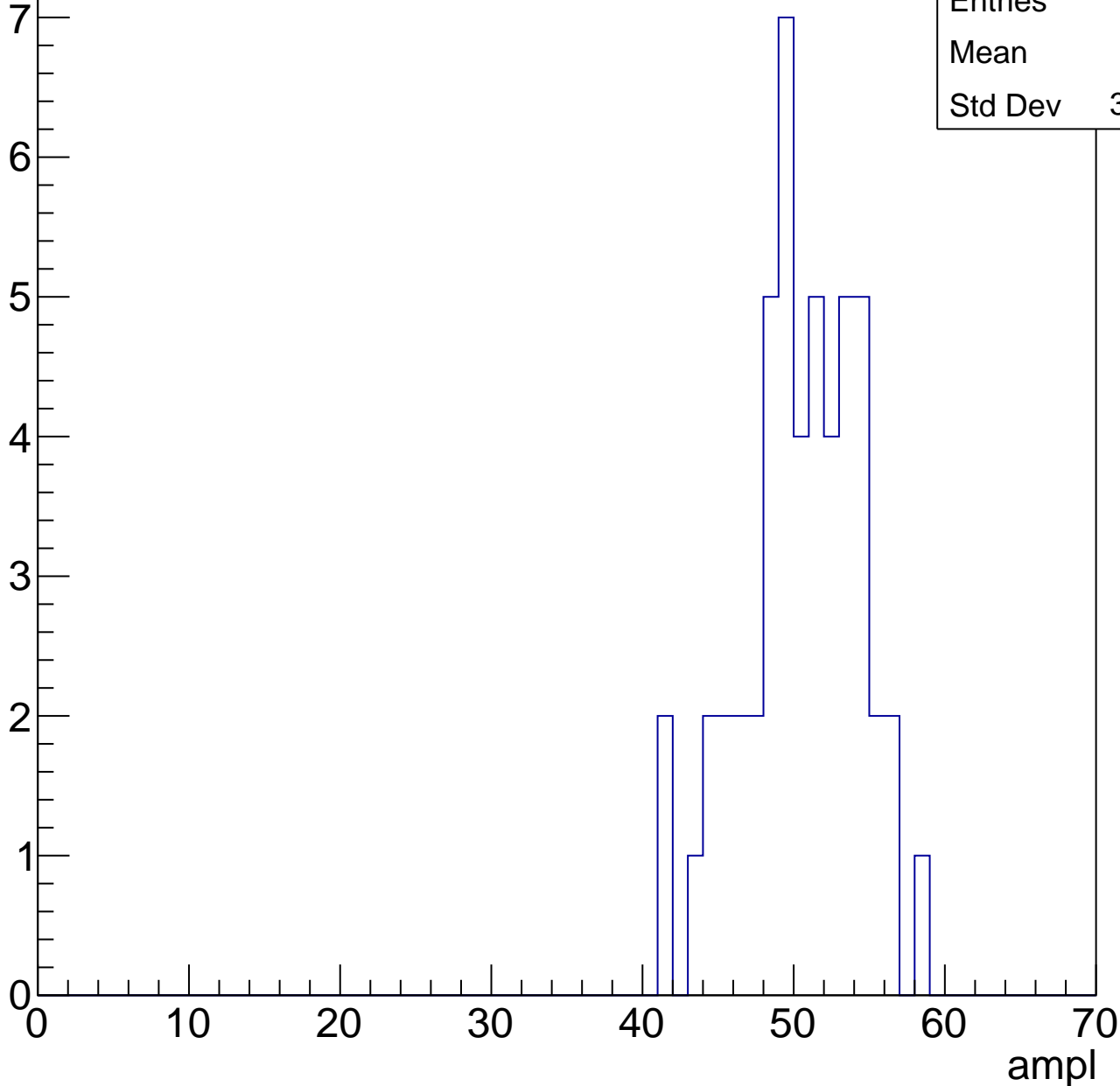


# B1L102S, U25-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	50
Std Dev	3.814

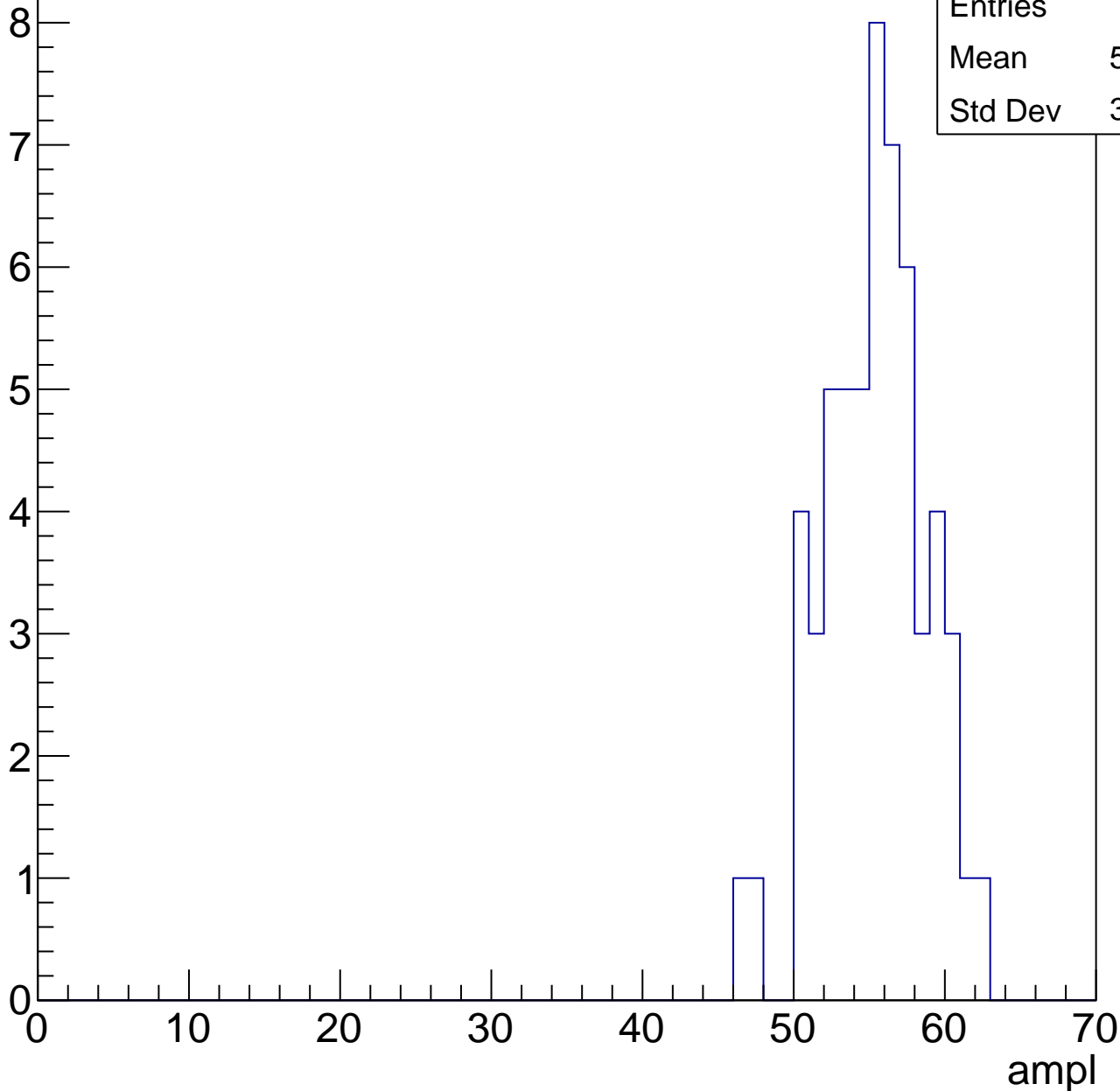


# B1L102S, U25-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.88
Std Dev	3.362

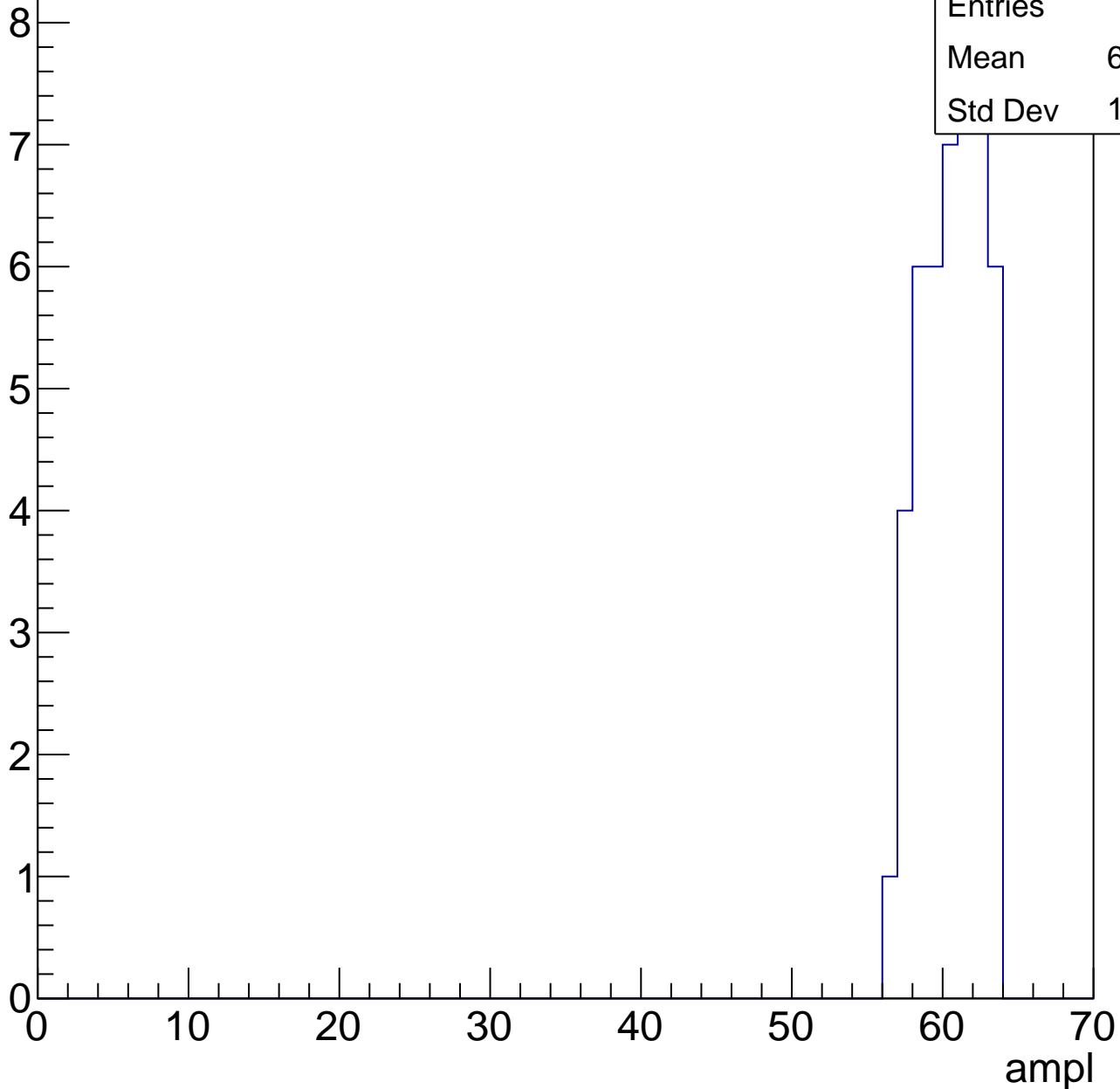


# B1L102S, U25-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	60.17
Std Dev	1.948



# B1L102S, U25-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	6
Mean	52
Std Dev	23.27



# B1L102S, U25-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch98, adc0

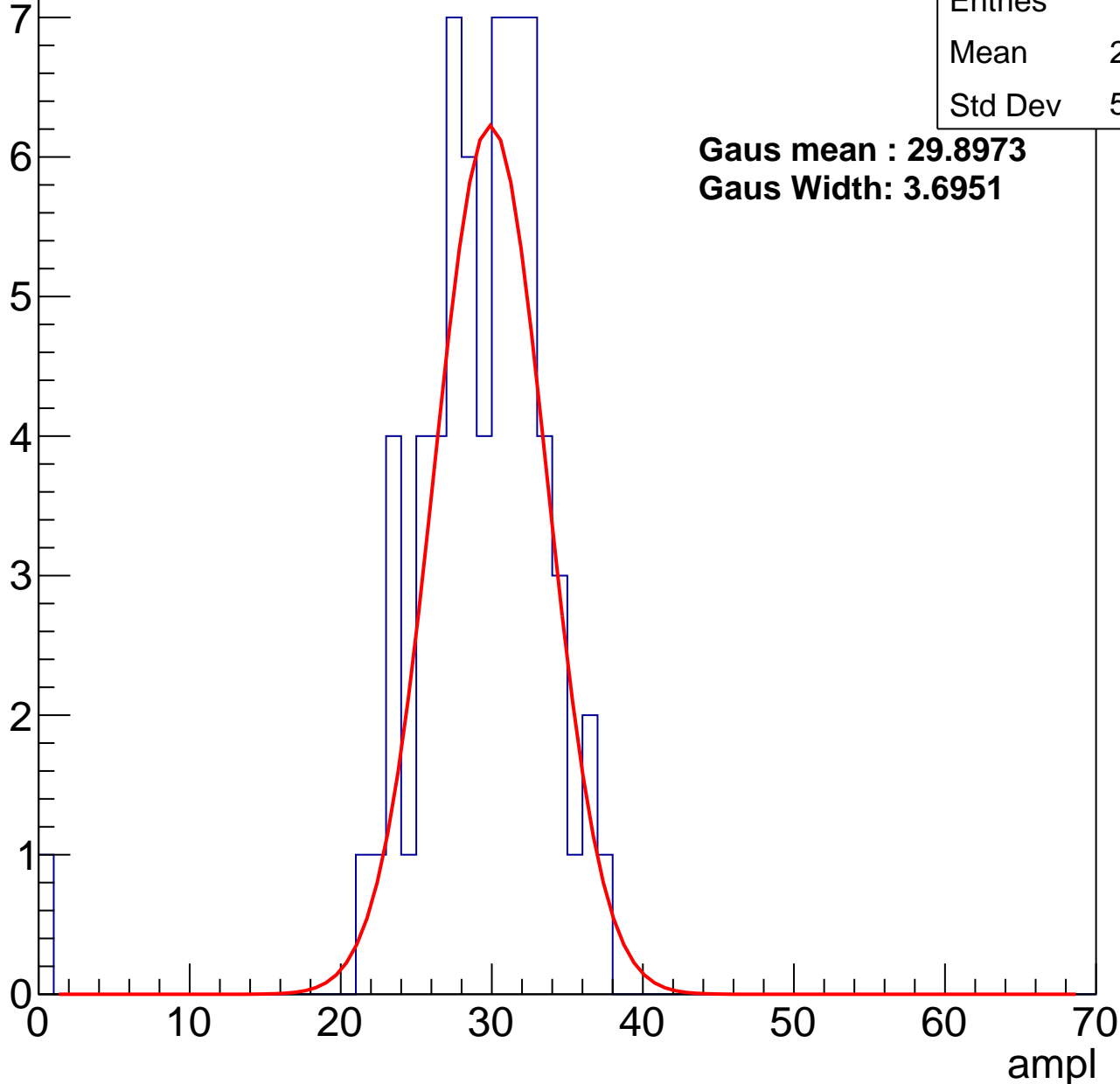
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.69
Std Dev	5.093

**Gaus mean : 29.8973**

**Gaus Width: 3.6951**



# B1L102S, U25-ch98, adc1

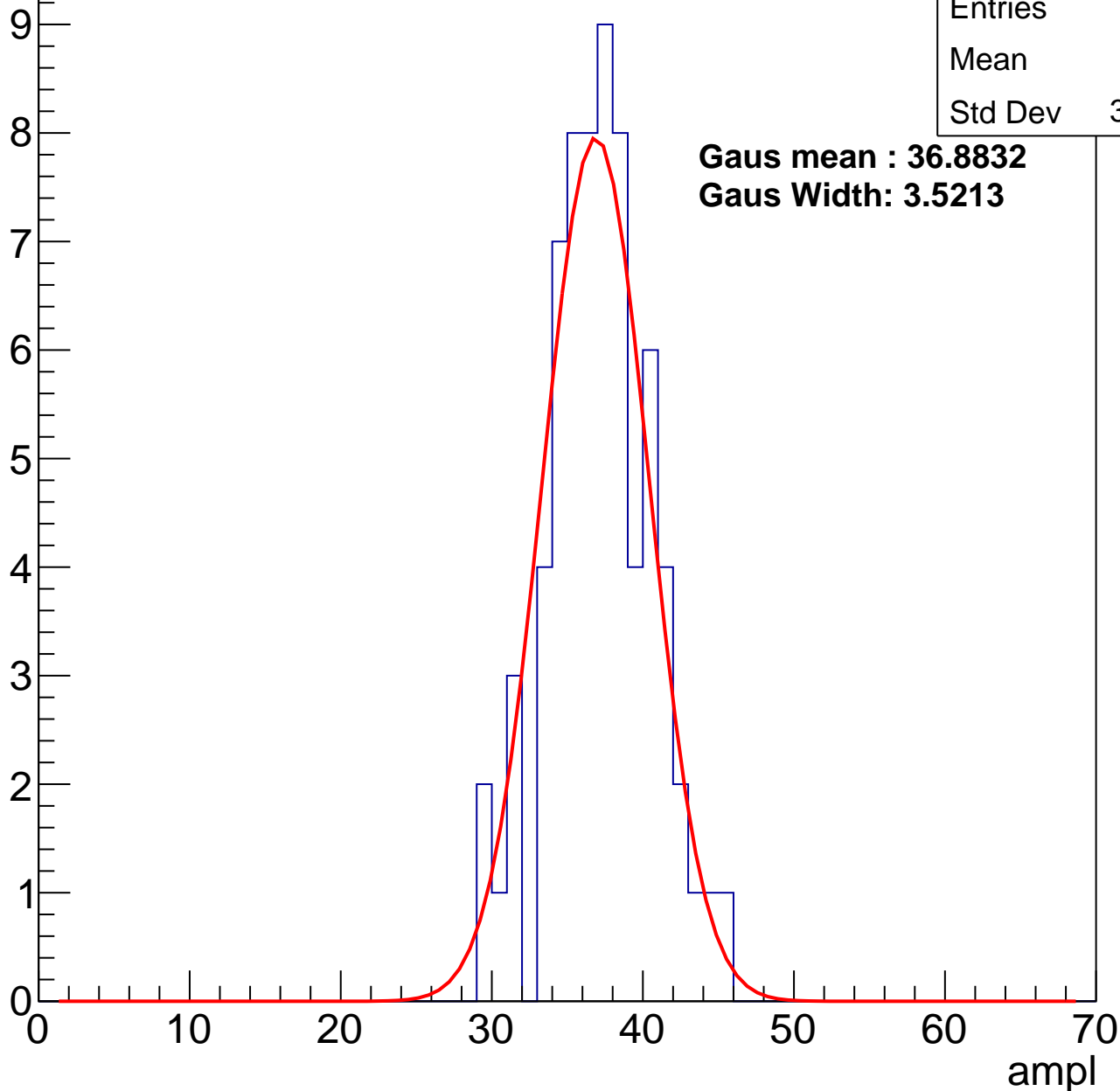
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.7
Std Dev	3.376

**Gaus mean : 36.8832**

**Gaus Width: 3.5213**



# B1L102S, U25-ch98, adc2

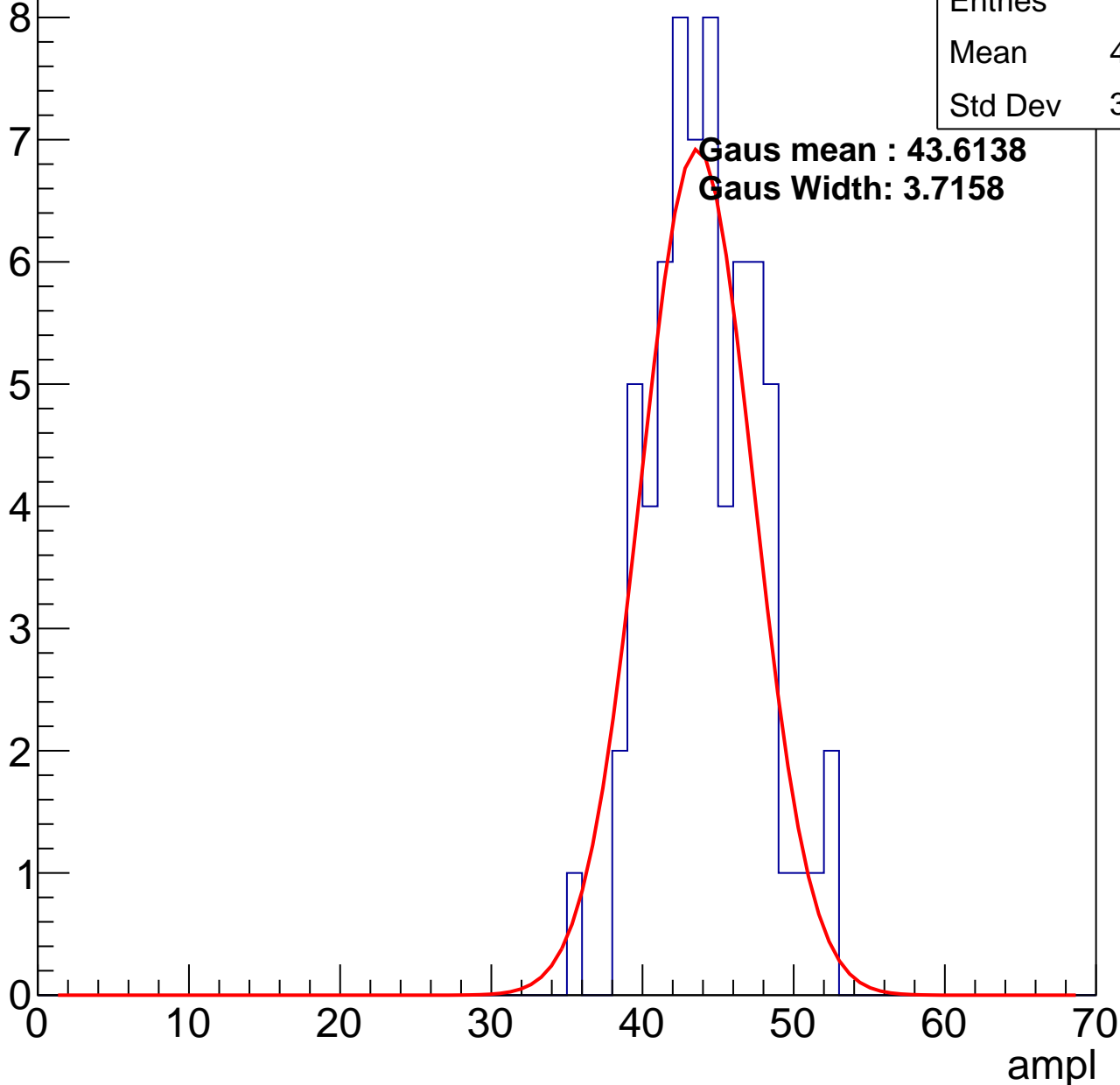
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.78
Std Dev	3.523

**Gaus mean : 43.6138**

**Gaus Width: 3.7158**

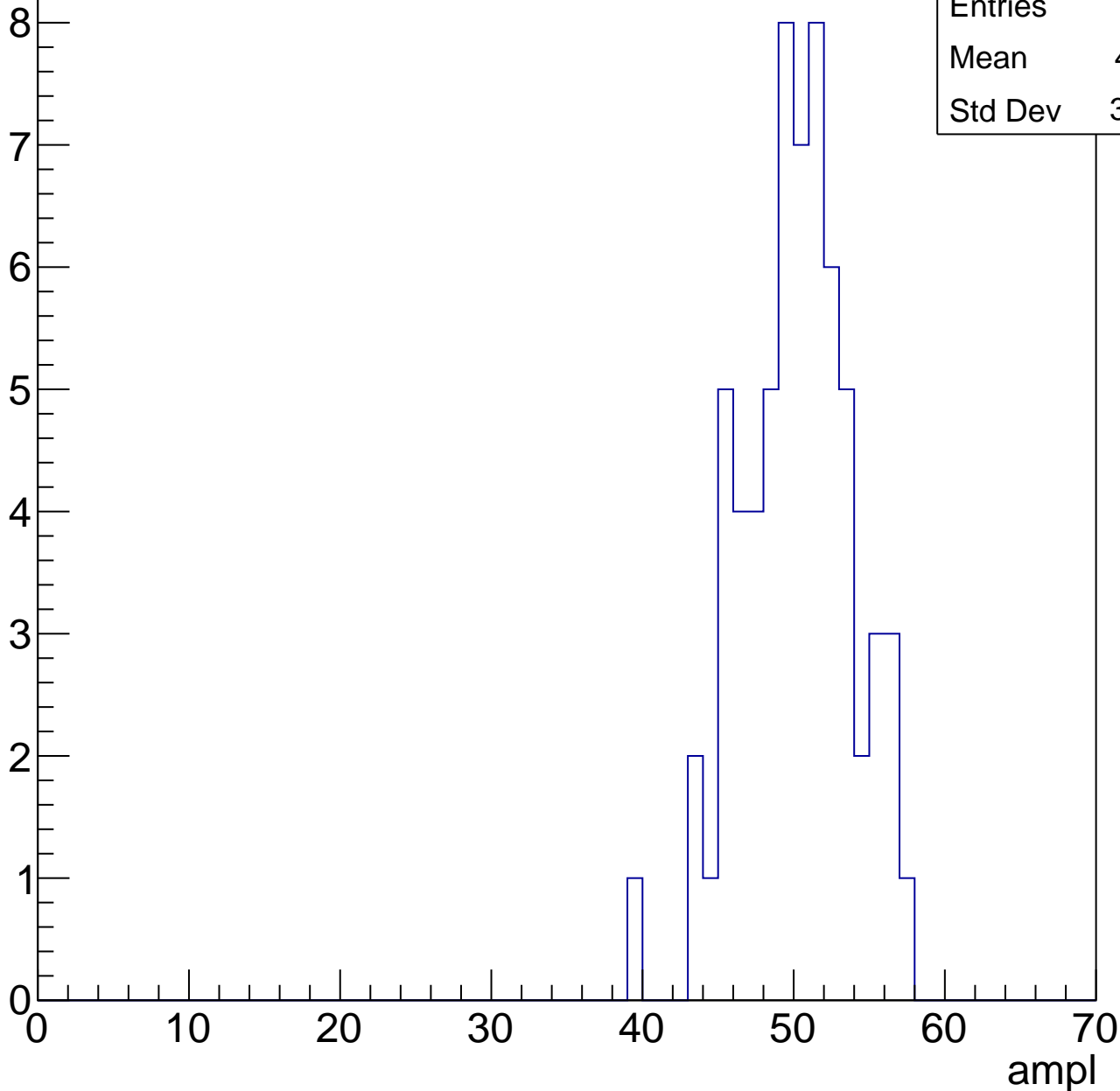


# B1L102S, U25-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.71
Std Dev	3.602

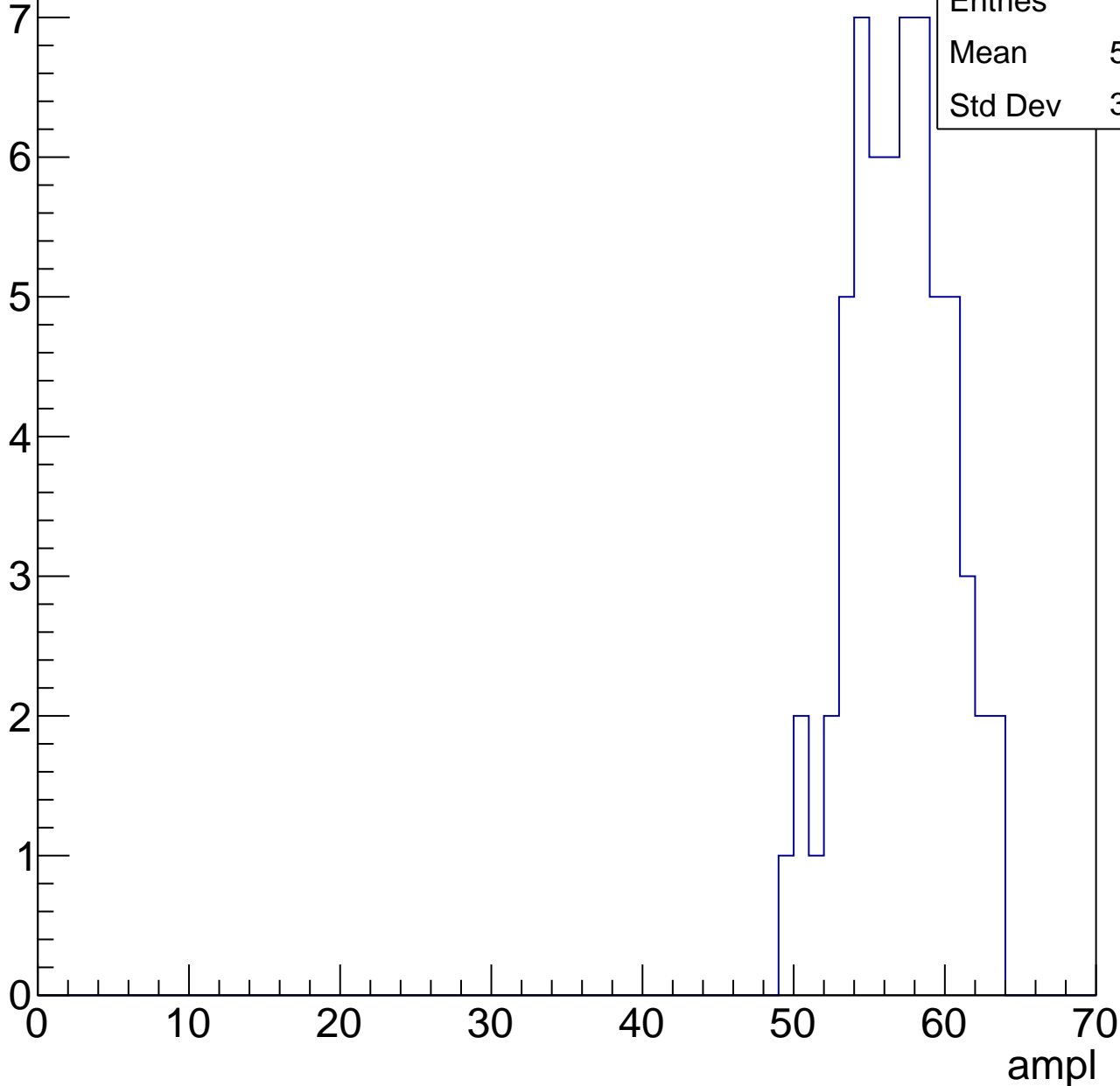


# B1L102S, U25-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	56.49
Std Dev	3.257

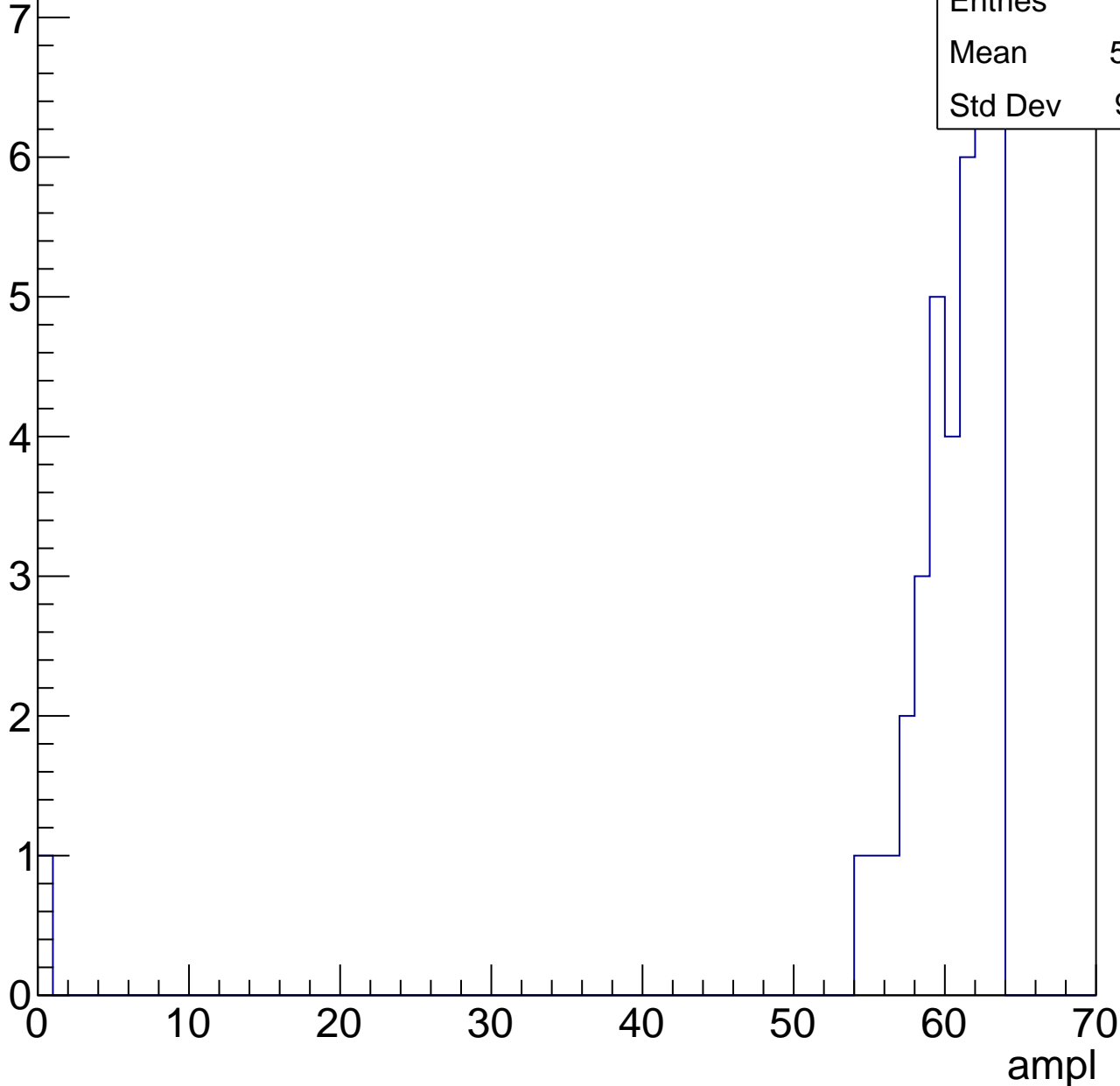


# B1L102S, U25-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.66
Std Dev	9.921



# B1L102S, U25-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

61.5

Std Dev

1.118



# B1L102S, U25-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch99, adc0

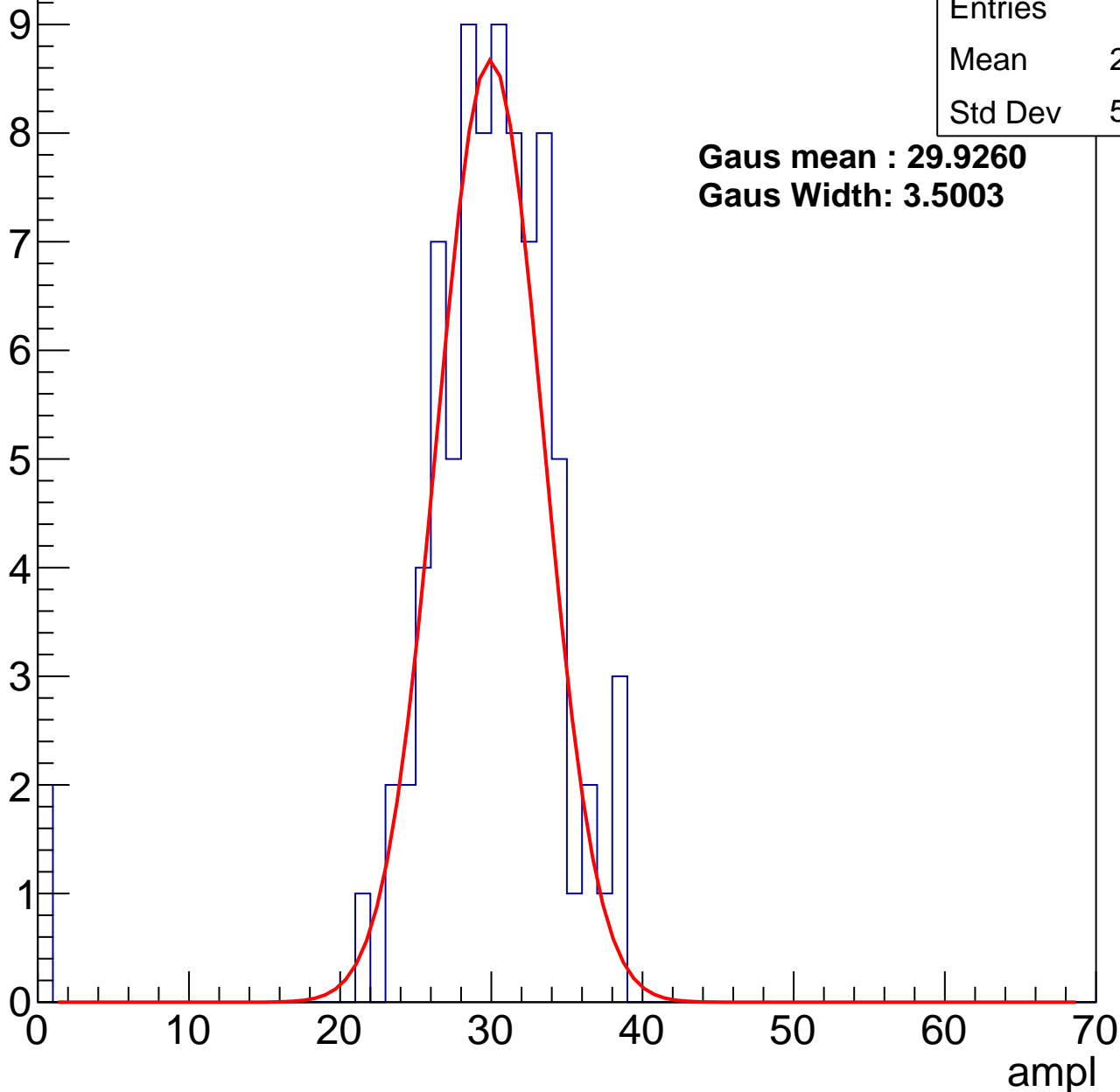
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	29.17
Std Dev	5.794

**Gaus mean : 29.9260**

**Gaus Width: 3.5003**



# B1L102S, U25-ch99, adc1

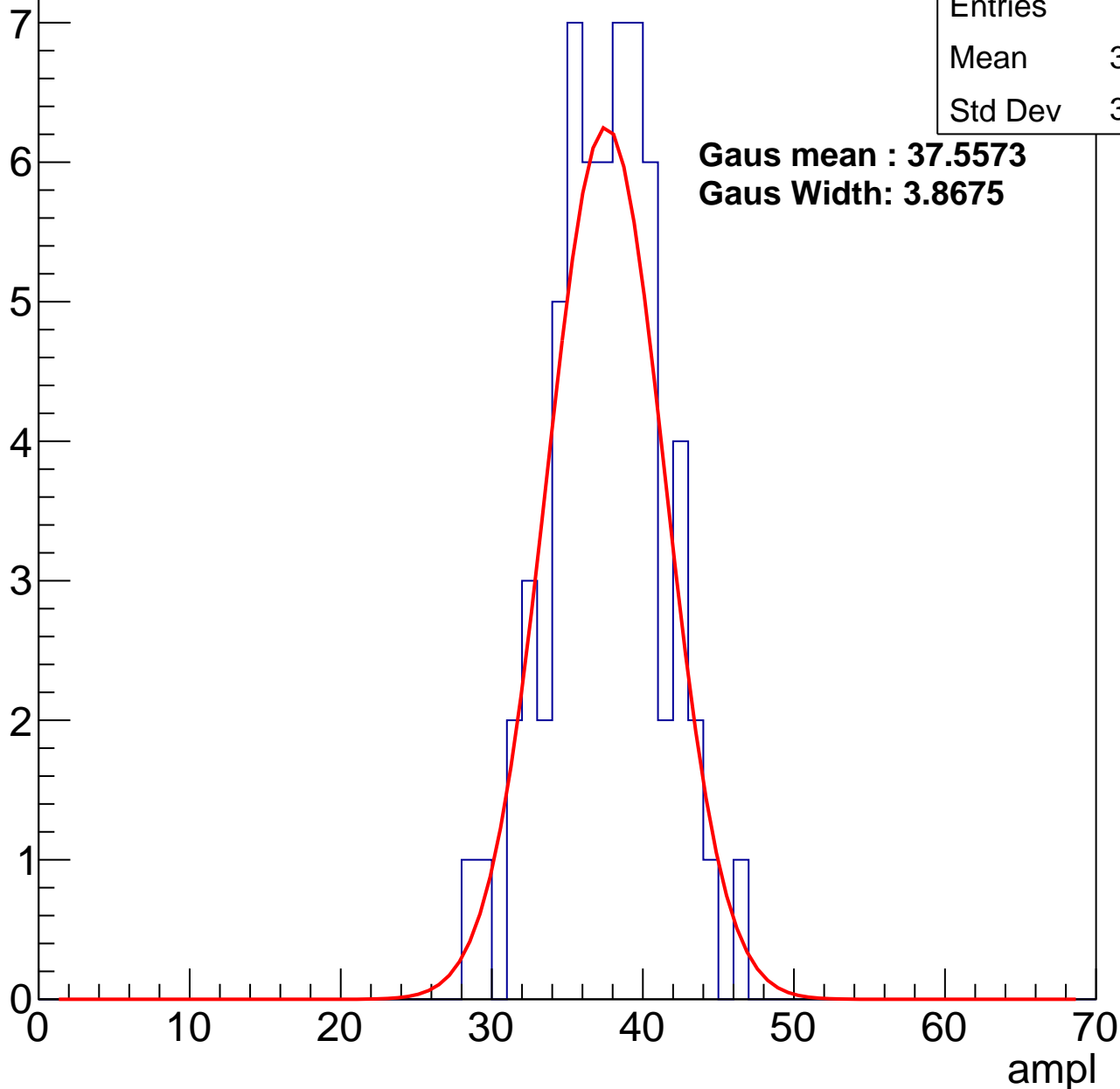
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.13
Std Dev	3.619

**Gaus mean : 37.5573**

**Gaus Width: 3.8675**



# B1L102S, U25-ch99, adc2

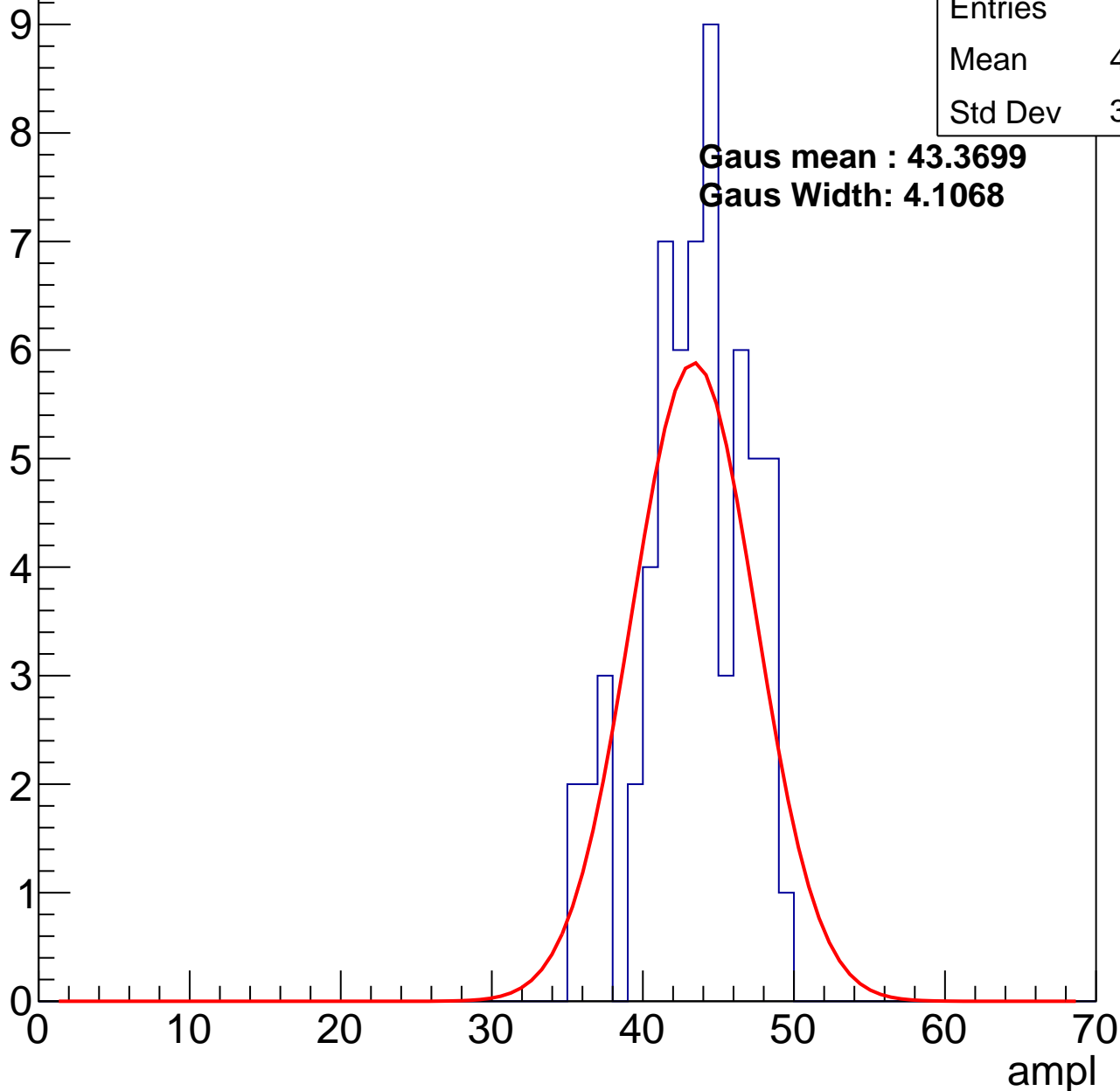
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.94
Std Dev	3.482

**Gaus mean : 43.3699**

**Gaus Width: 4.1068**

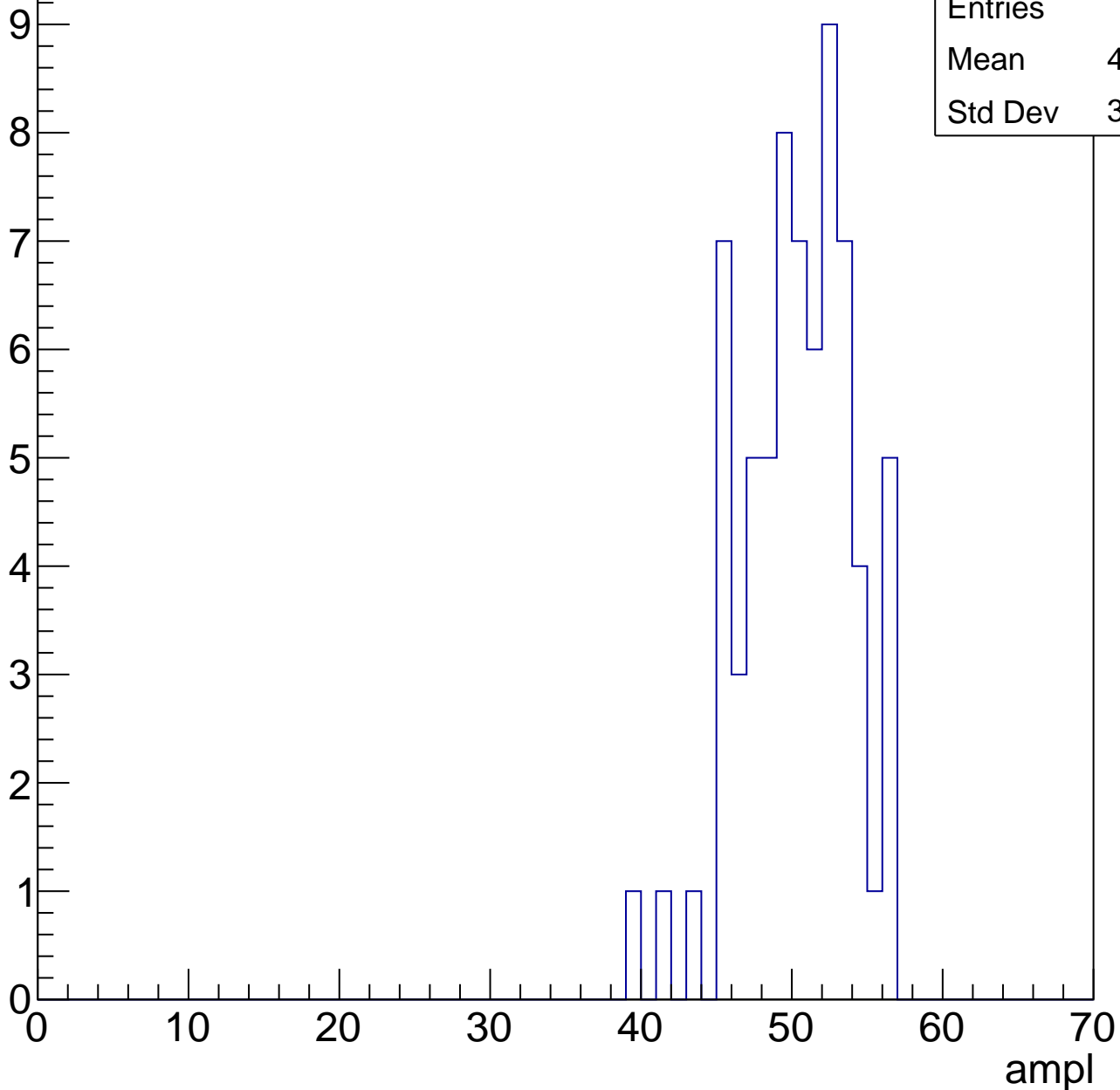


# B1L102S, U25-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.84
Std Dev	3.624

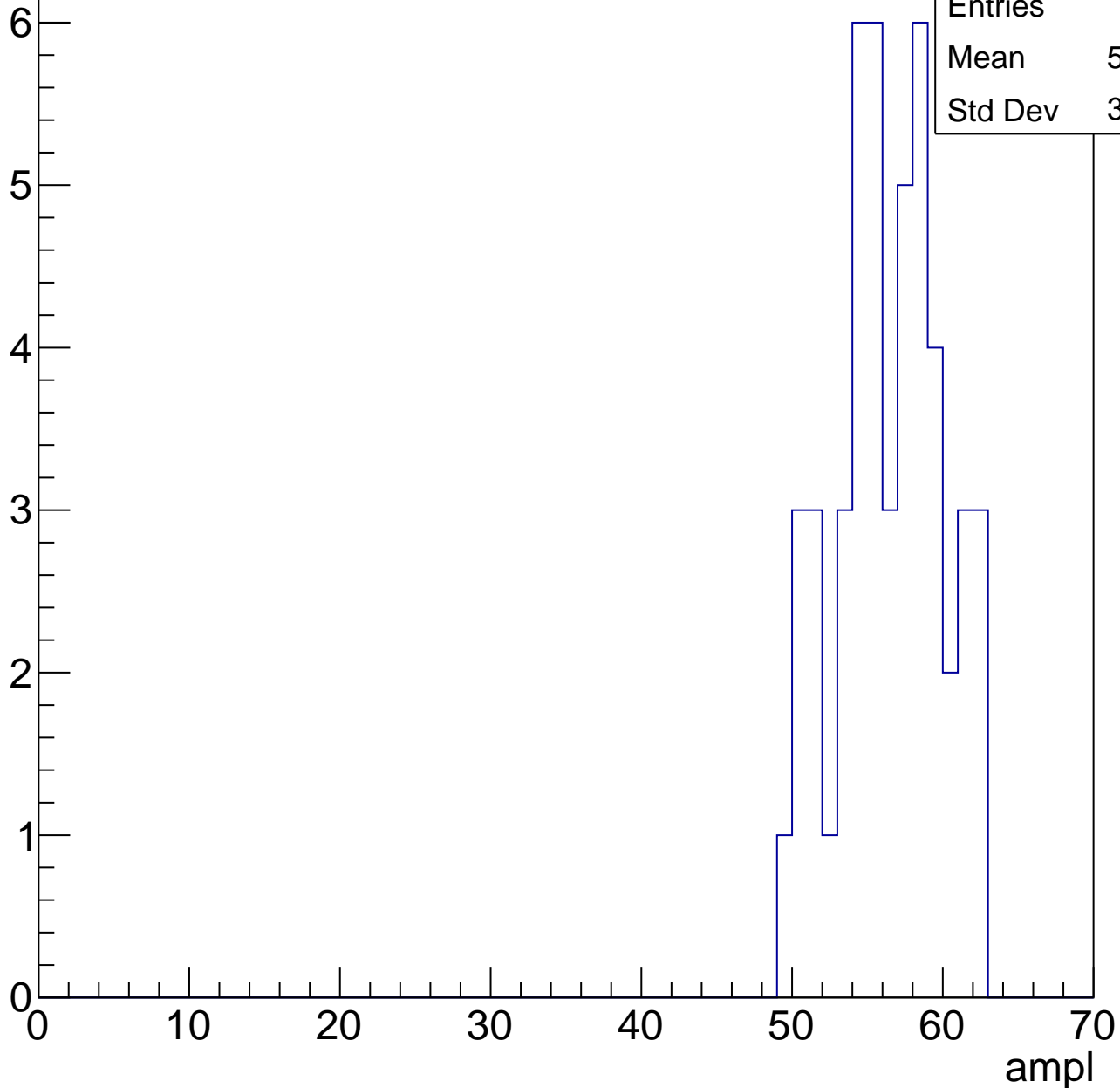


# B1L102S, U25-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.98
Std Dev	3.455

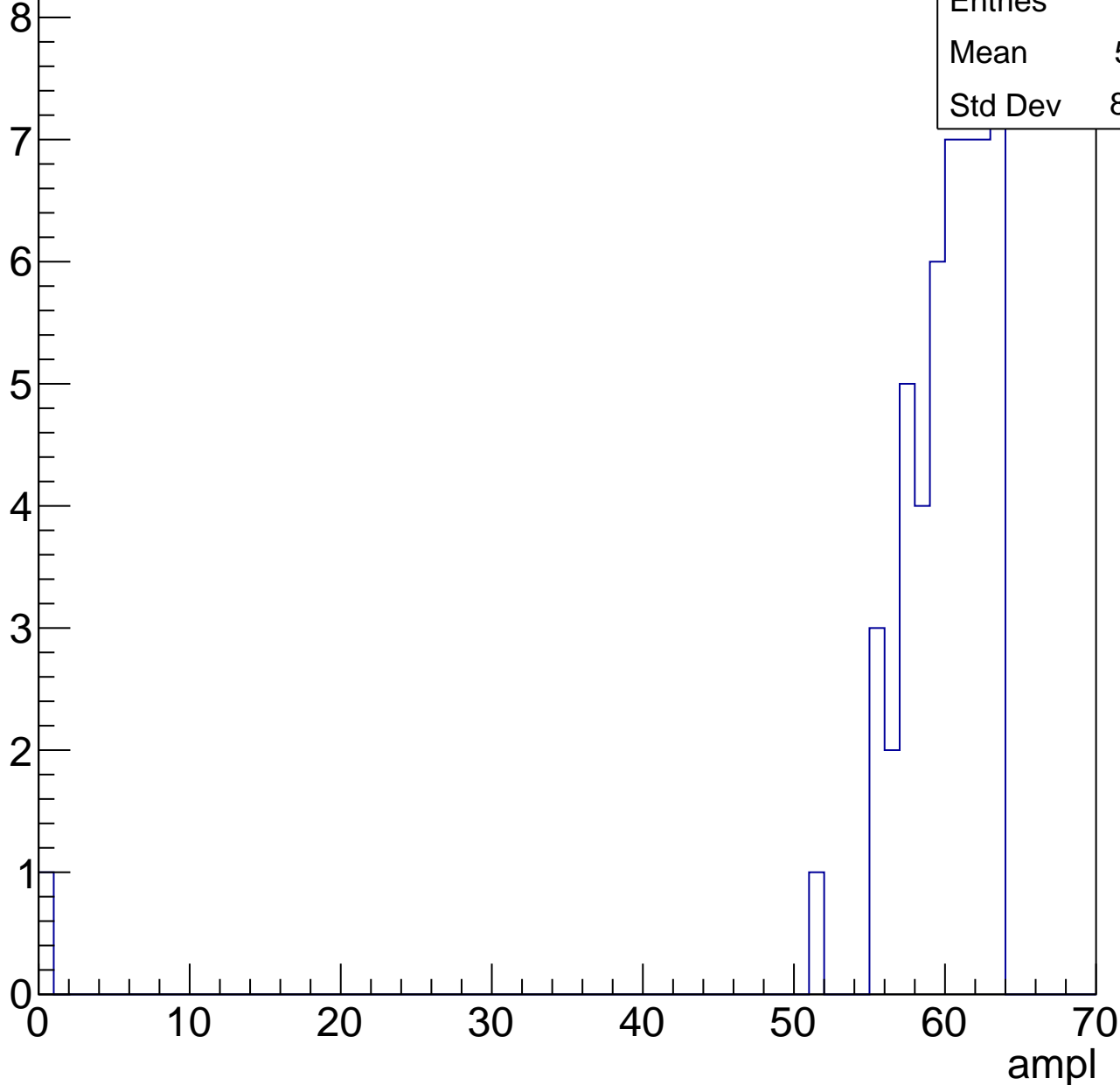


# B1L102S, U25-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.51
Std Dev	8.687



# B1L102S, U25-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch100, adc0

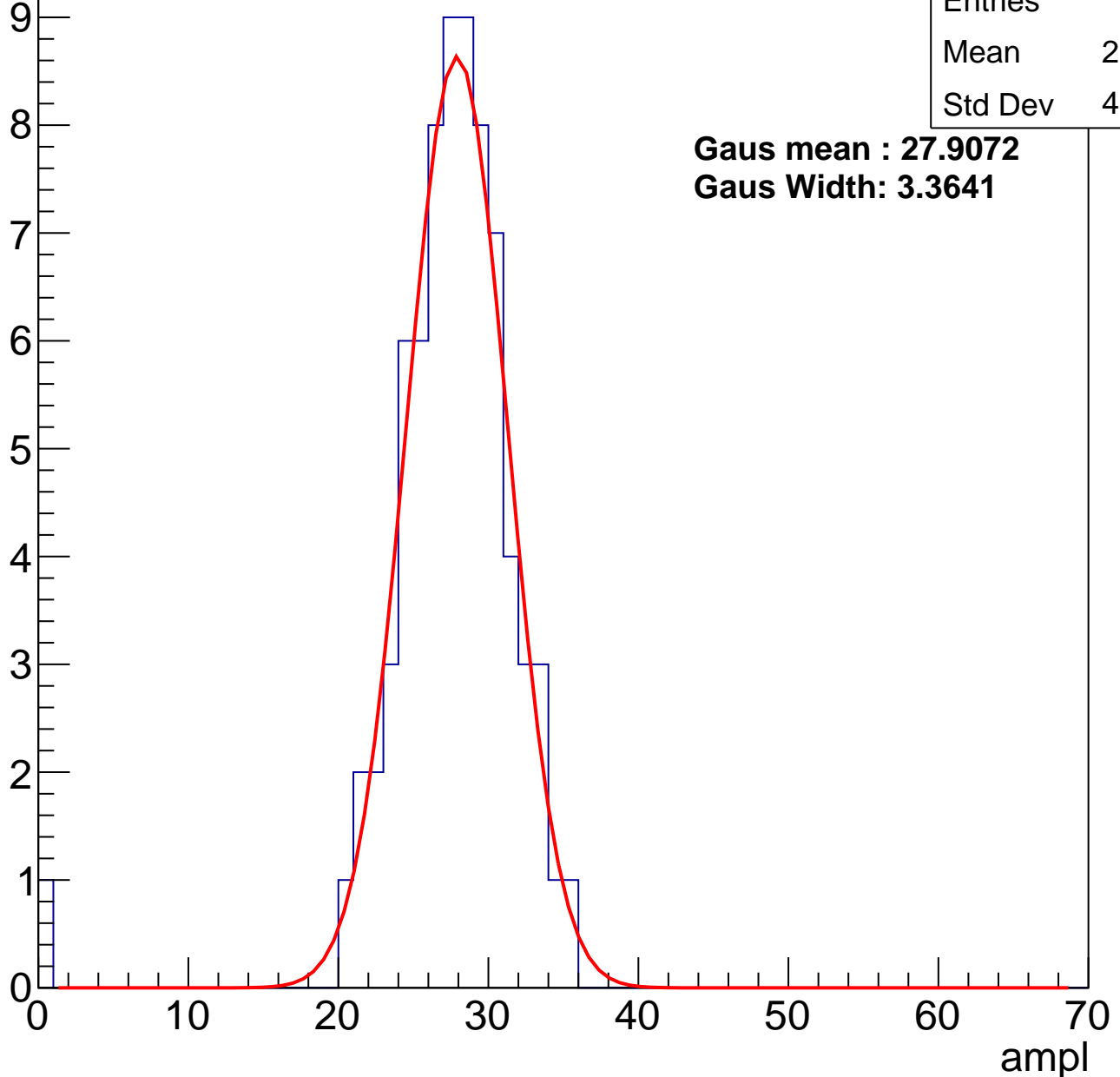
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.05
Std Dev	4.499

**Gaus mean : 27.9072**

**Gaus Width: 3.3641**



# B1L102S, U25-ch100, adc1

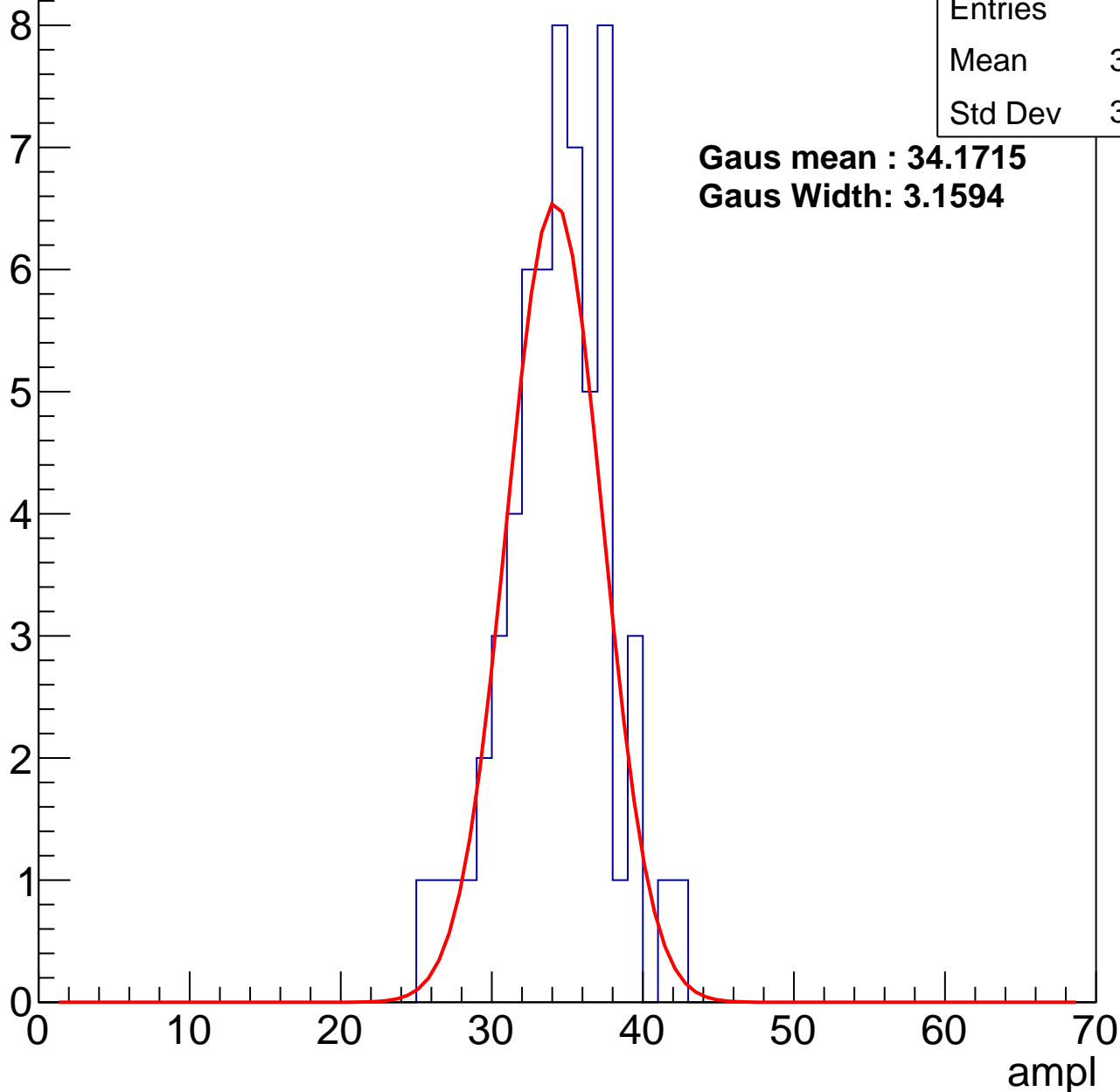
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	33.88
Std Dev	3.435

**Gaus mean : 34.1715**

**Gaus Width: 3.1594**



# B1L102S, U25-ch100, adc2

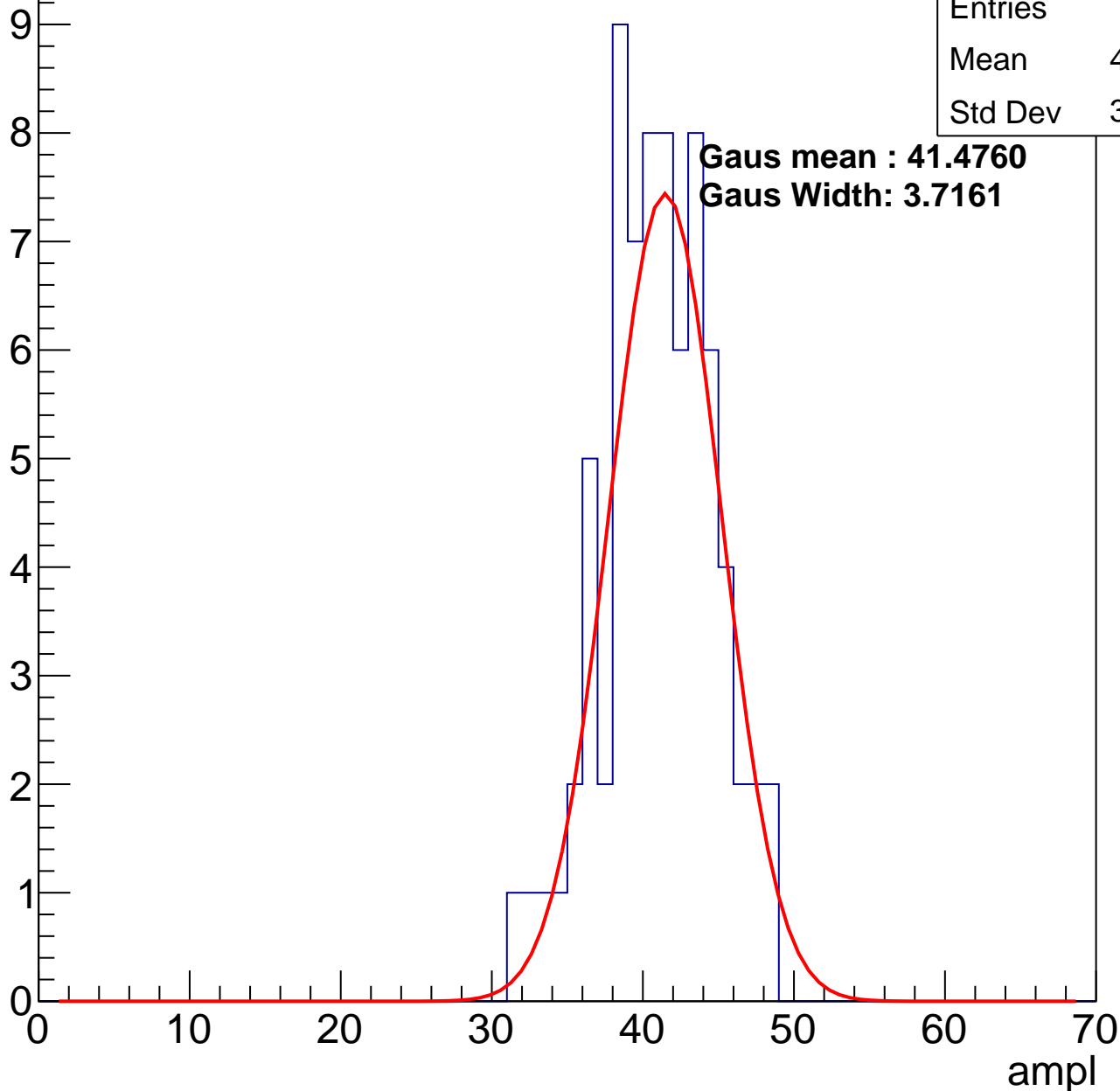
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	40.52
Std Dev	3.649

**Gaus mean : 41.4760**

**Gaus Width: 3.7161**

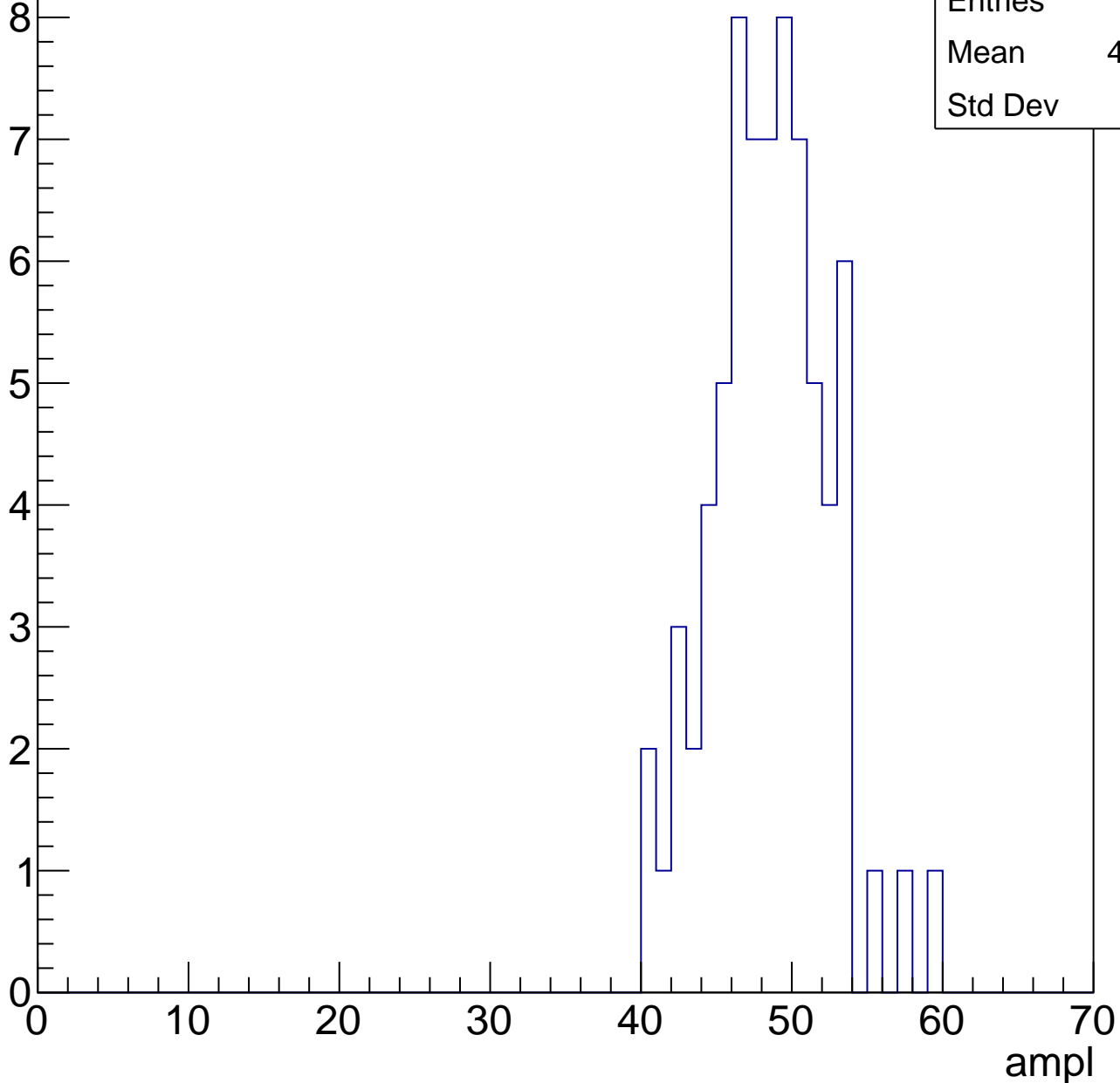


# B1L102S, U25-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.07
Std Dev	3.78

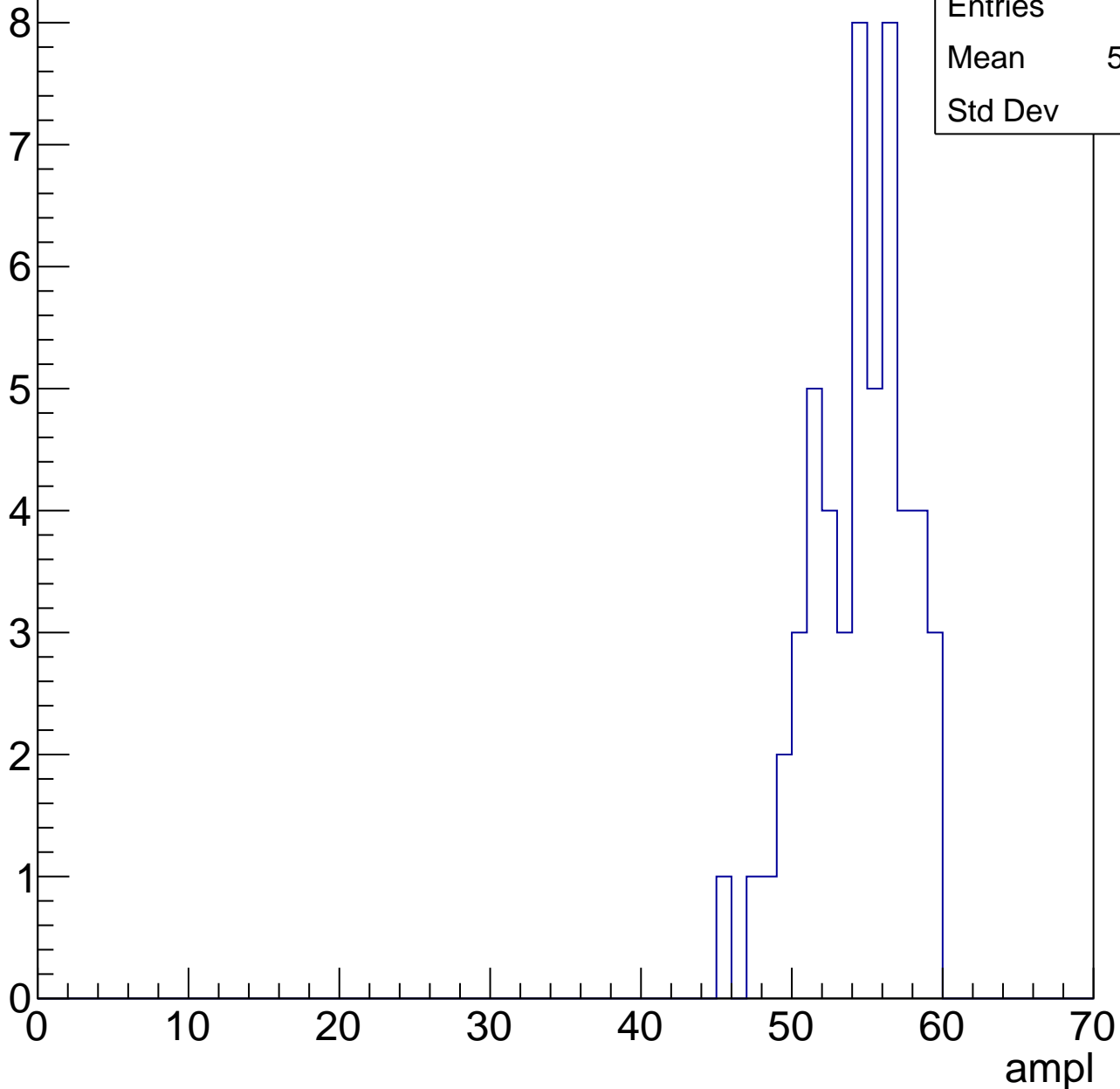


# B1L102S, U25-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.88
Std Dev	3.22

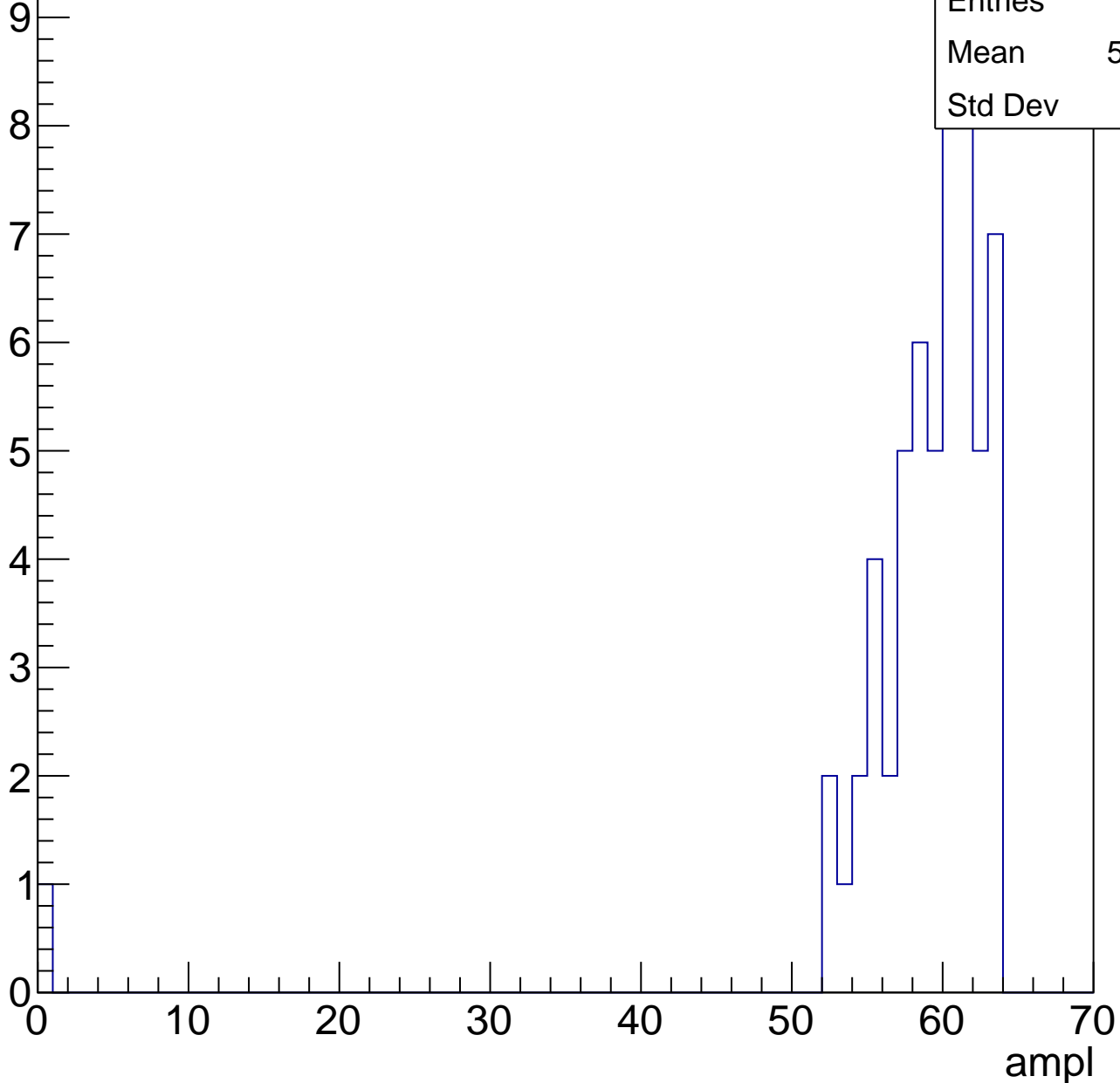


# B1L102S, U25-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

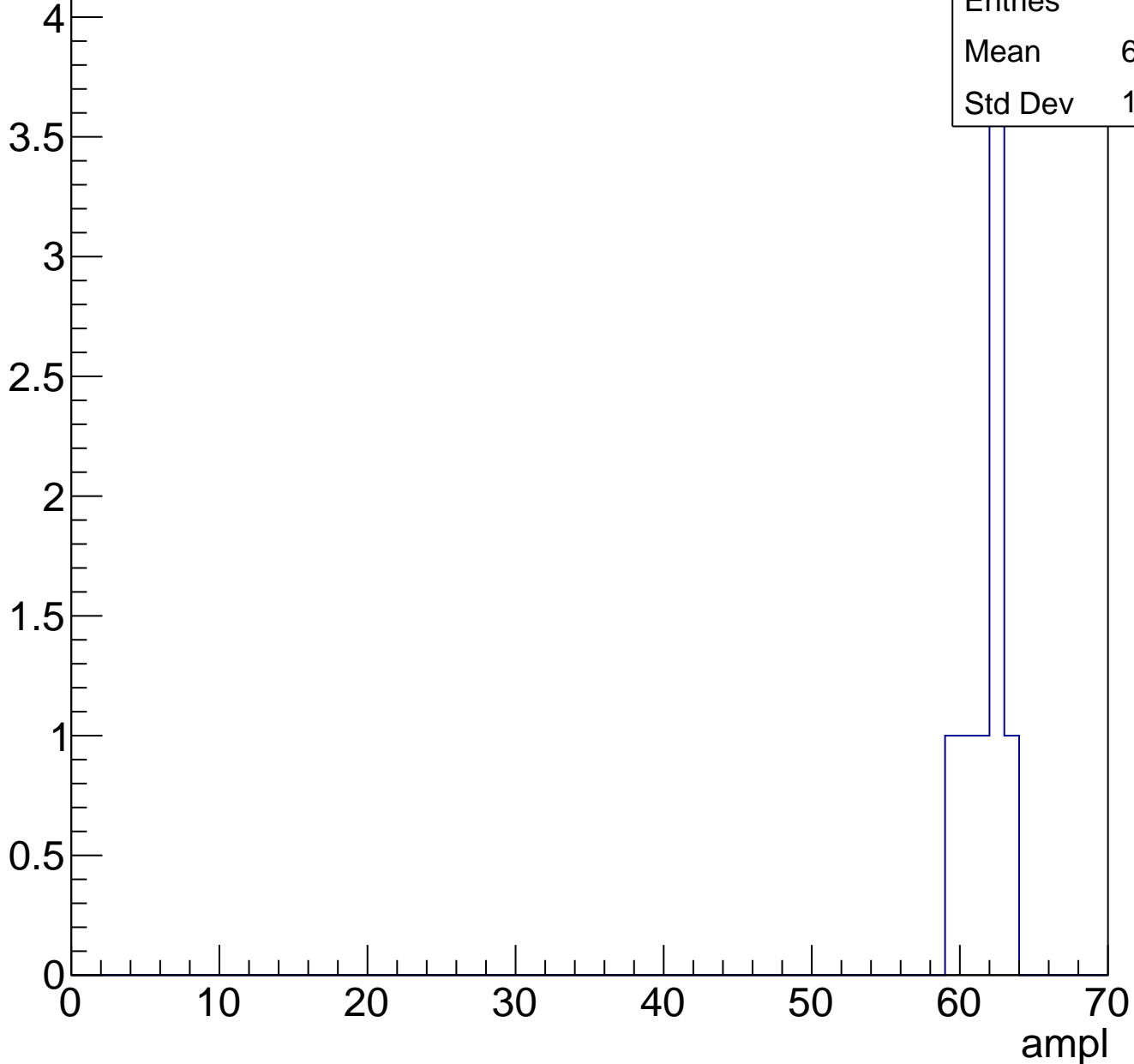
Entries	58
Mean	58.02
Std Dev	8.22



# B1L102S, U25-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch101, adc0

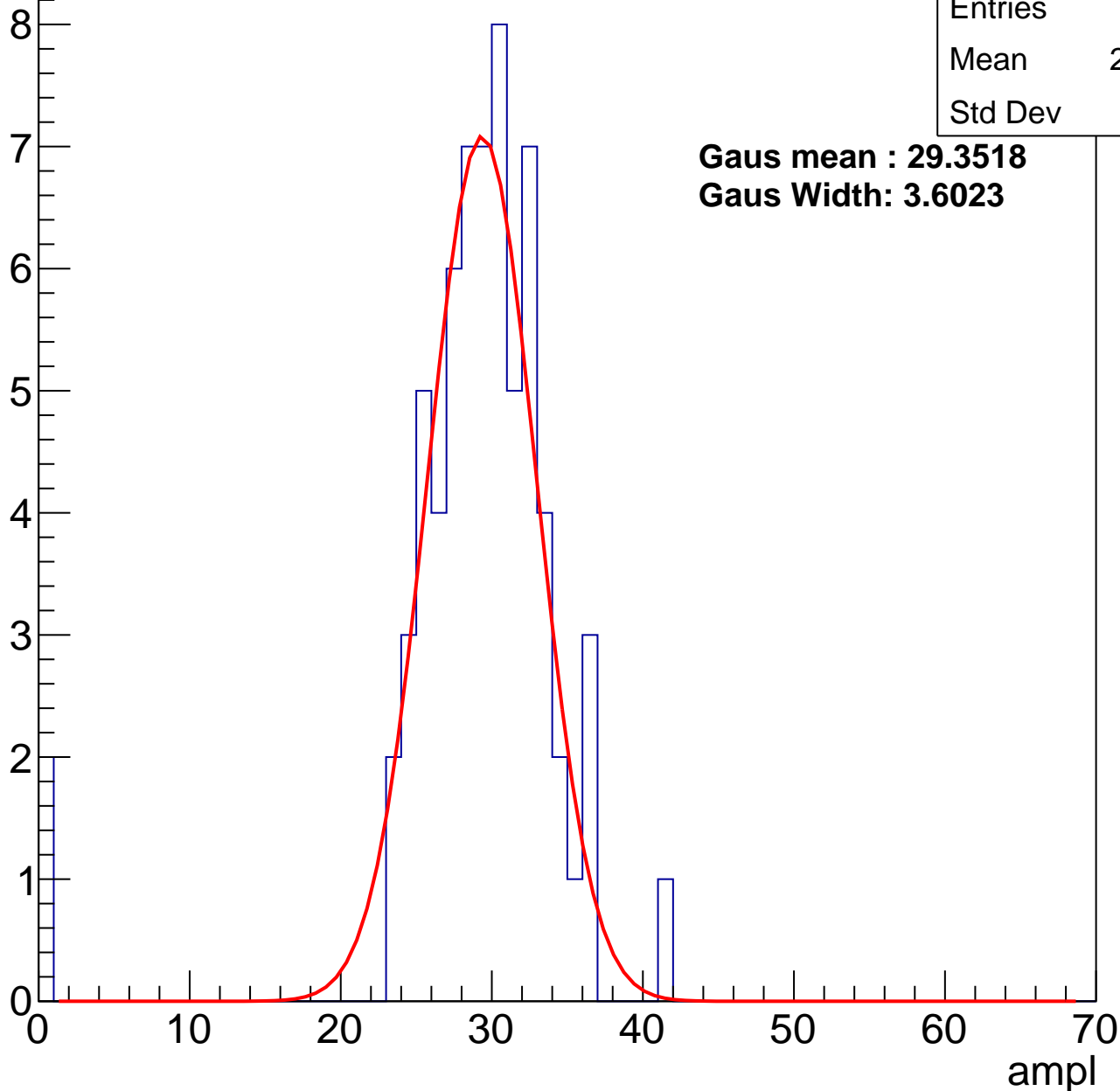
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.52
Std Dev	6.1

**Gaus mean : 29.3518**

**Gaus Width: 3.6023**



# B1L102S, U25-ch101, adc1

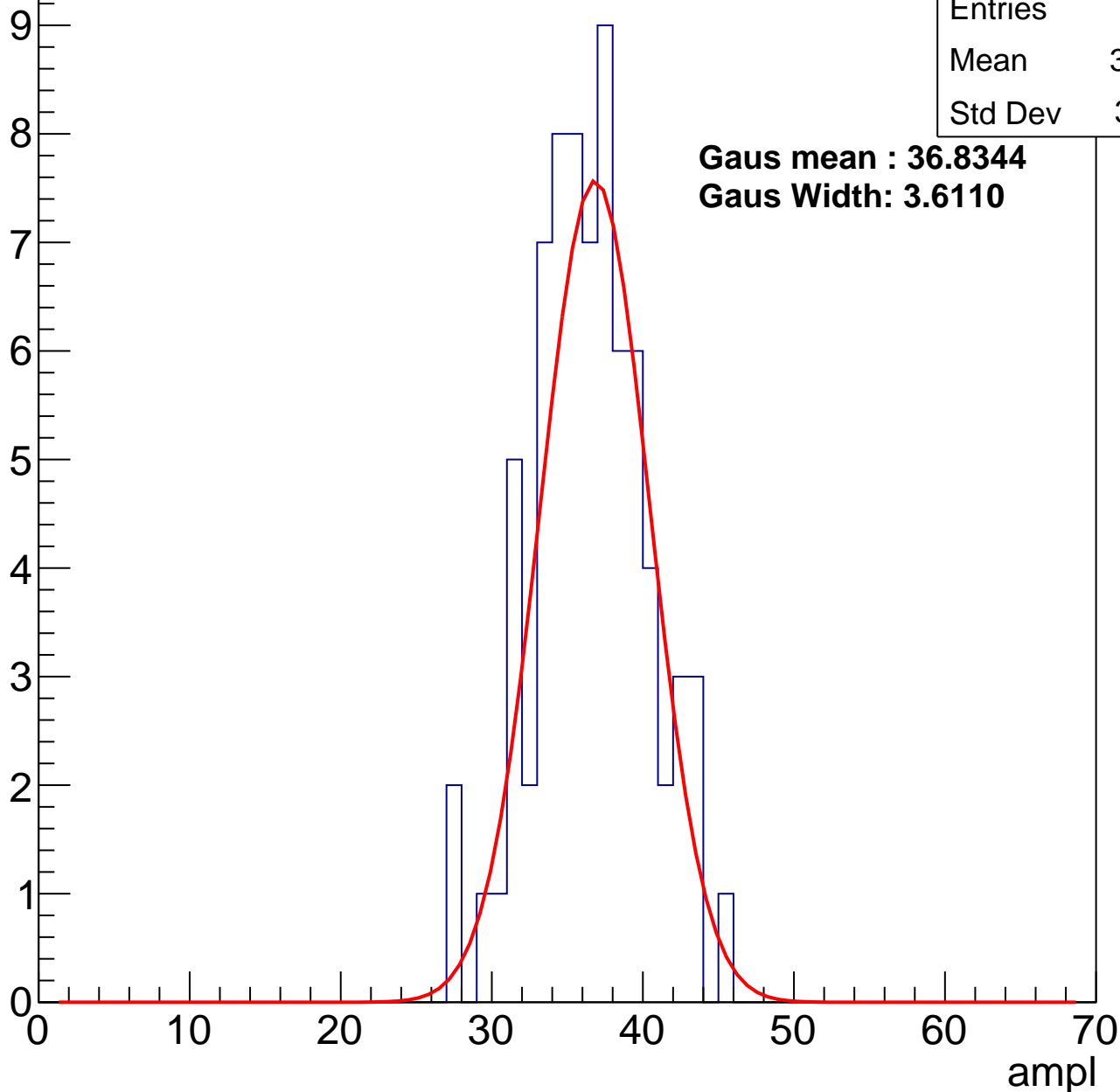
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.05
Std Dev	3.731

**Gaus mean : 36.8344**

**Gaus Width: 3.6110**



# B1L102S, U25-ch101, adc2

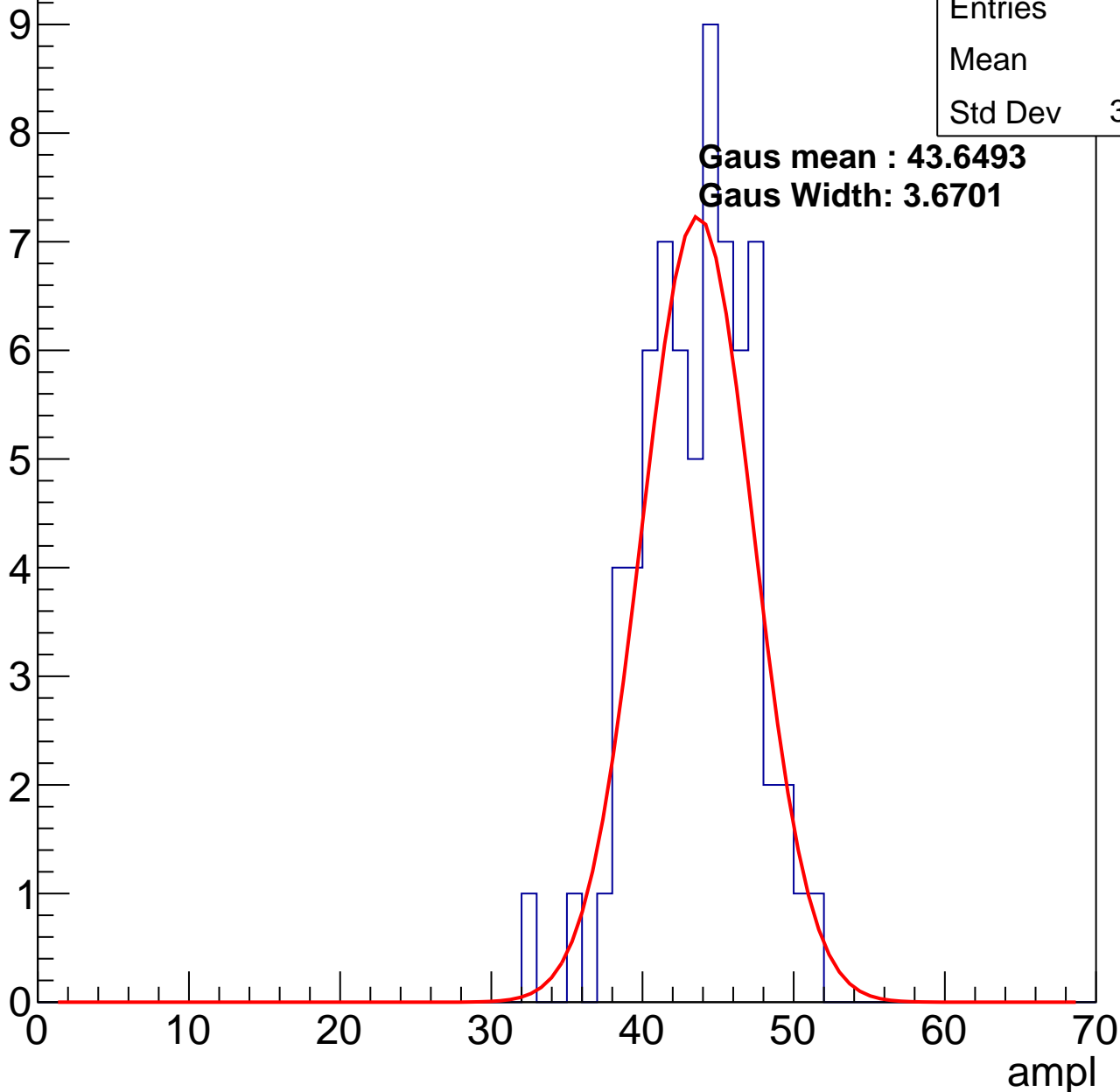
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.1
Std Dev	3.606

**Gaus mean : 43.6493**

**Gaus Width: 3.6701**

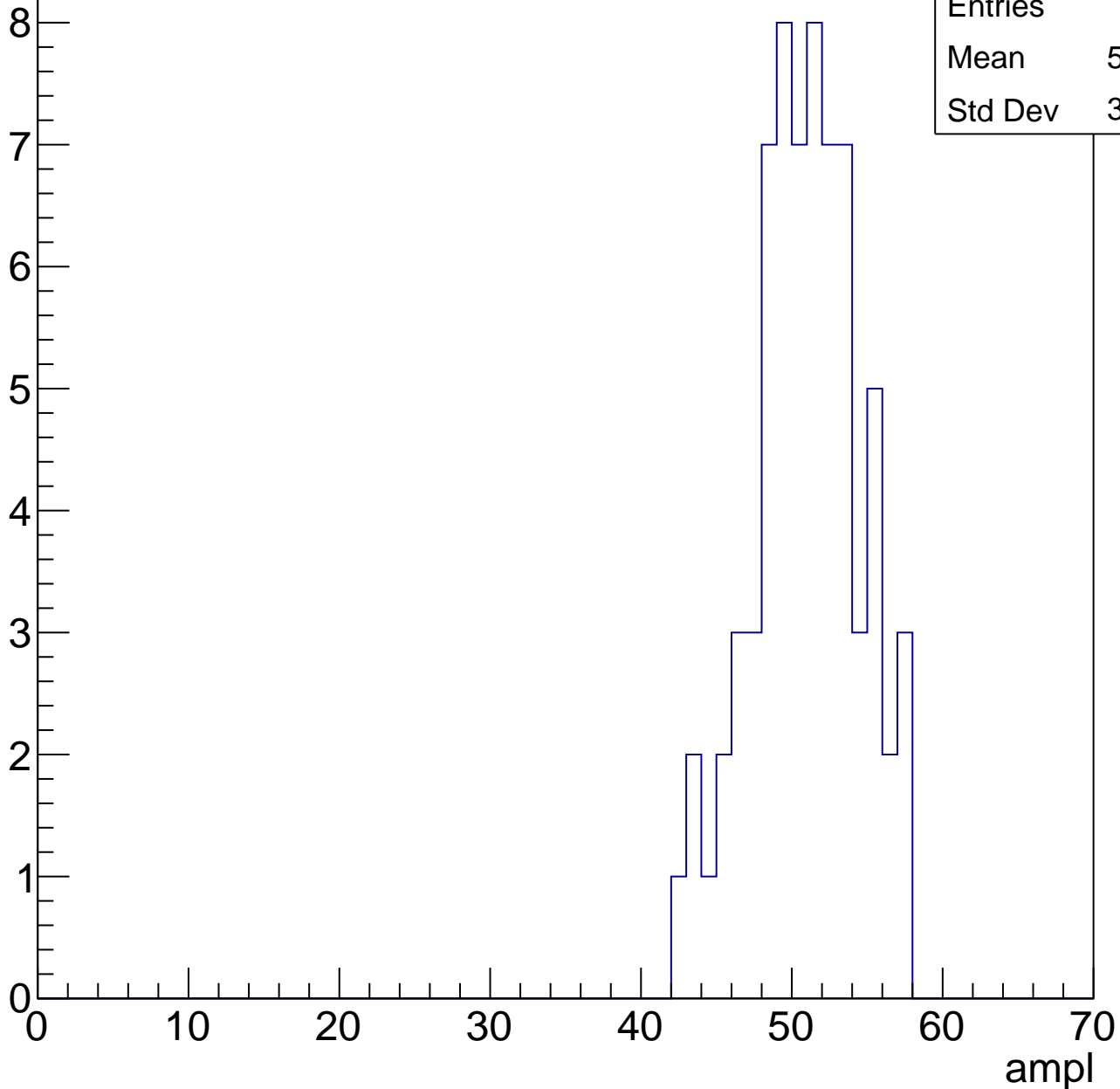


# B1L102S, U25-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	50.46
Std Dev	3.479

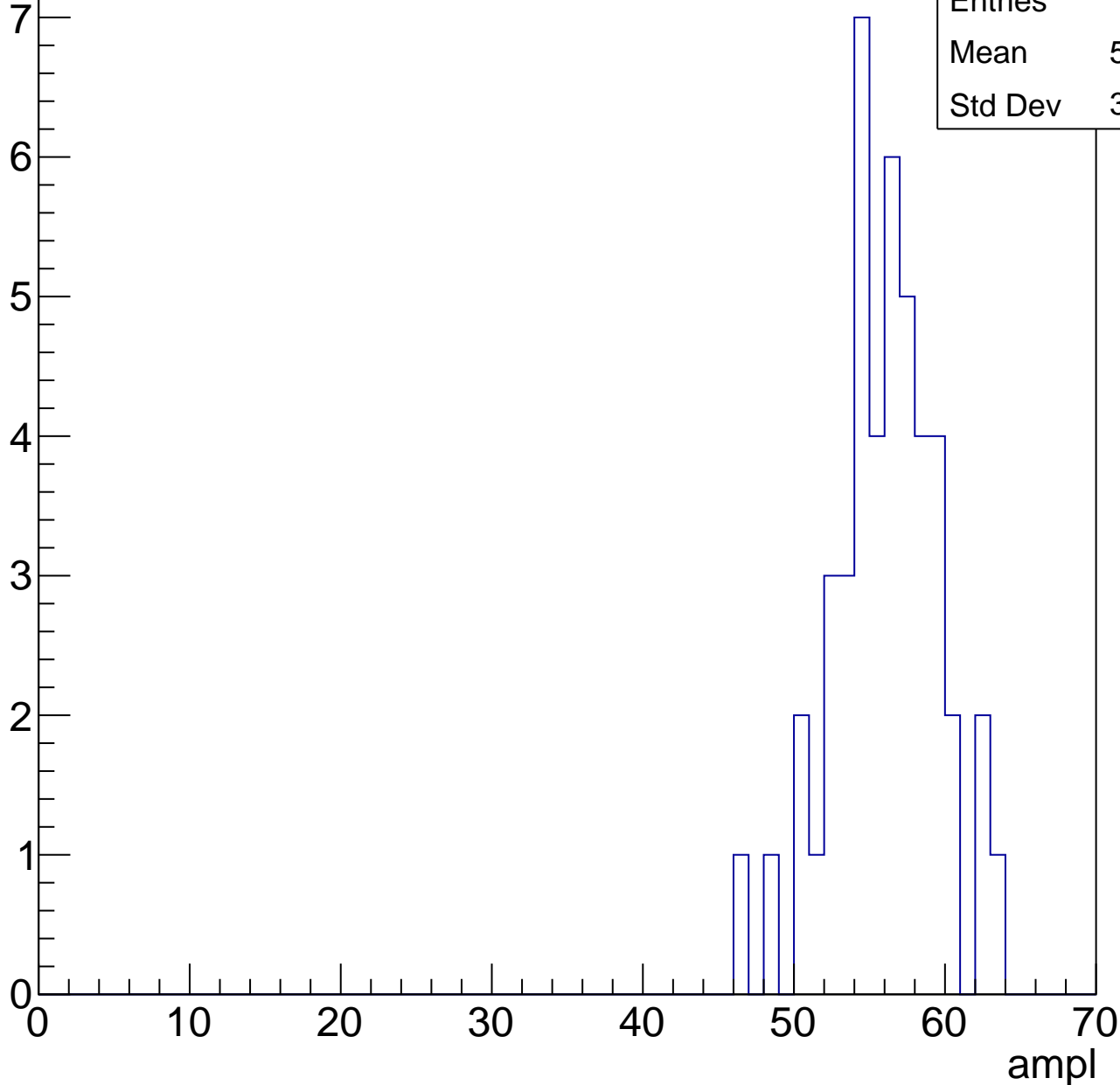


# B1L102S, U25-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.52
Std Dev	3.512

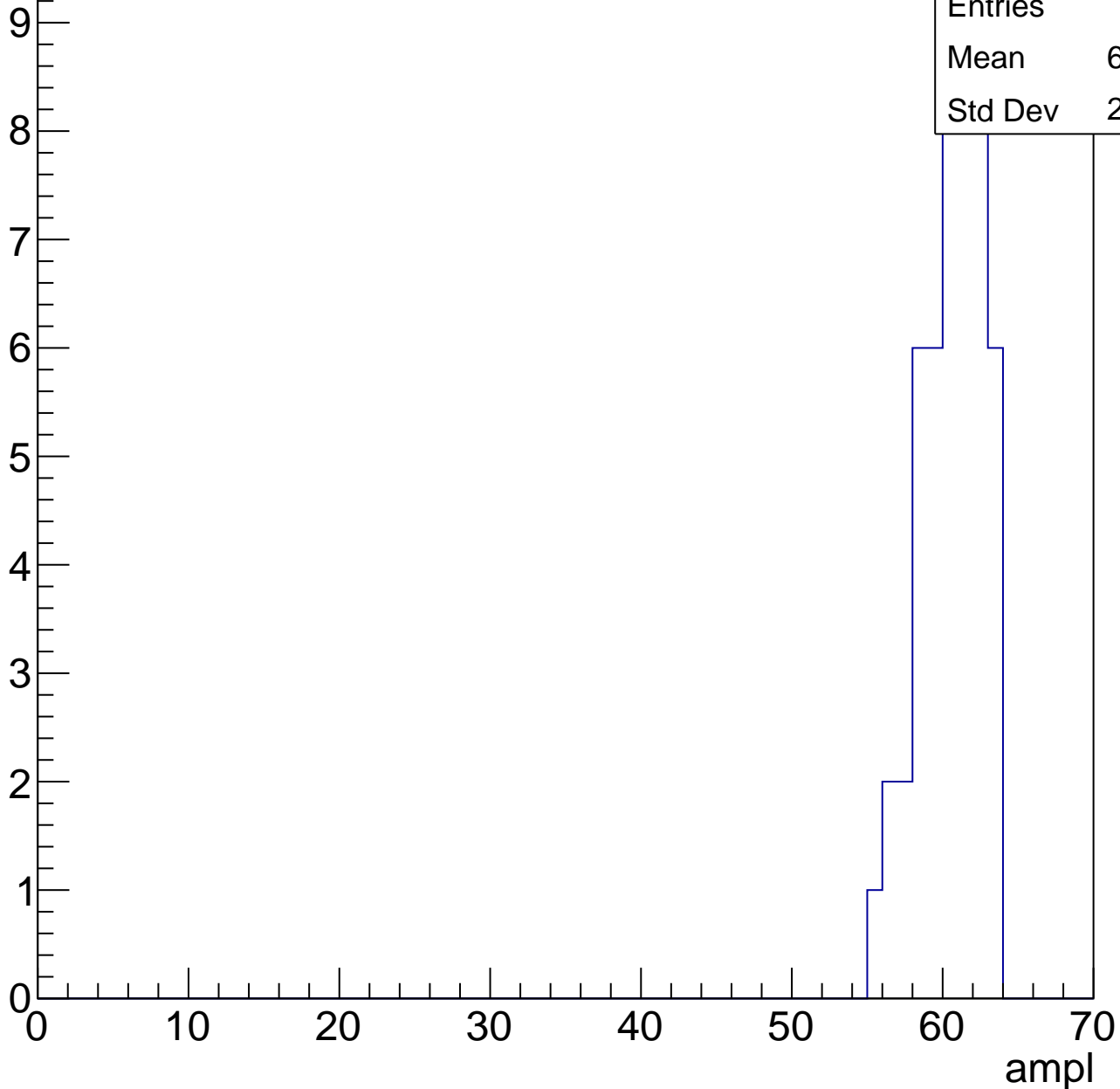


# B1L102S, U25-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

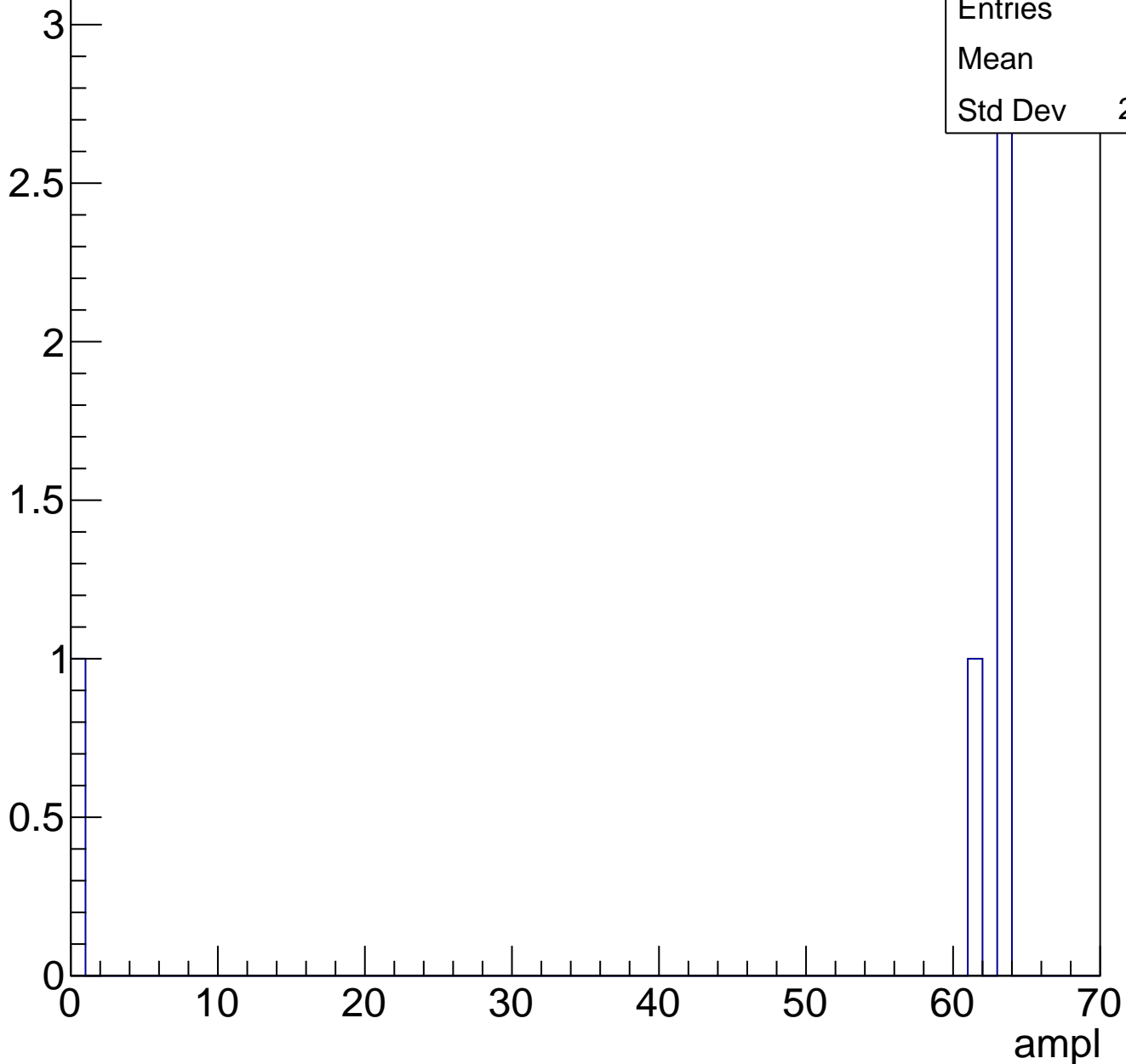
Entries	48
Mean	60.12
Std Dev	2.037



# B1L102S, U25-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch102, adc0

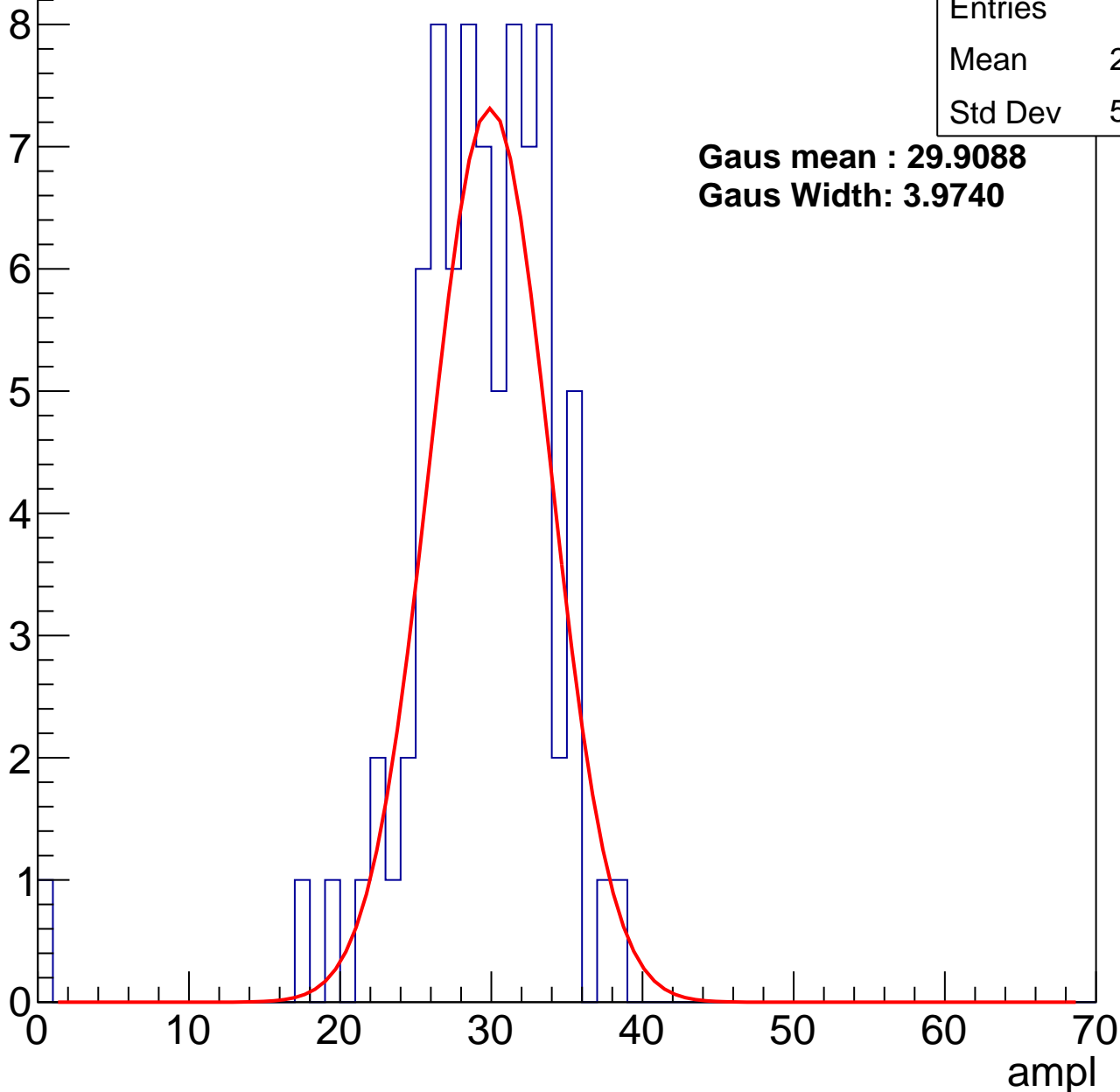
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.68
Std Dev	5.125

**Gaus mean : 29.9088**

**Gaus Width: 3.9740**



# B1L102S, U25-ch102, adc1

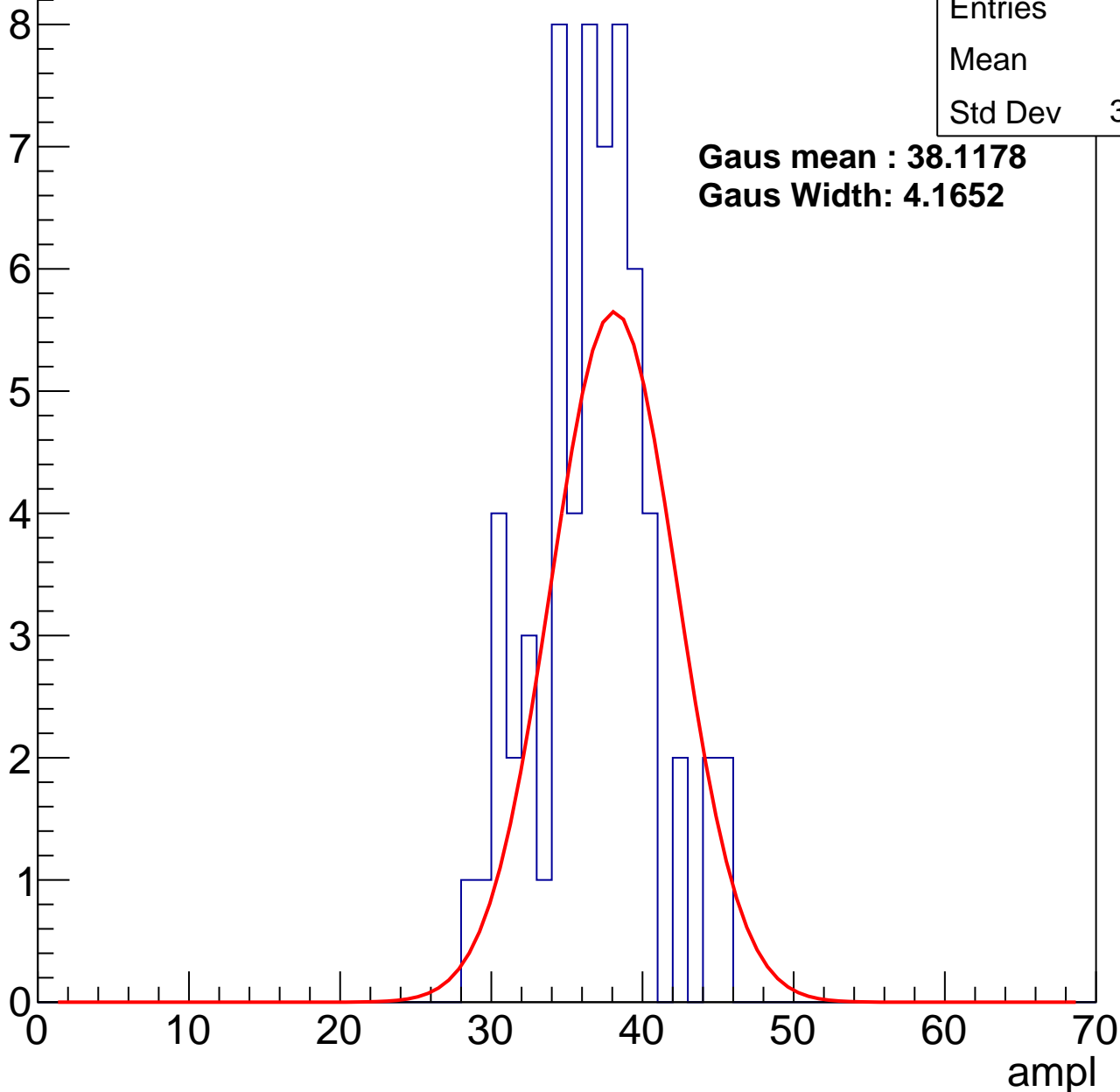
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.3
Std Dev	3.807

**Gaus mean : 38.1178**

**Gaus Width: 4.1652**



# B1L102S, U25-ch102, adc2

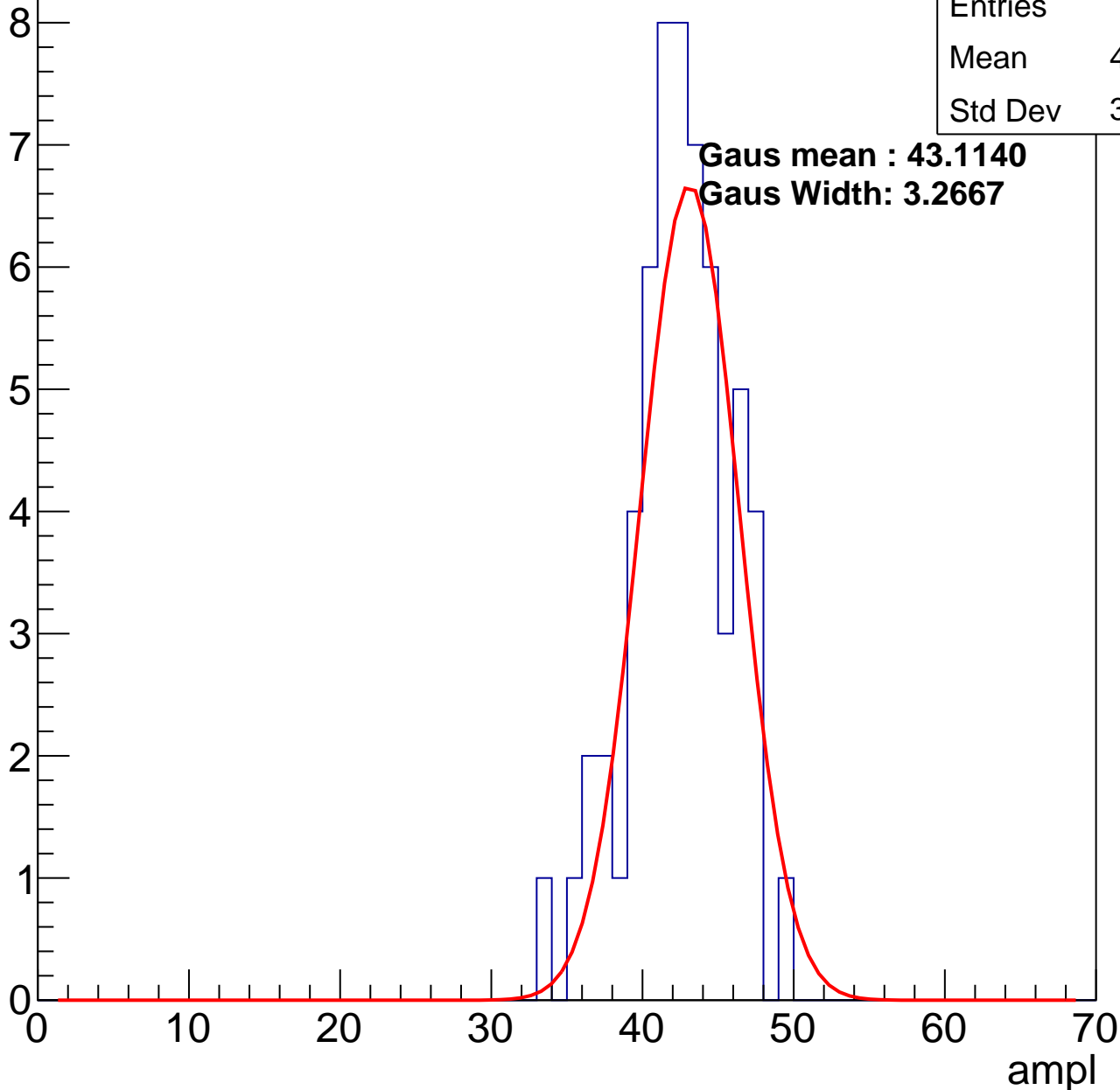
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.02
Std Dev	3.249

**Gaus mean : 43.1140**

**Gaus Width: 3.2667**

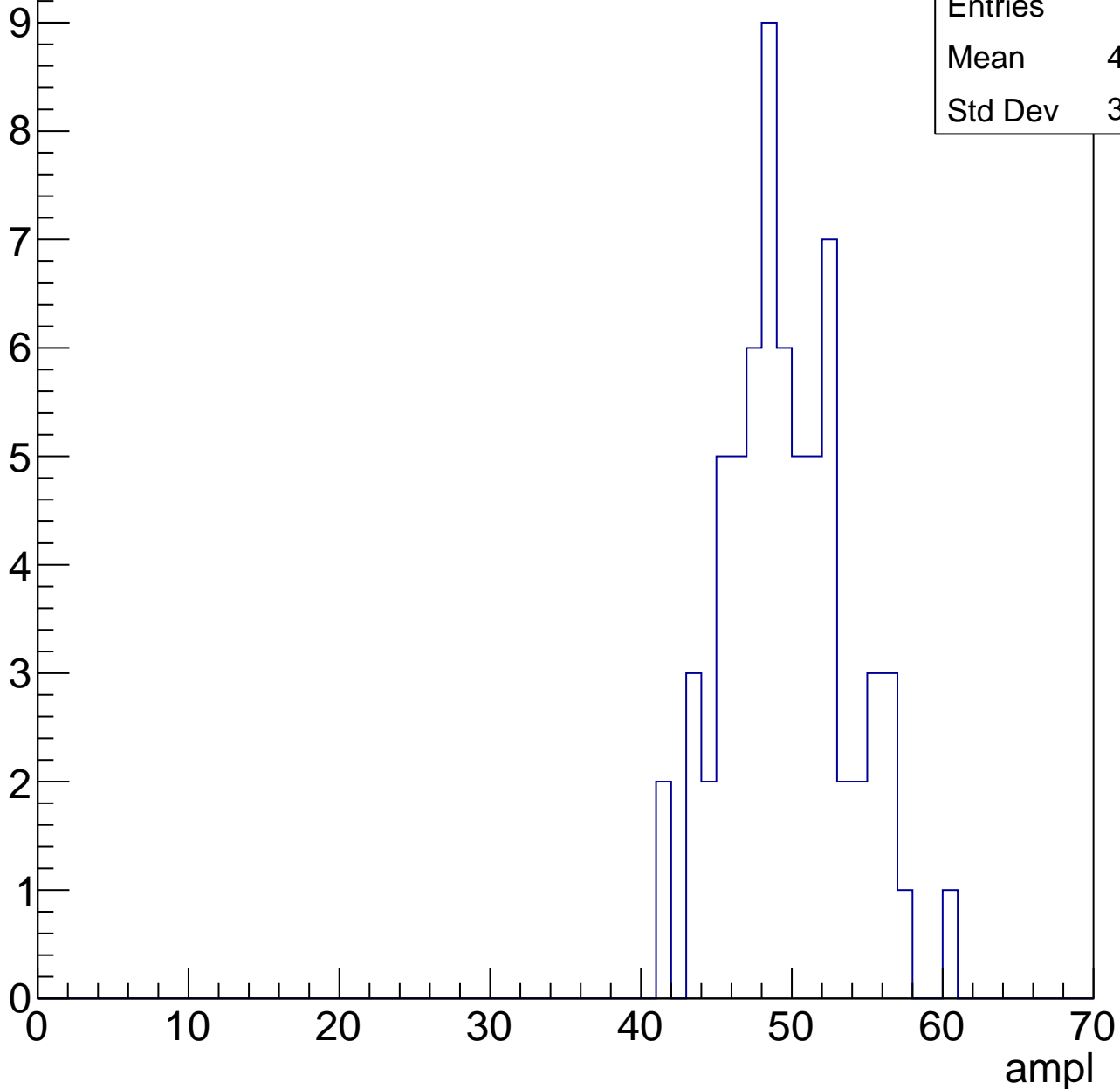


# B1L102S, U25-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.18
Std Dev	3.962

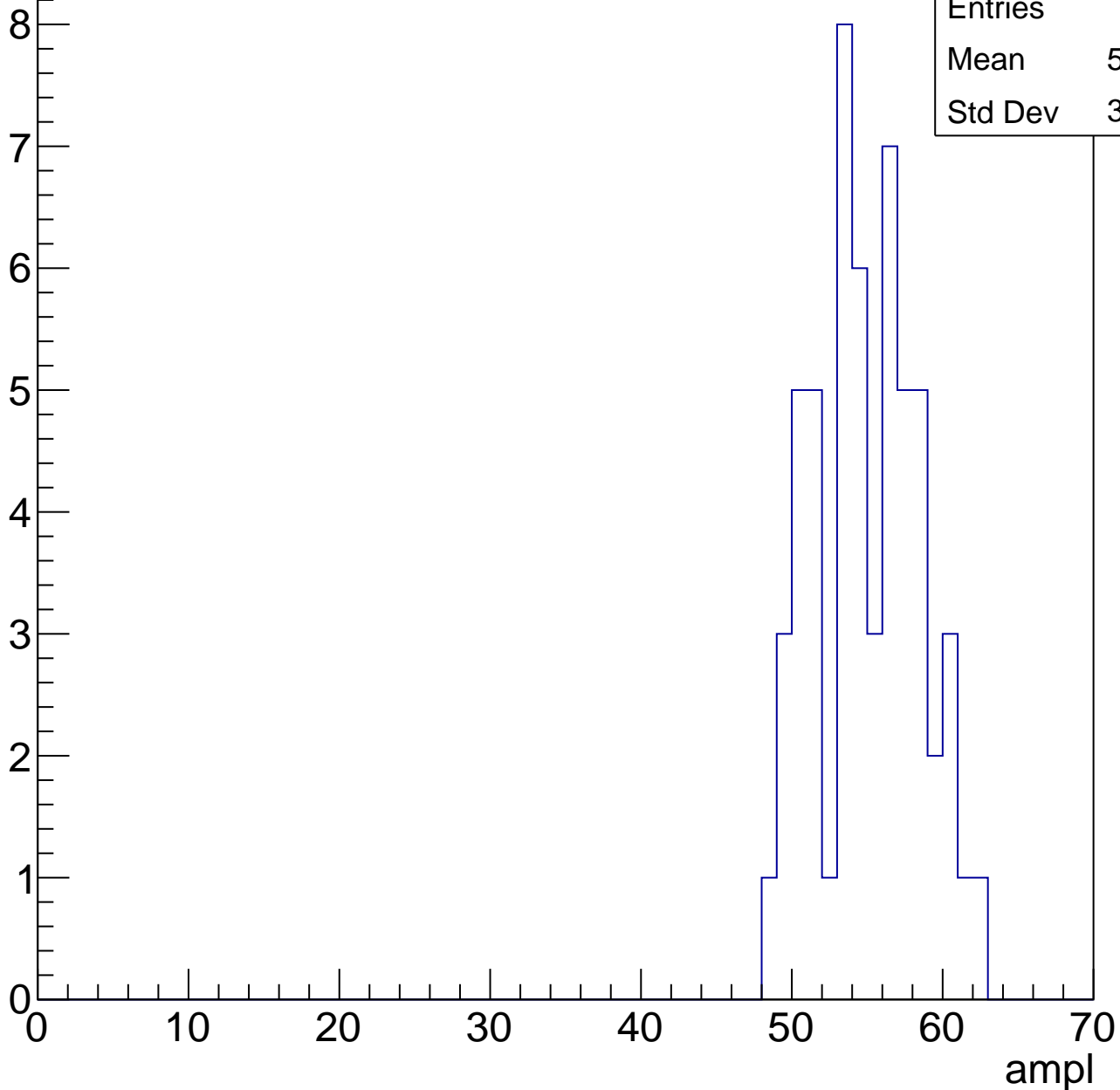


# B1L102S, U25-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.52
Std Dev	3.433

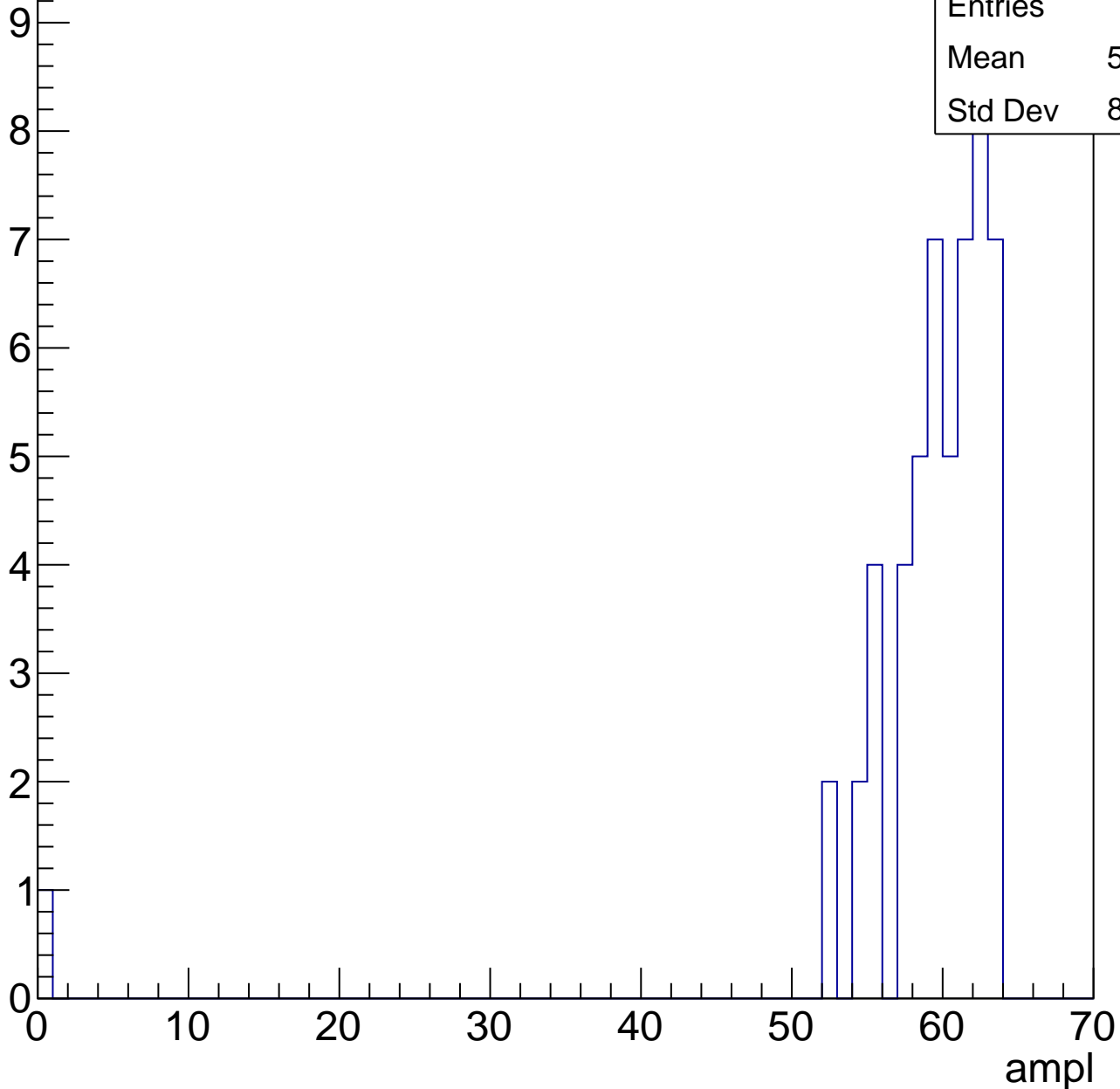


# B1L102S, U25-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

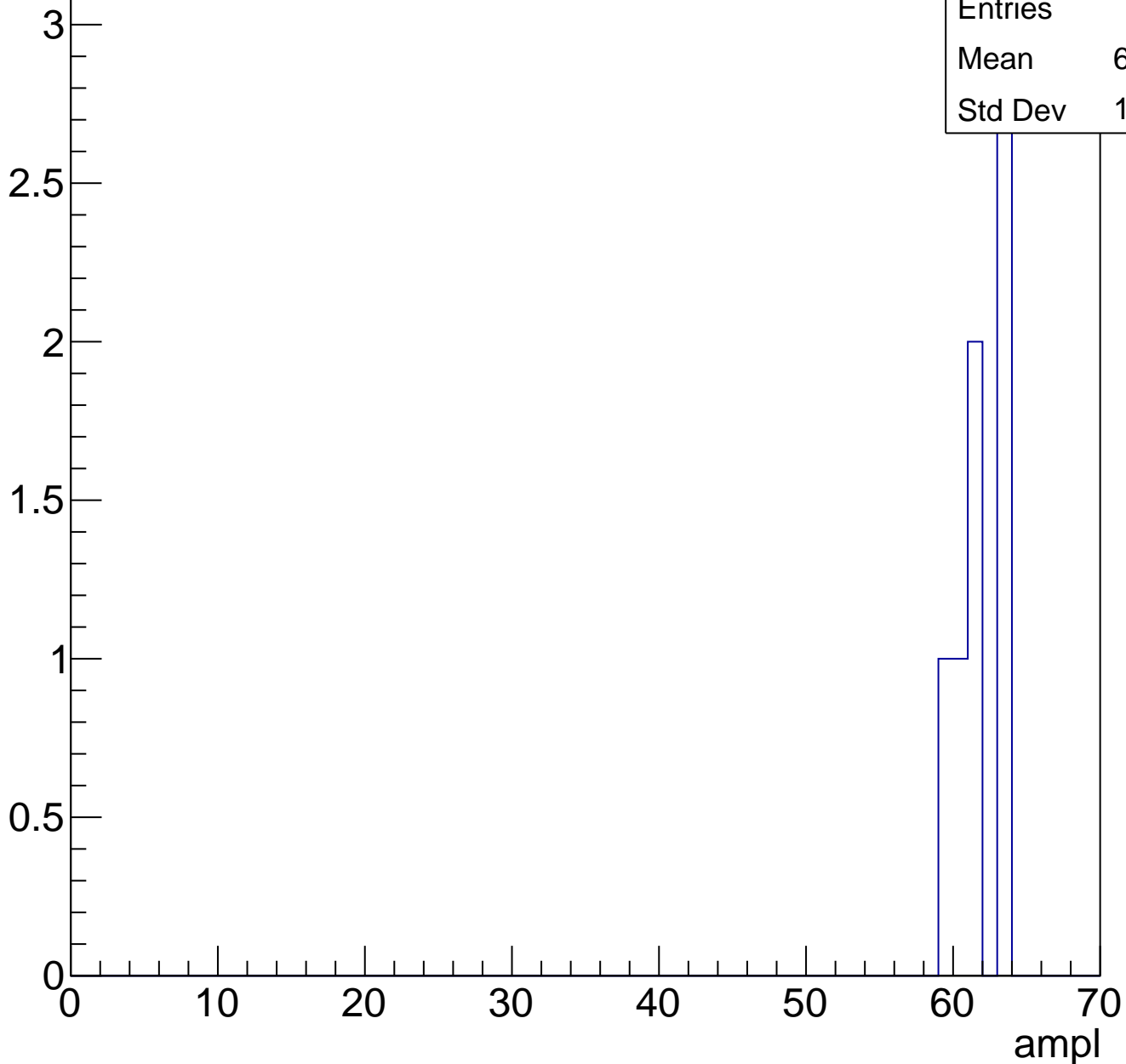
Entries	53
Mean	58.28
Std Dev	8.592



# B1L102S, U25-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch103, adc0

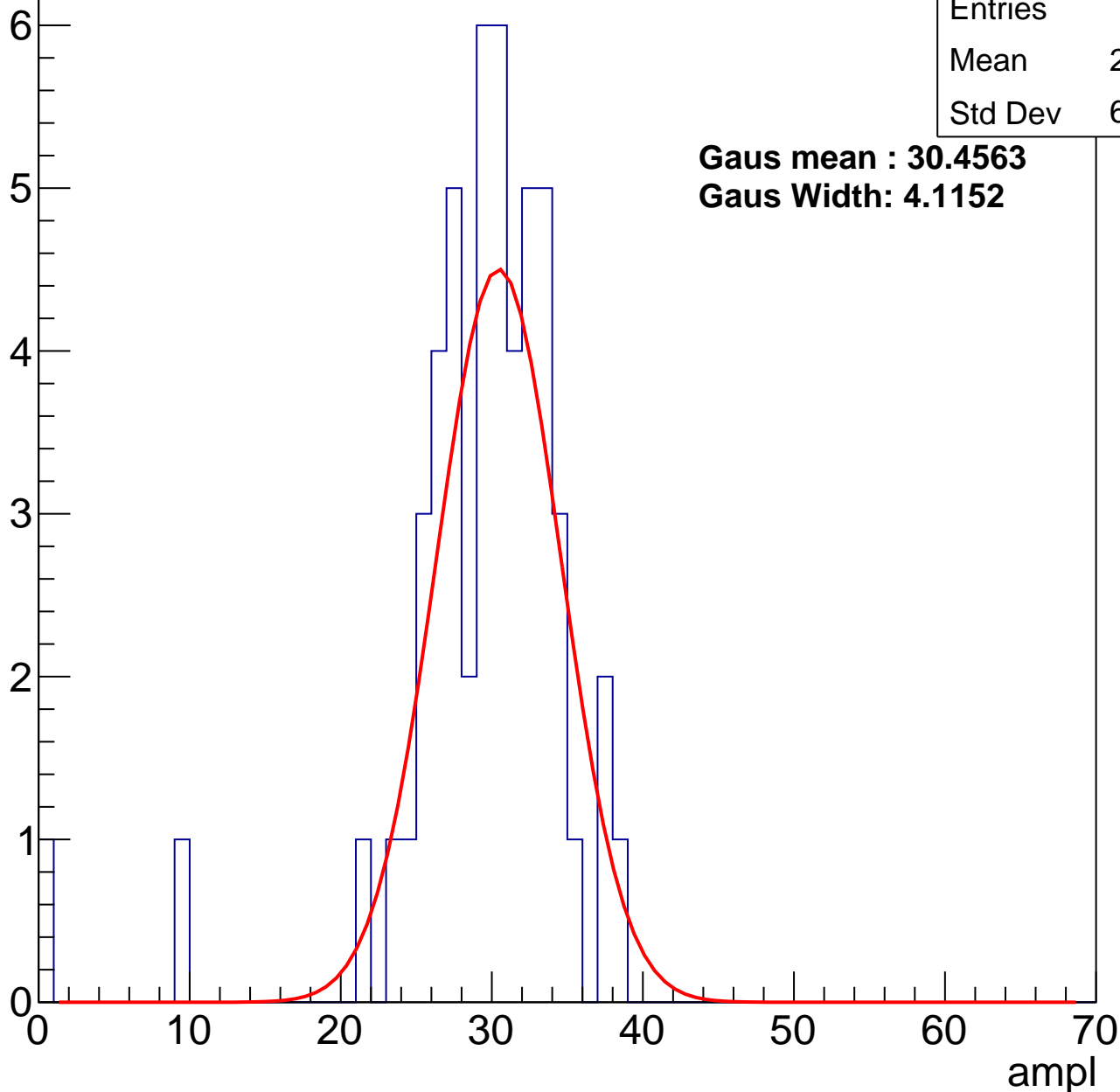
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	28.83
Std Dev	6.104

**Gaus mean : 30.4563**

**Gaus Width: 4.1152**



# B1L102S, U25-ch103, adc1

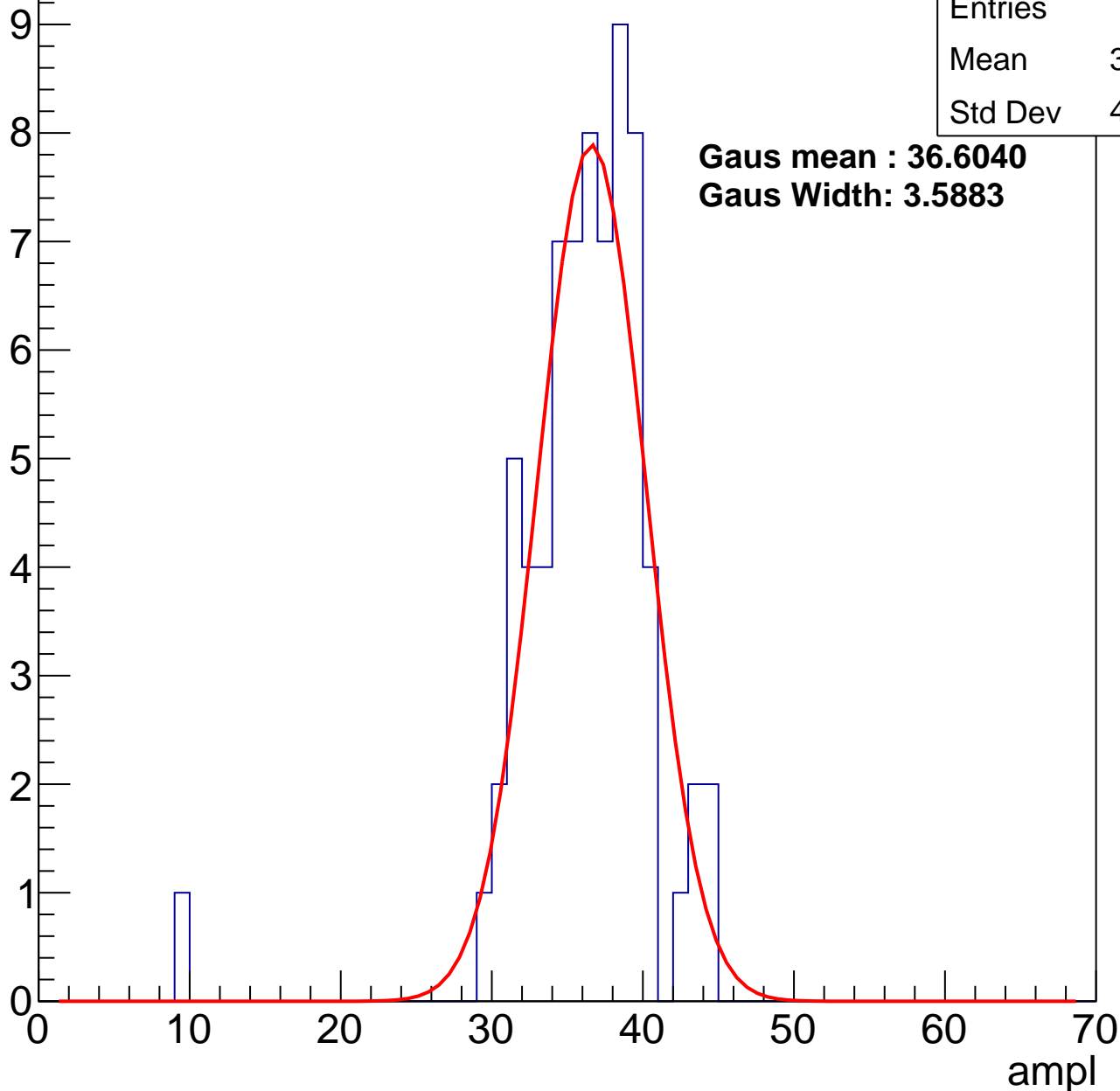
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.74
Std Dev	4.634

**Gaus mean : 36.6040**

**Gaus Width: 3.5883**



# B1L102S, U25-ch103, adc2

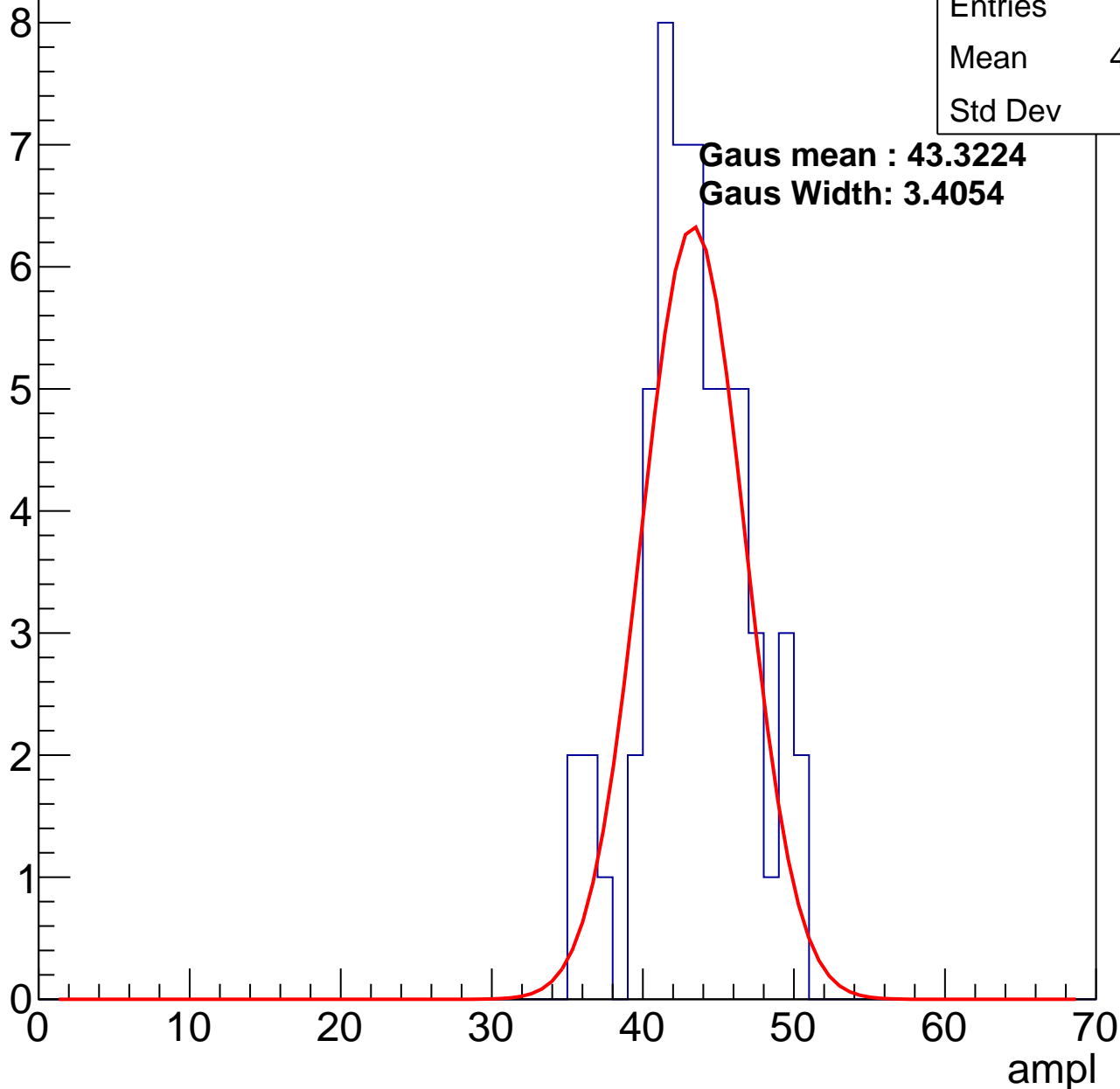
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.95
Std Dev	3.54

**Gaus mean : 43.3224**

**Gaus Width: 3.4054**

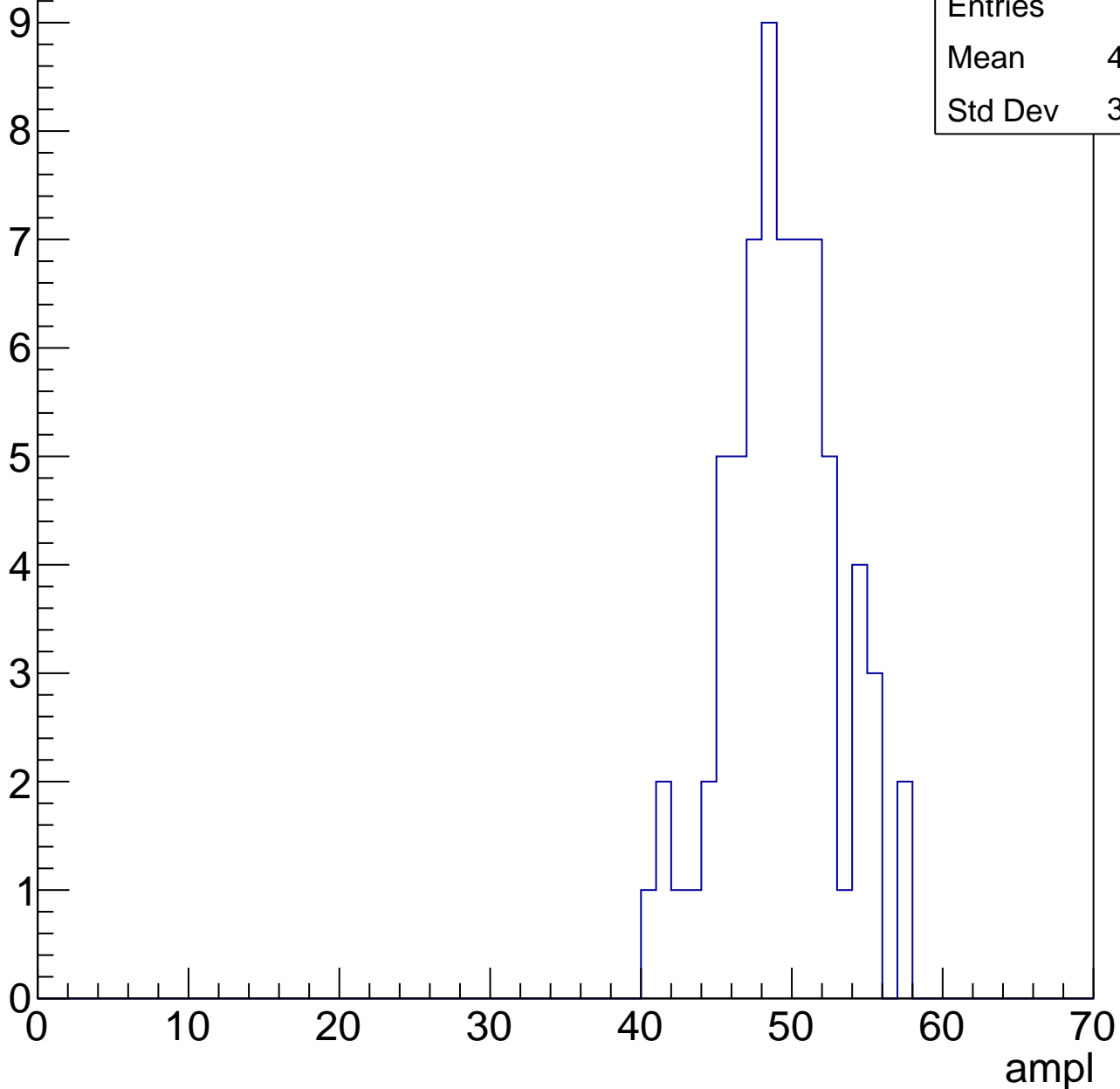


# B1L102S, U25-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.83
Std Dev	3.683

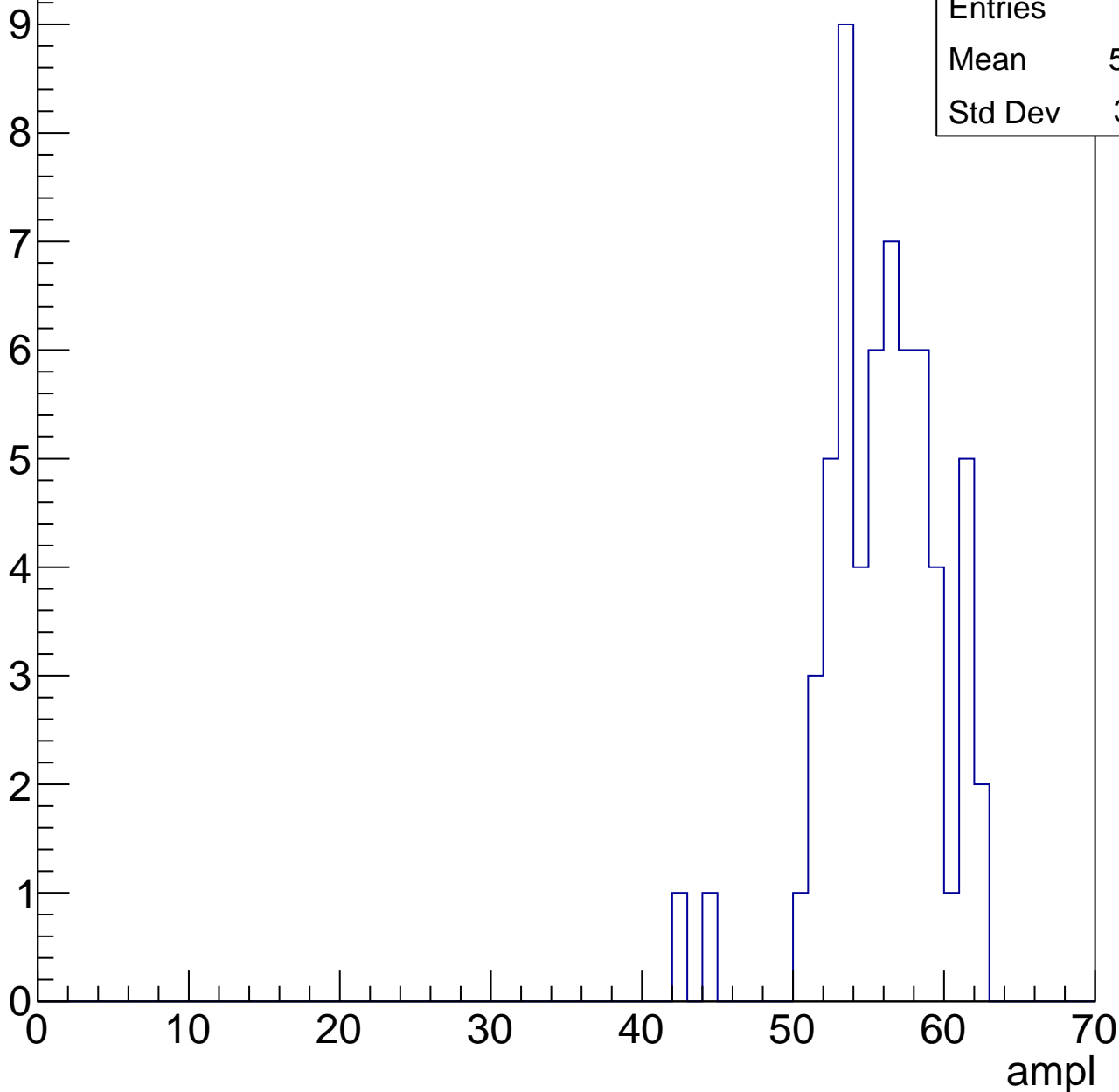


# B1L102S, U25-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.39
Std Dev	3.821

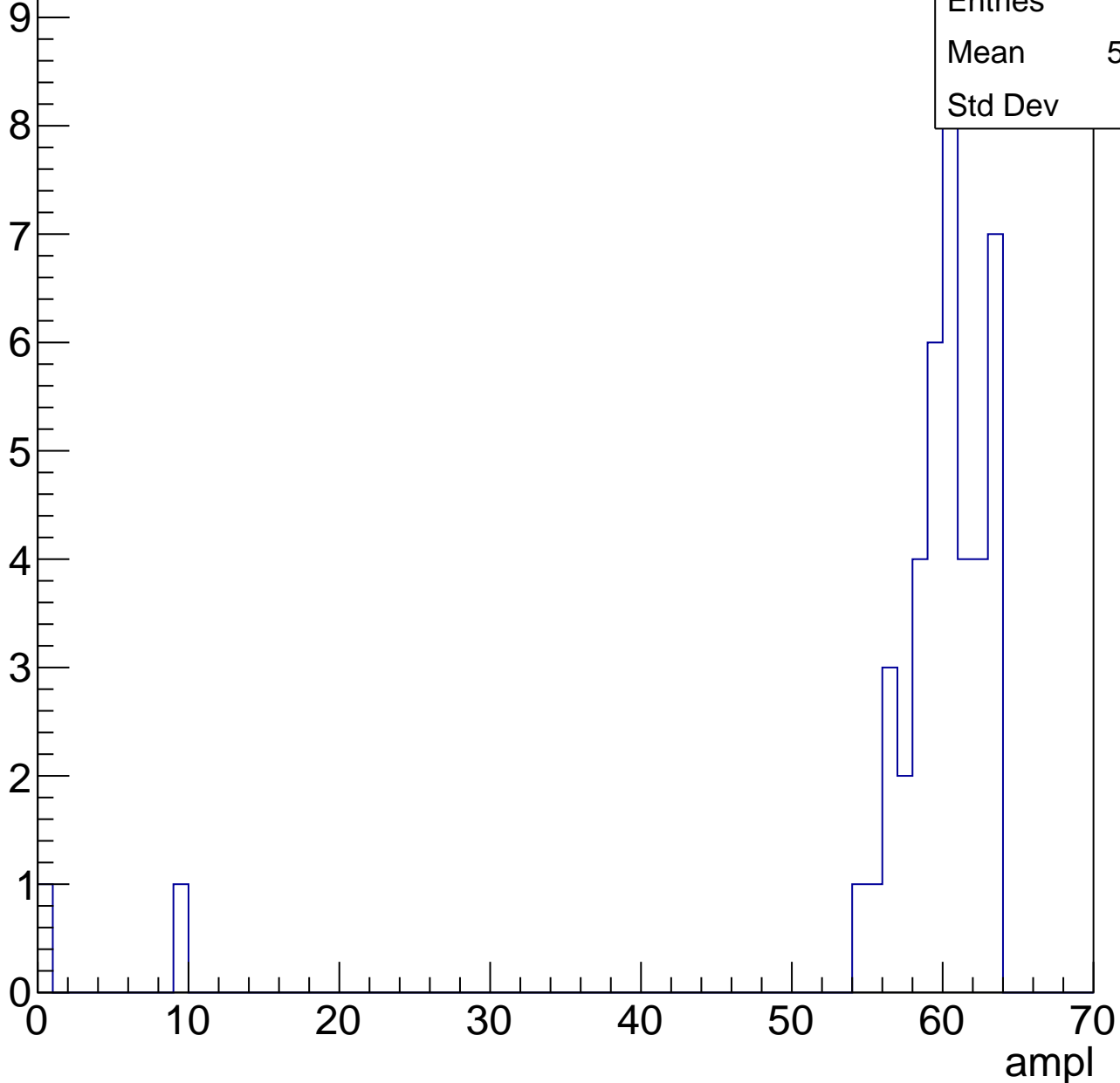


# B1L102S, U25-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

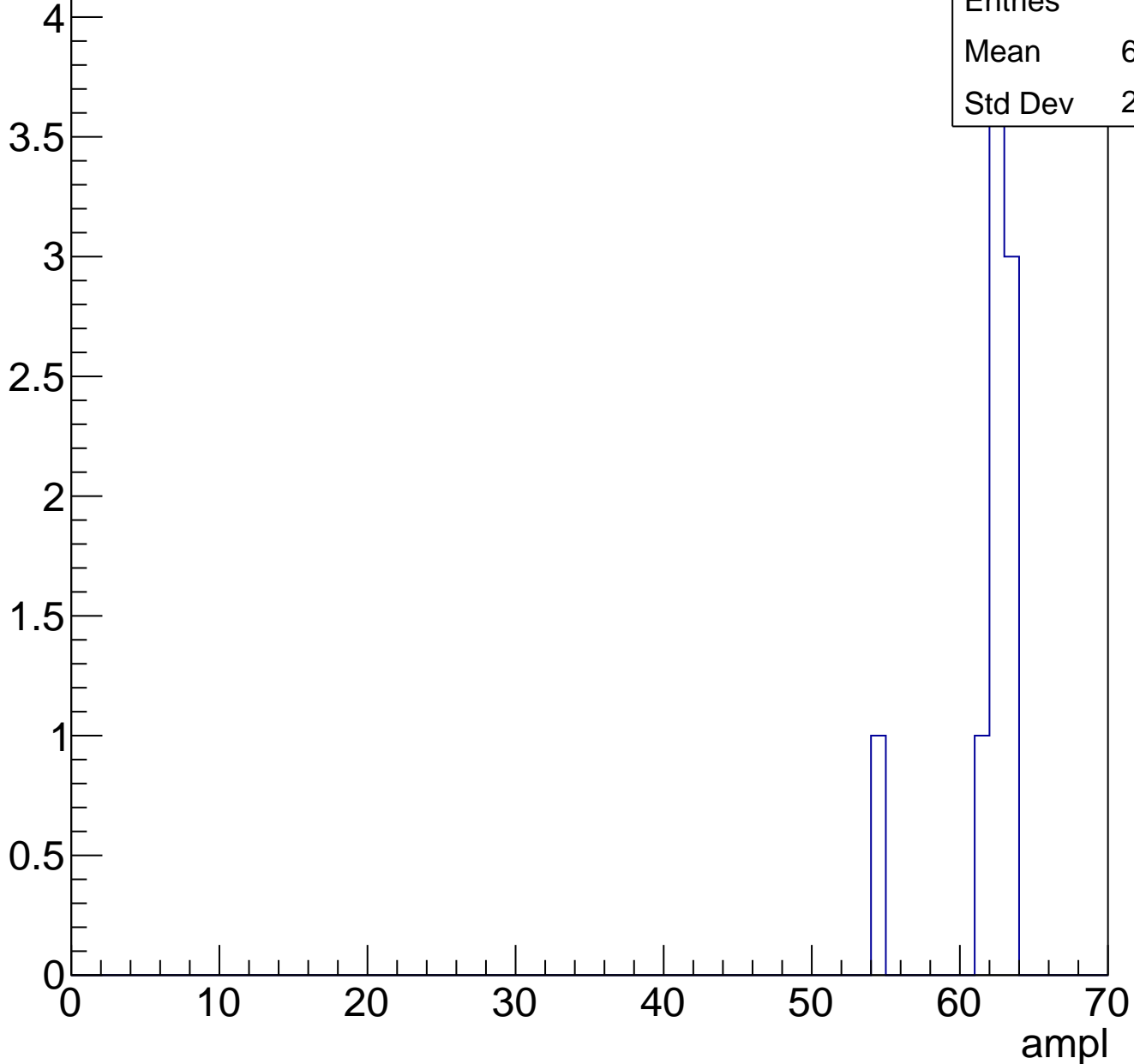
Entries	43
Mean	57.19
Std Dev	11.9



# B1L102S, U25-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch104, adc0

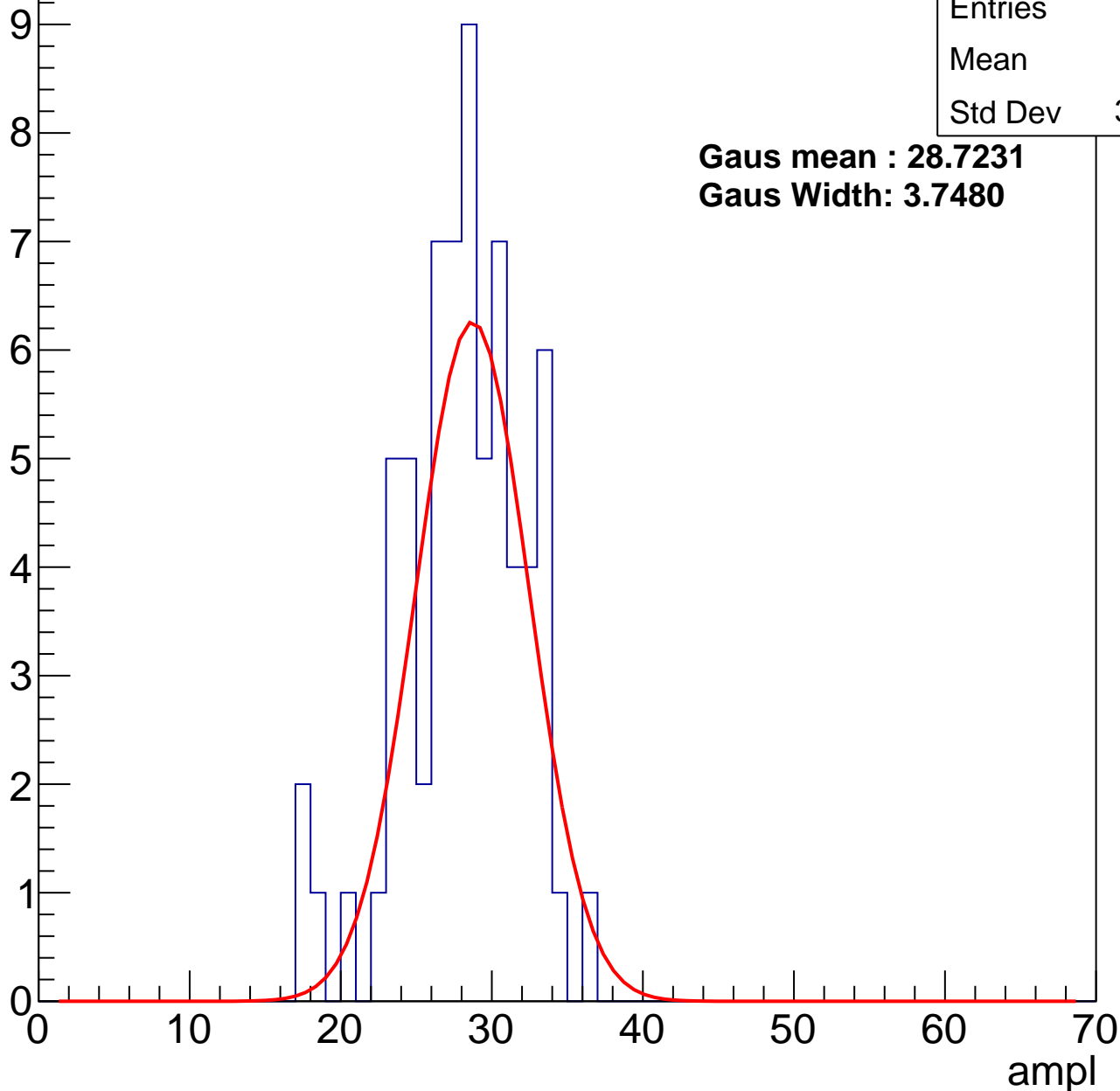
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.6
Std Dev	3.971

**Gaus mean : 28.7231**

**Gaus Width: 3.7480**



# B1L102S, U25-ch104, adc1

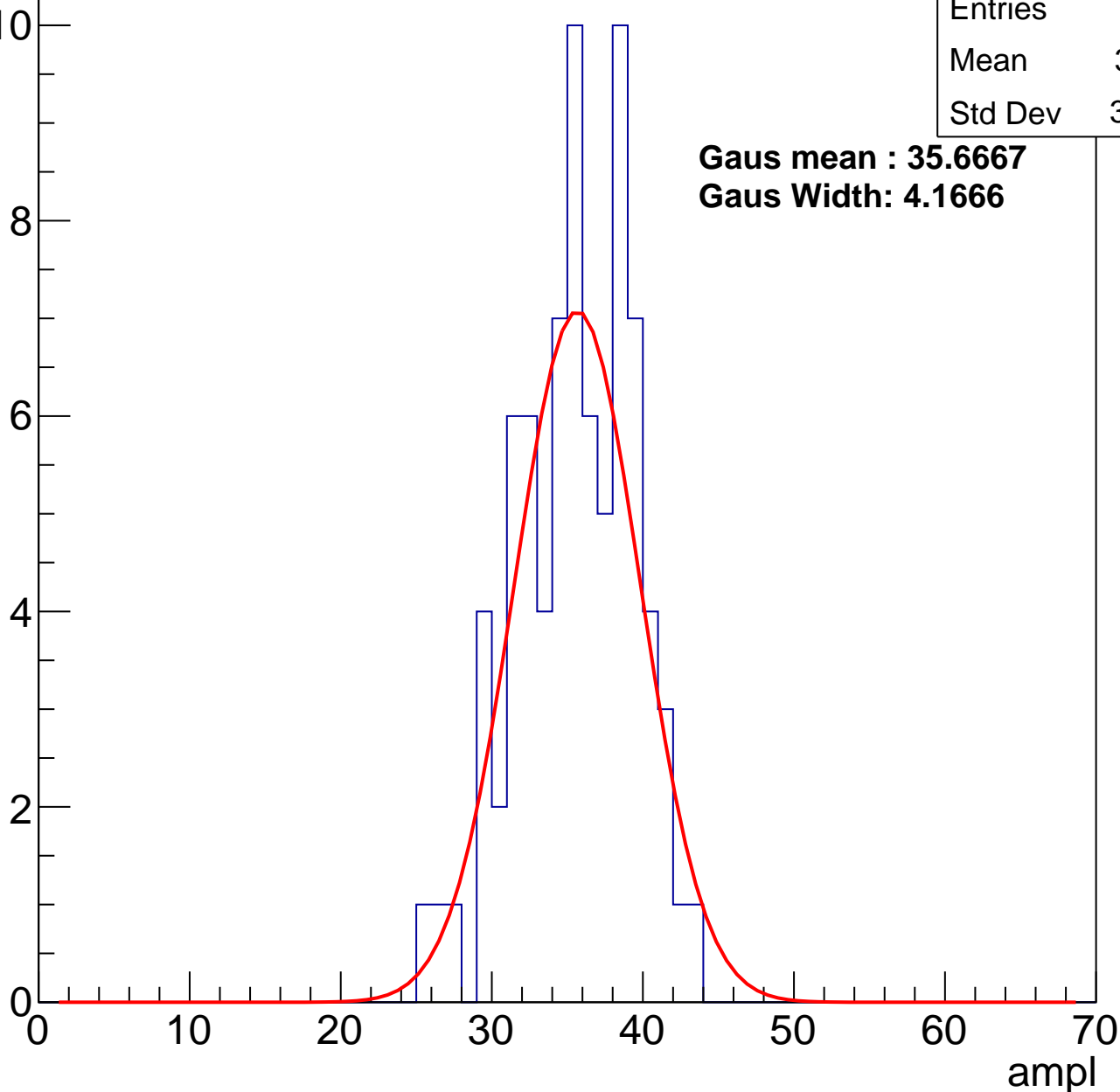
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.11
Std Dev	3.832

**Gaus mean : 35.6667**

**Gaus Width: 4.1666**



# B1L102S, U25-ch104, adc2

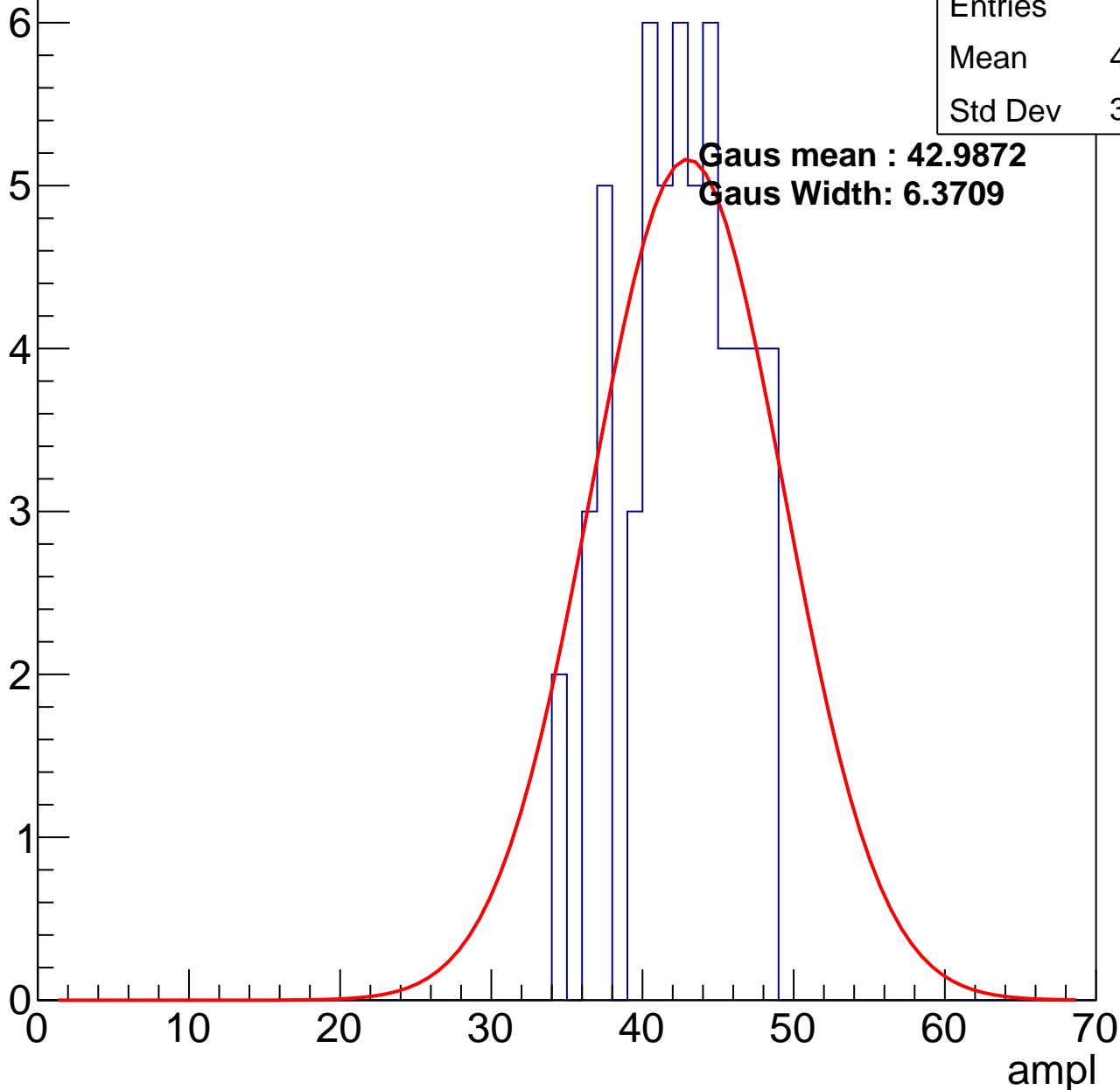
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.07
Std Dev	3.722

**Gaus mean : 42.9872**

**Gaus Width: 6.3709**

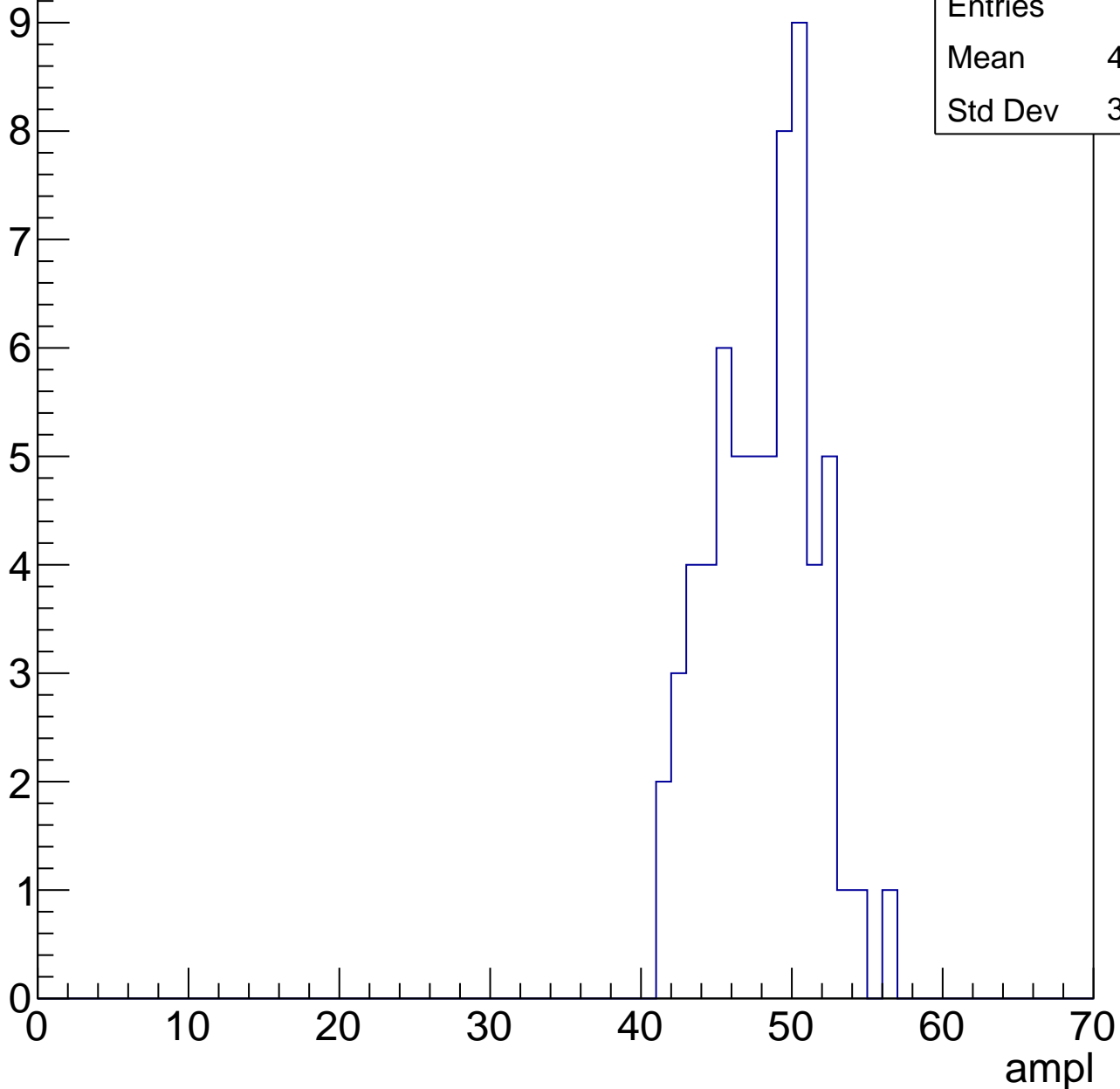


# B1L102S, U25-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.62
Std Dev	3.392

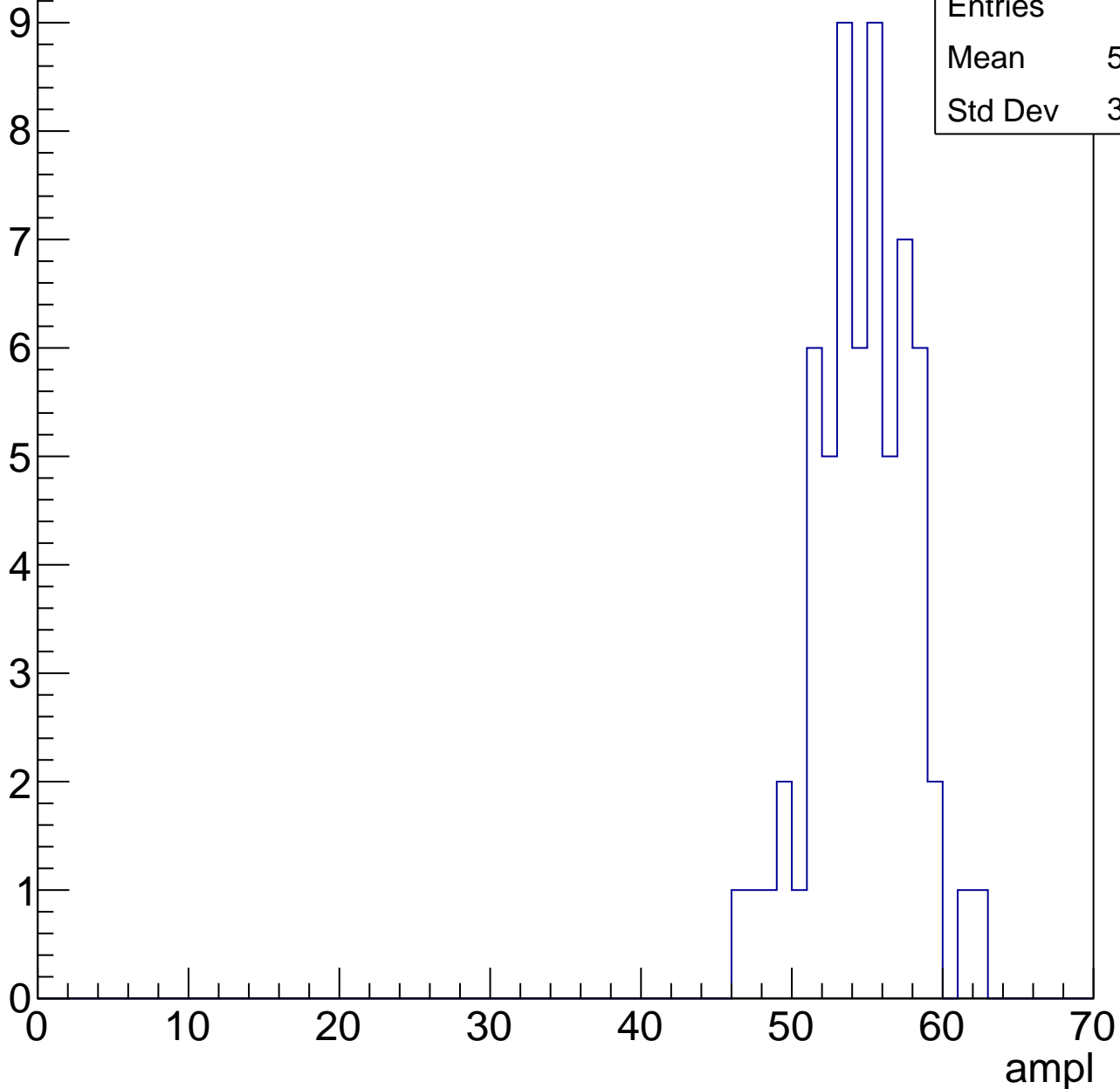


# B1L102S, U25-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.27
Std Dev	3.198

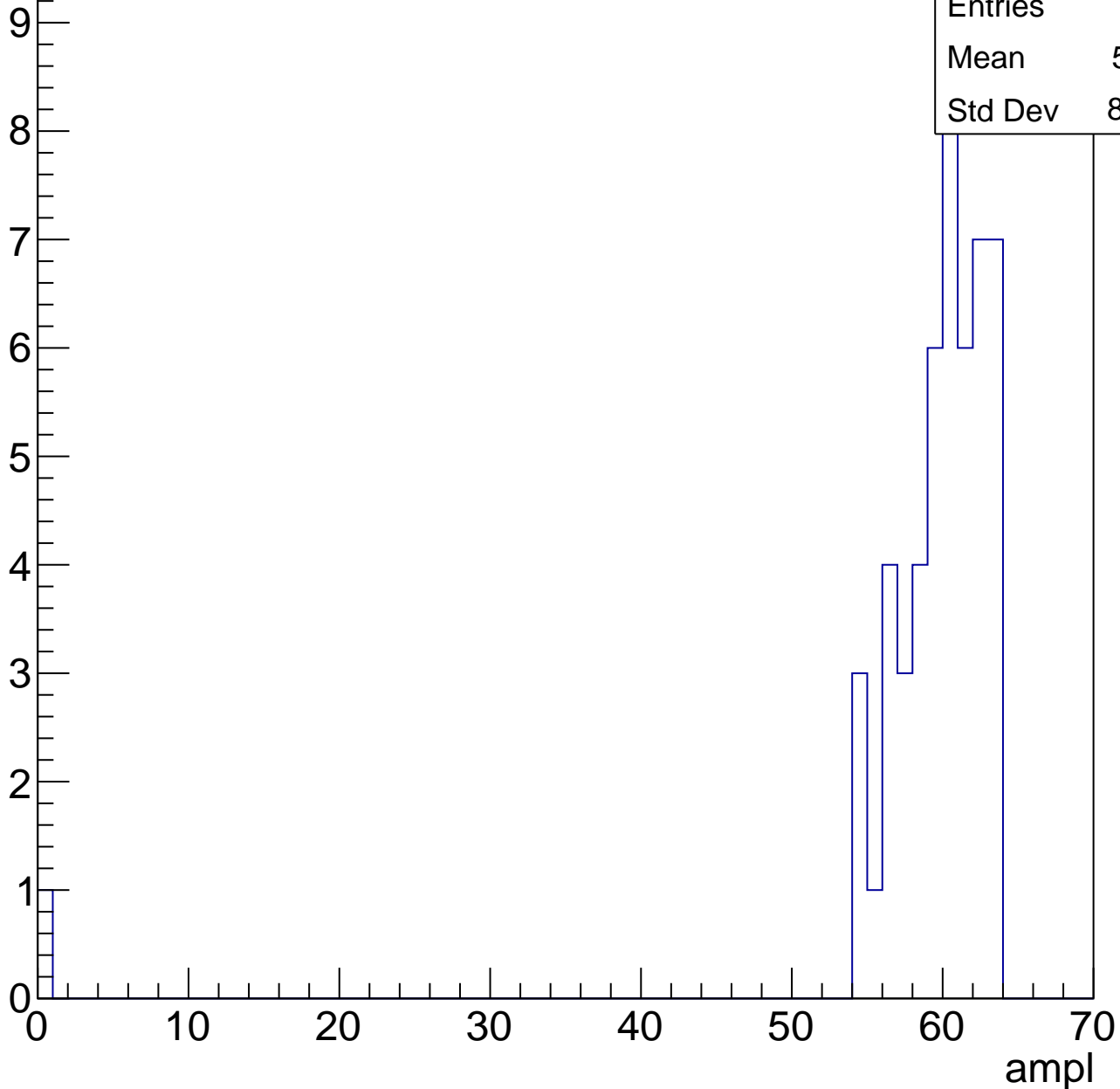


# B1L102S, U25-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.41
Std Dev	8.648



# B1L102S, U25-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries	11
Mean	61.27
Std Dev	1.42



# B1L102S, U25-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch105, adc0

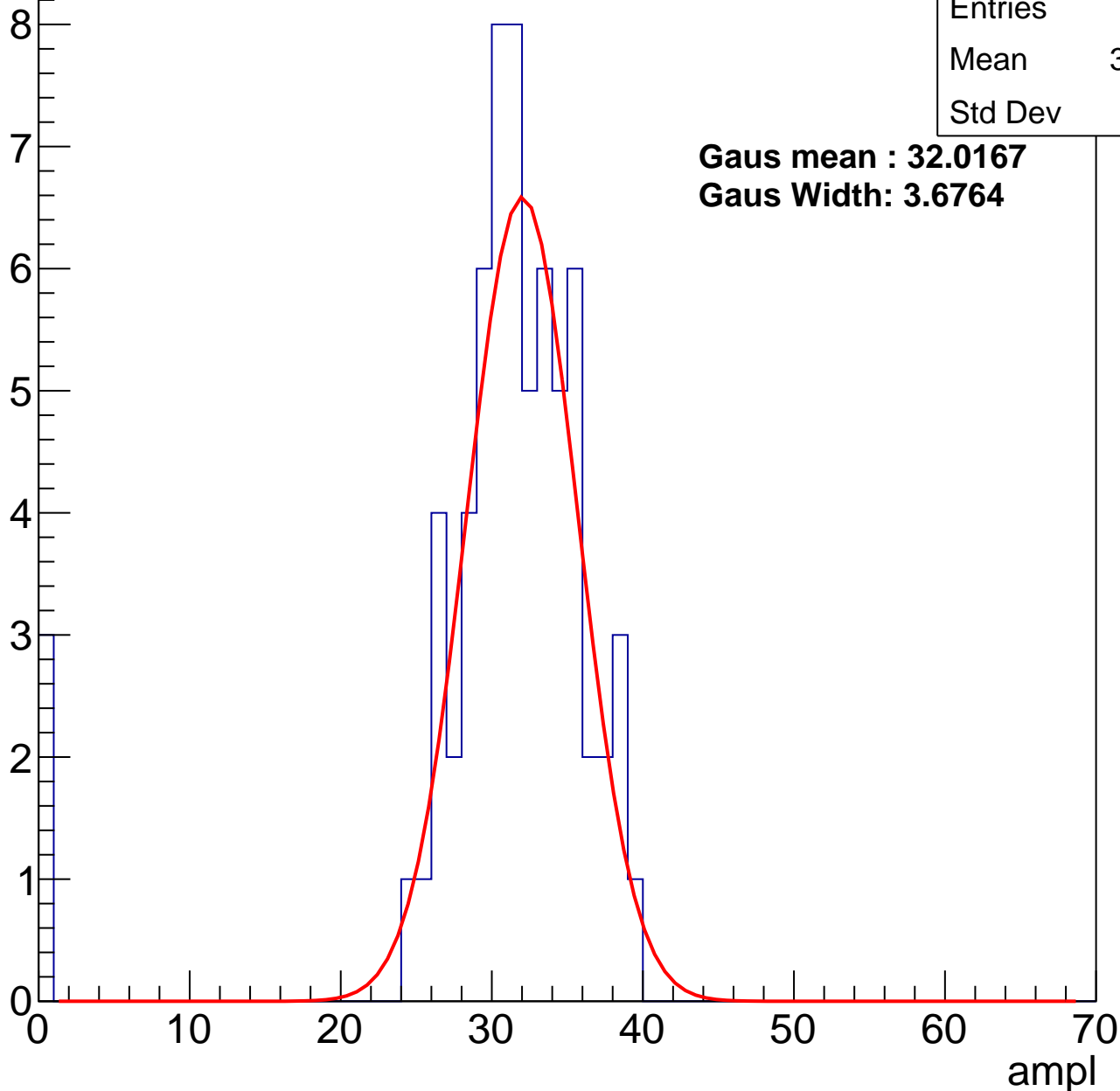
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.12
Std Dev	7.35

**Gaus mean : 32.0167**

**Gaus Width: 3.6764**



# B1L102S, U25-ch105, adc1

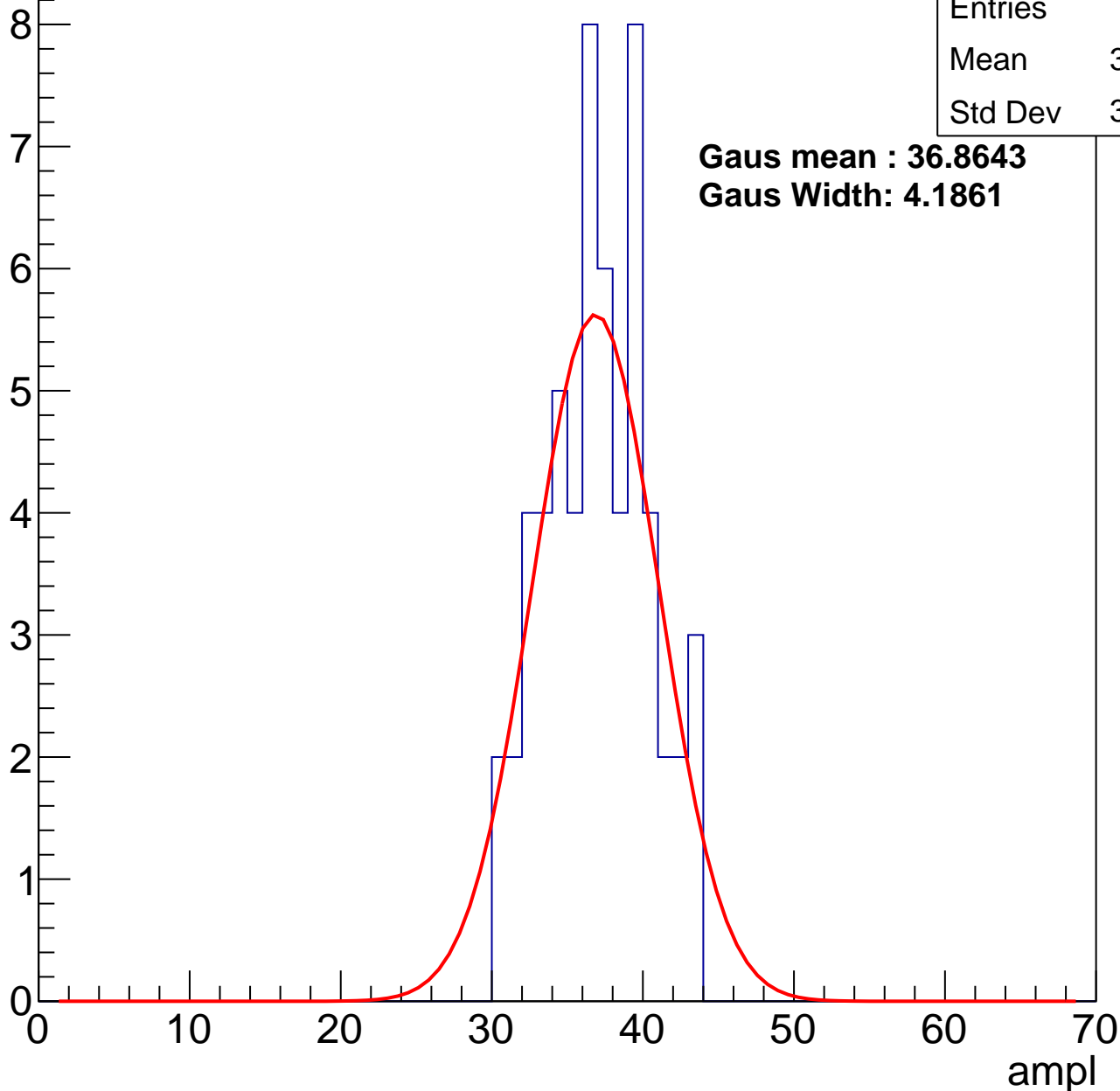
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	36.57
Std Dev	3.359

**Gaus mean : 36.8643**

**Gaus Width: 4.1861**



# B1L102S, U25-ch105, adc2

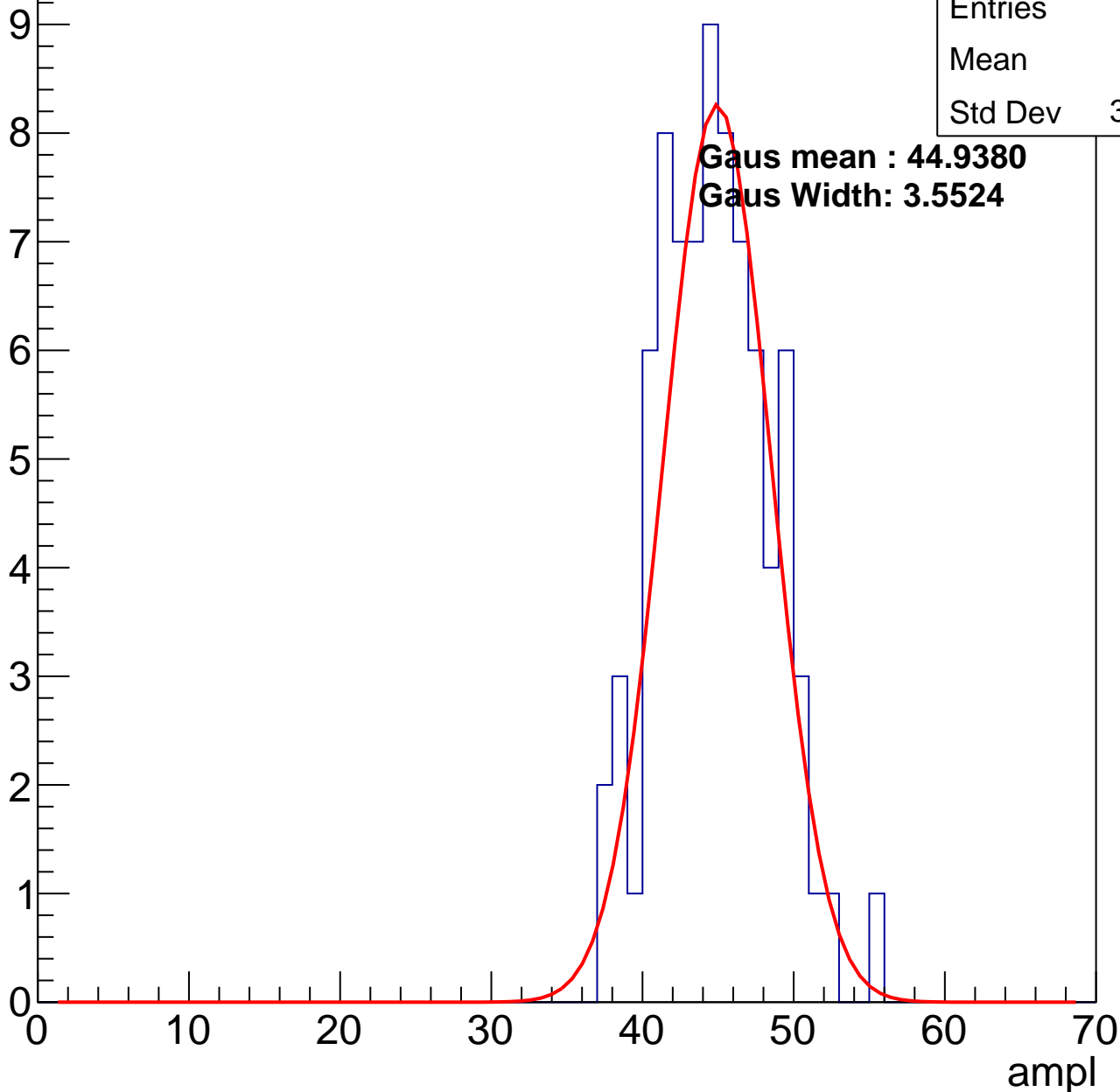
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	44.3
Std Dev	3.662

**Gaus mean : 44.9380**

**Gaus Width: 3.5524**

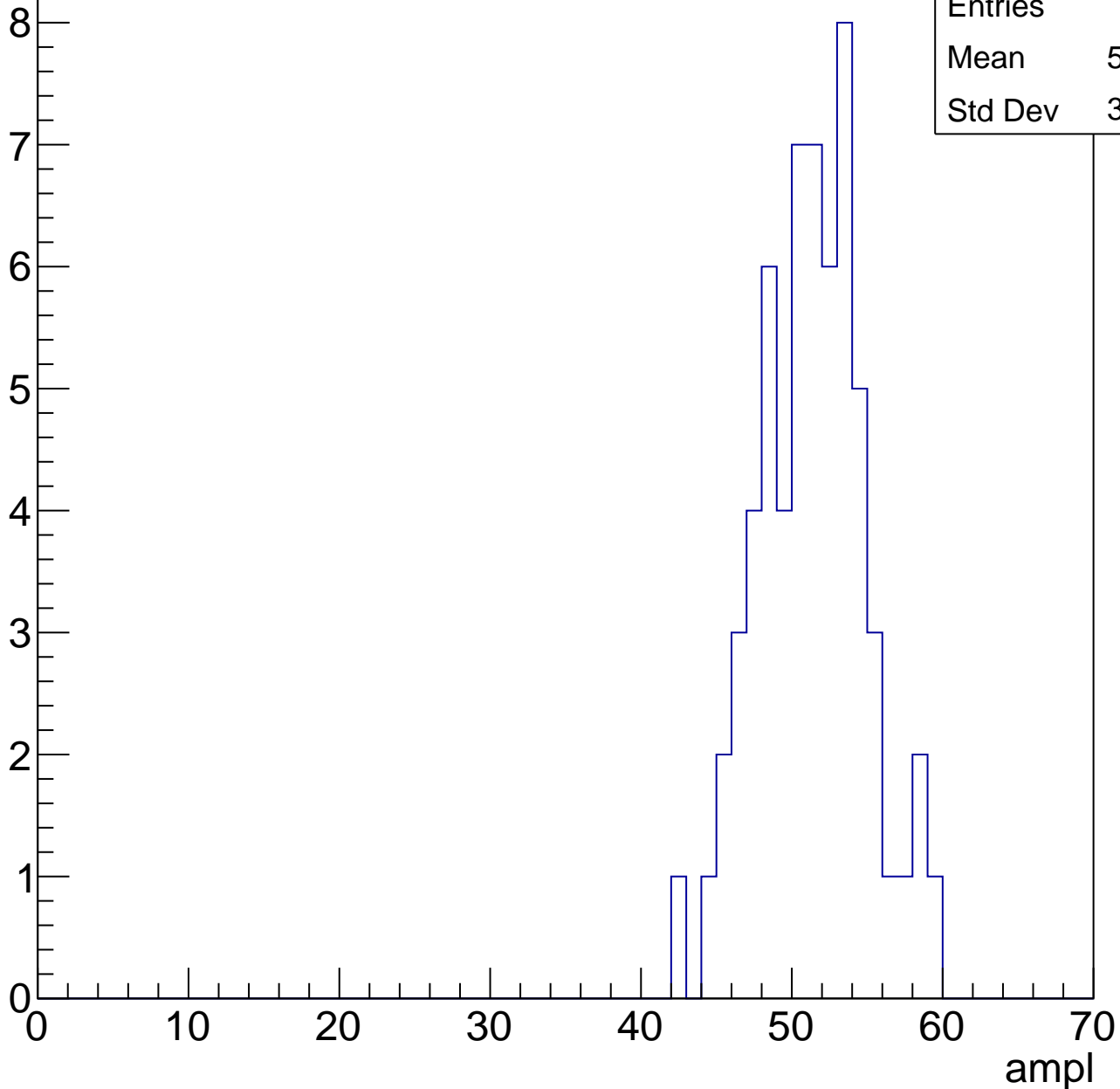


# B1L102S, U25-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.84
Std Dev	3.525

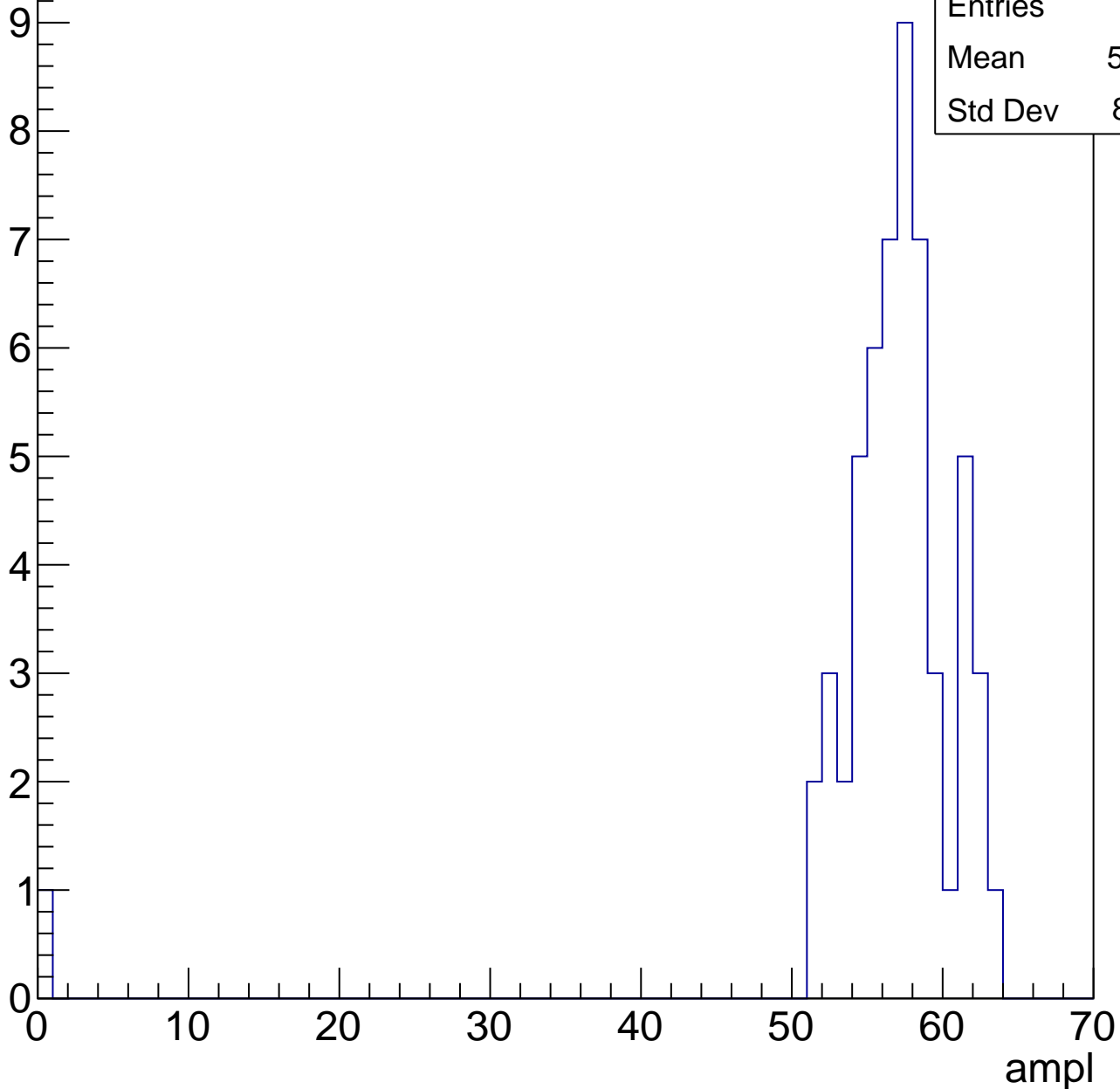


# B1L102S, U25-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

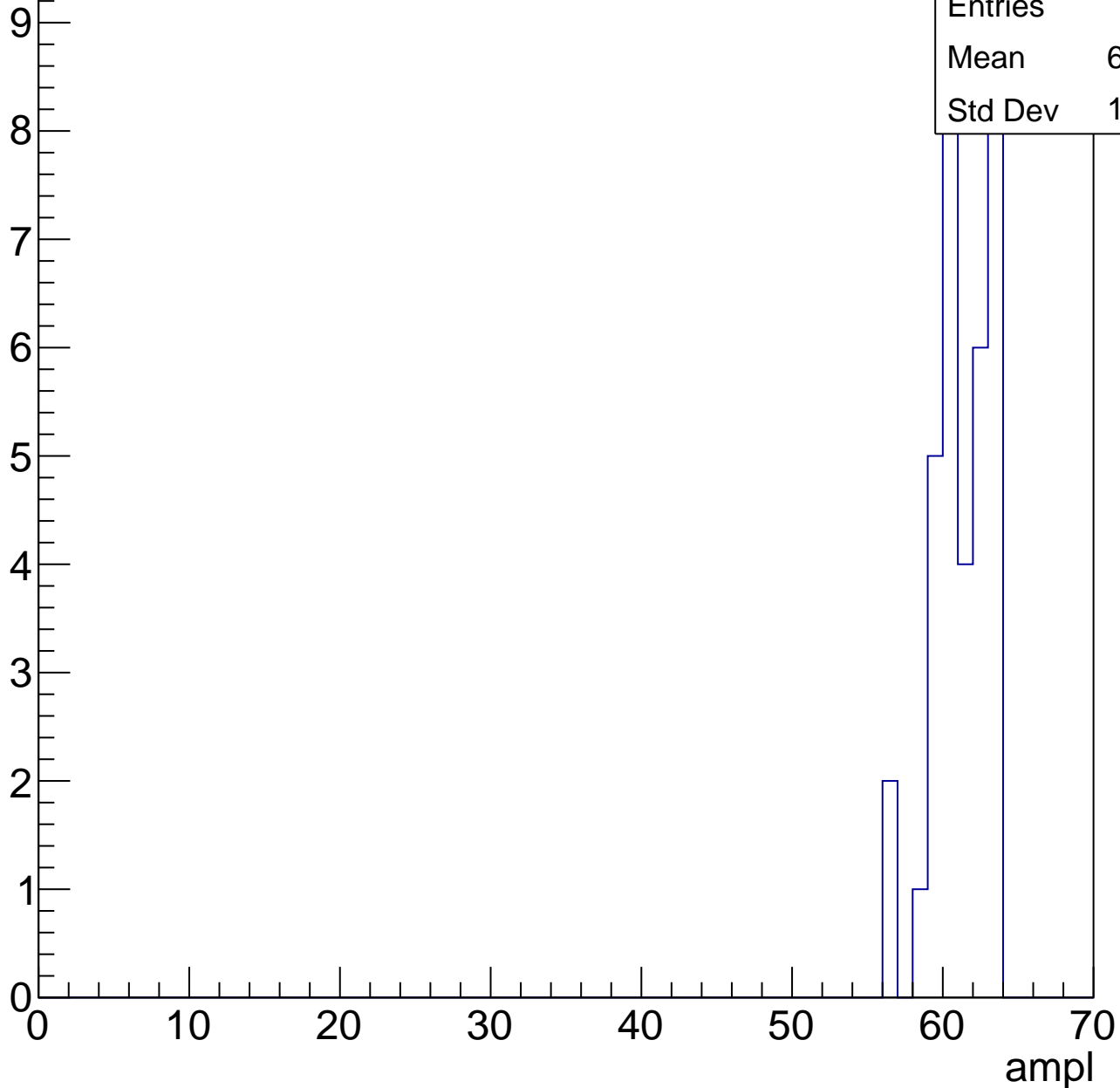
Entries	55
Mean	55.75
Std Dev	8.131



# B1L102S, U25-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

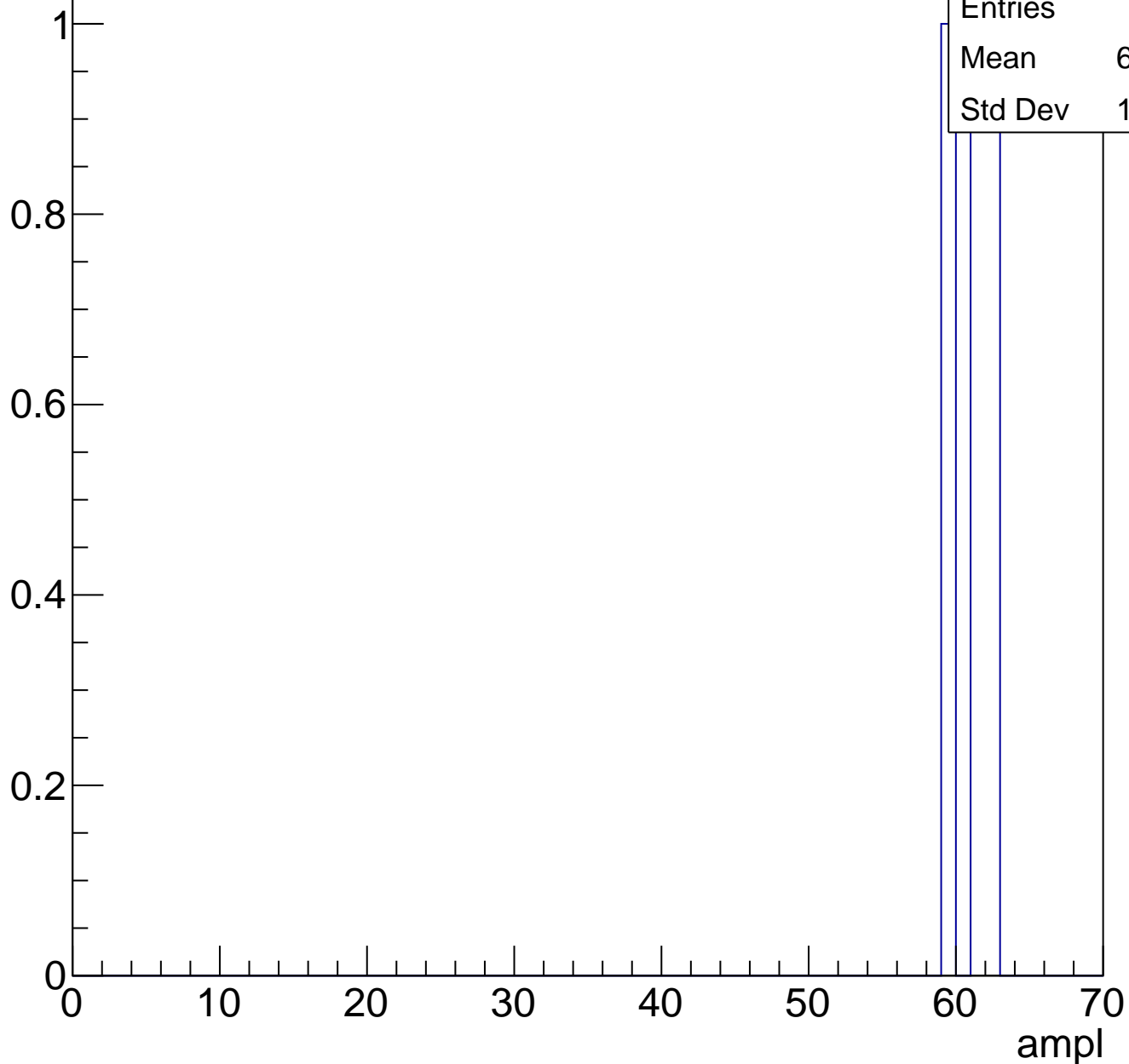
Entry



# B1L102S, U25-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch106, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	62
Mean	24.74
Std Dev	5.677

**Gaus mean : 26.4205**

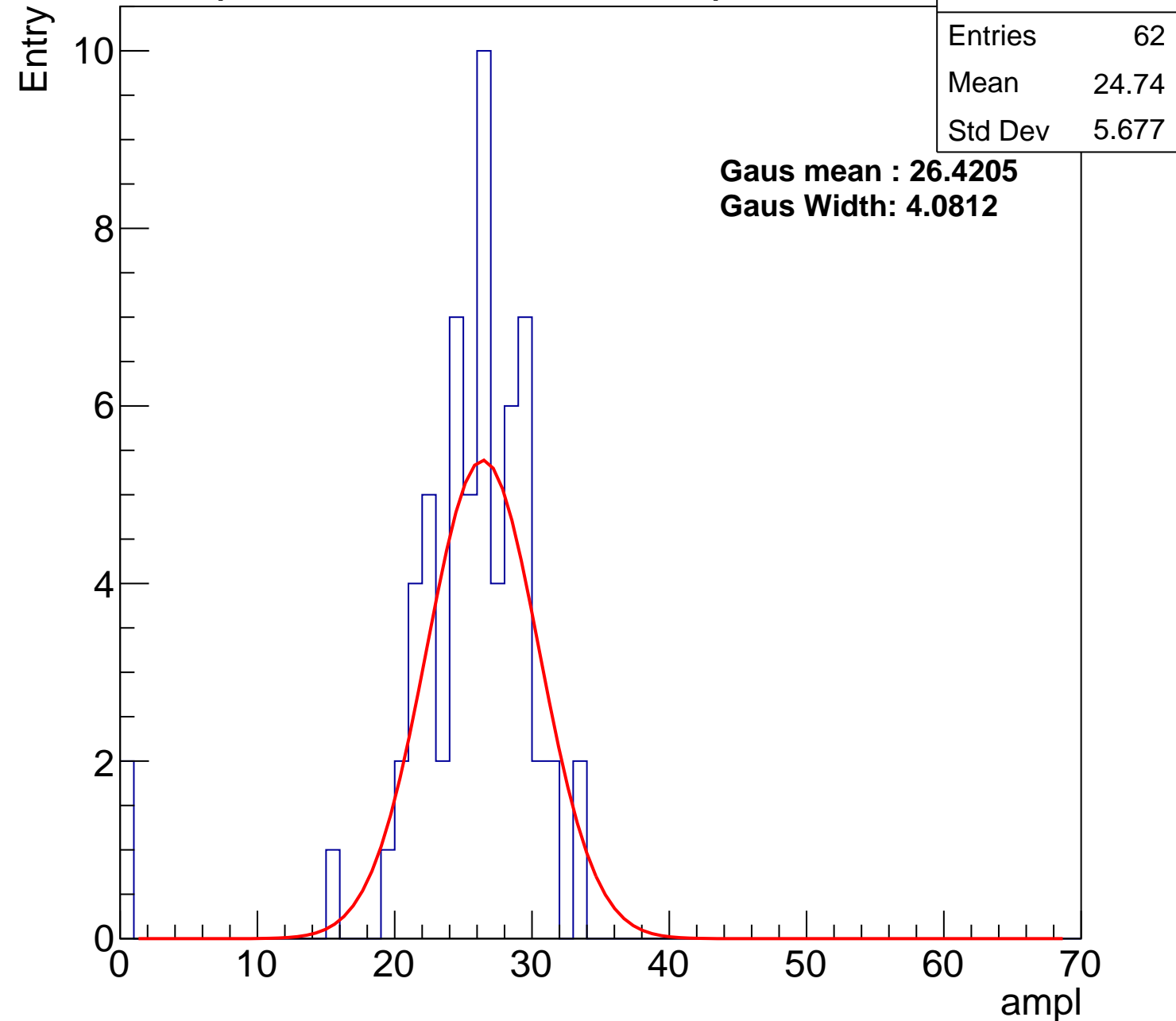
**Gaus Width: 4.0812**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch106, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	75
Mean	32.6
Std Dev	3.453

**Gaus mean : 33.8866**

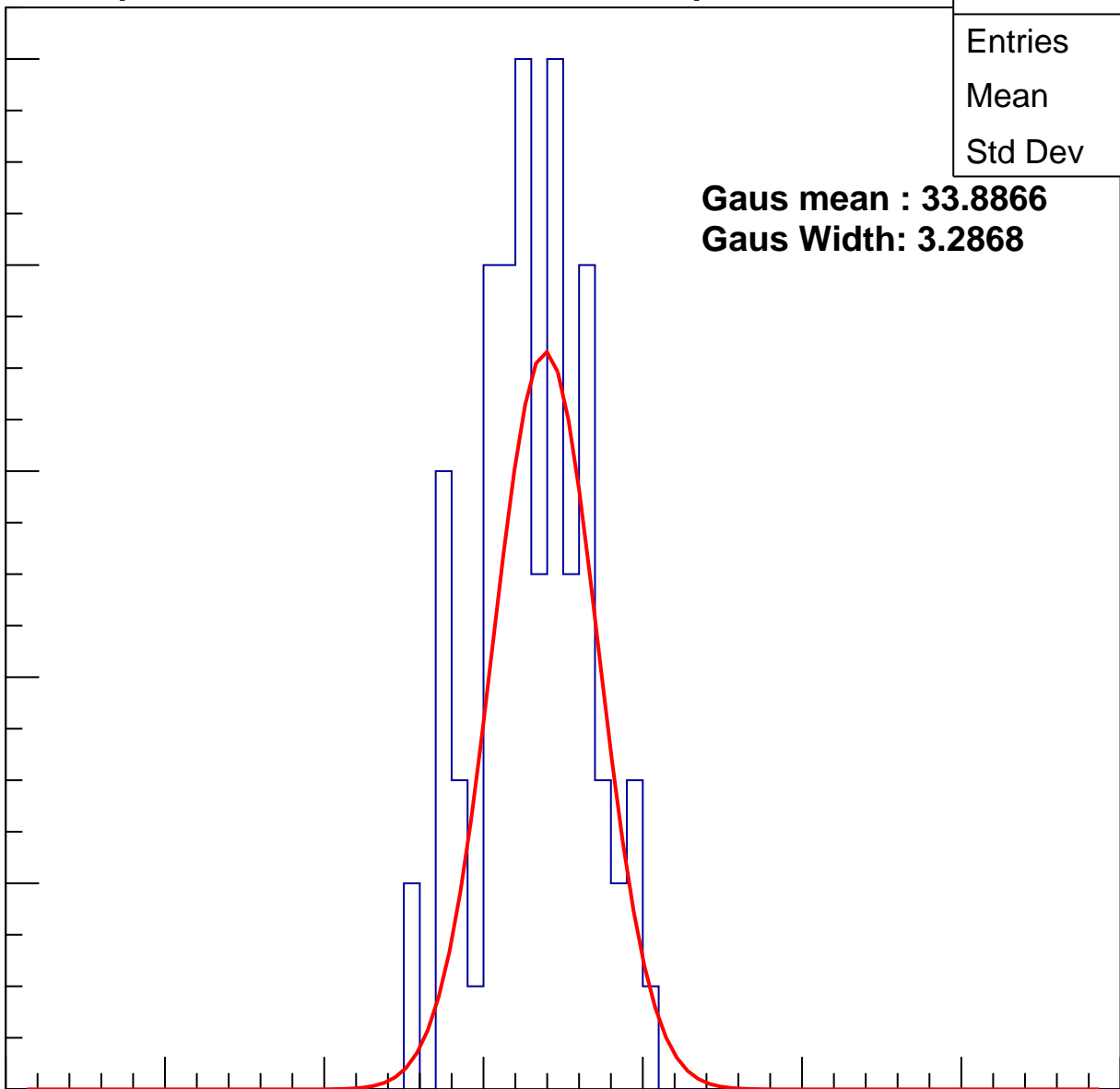
**Gaus Width: 3.2868**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch106, adc2

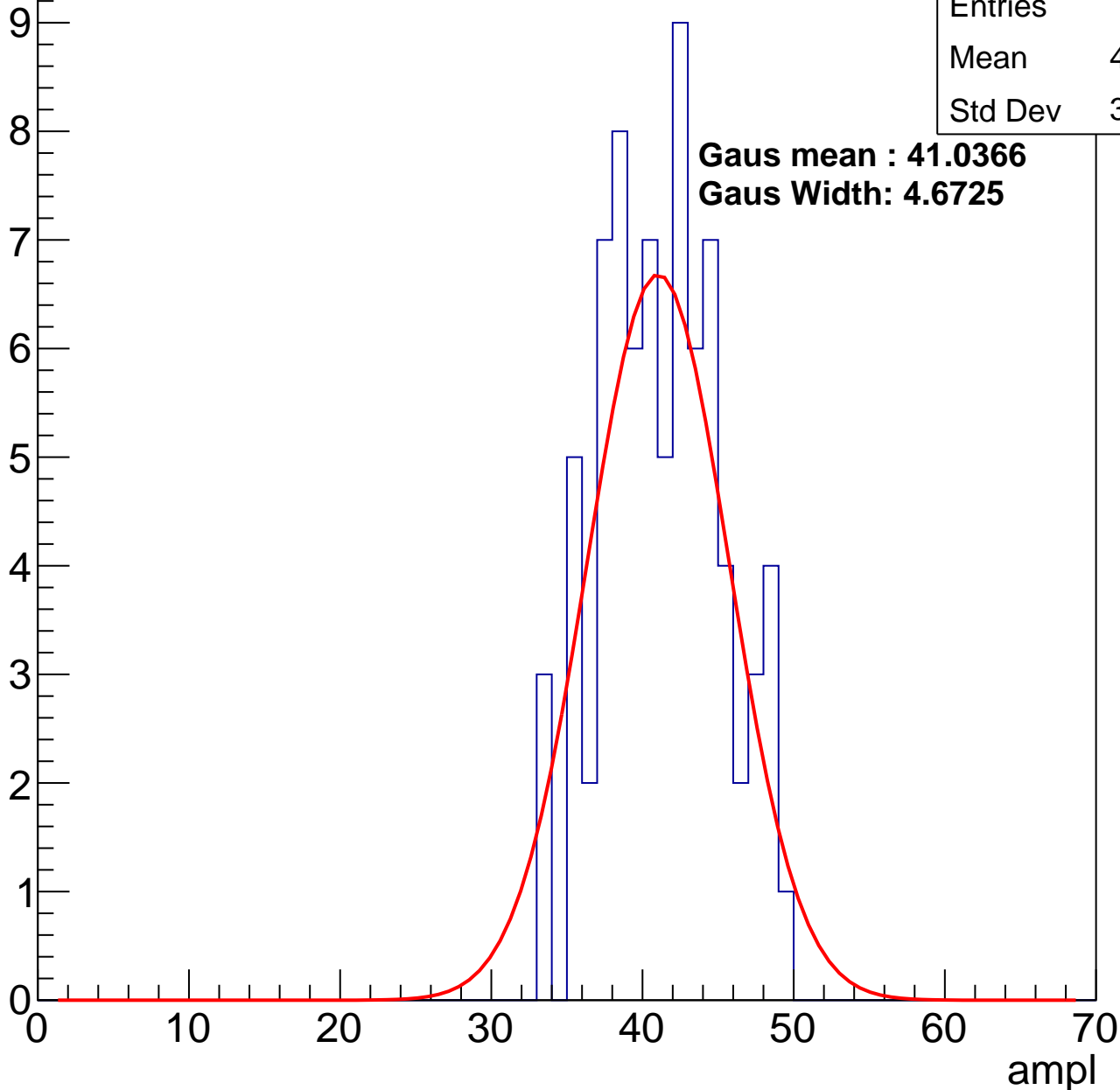
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	40.84
Std Dev	3.918

**Gaus mean : 41.0366**

**Gaus Width: 4.6725**

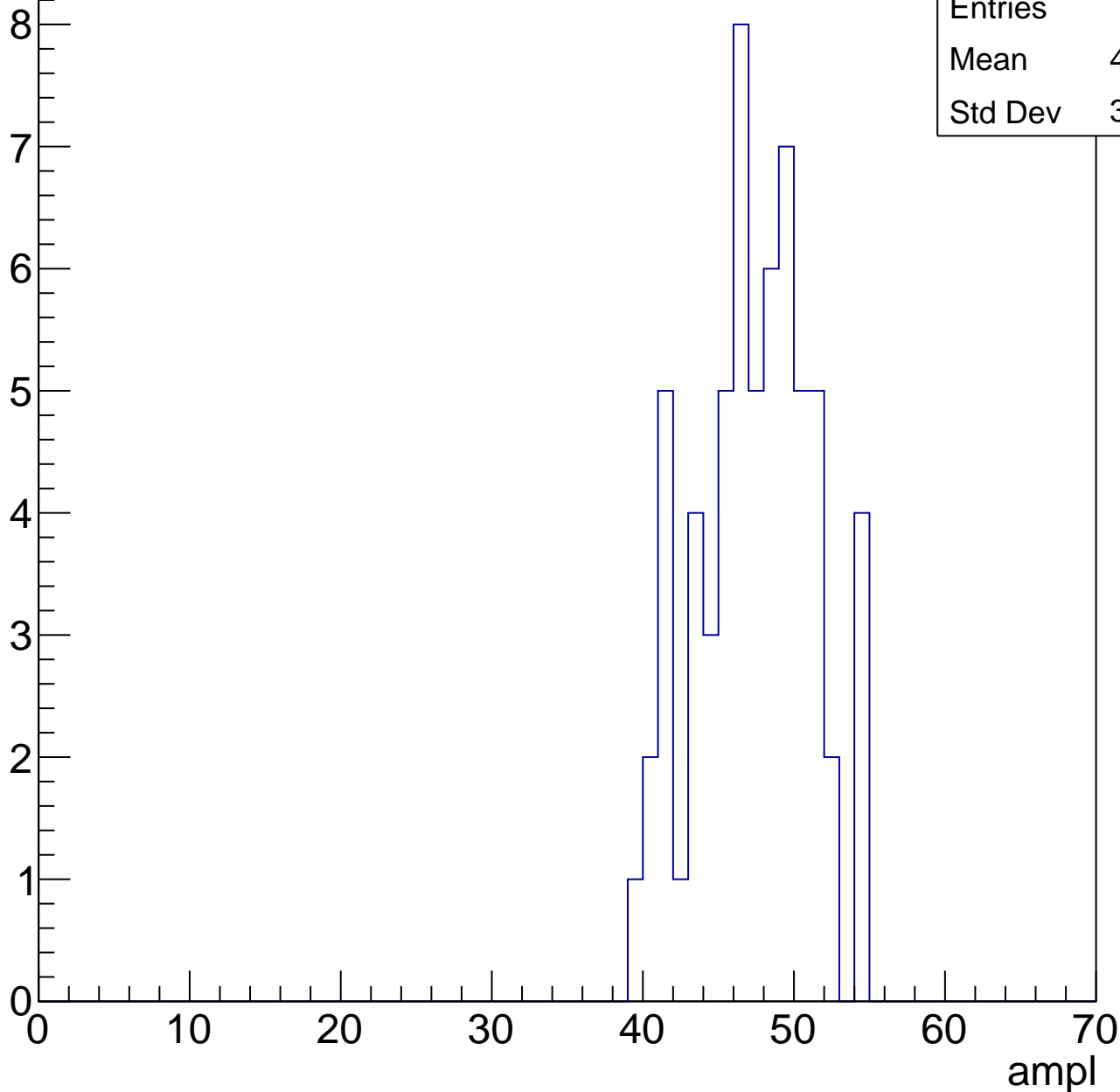


# B1L102S, U25-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	46.89
Std Dev	3.759

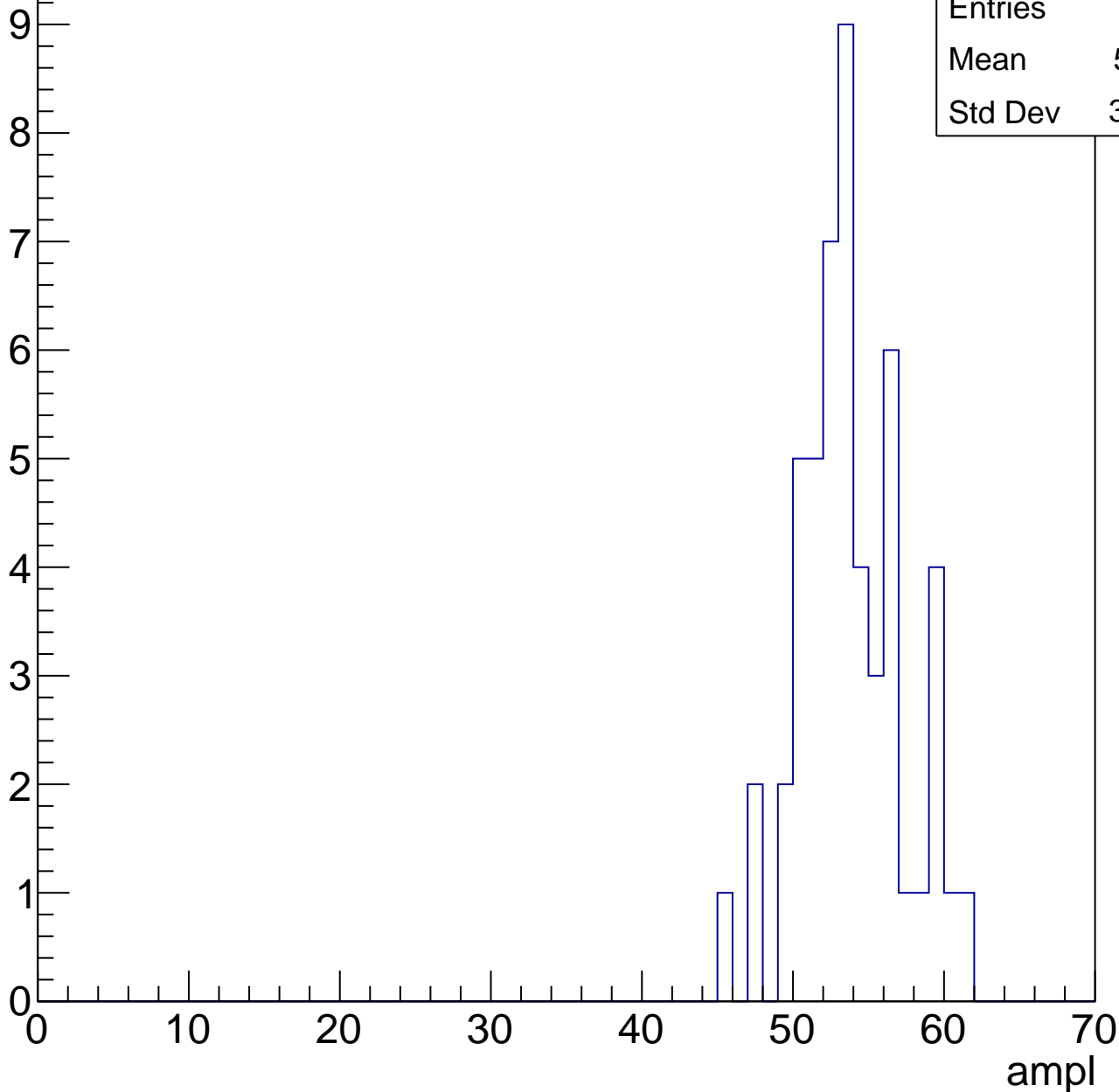


# B1L102S, U25-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.31
Std Dev	3.406

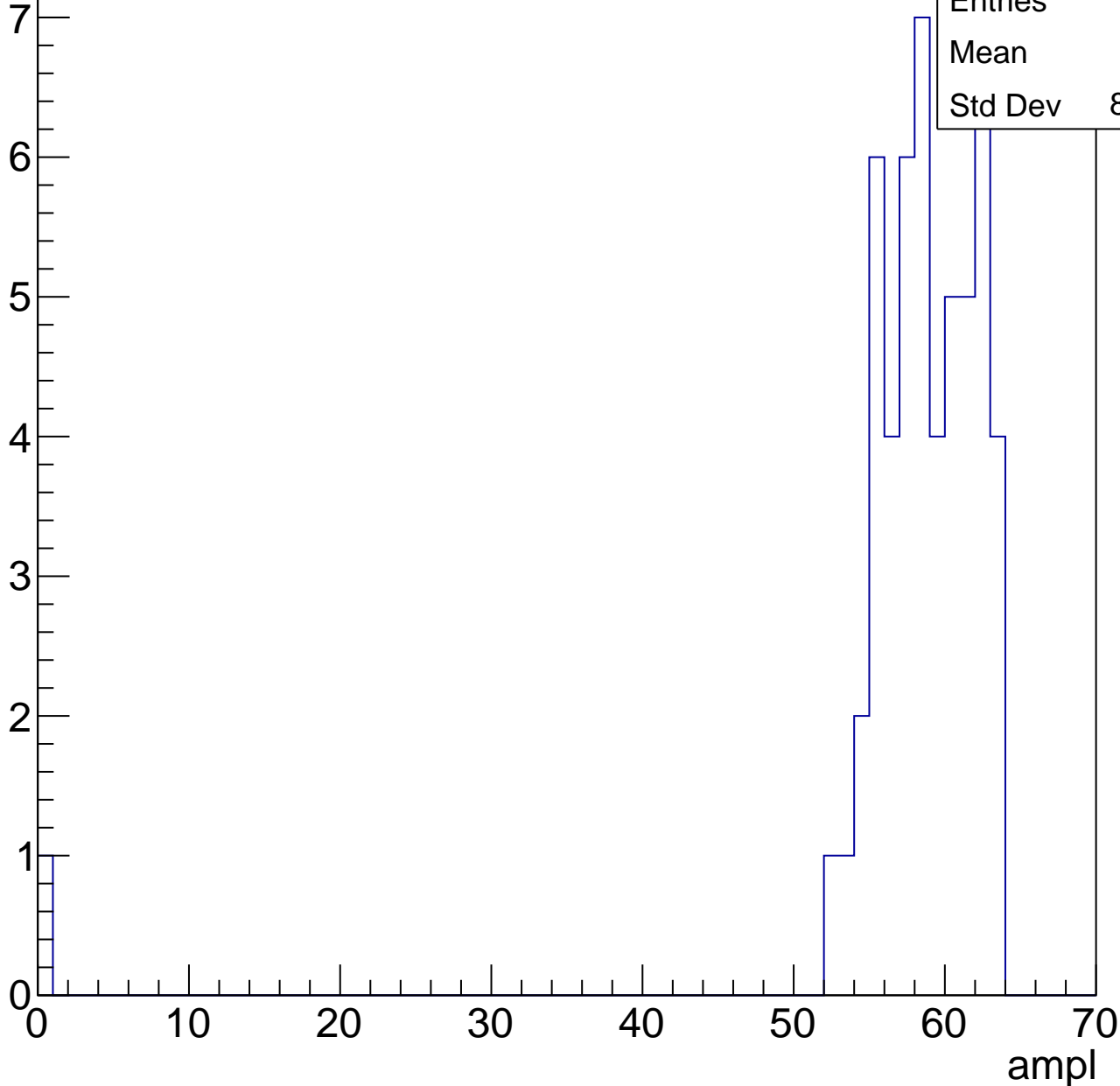


# B1L102S, U25-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.4
Std Dev	8.462

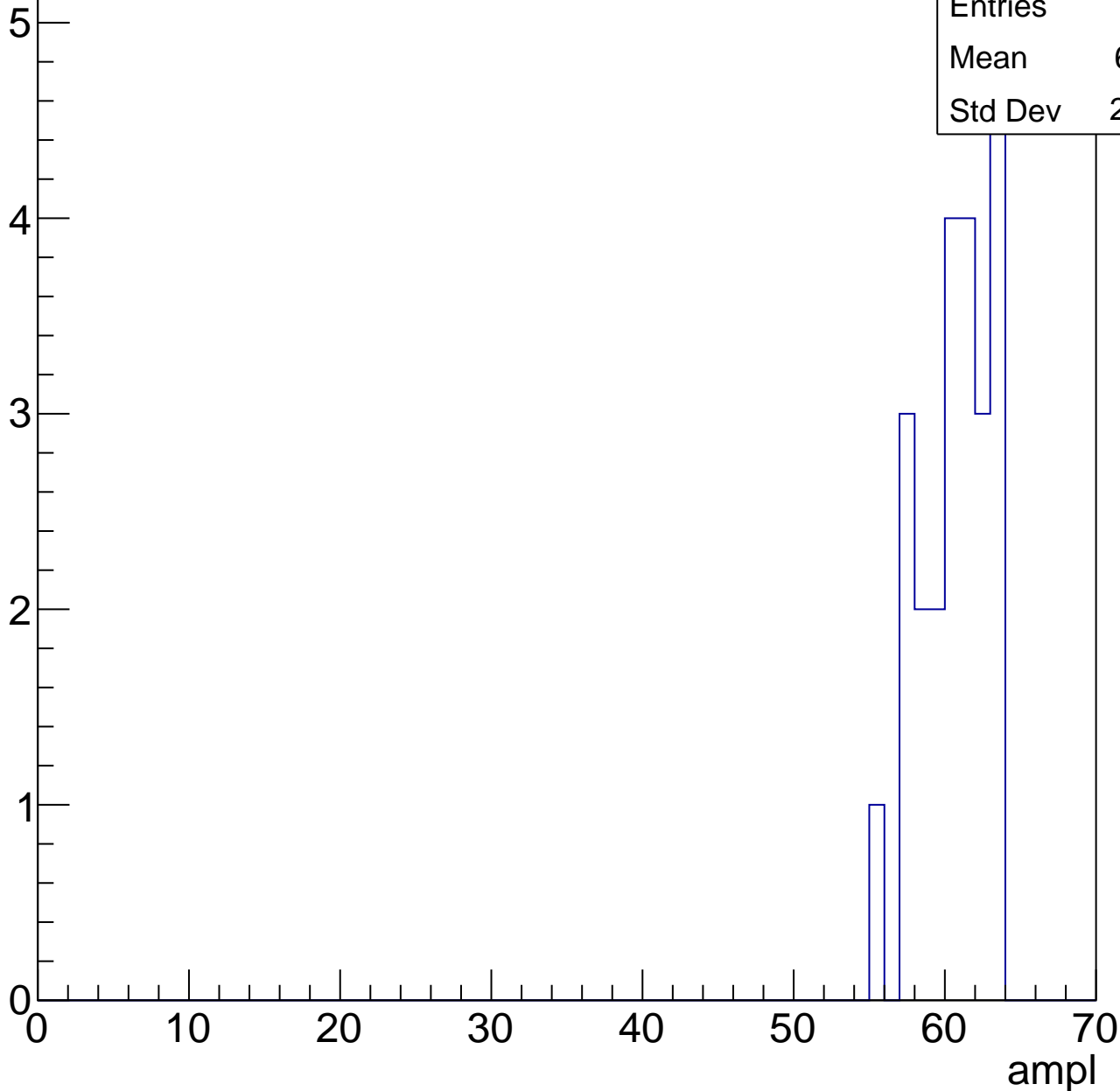


# B1L102S, U25-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	24
Mean	60.21
Std Dev	2.254





# B1L102S, U25-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch107, adc0

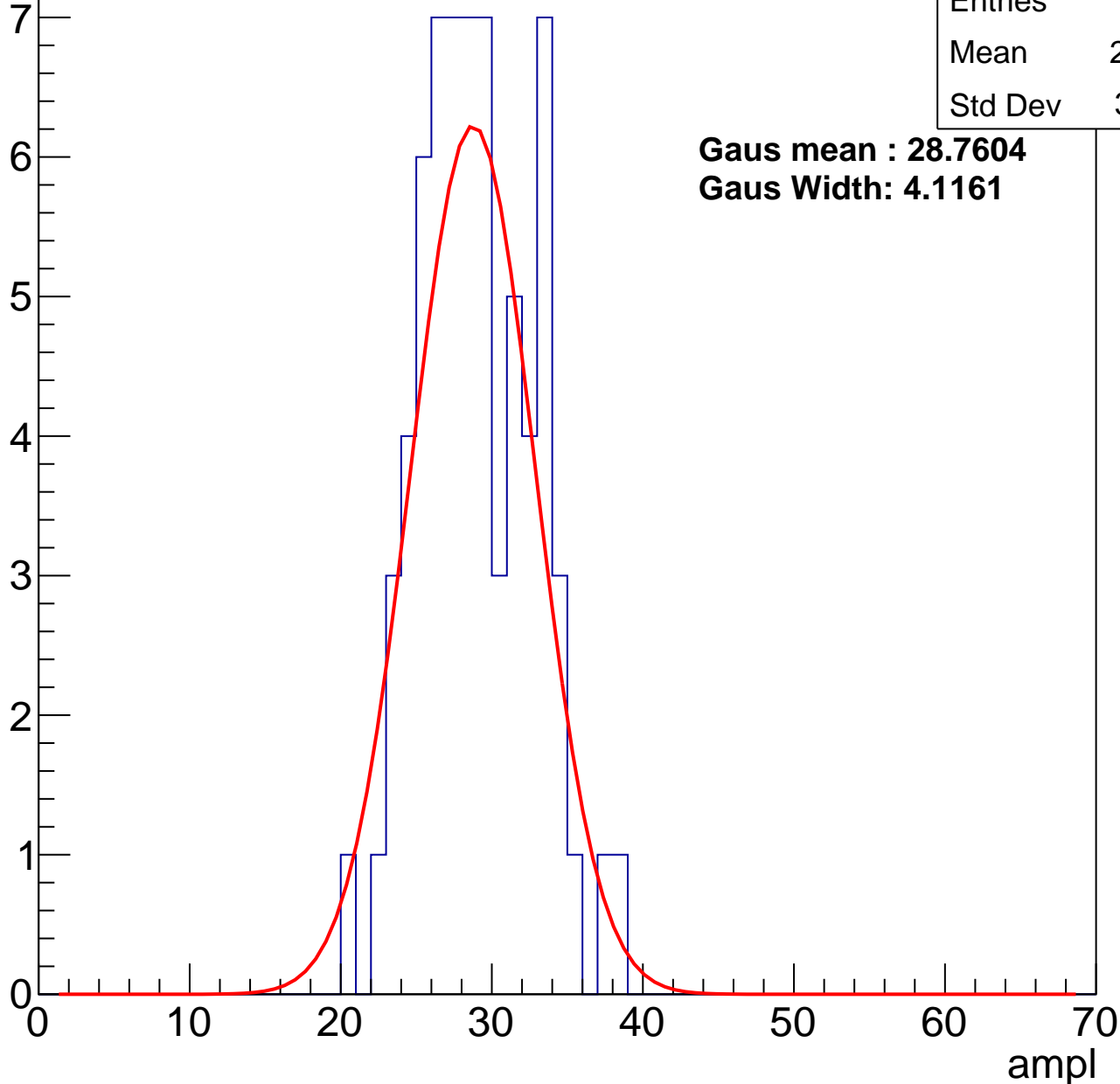
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.57
Std Dev	3.731

**Gaus mean : 28.7604**

**Gaus Width: 4.1161**



# B1L102S, U25-ch107, adc1

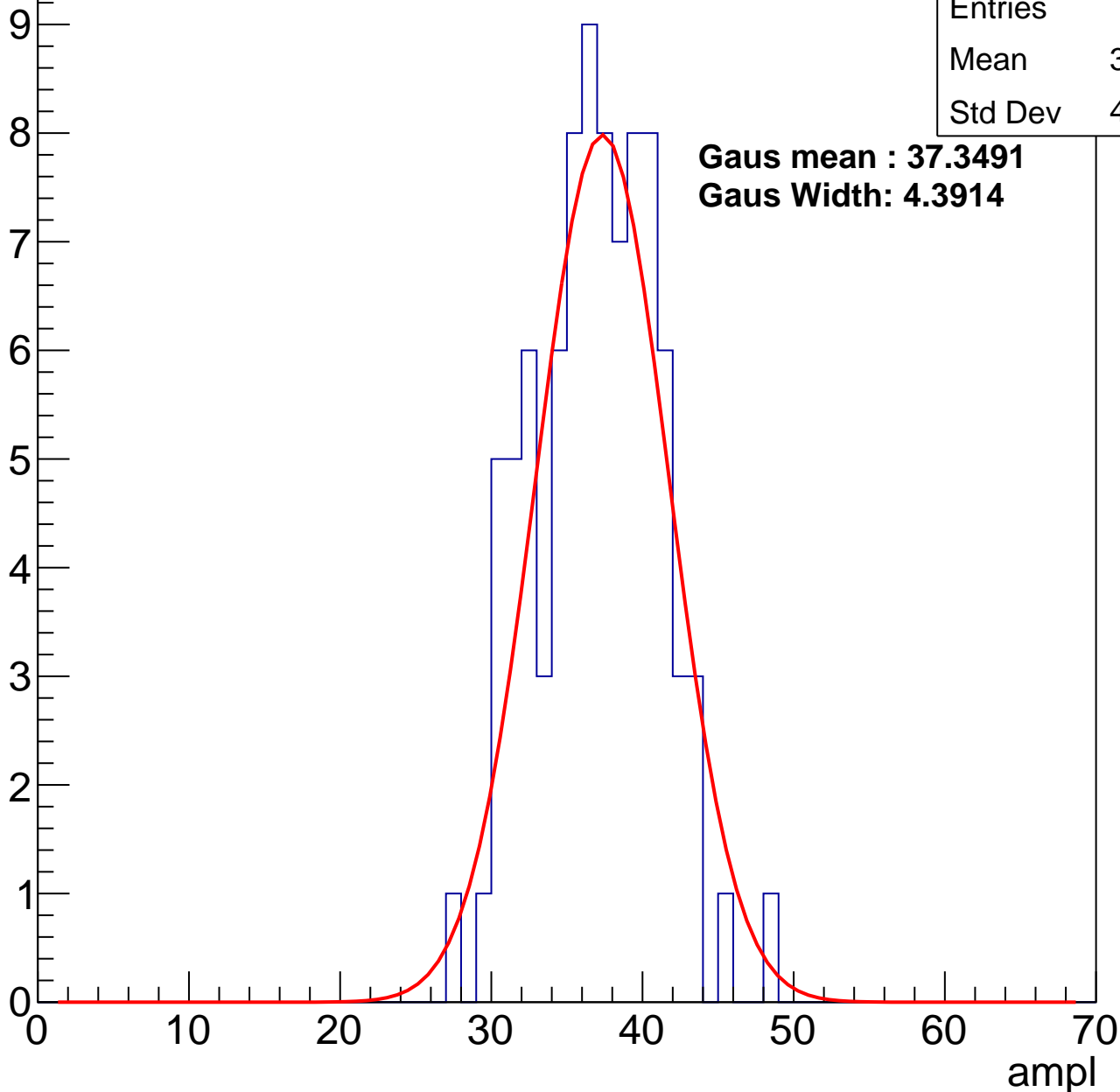
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	89
Mean	36.49
Std Dev	4.014

**Gaus mean : 37.3491**

**Gaus Width: 4.3914**



# B1L102S, U25-ch107, adc2

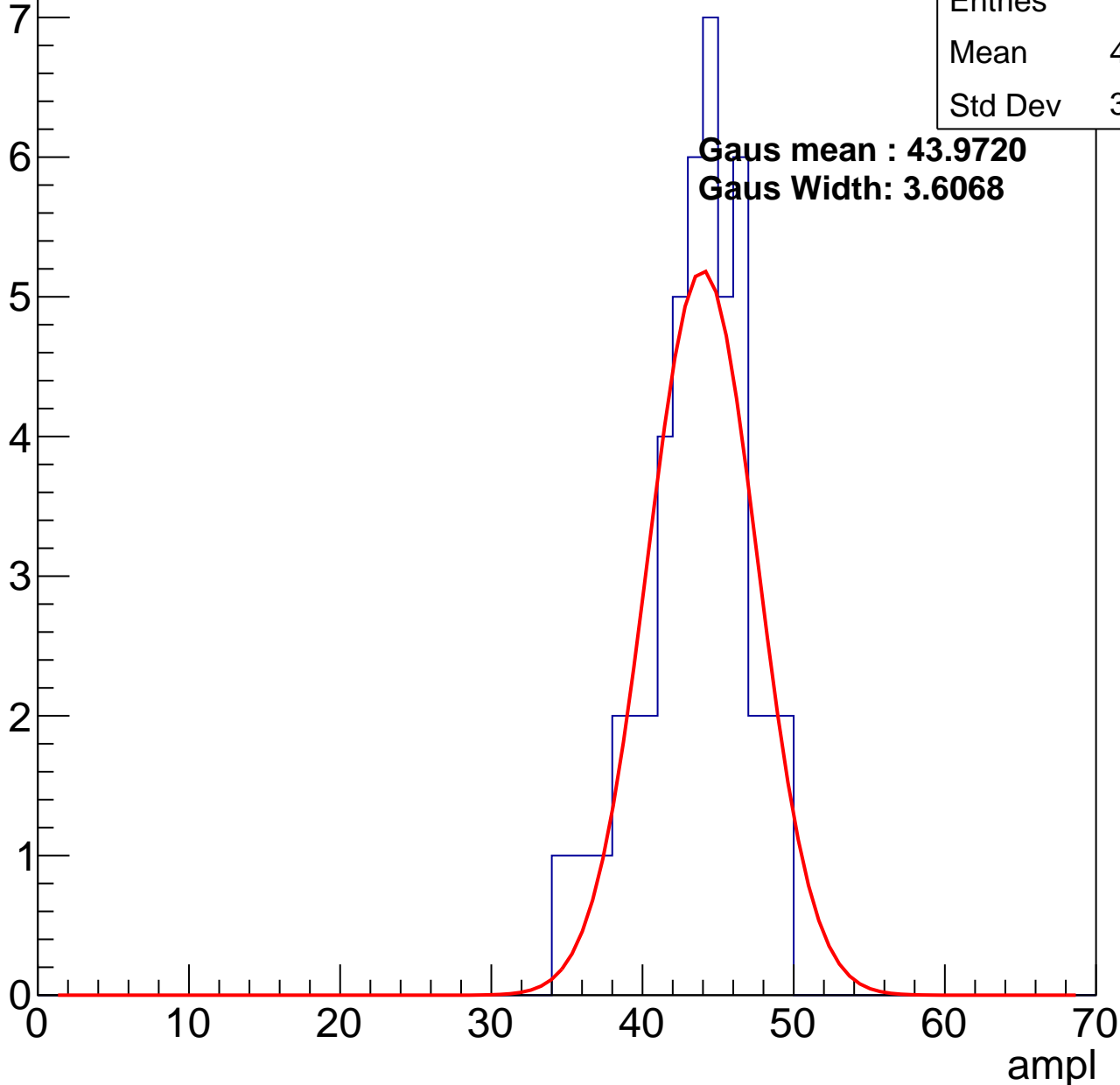
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.96
Std Dev	3.458

**Gaus mean : 43.9720**

**Gaus Width: 3.6068**

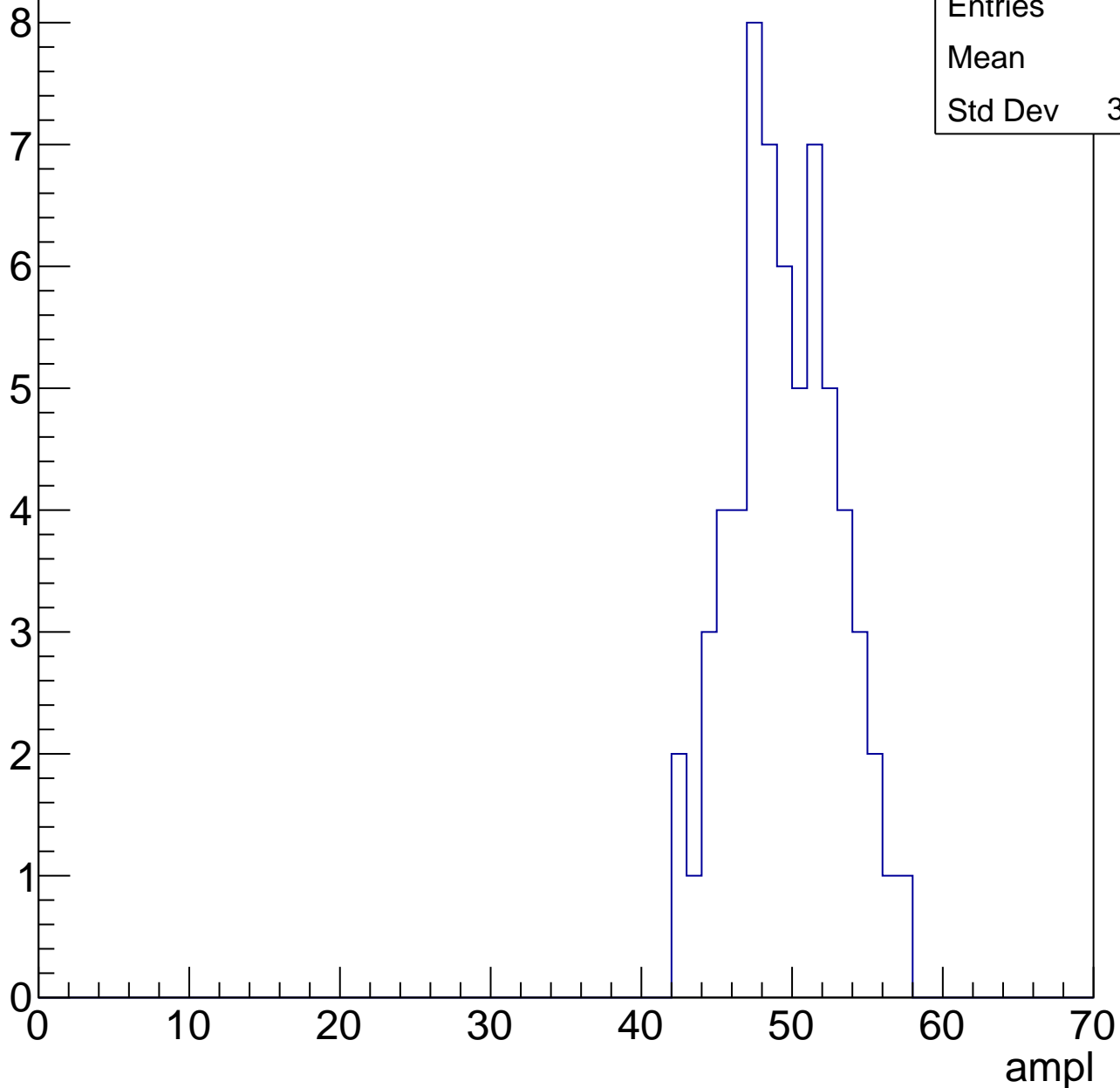


# B1L102S, U25-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.1
Std Dev	3.449

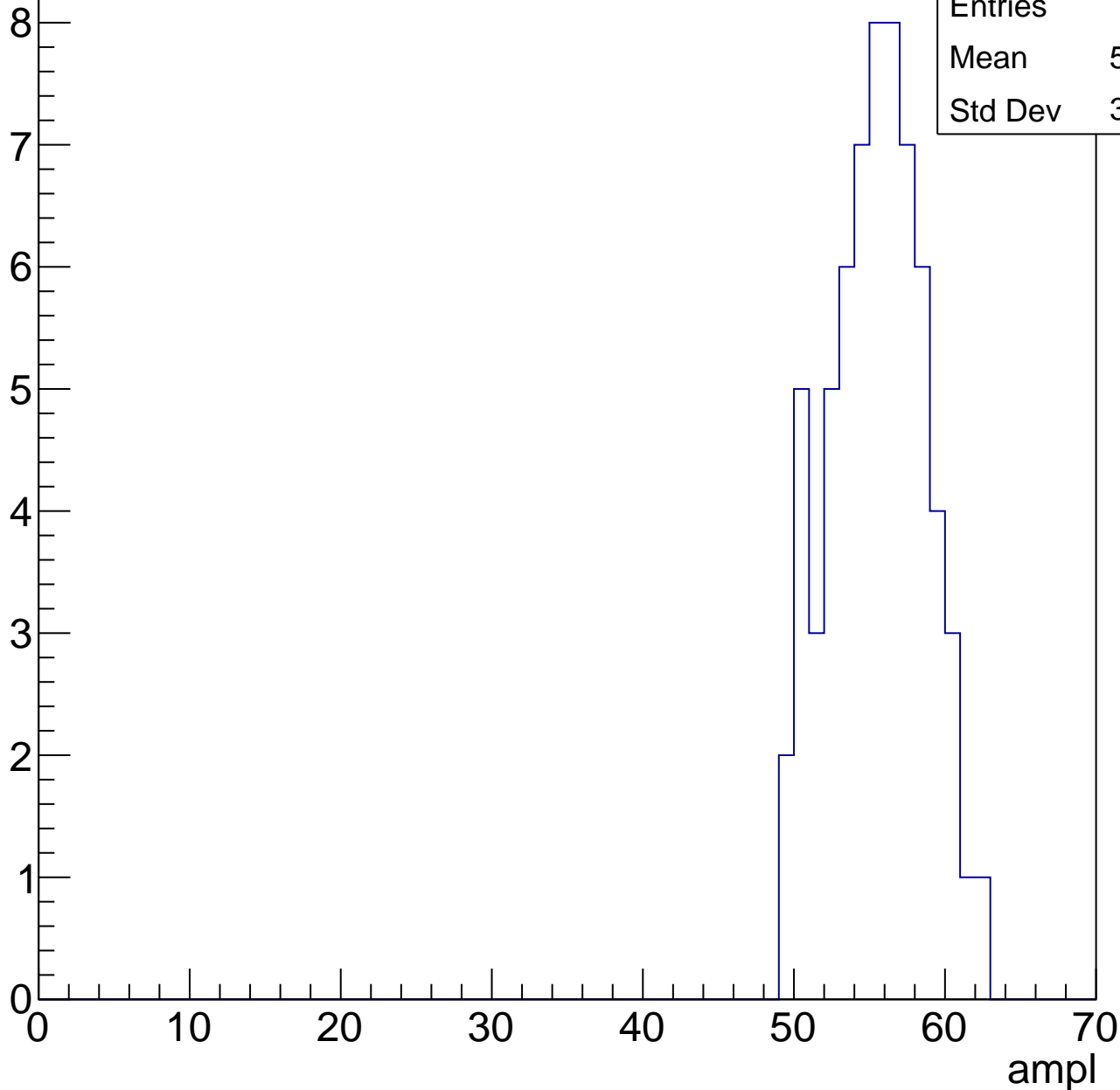


# B1L102S, U25-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.02
Std Dev	3.102

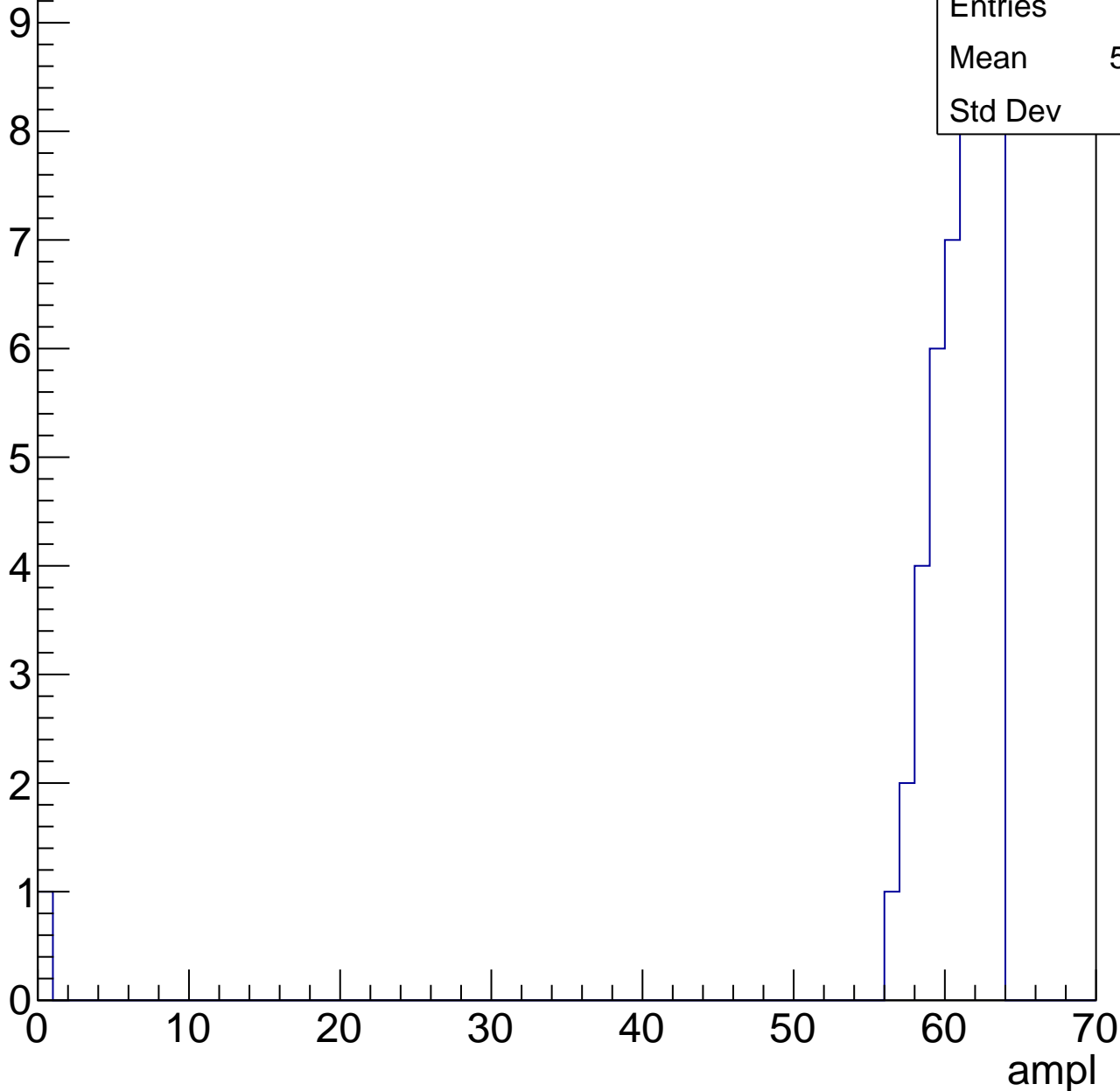


# B1L102S, U25-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.24
Std Dev	9.02



# B1L102S, U25-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch108, adc0

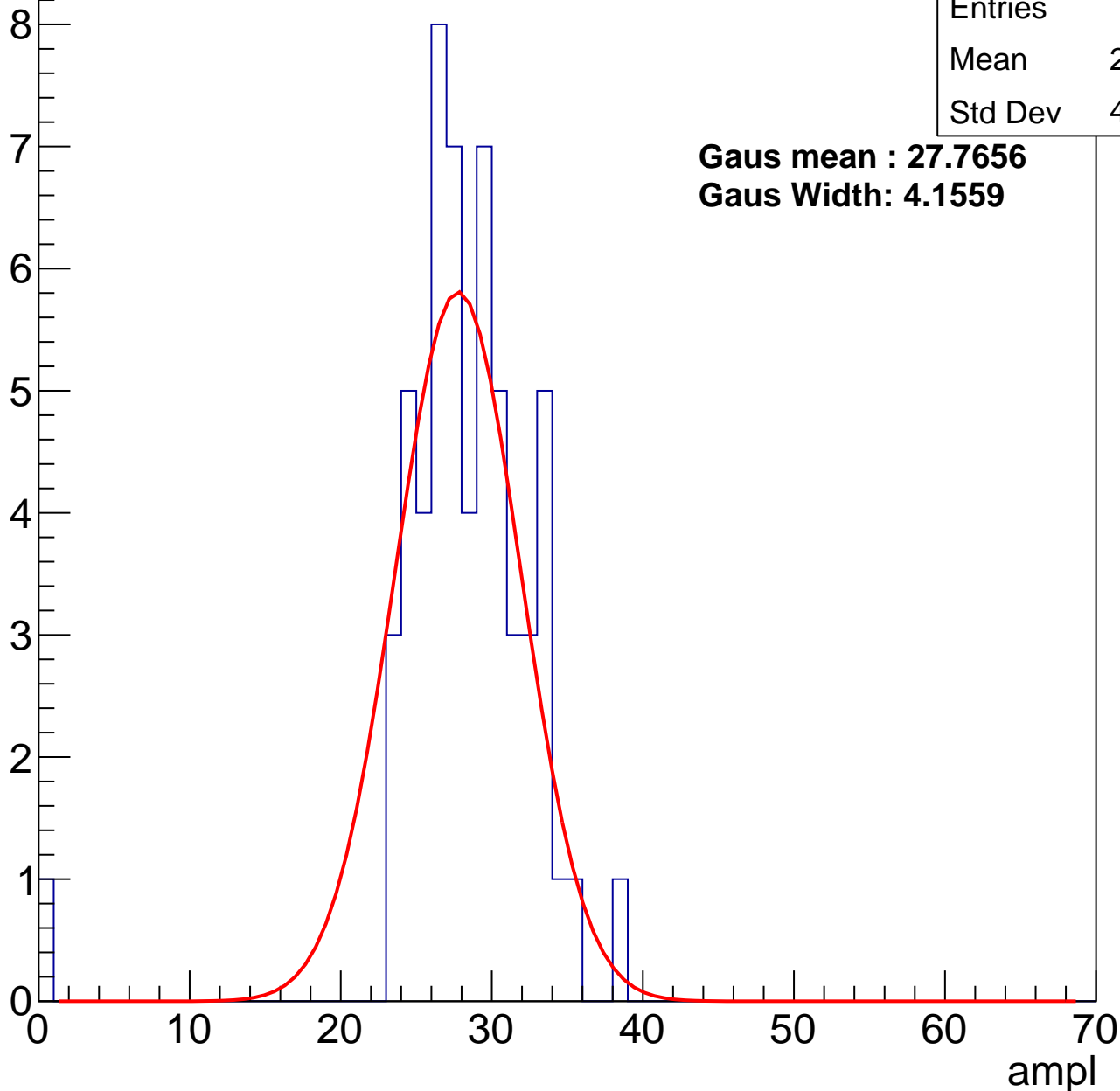
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.79
Std Dev	4.954

**Gaus mean : 27.7656**

**Gaus Width: 4.1559**



# B1L102S, U25-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	33.73
Std Dev	3.812

**Gaus mean : 34.6464**

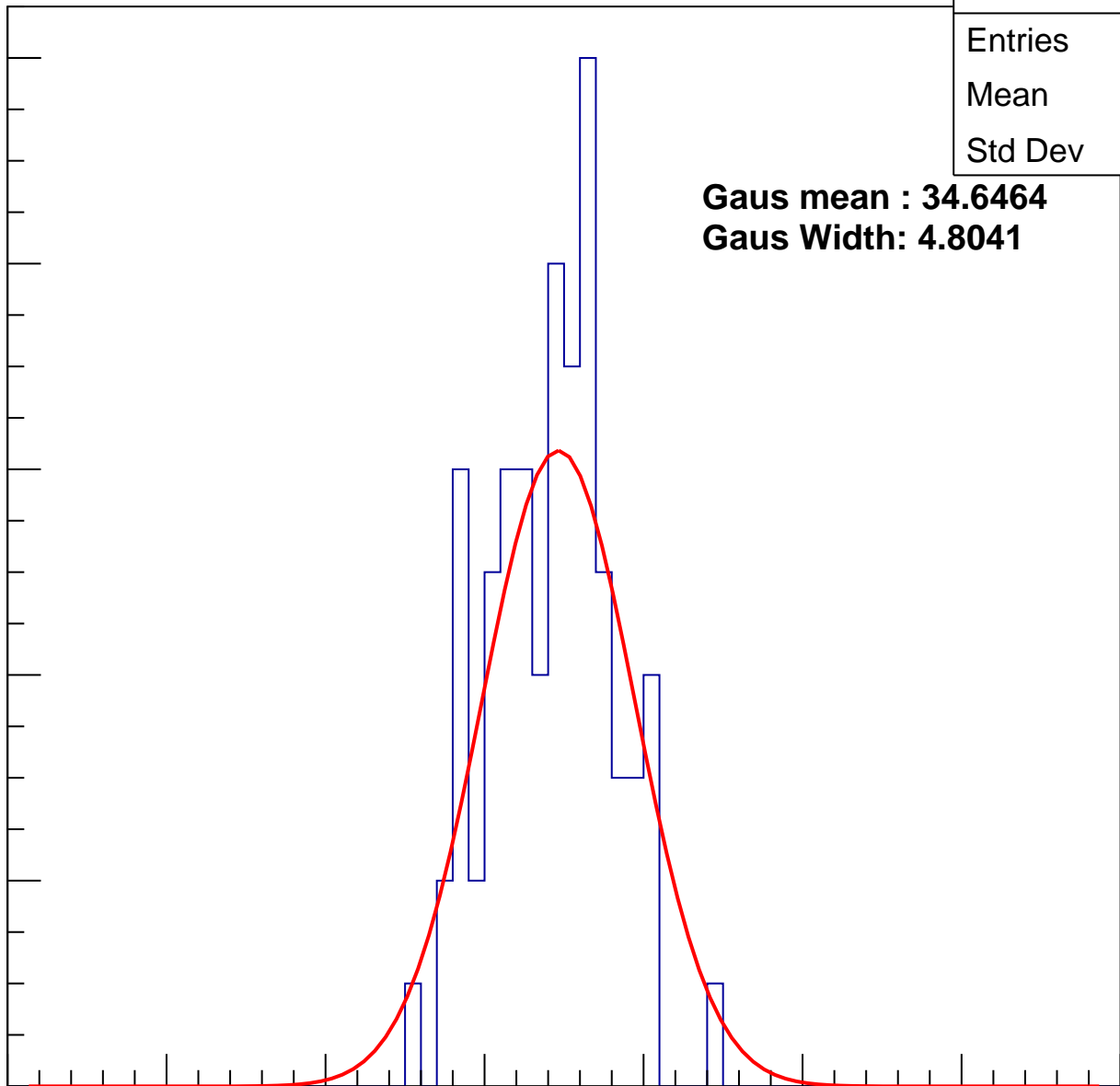
**Gaus Width: 4.8041**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch108, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	64
Mean	40.59
Std Dev	3.63

**Gaus mean : 40.8152**

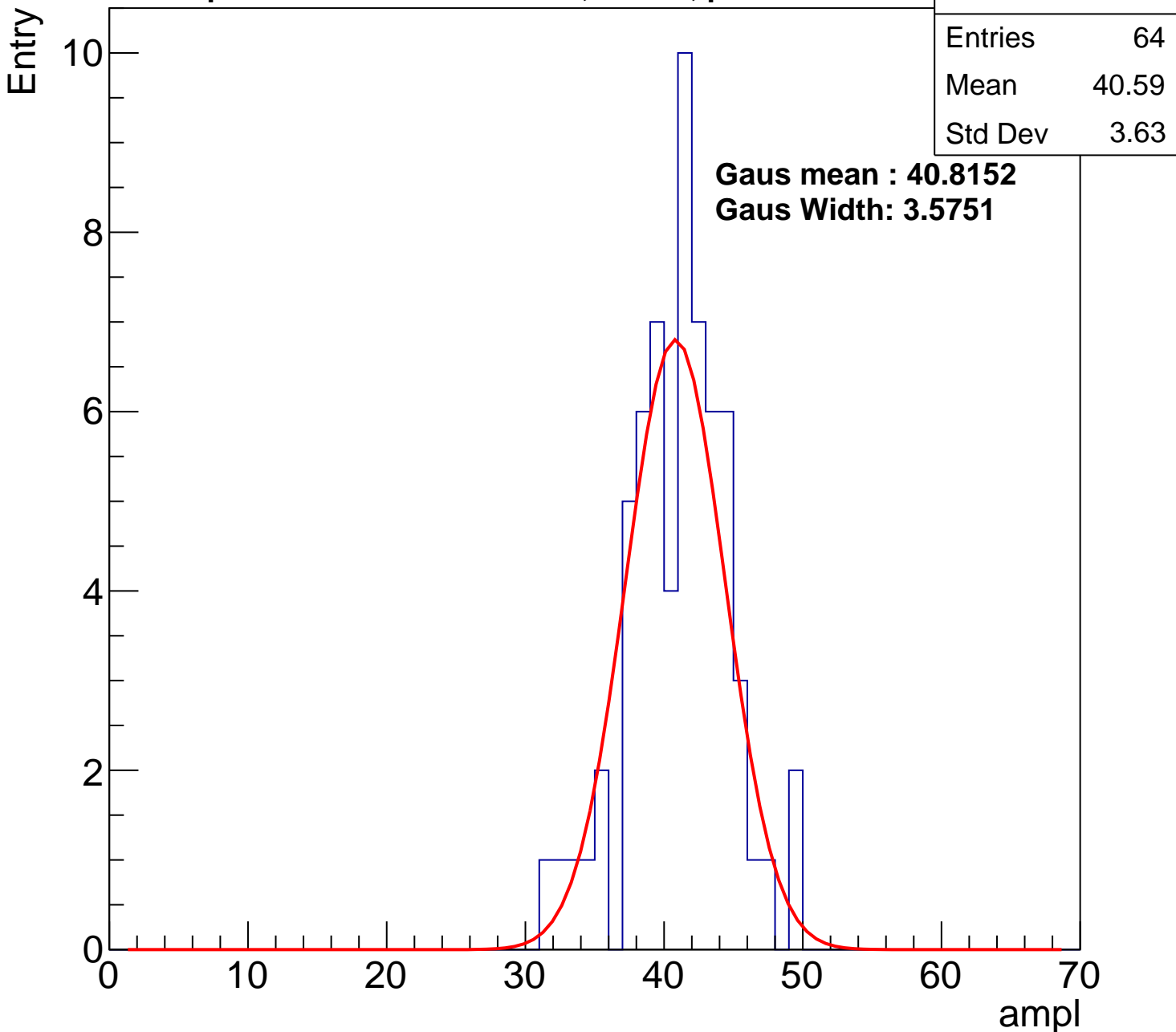
**Gaus Width: 3.5751**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

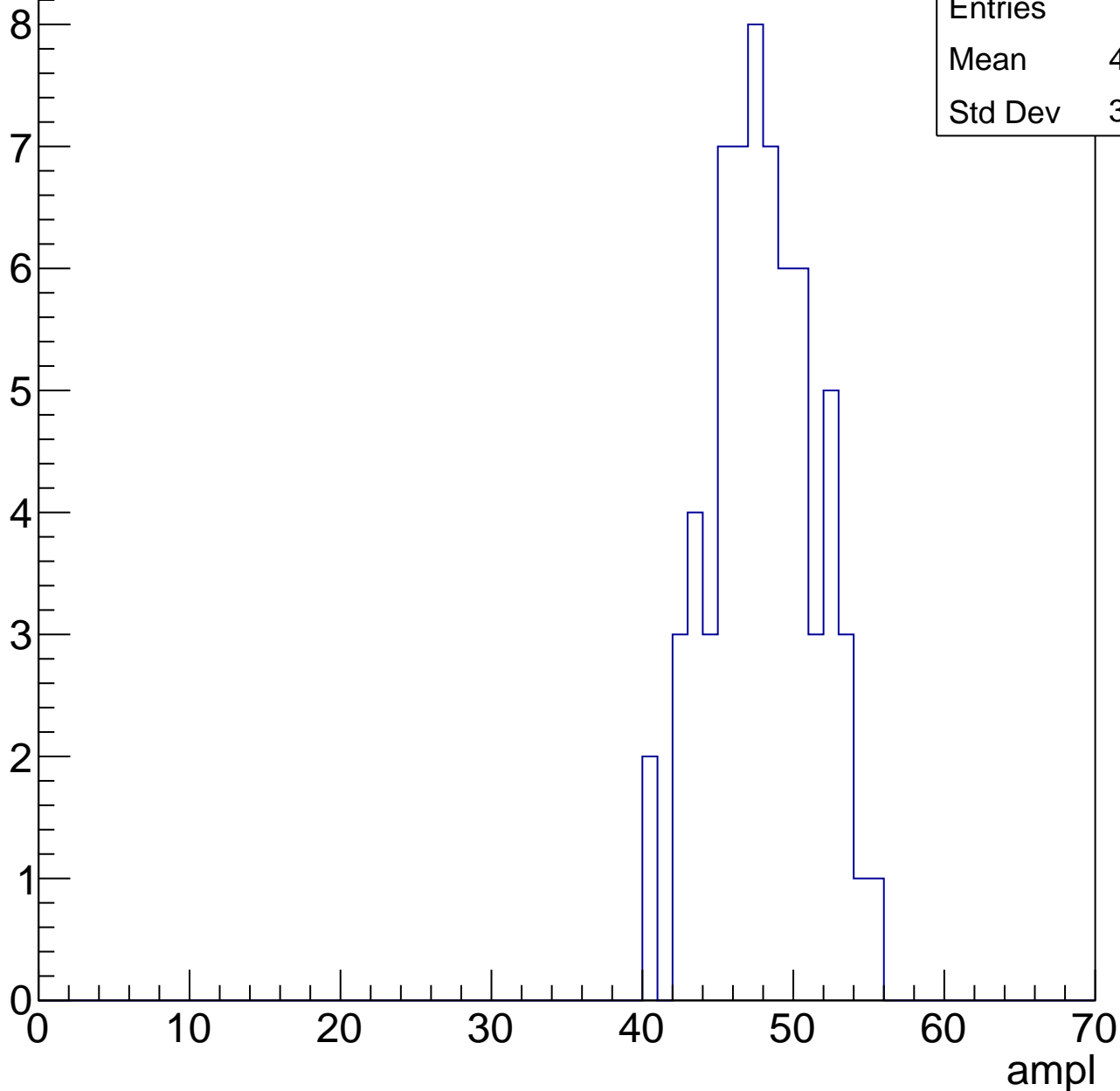


# B1L102S, U25-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.48
Std Dev	3.399

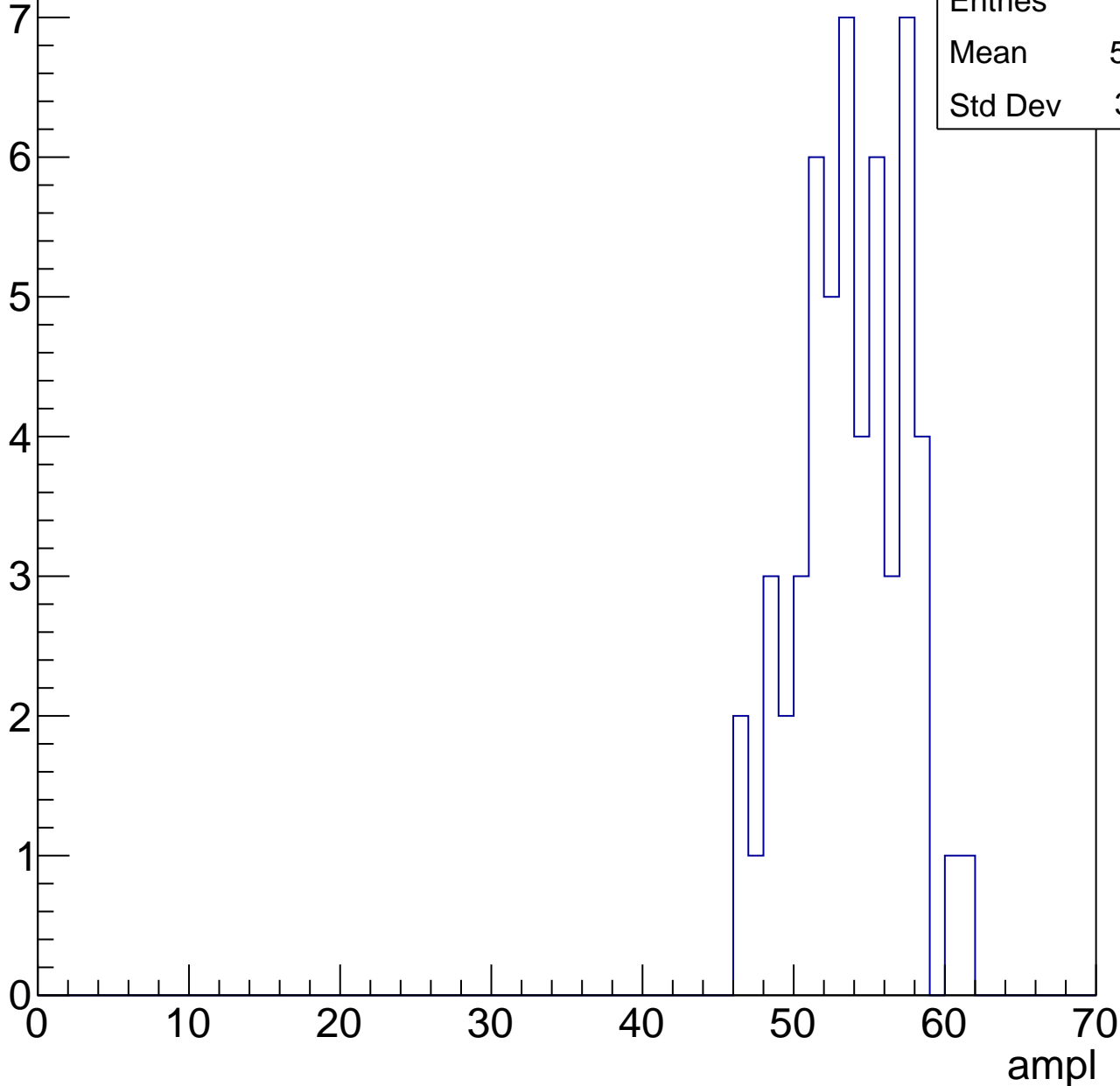


# B1L102S, U25-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

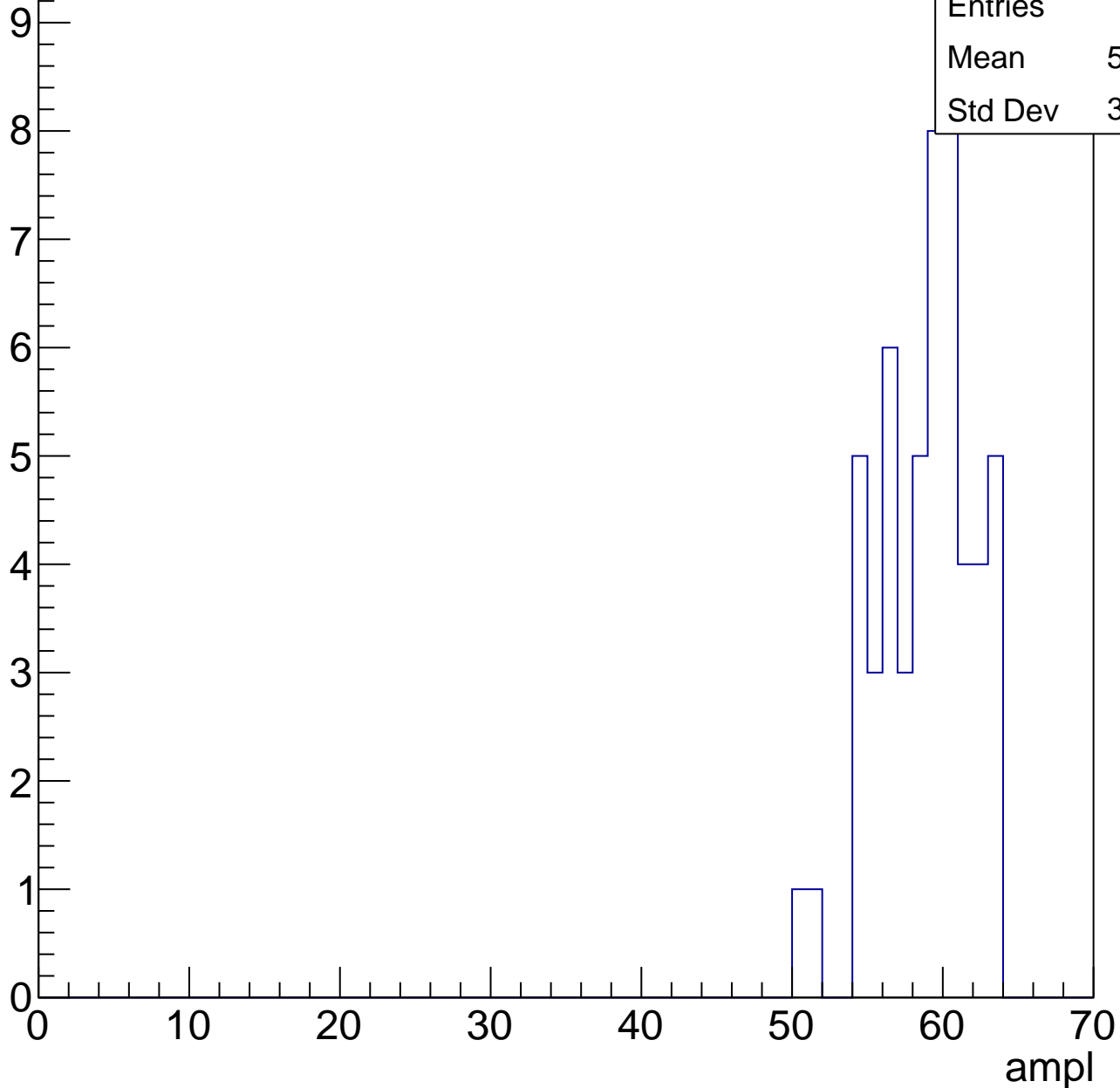
Entries	55
Mean	53.35
Std Dev	3.491



# B1L102S, U25-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

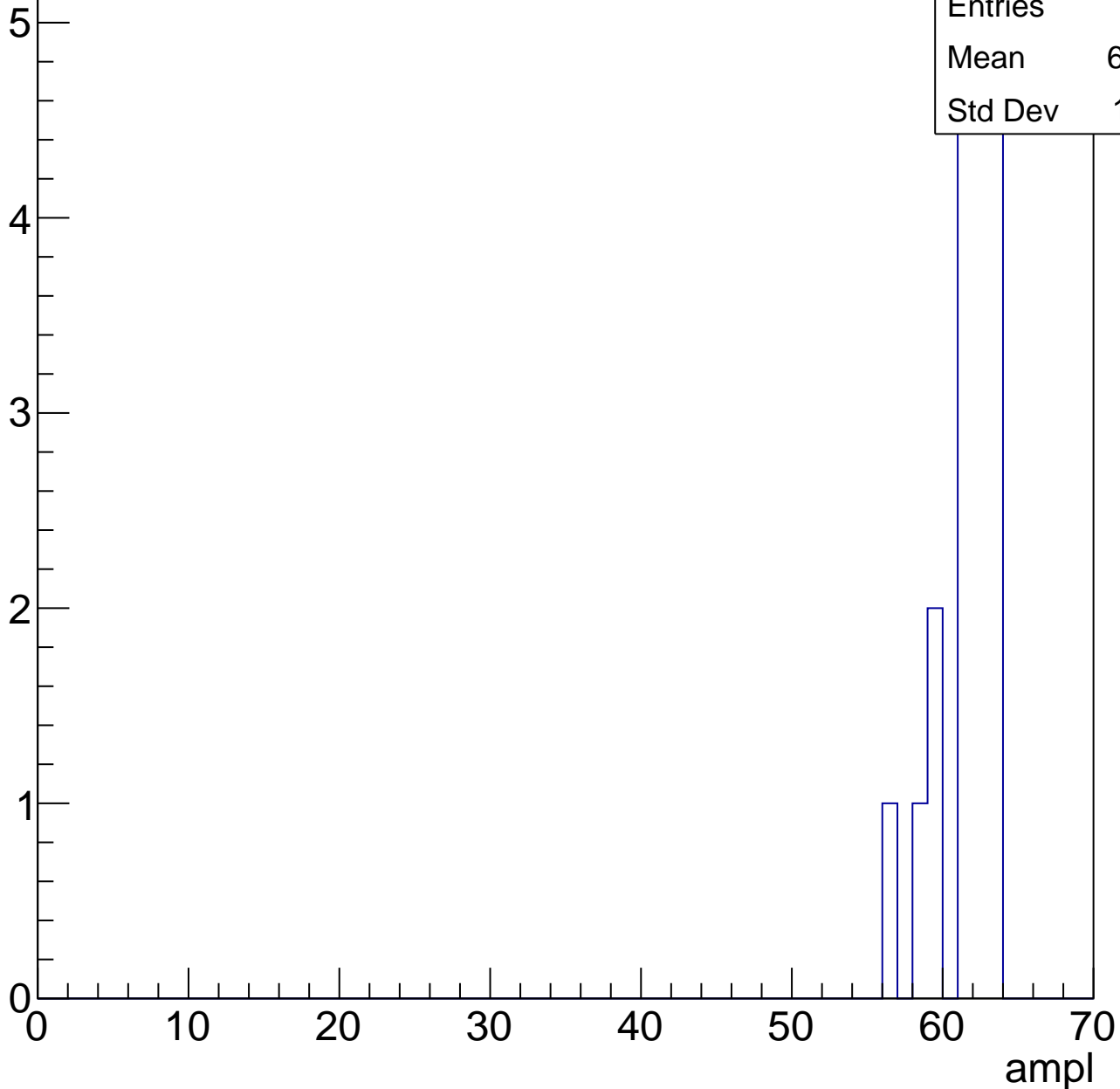


# B1L102S, U25-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.16
Std Dev	1.871

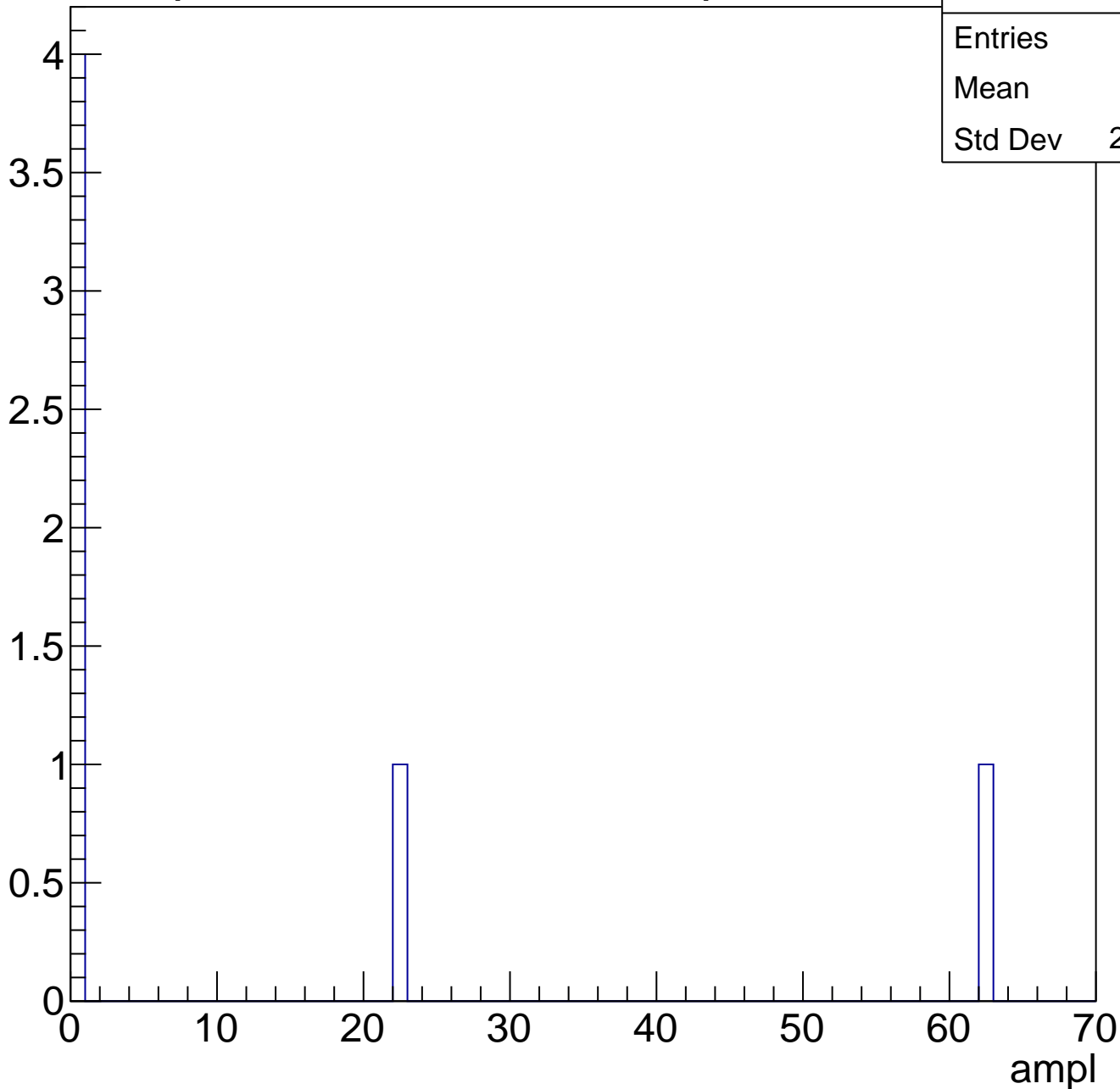




# B1L102S, U25-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	6
Mean	14
Std Dev	22.92

# B1L102S, U25-ch109, adc0

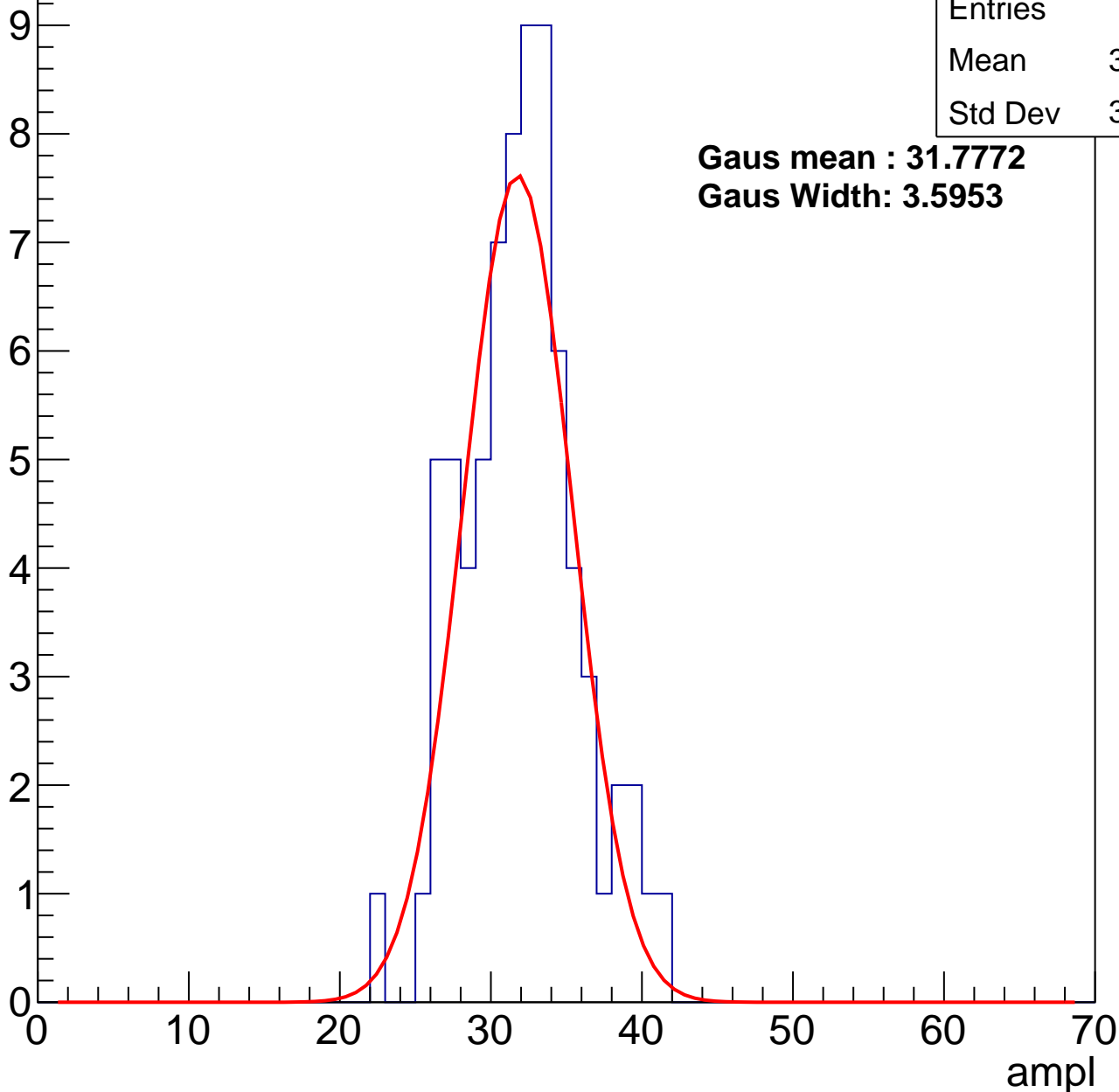
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	74
Mean	31.57
Std Dev	3.753

**Gaus mean : 31.7772**

**Gaus Width: 3.5953**



# B1L102S, U25-ch109, adc1

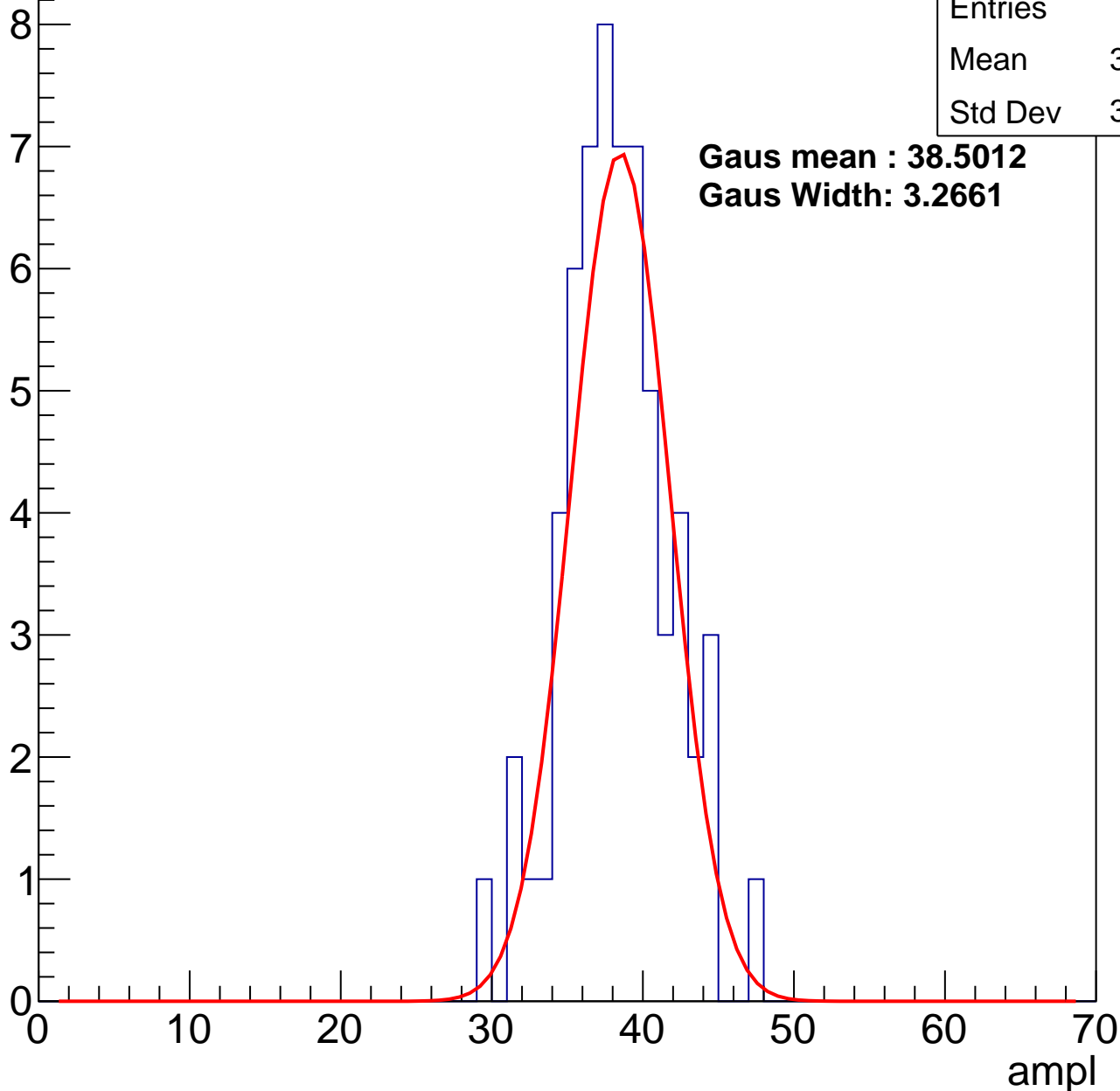
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.82
Std Dev	3.476

**Gaus mean : 38.5012**

**Gaus Width: 3.2661**



# B1L102S, U25-ch109, adc2

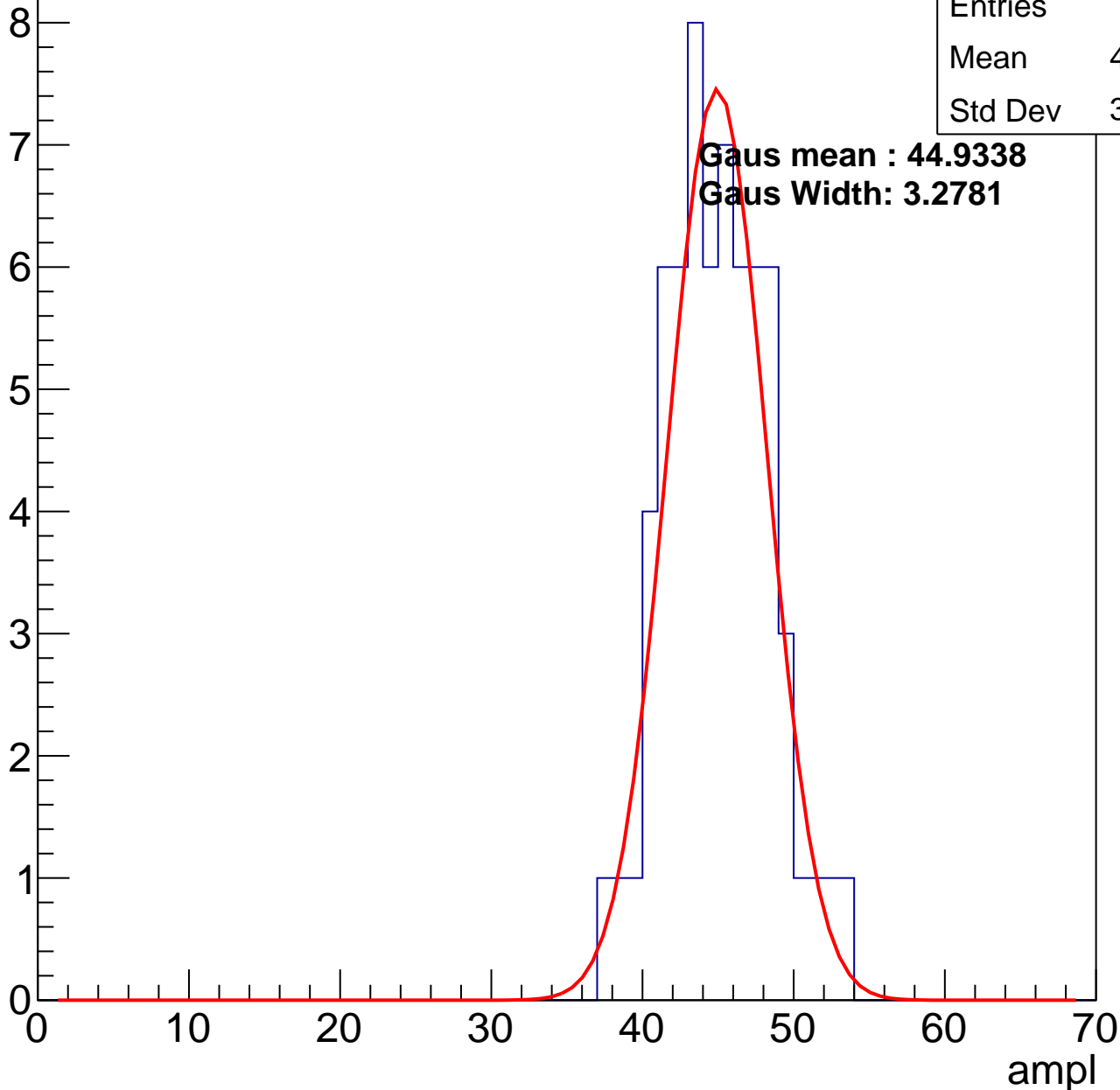
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.52
Std Dev	3.347

**Gaus mean : 44.9338**

**Gaus Width: 3.2781**

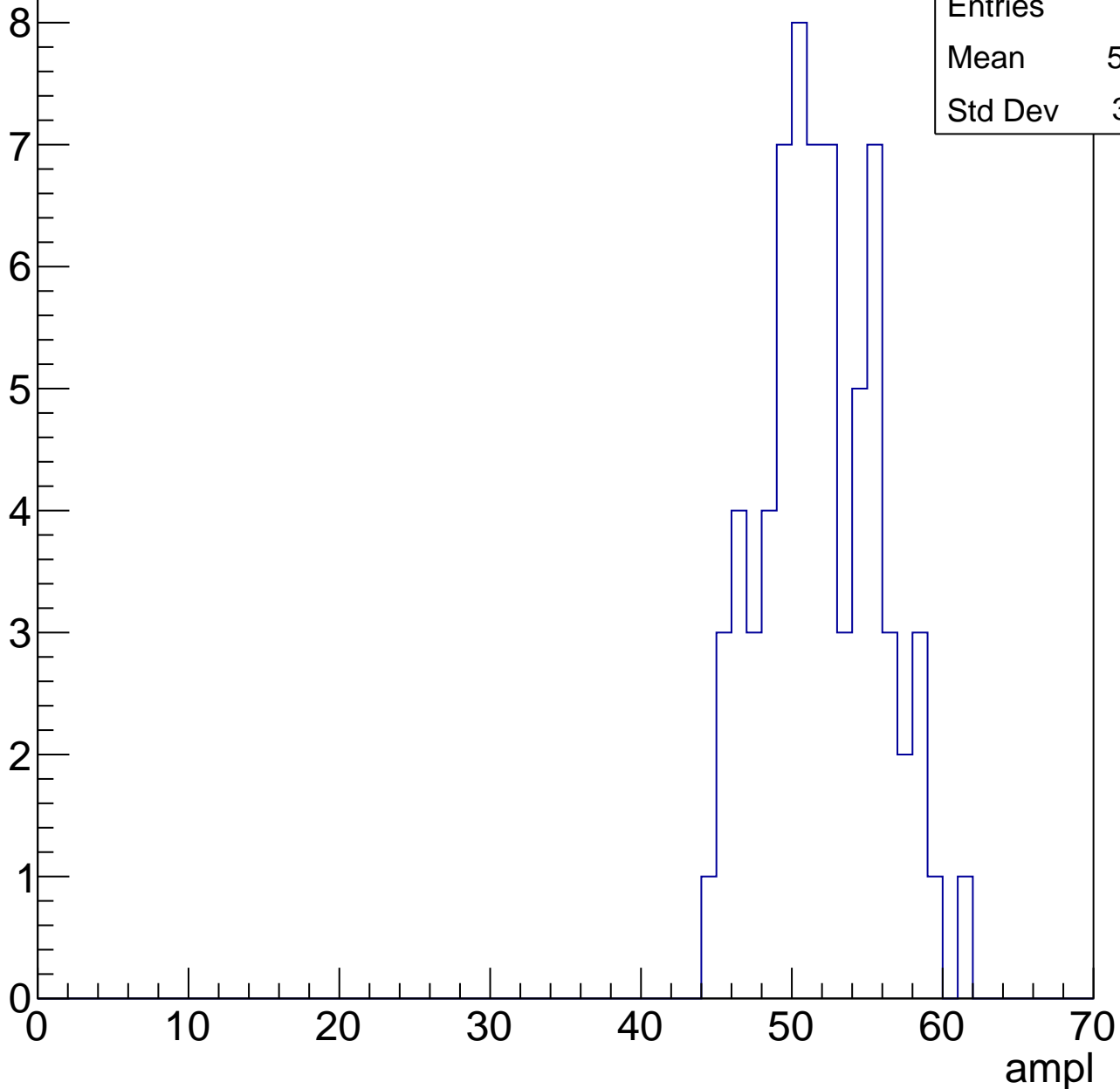


# B1L102S, U25-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	51.45
Std Dev	3.801

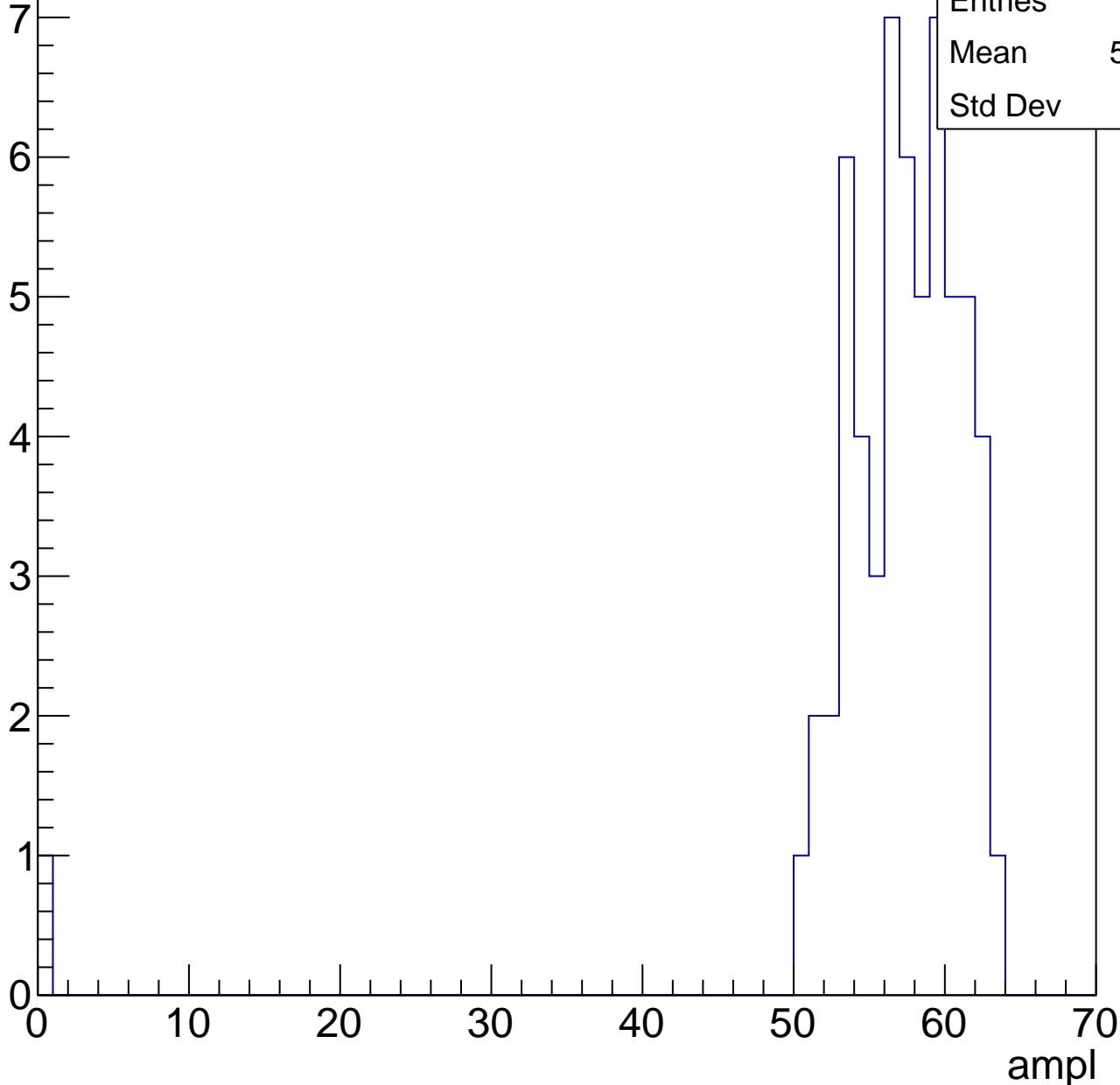


# B1L102S, U25-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.07
Std Dev	8.04

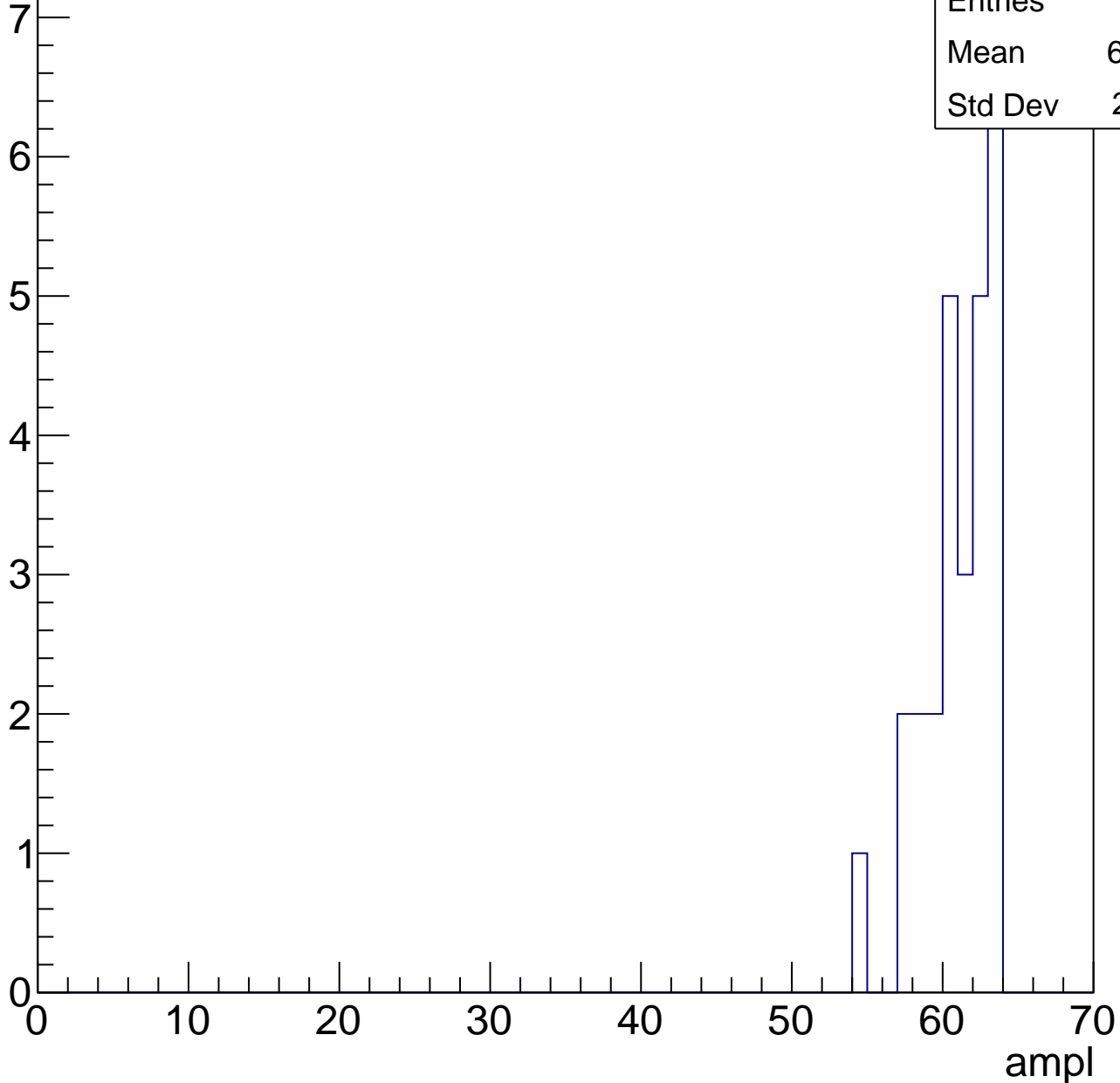


# B1L102S, U25-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	27
Mean	60.59
Std Dev	2.281



# B1L102S, U25-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch110, adc0

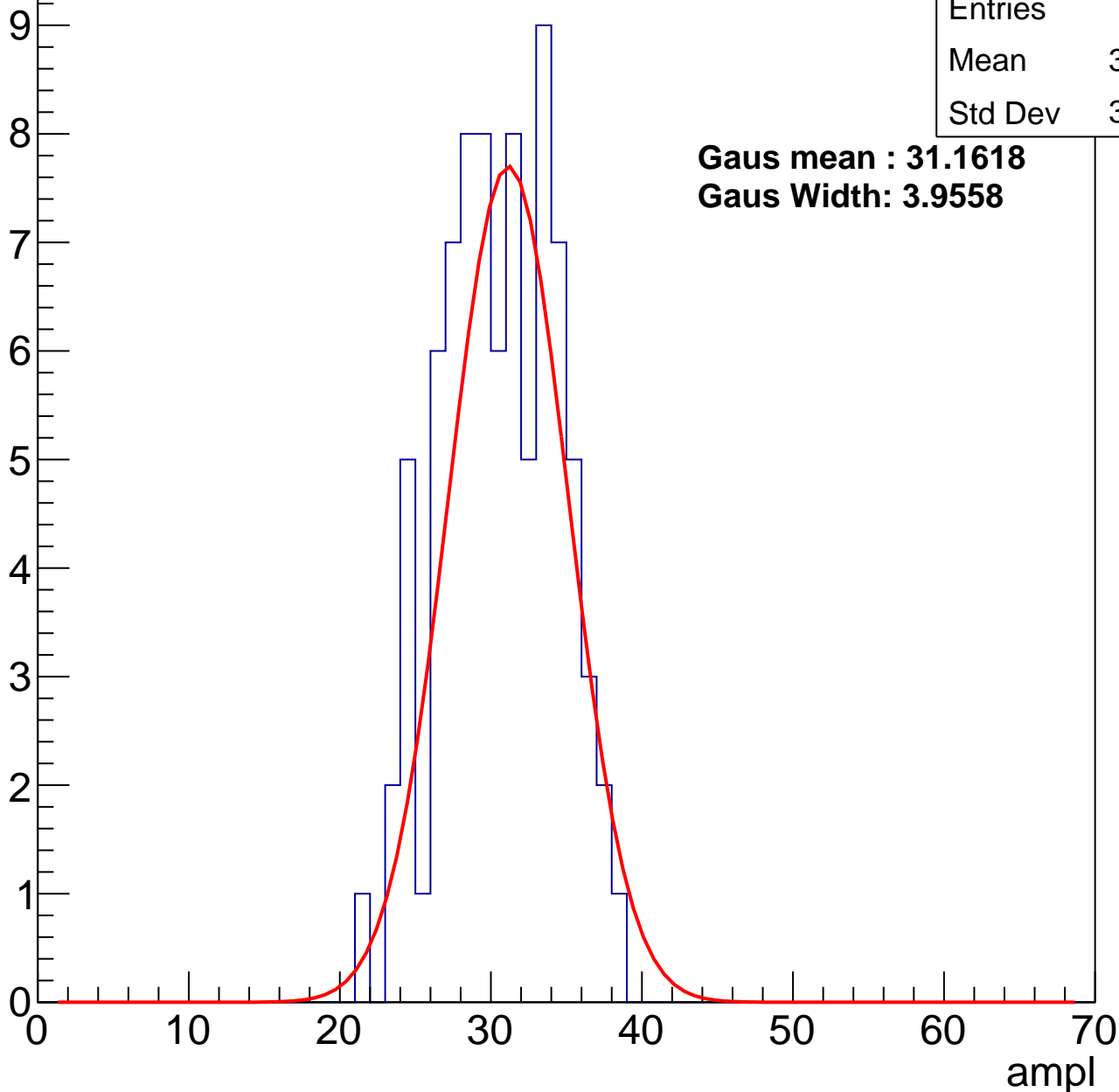
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	84
Mean	30.13
Std Dev	3.776

**Gaus mean : 31.1618**

**Gaus Width: 3.9558**



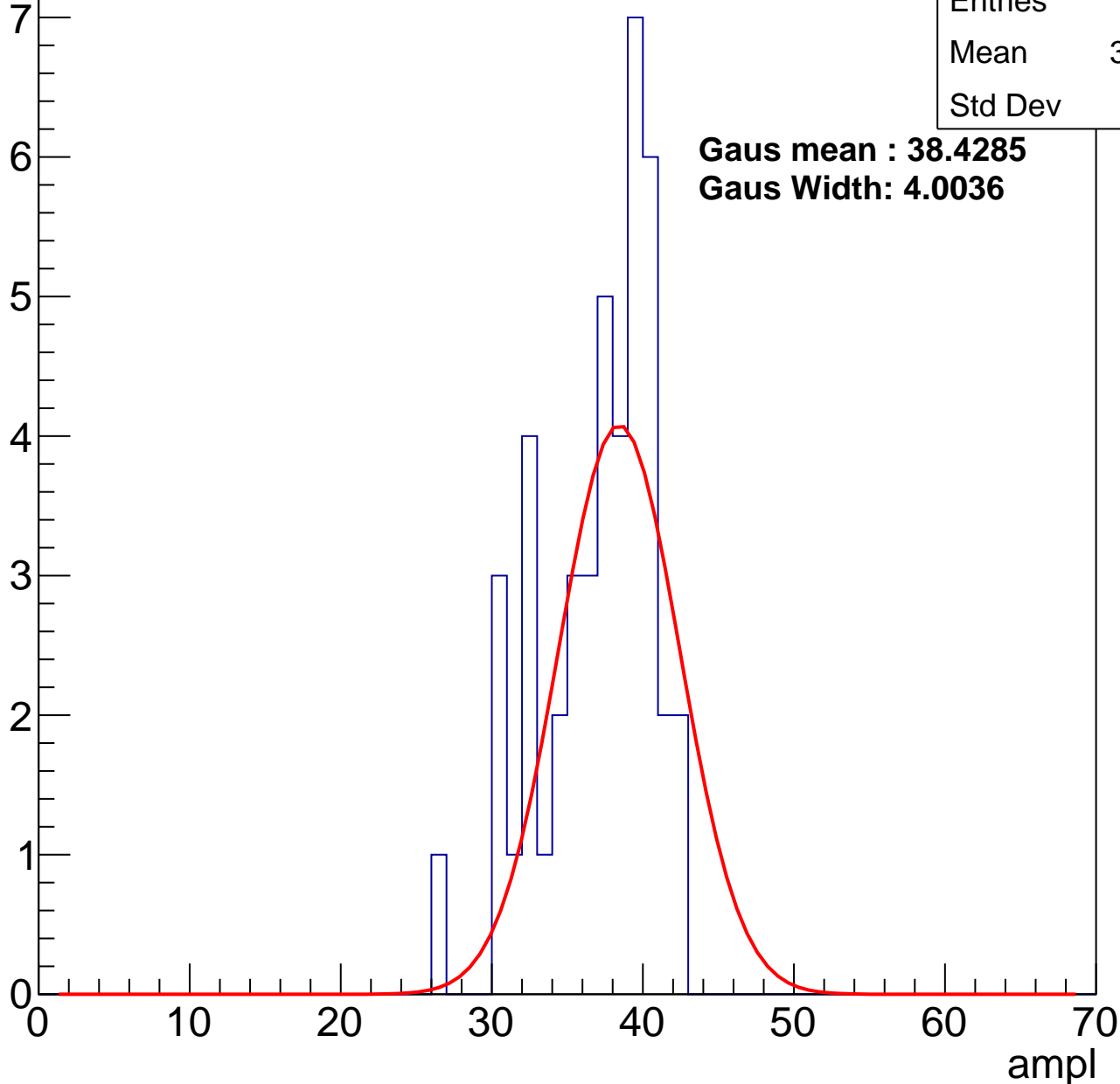
# B1L102S, U25-ch110, adc1

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	44
Mean	36.48
Std Dev	3.72

**Gaus mean : 38.4285**  
**Gaus Width: 4.0036**



# B1L102S, U25-ch110, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	75
Mean	41.93
Std Dev	3.838

**Gaus mean : 42.5823**

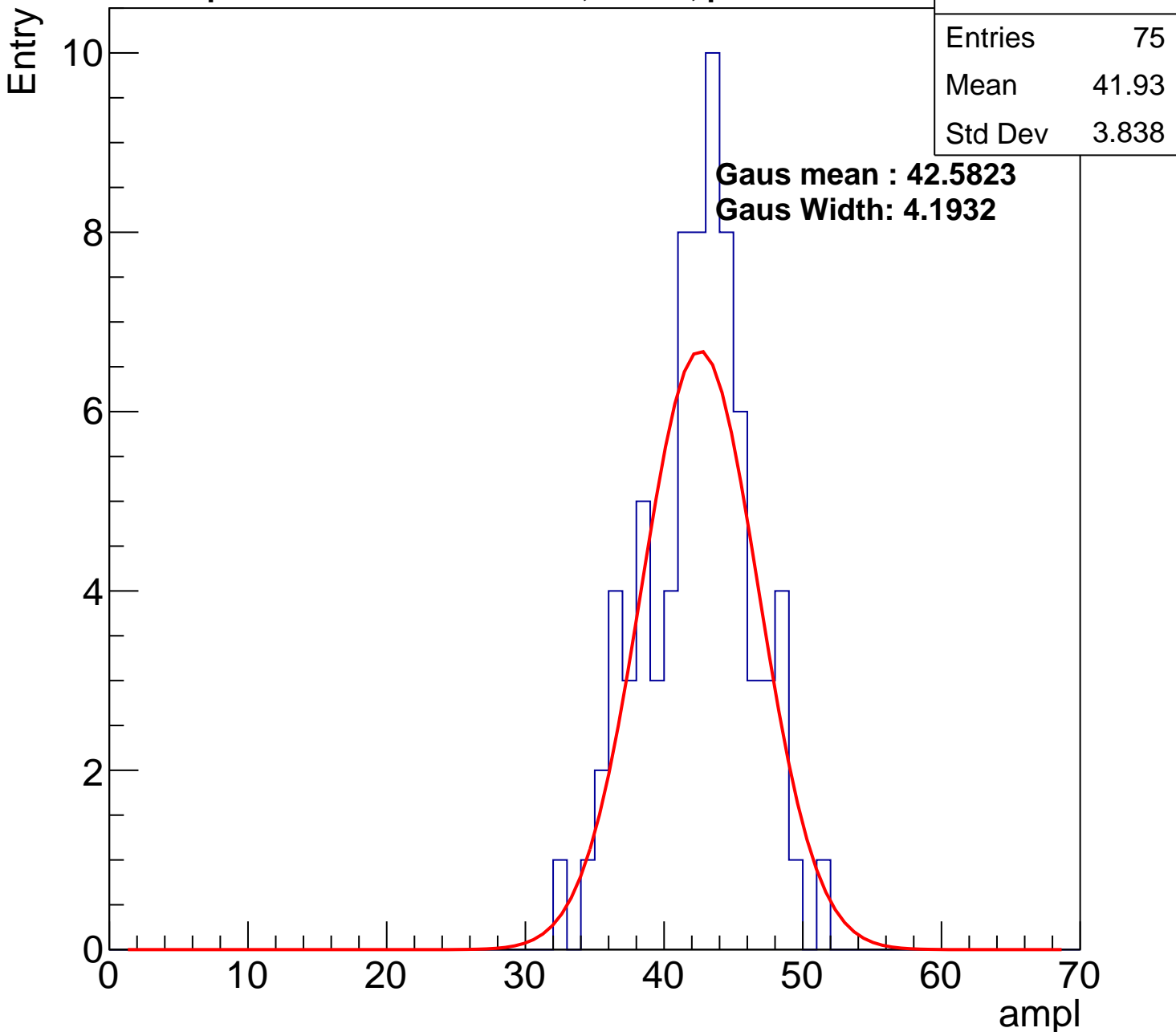
**Gaus Width: 4.1932**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

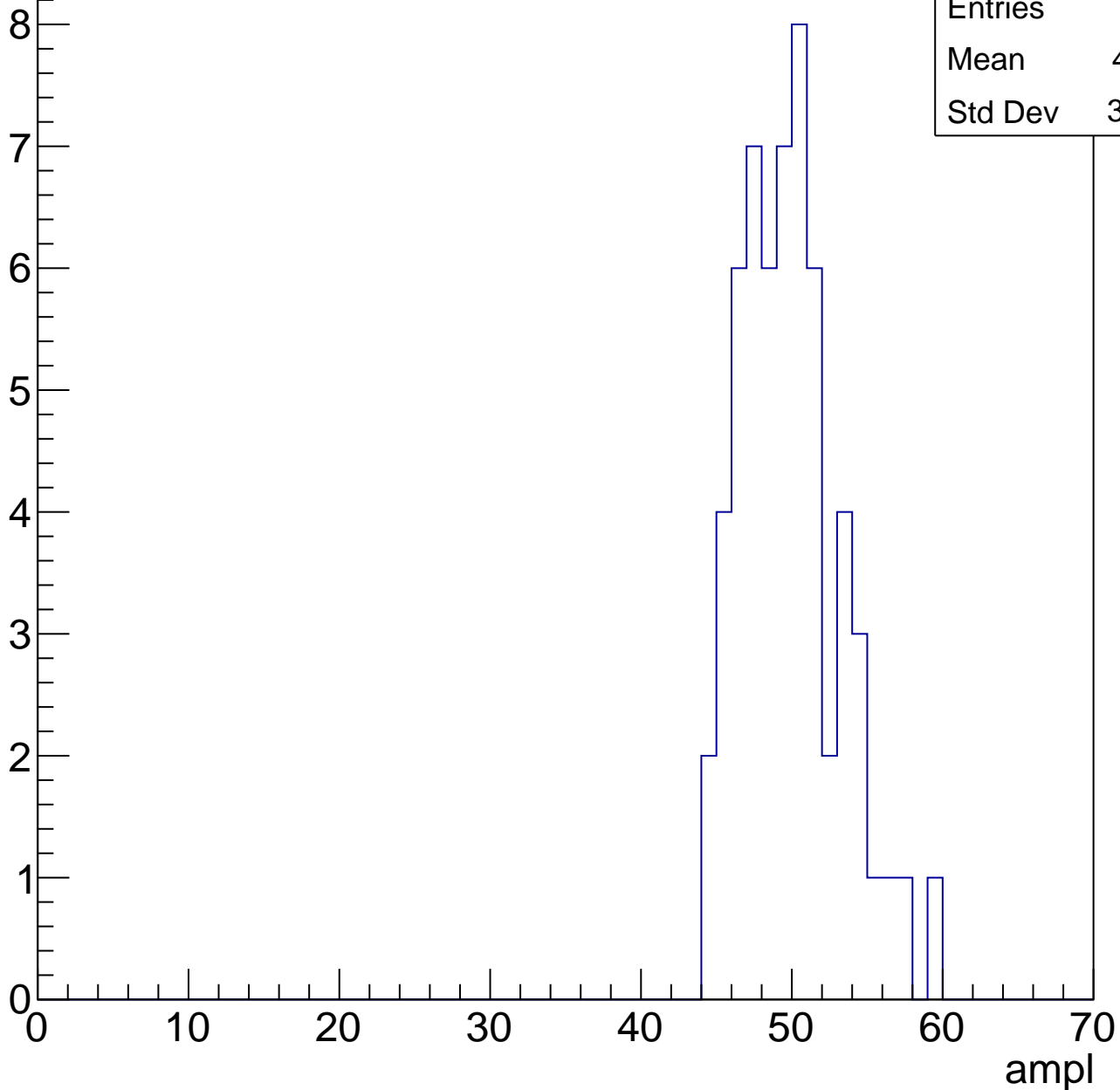


# B1L102S, U25-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.41
Std Dev	3.268

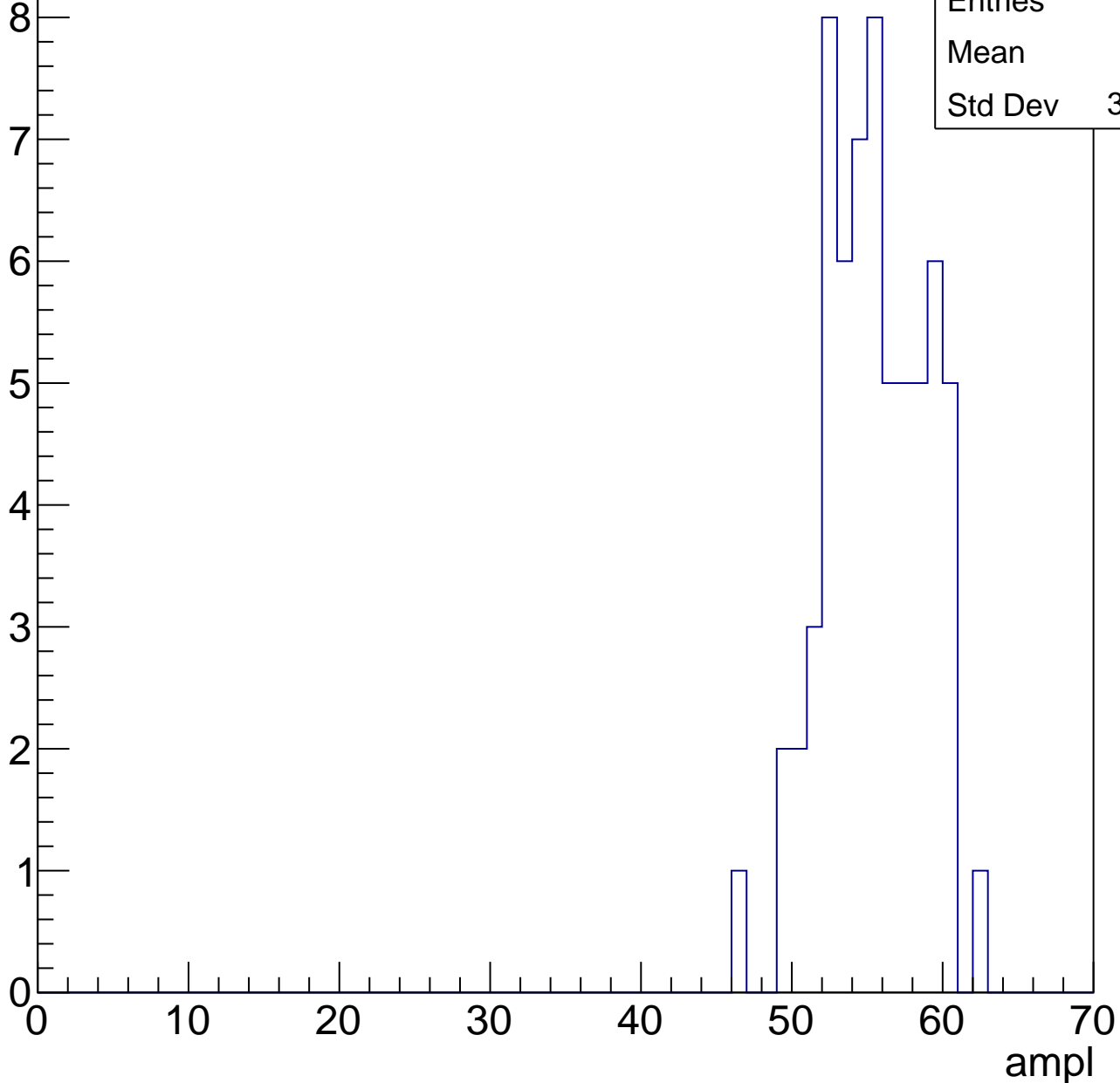


# B1L102S, U25-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55
Std Dev	3.293

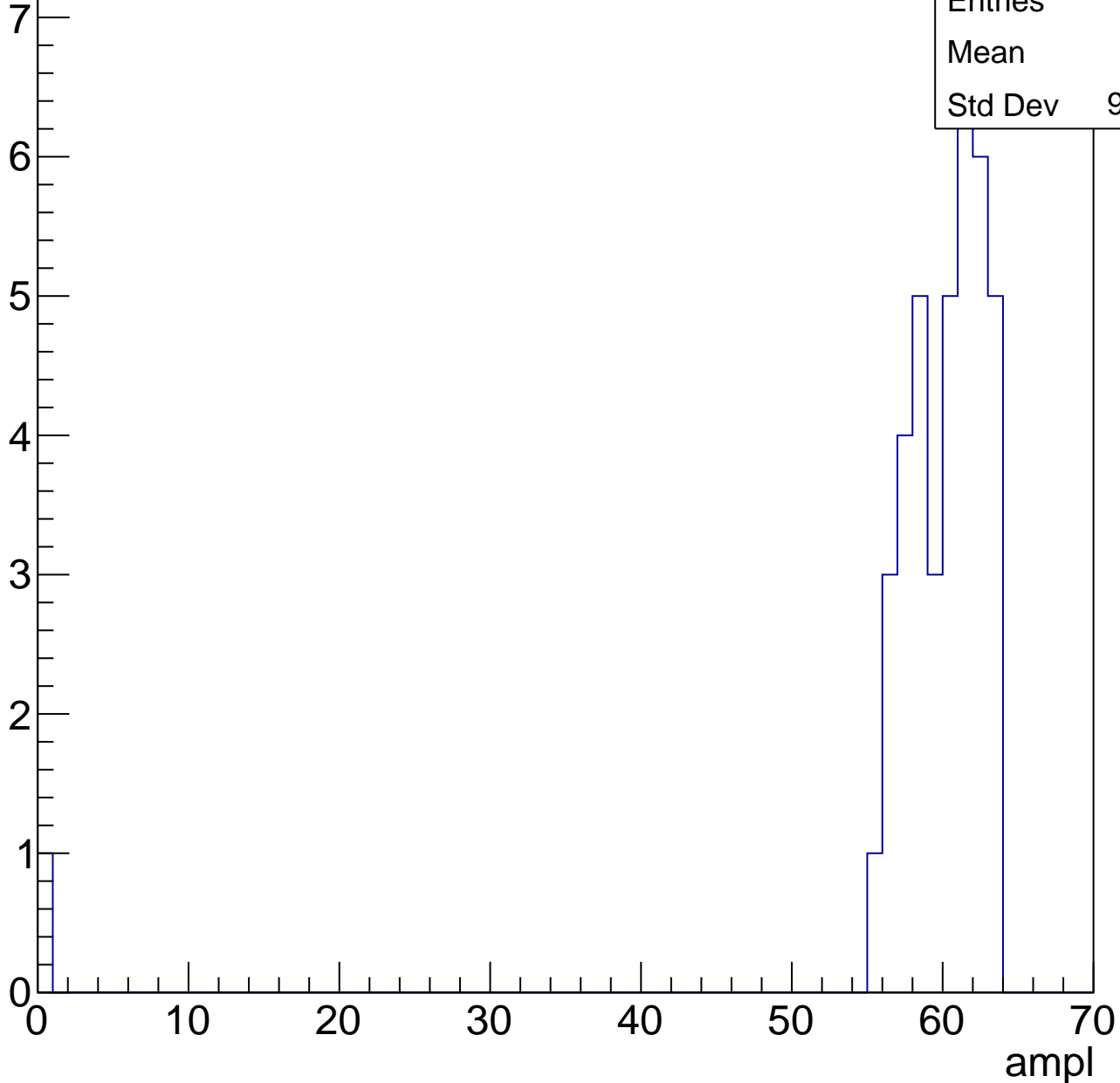


# B1L102S, U25-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.3
Std Dev	9.608

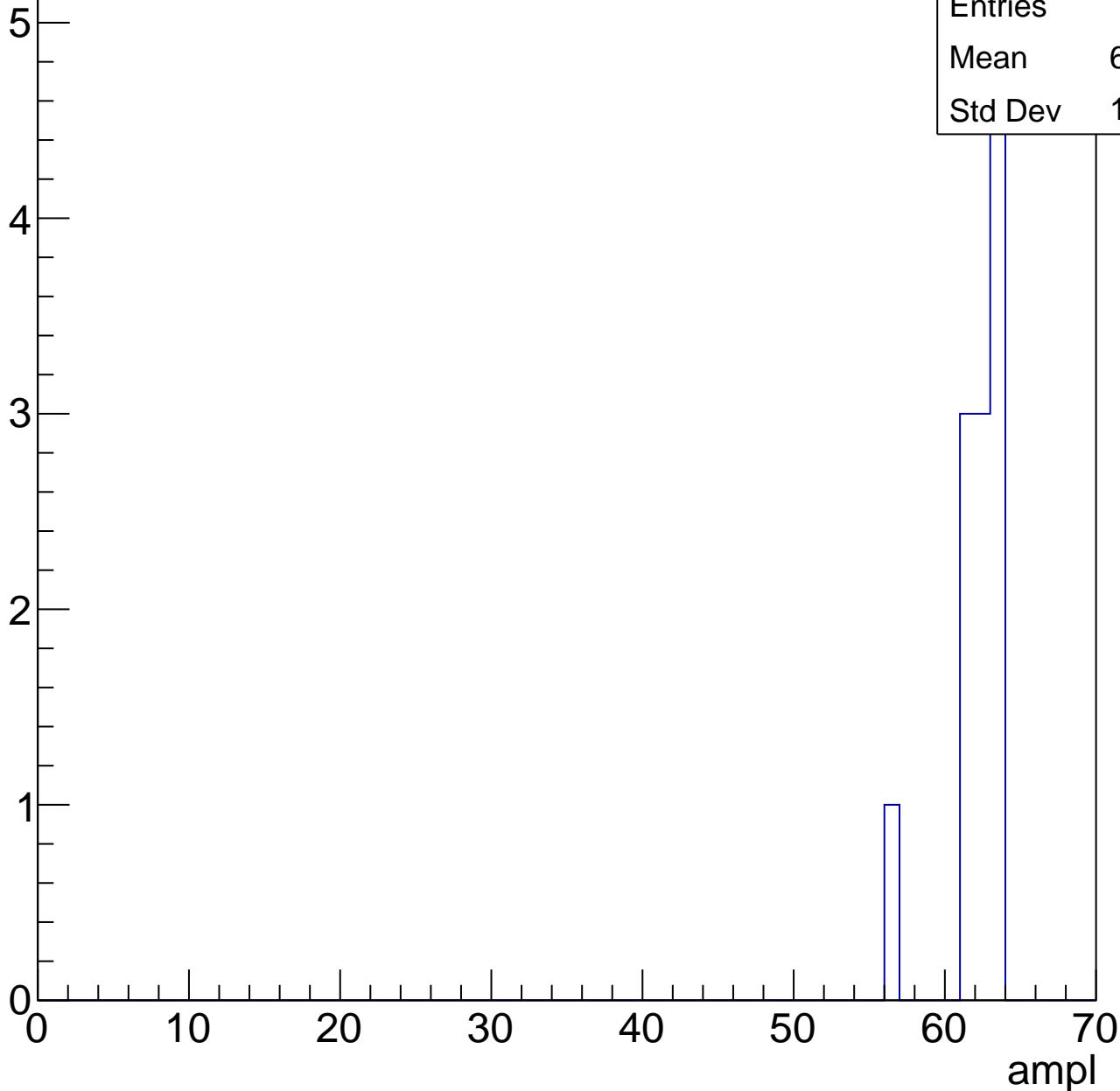


# B1L102S, U25-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.67
Std Dev	1.886





# B1L102S, U25-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L102S, U25-ch111, adc0

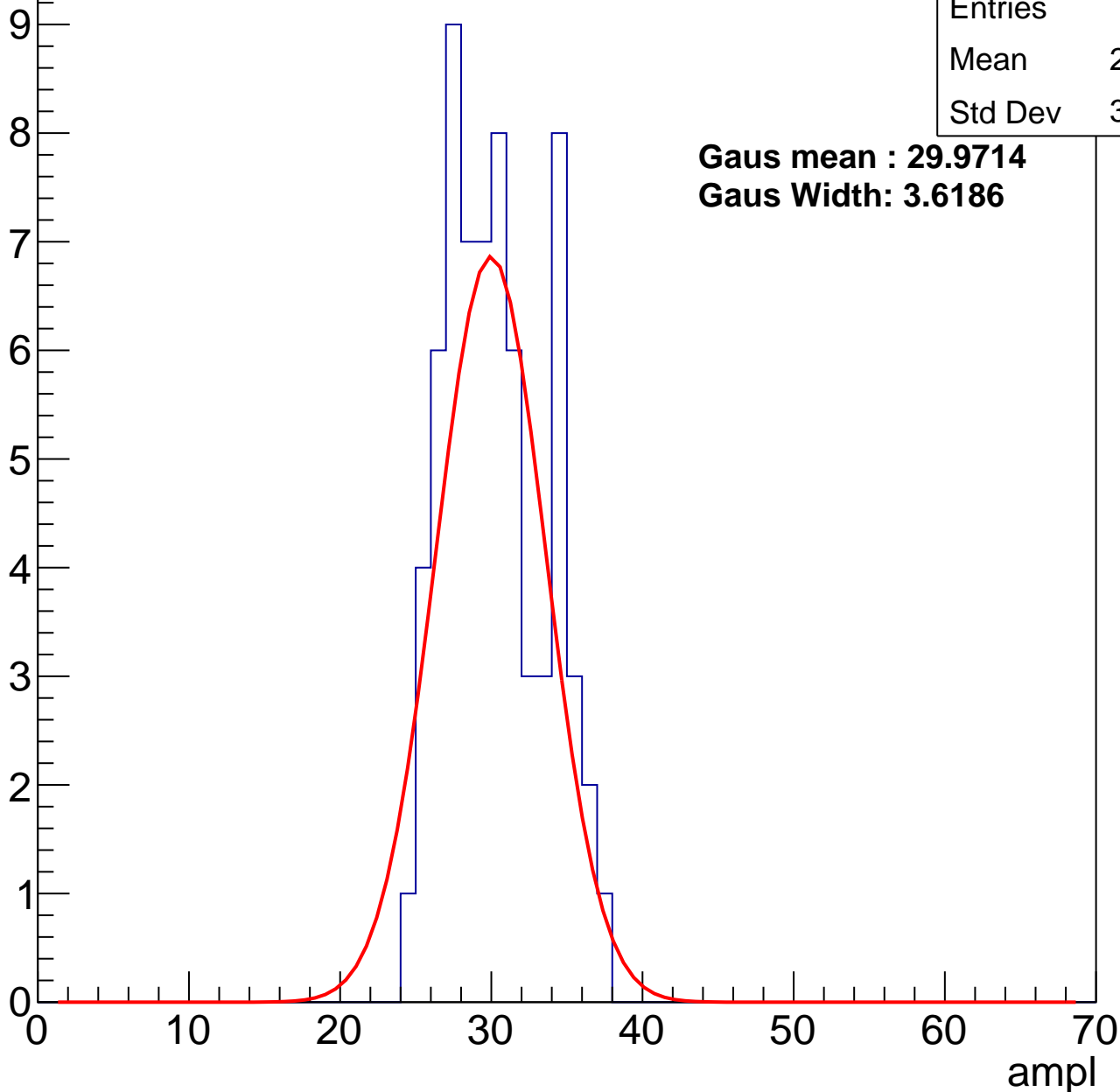
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.84
Std Dev	3.243

**Gaus mean : 29.9714**

**Gaus Width: 3.6186**



# B1L102S, U25-ch111, adc1

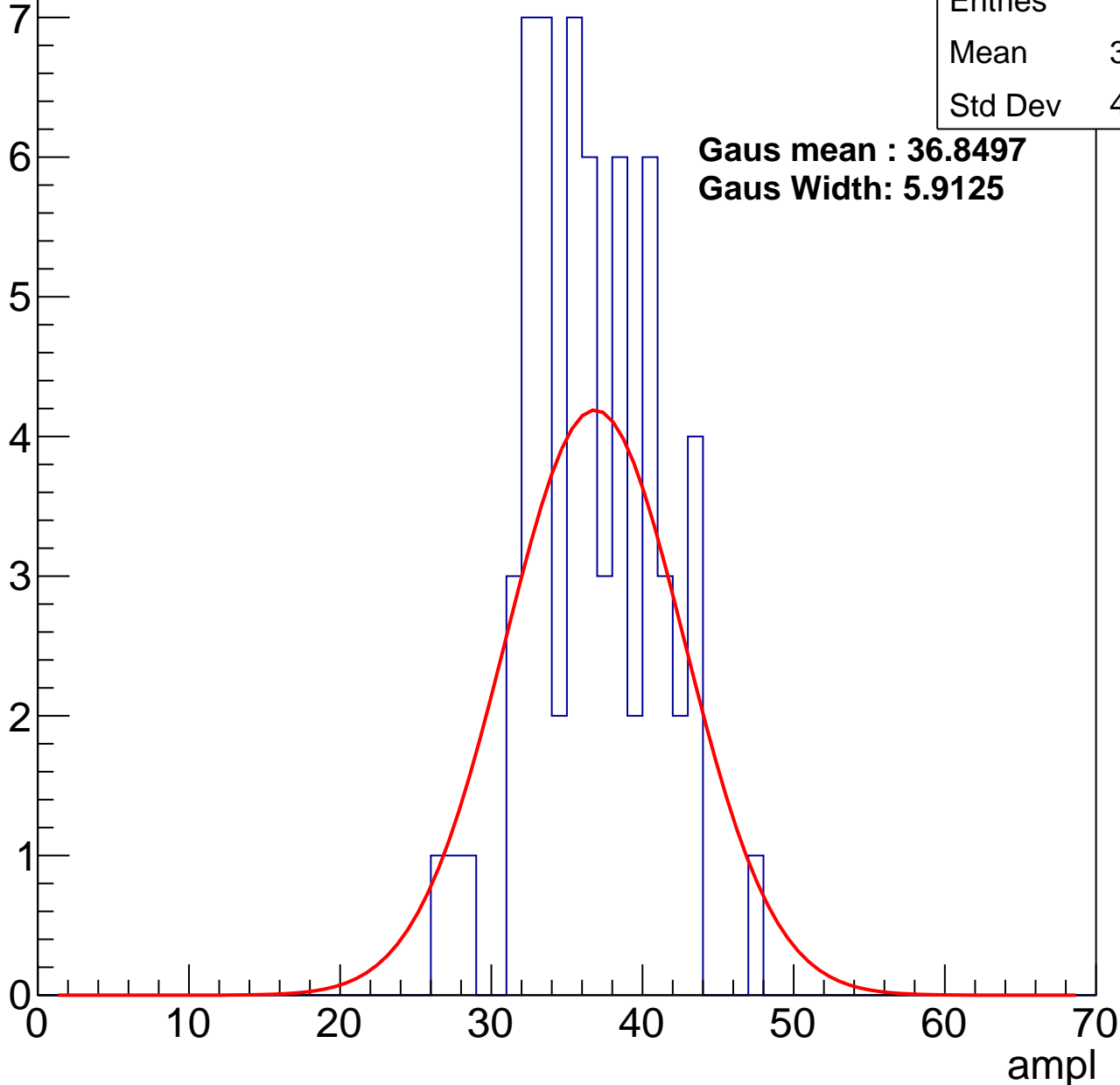
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.15
Std Dev	4.238

**Gaus mean : 36.8497**

**Gaus Width: 5.9125**



# B1L102S, U25-ch111, adc2

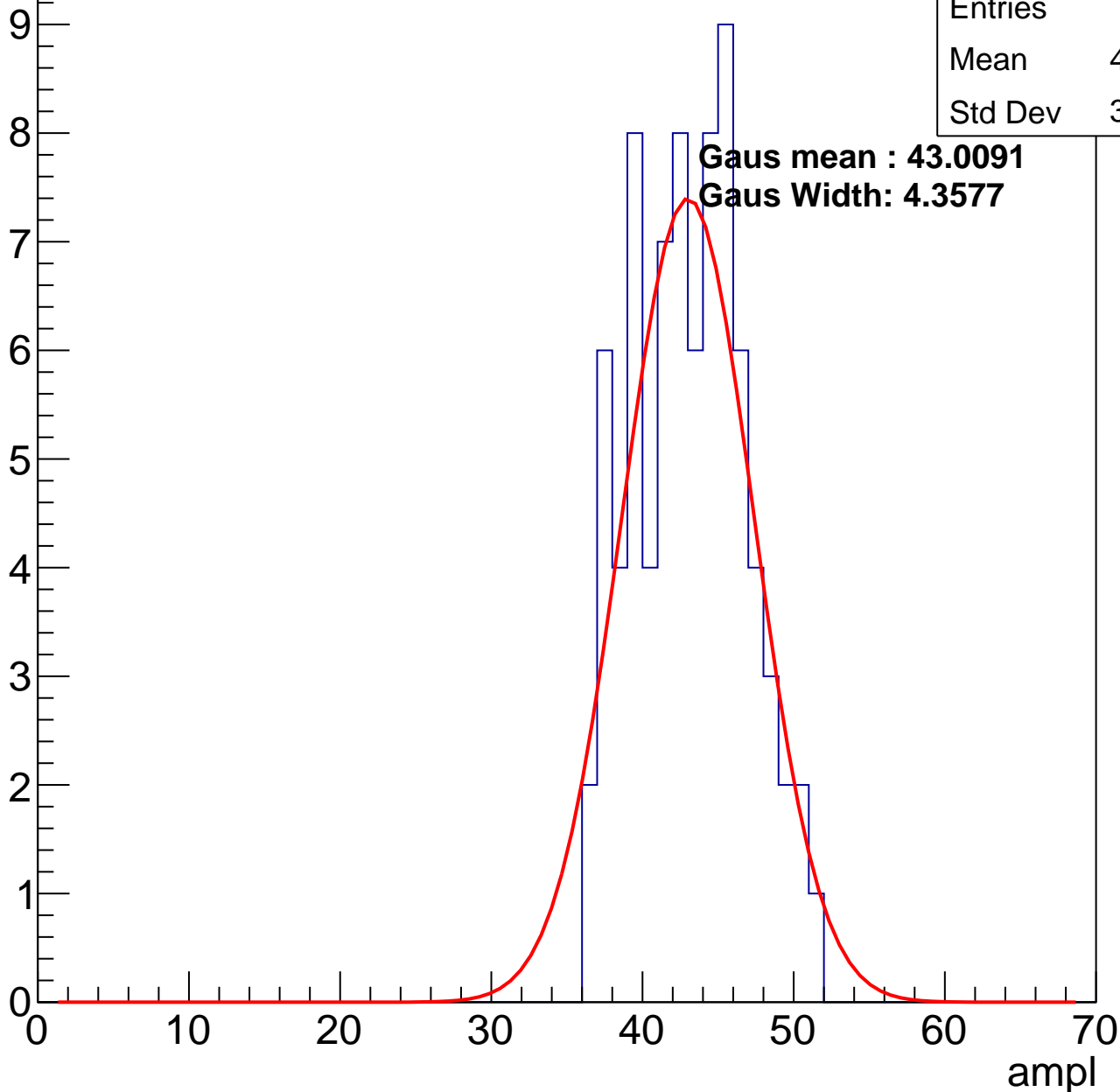
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	80
Mean	42.66
Std Dev	3.667

**Gaus mean : 43.0091**

**Gaus Width: 4.3577**

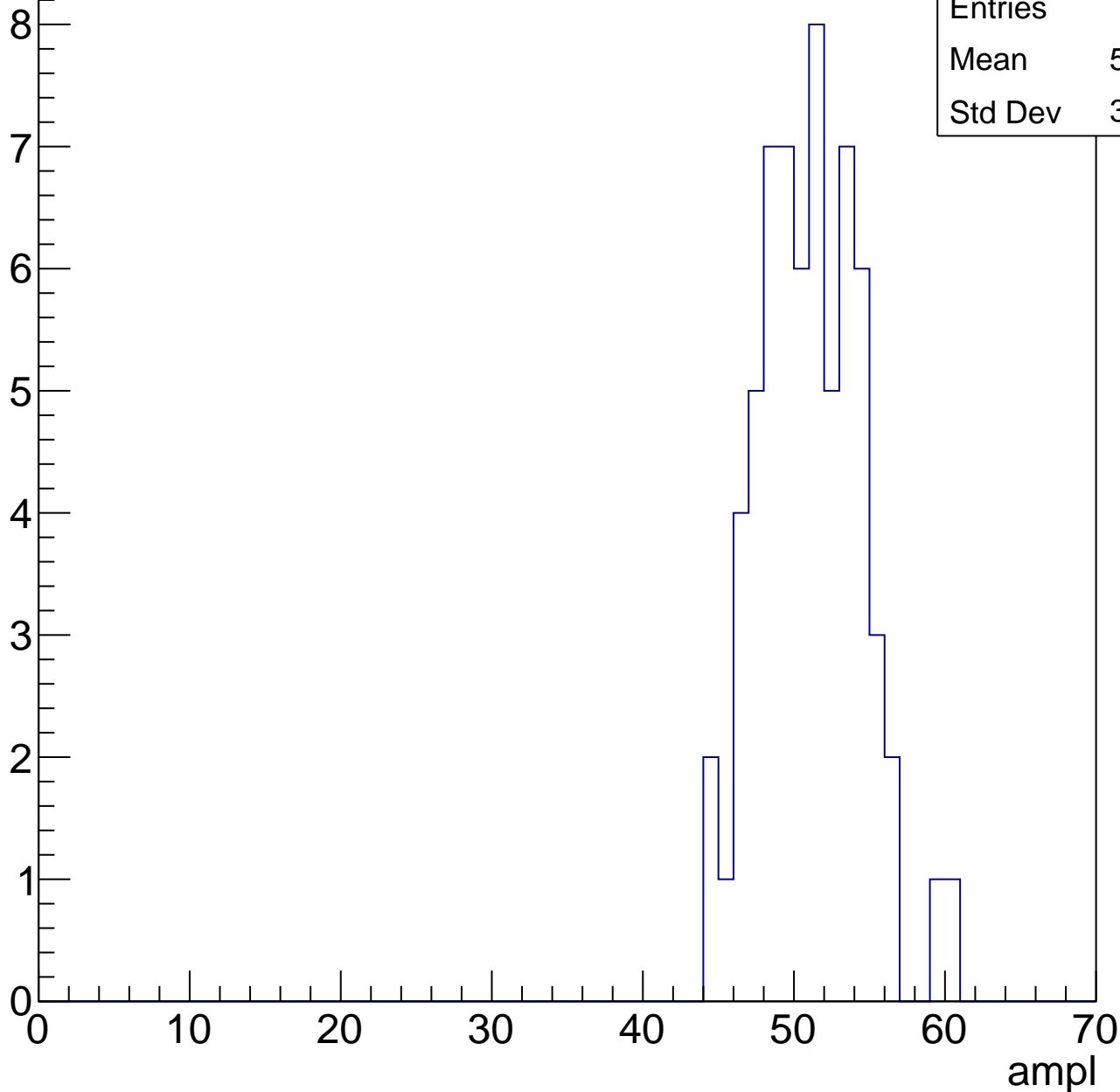


# B1L102S, U25-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	50.62
Std Dev	3.354

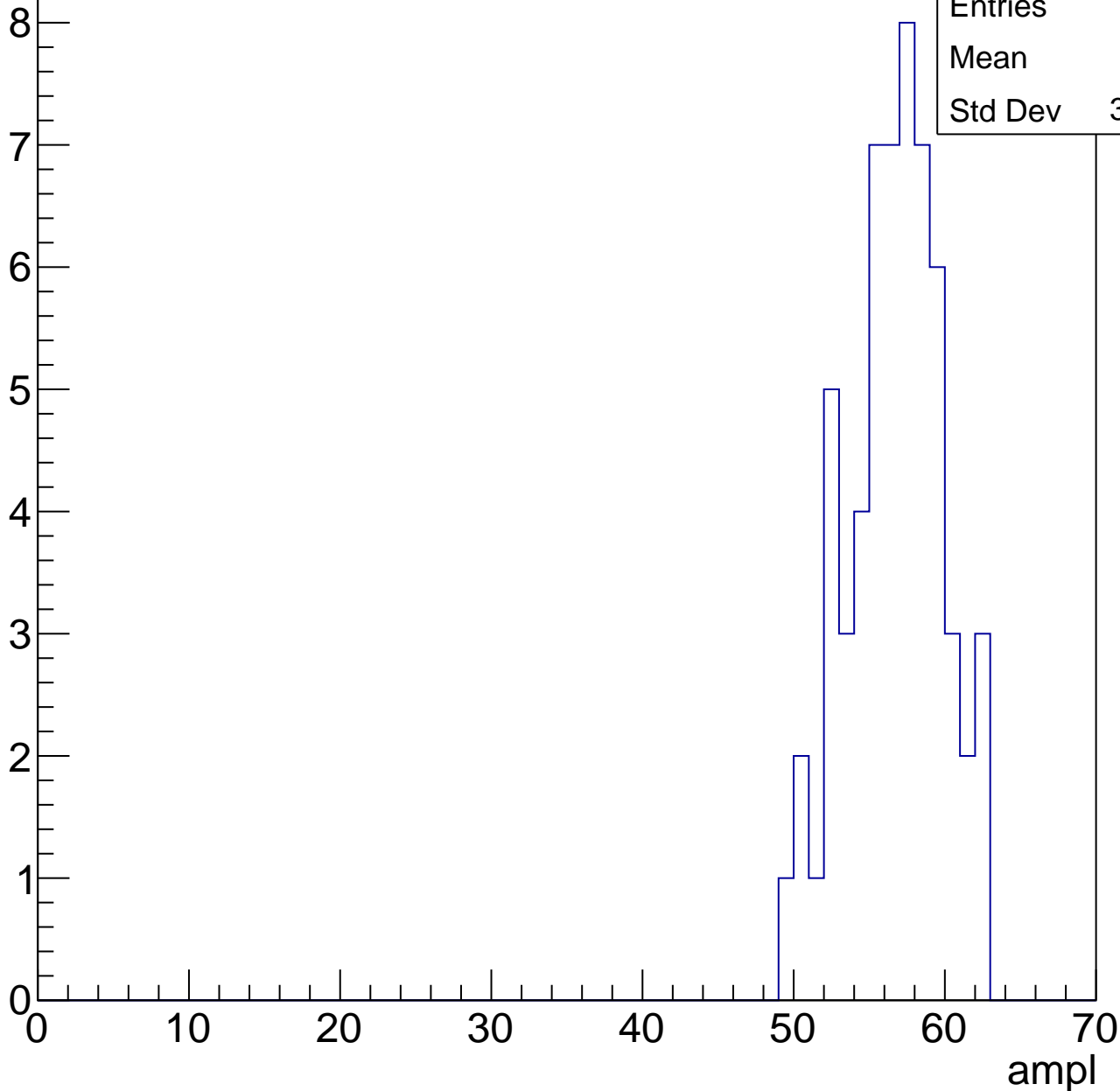


# B1L102S, U25-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.2
Std Dev	3.107

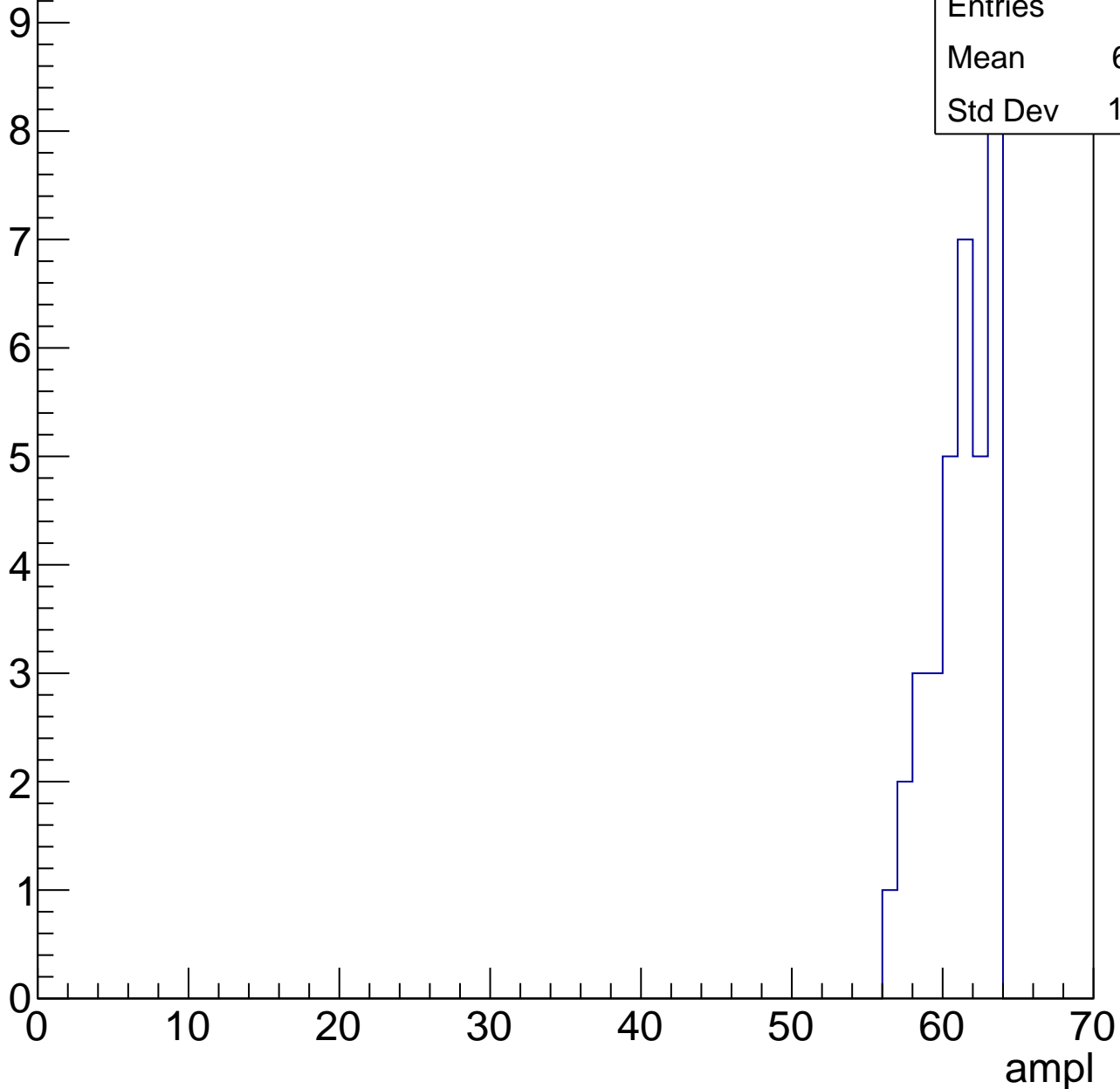


# B1L102S, U25-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.71
Std Dev	1.994



# B1L102S, U25-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

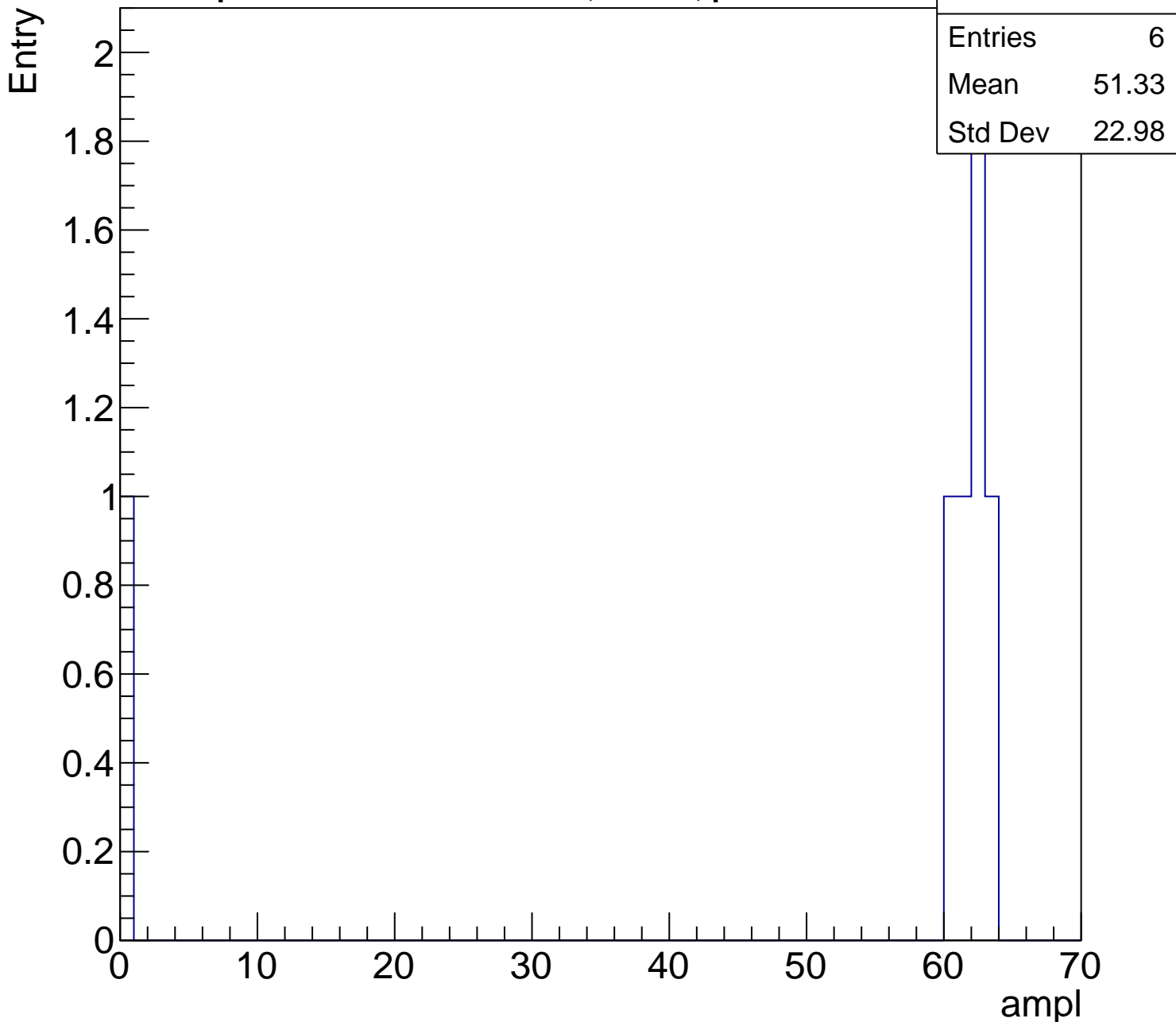
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.98

ampl

0 10 20 30 40 50 60 70





# B1L102S, U25-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L102S, U25-ch112, adc0

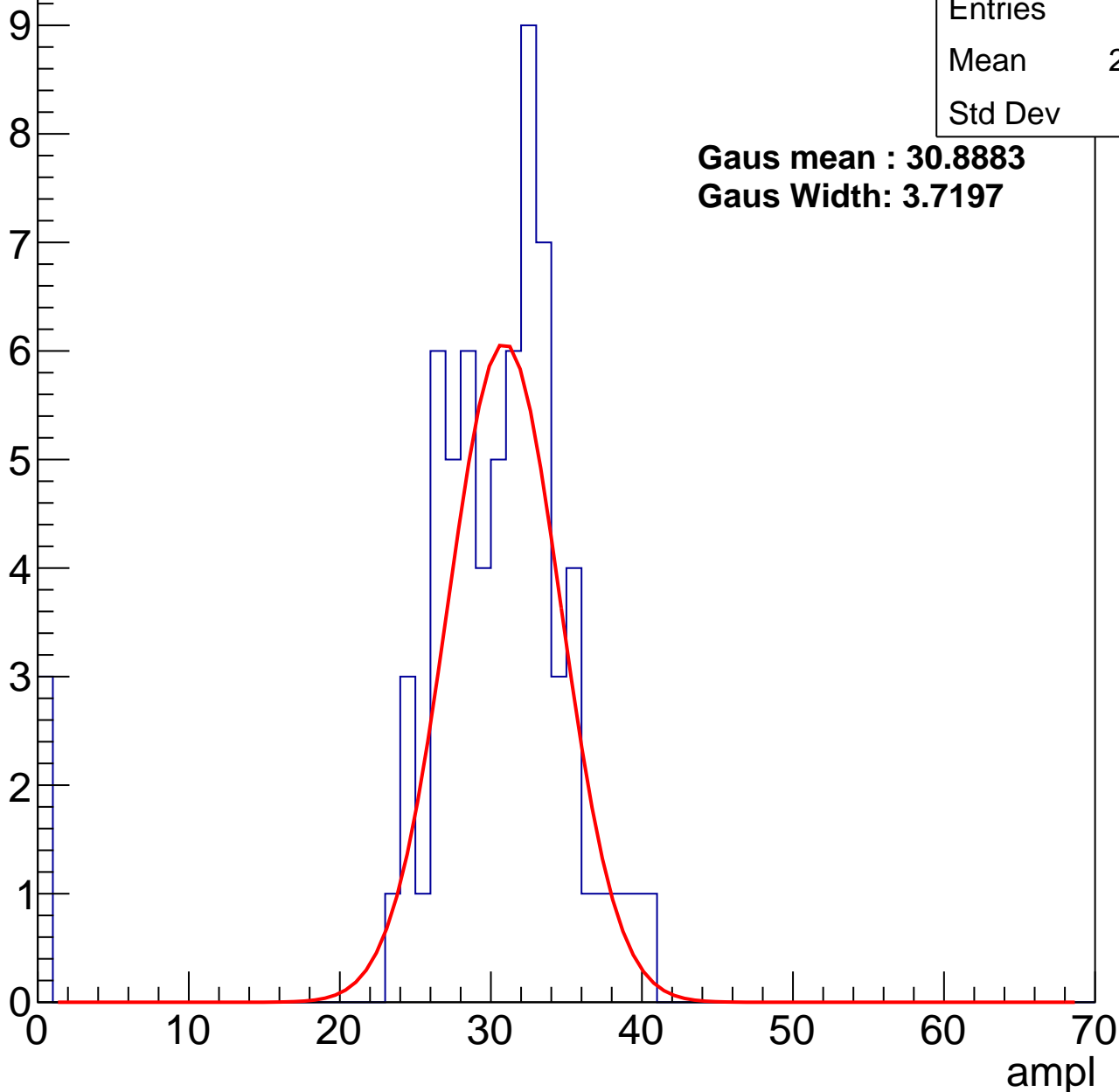
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.15
Std Dev	7.26

**Gaus mean : 30.8883**

**Gaus Width: 3.7197**



# B1L102S, U25-ch112, adc1

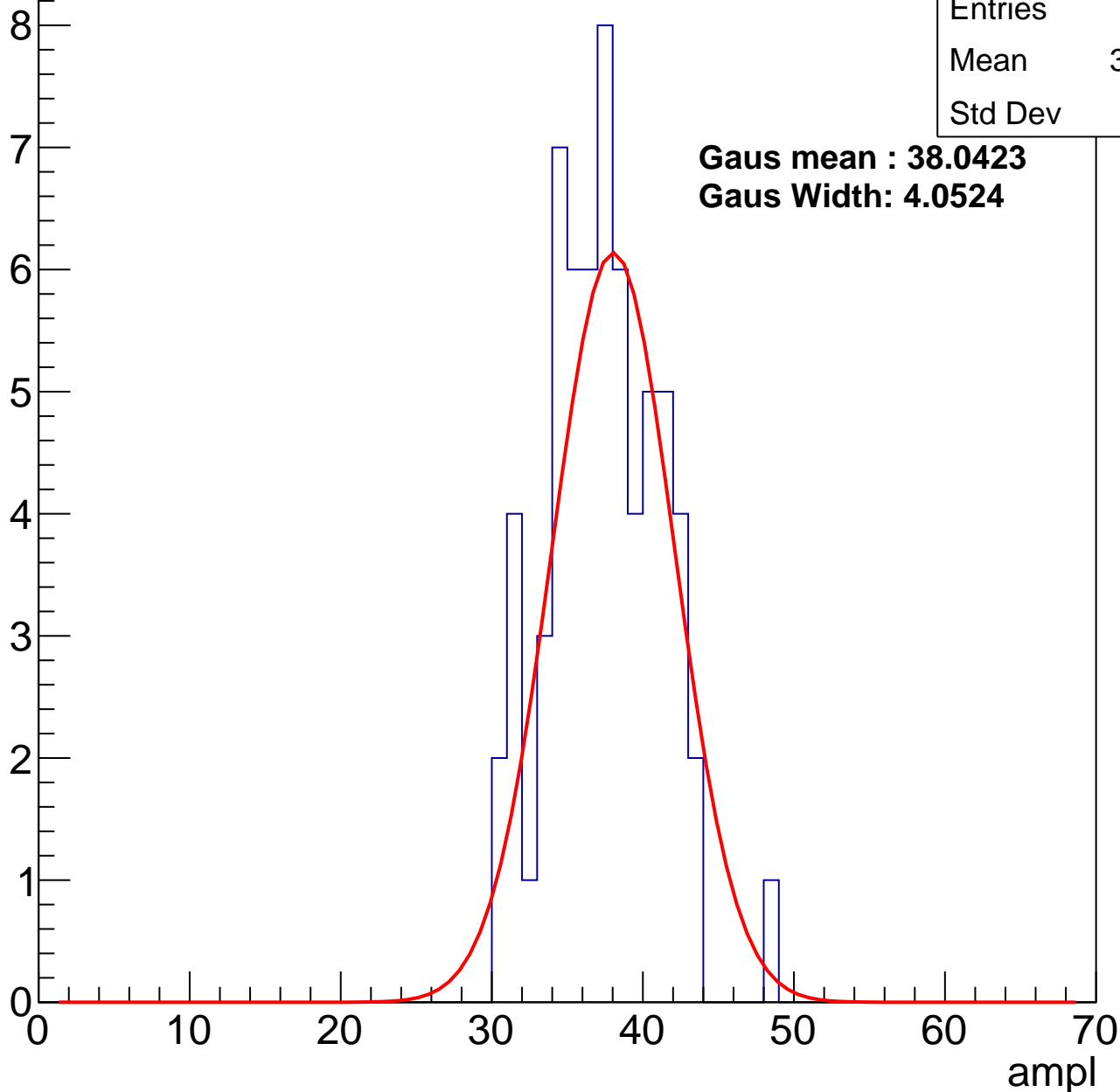
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.97
Std Dev	3.64

**Gaus mean : 38.0423**

**Gaus Width: 4.0524**



# B1L102S, U25-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	64
Mean	42.91
Std Dev	3.436

**Gaus mean : 43.3923**

**Gaus Width: 3.4499**

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

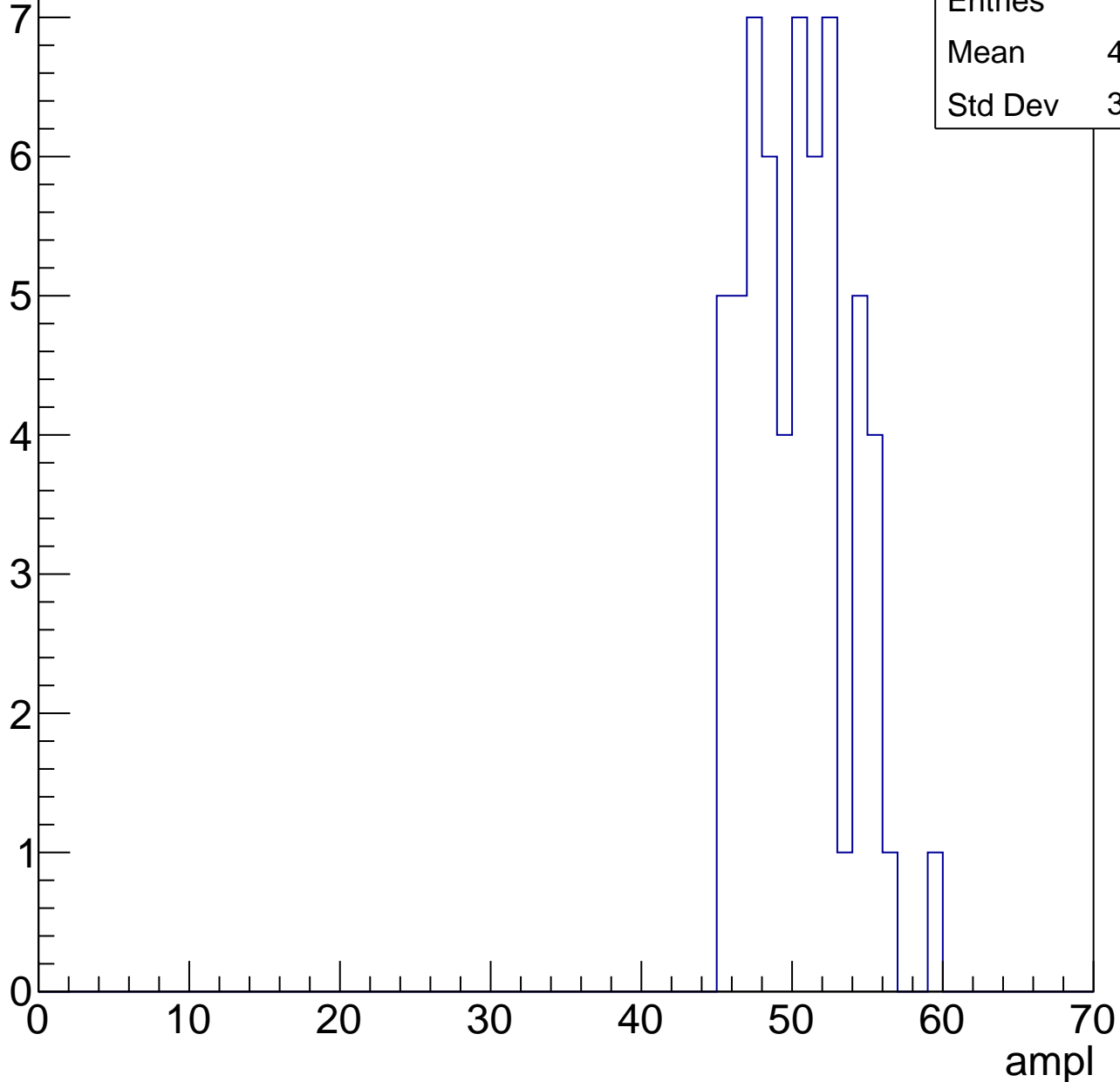


# B1L102S, U25-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.93
Std Dev	3.283

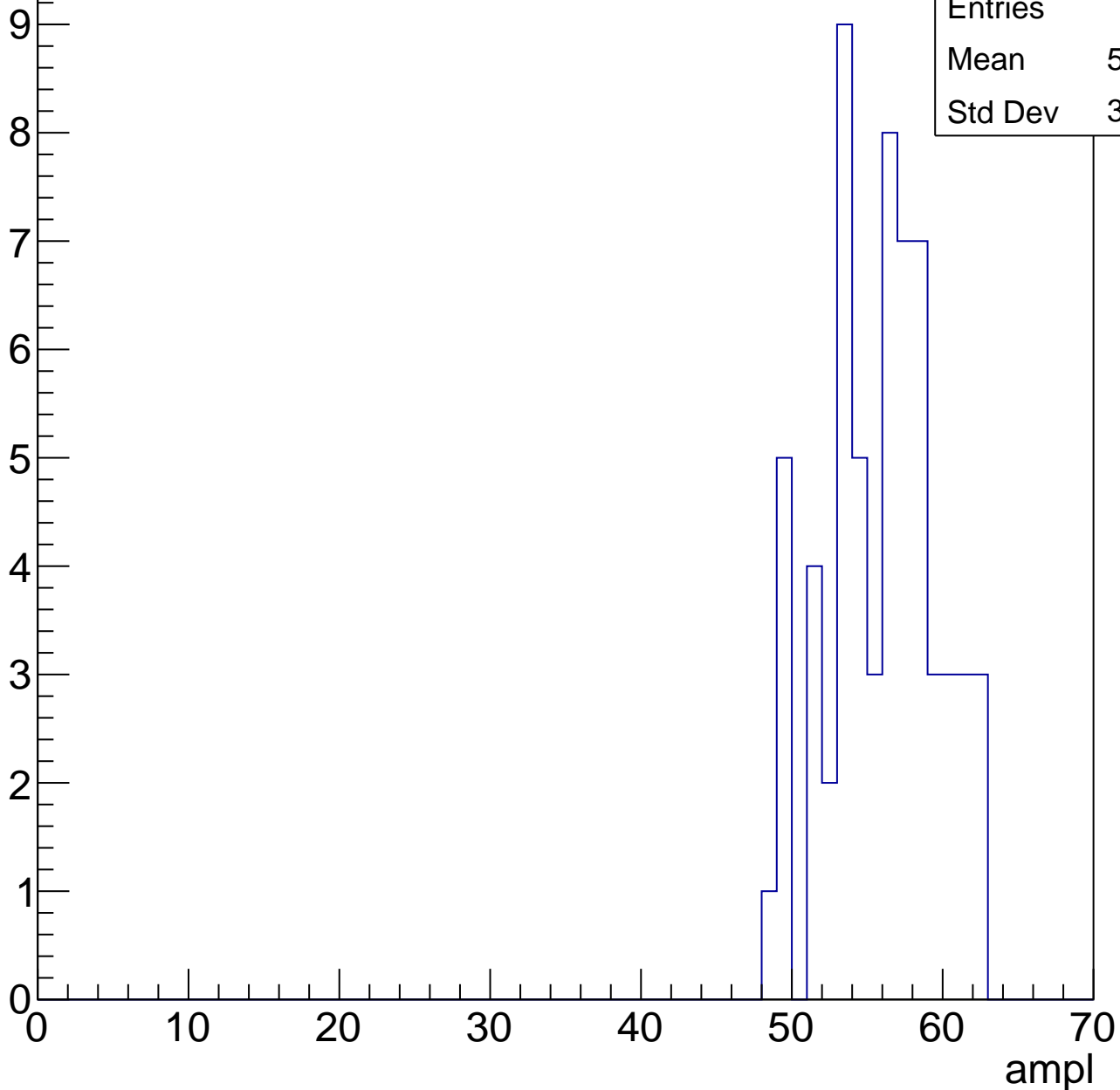


# B1L102S, U25-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.43
Std Dev	3.602

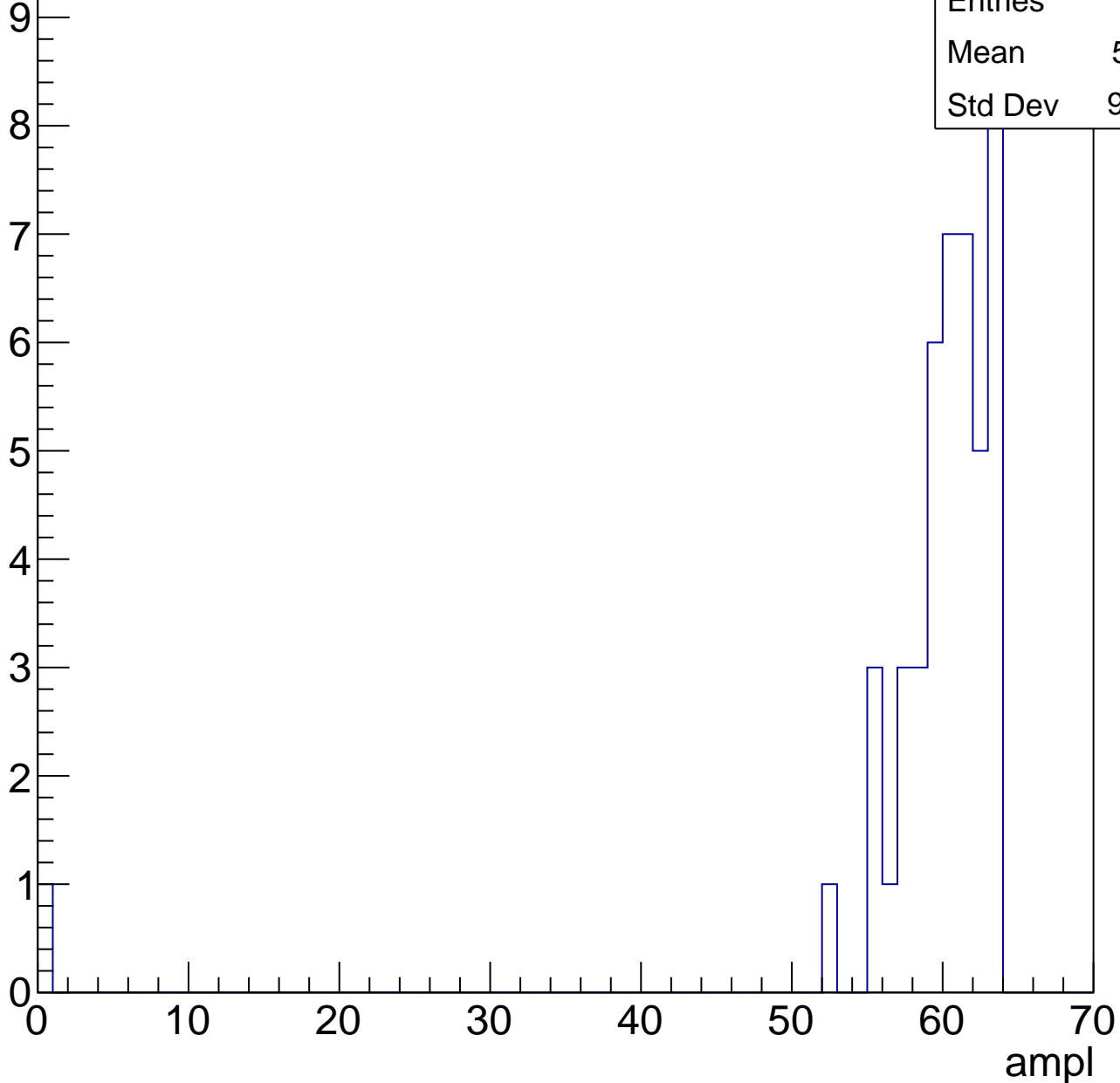


# B1L102S, U25-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.61
Std Dev	9.112



# B1L102S, U25-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

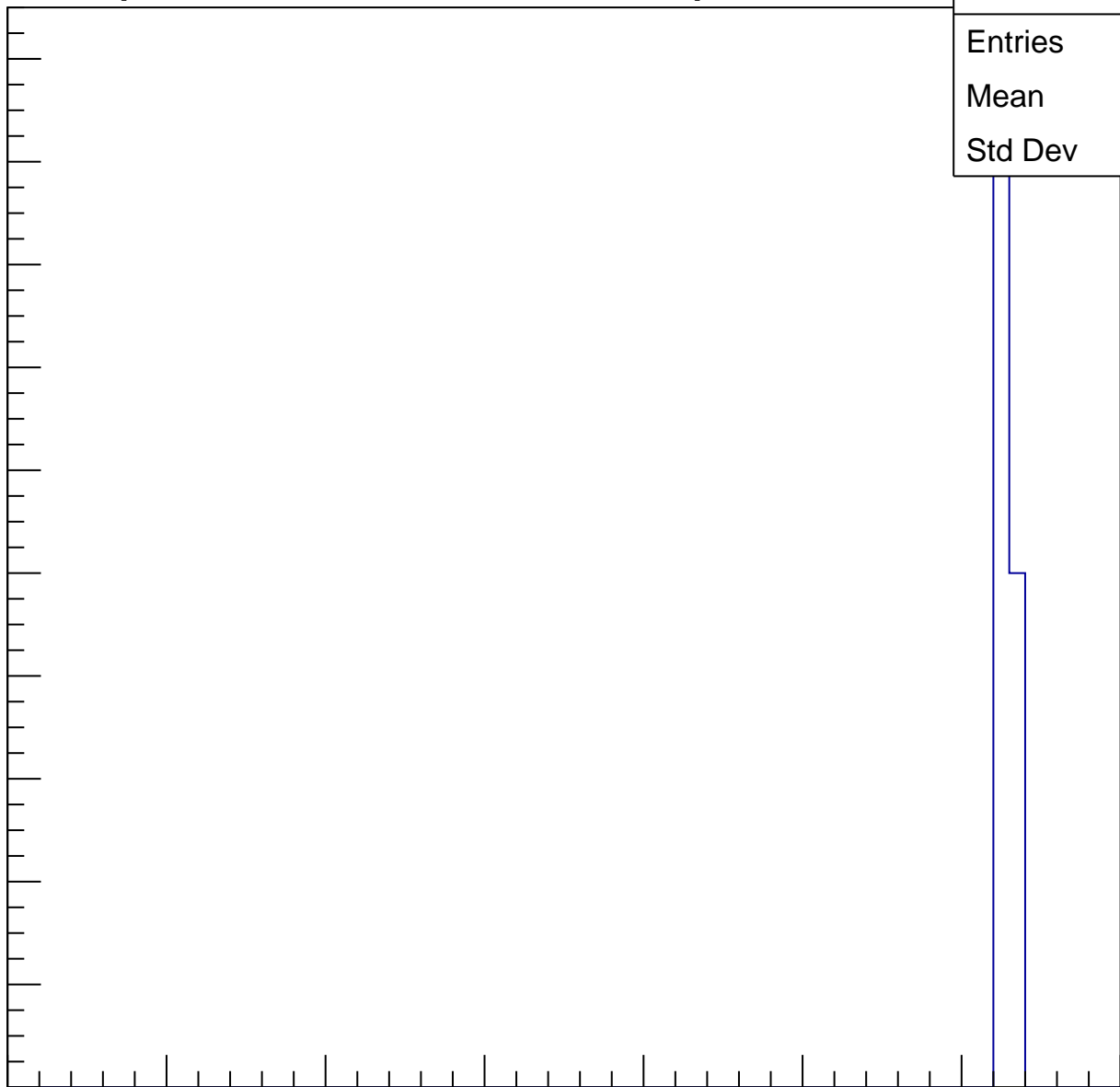
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl





# B1L102S, U25-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch113, adc0

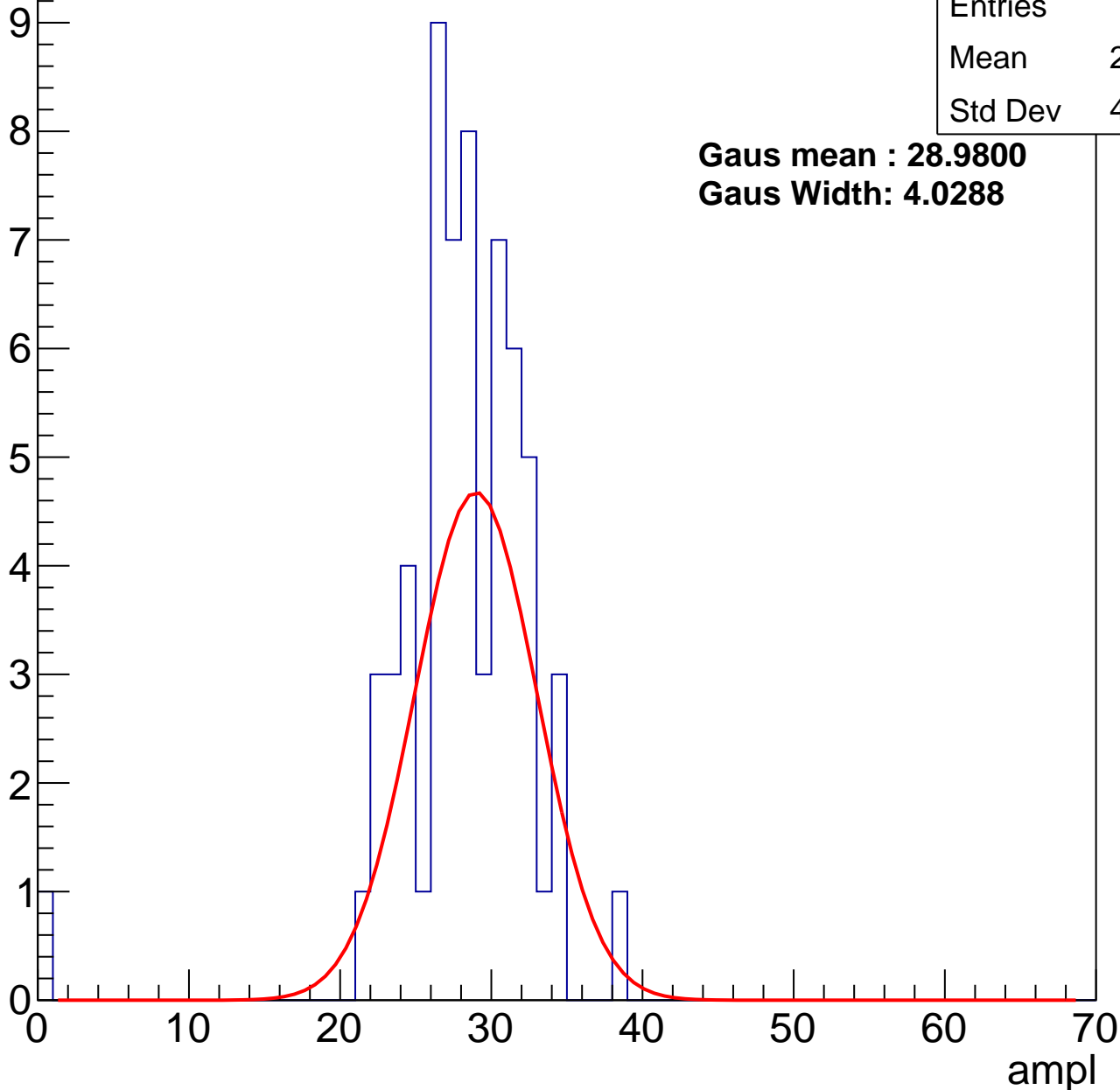
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.62
Std Dev	4.913

**Gaus mean : 28.9800**

**Gaus Width: 4.0288**



# B1L102S, U25-ch113, adc1

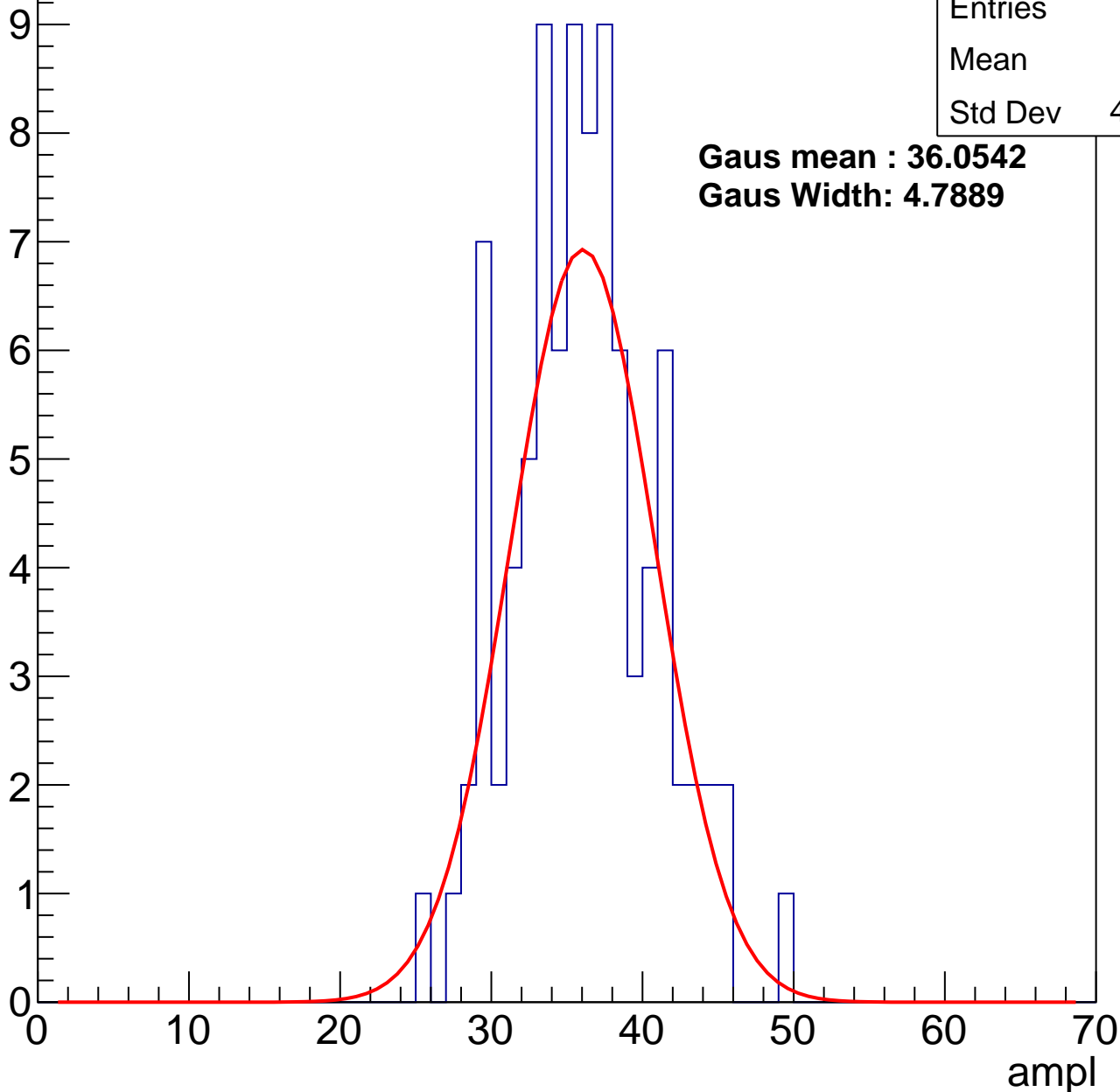
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	91
Mean	35.6
Std Dev	4.603

**Gaus mean : 36.0542**

**Gaus Width: 4.7889**



# B1L102S, U25-ch113, adc2

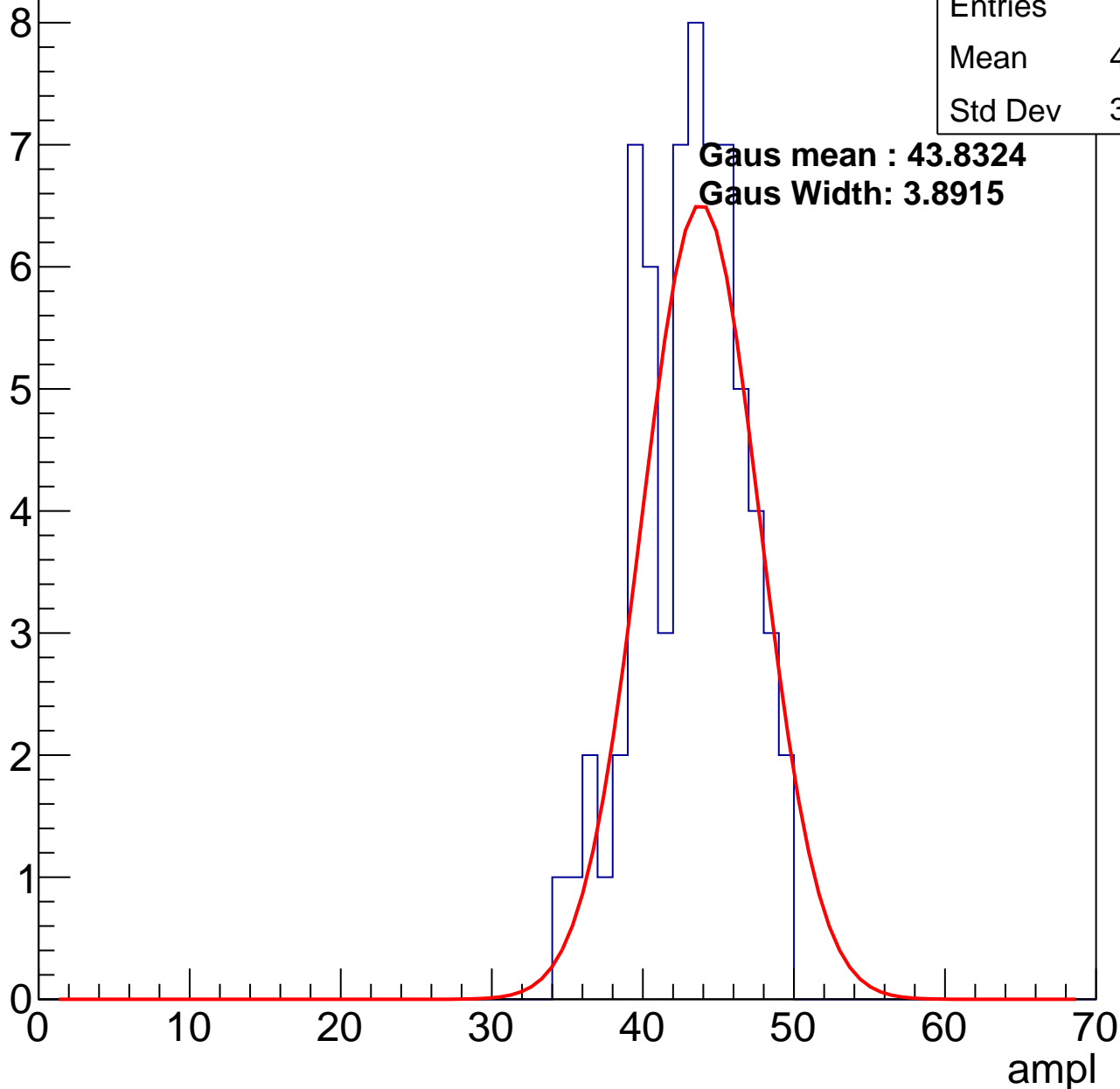
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.59
Std Dev	3.468

**Gaus mean : 43.8324**

**Gaus Width: 3.8915**

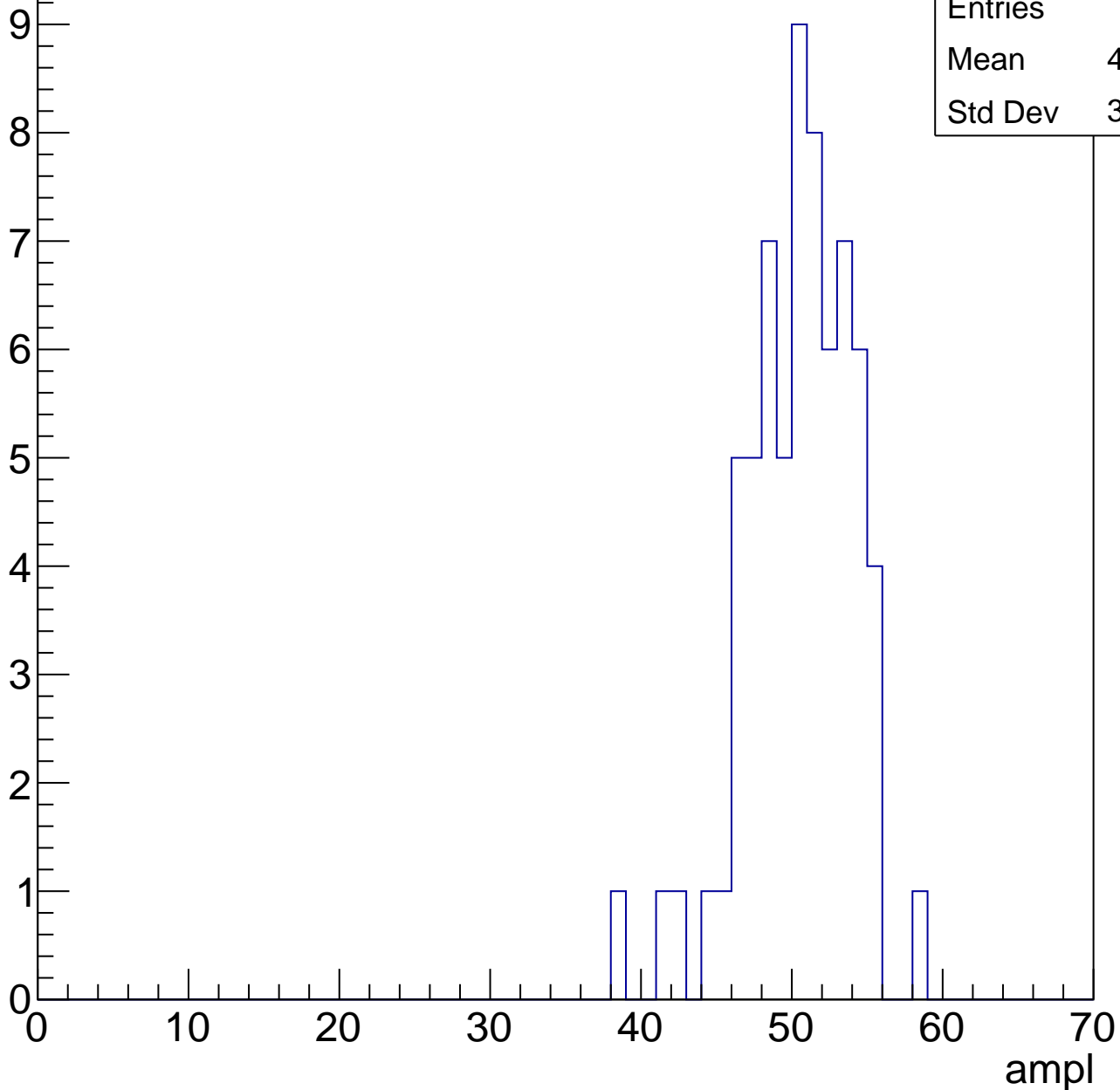


# B1L102S, U25-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.99
Std Dev	3.562

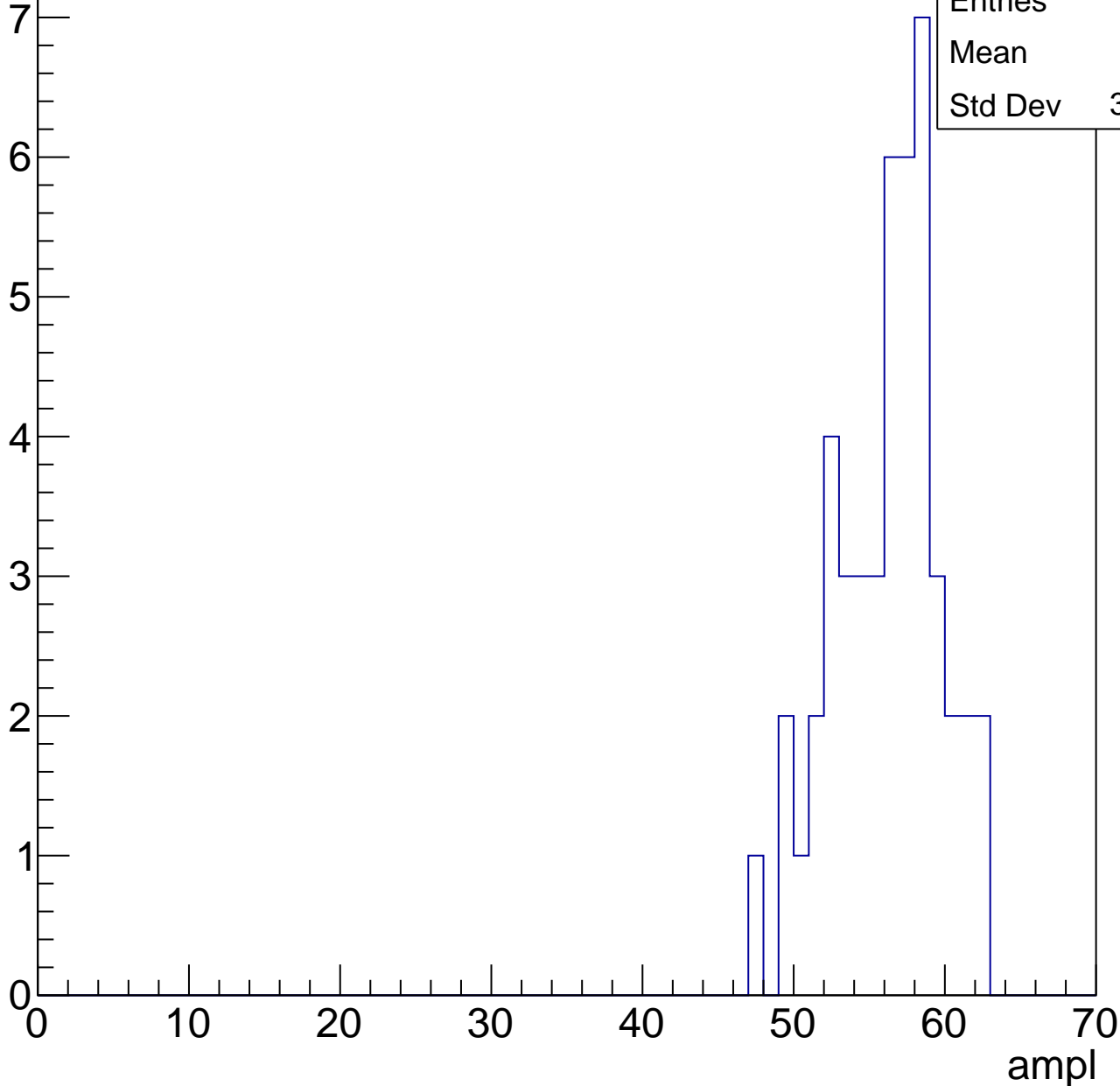


# B1L102S, U25-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.7
Std Dev	3.518

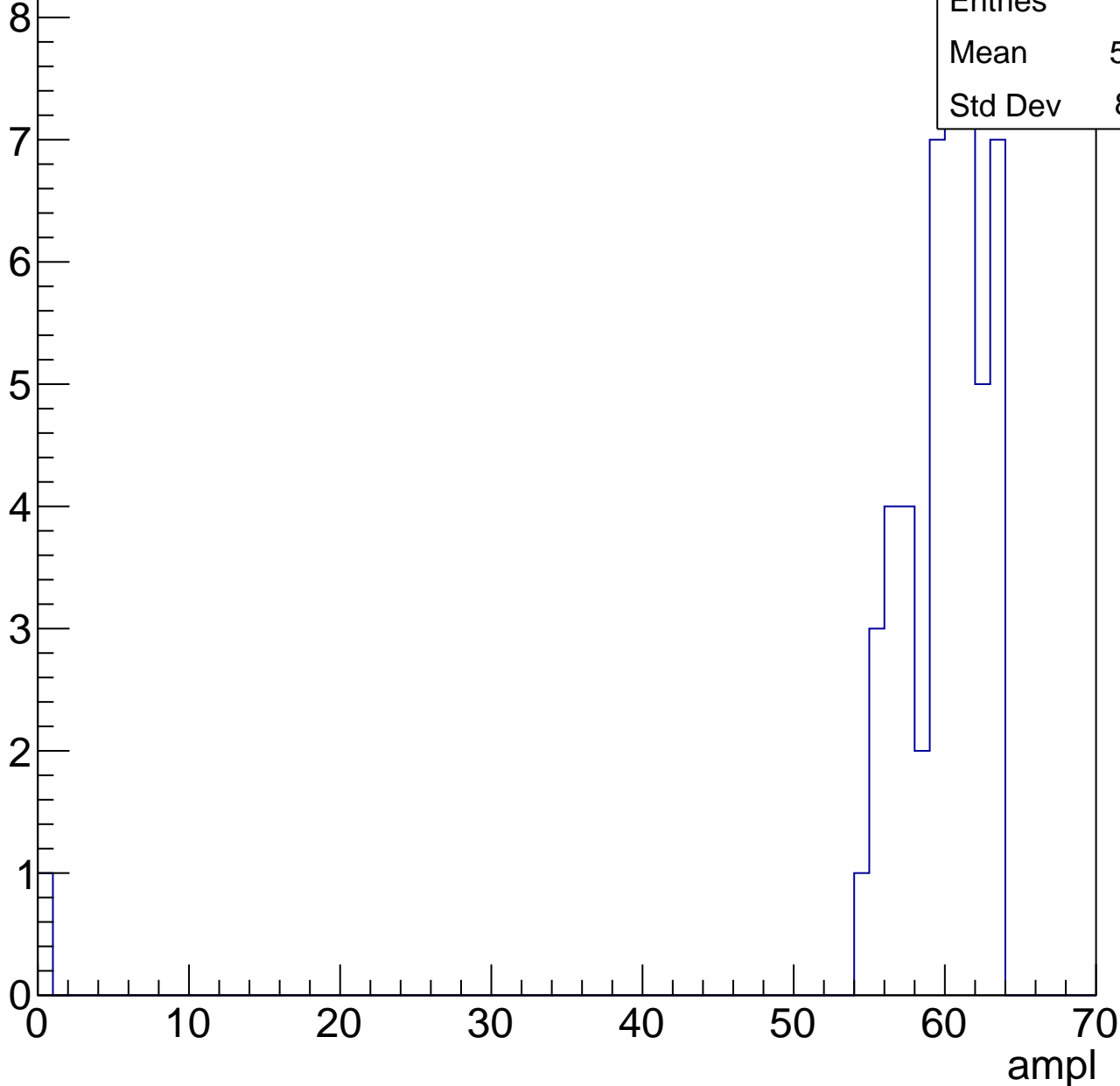


# B1L102S, U25-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.38
Std Dev	8.701



# B1L102S, U25-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L102S, U25-ch114, adc0

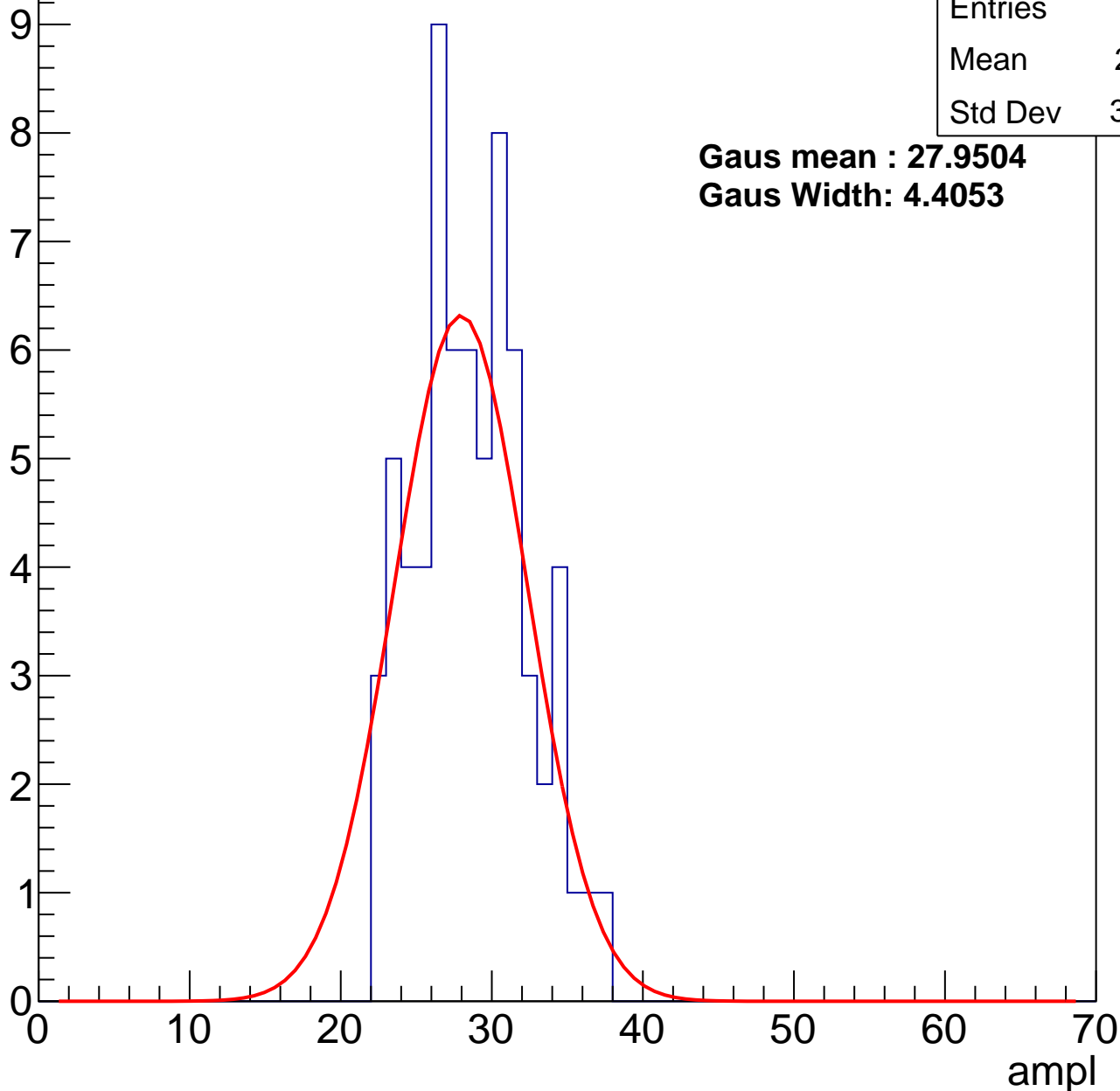
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.21
Std Dev	3.636

**Gaus mean : 27.9504**

**Gaus Width: 4.4053**



# B1L102S, U25-ch114, adc1

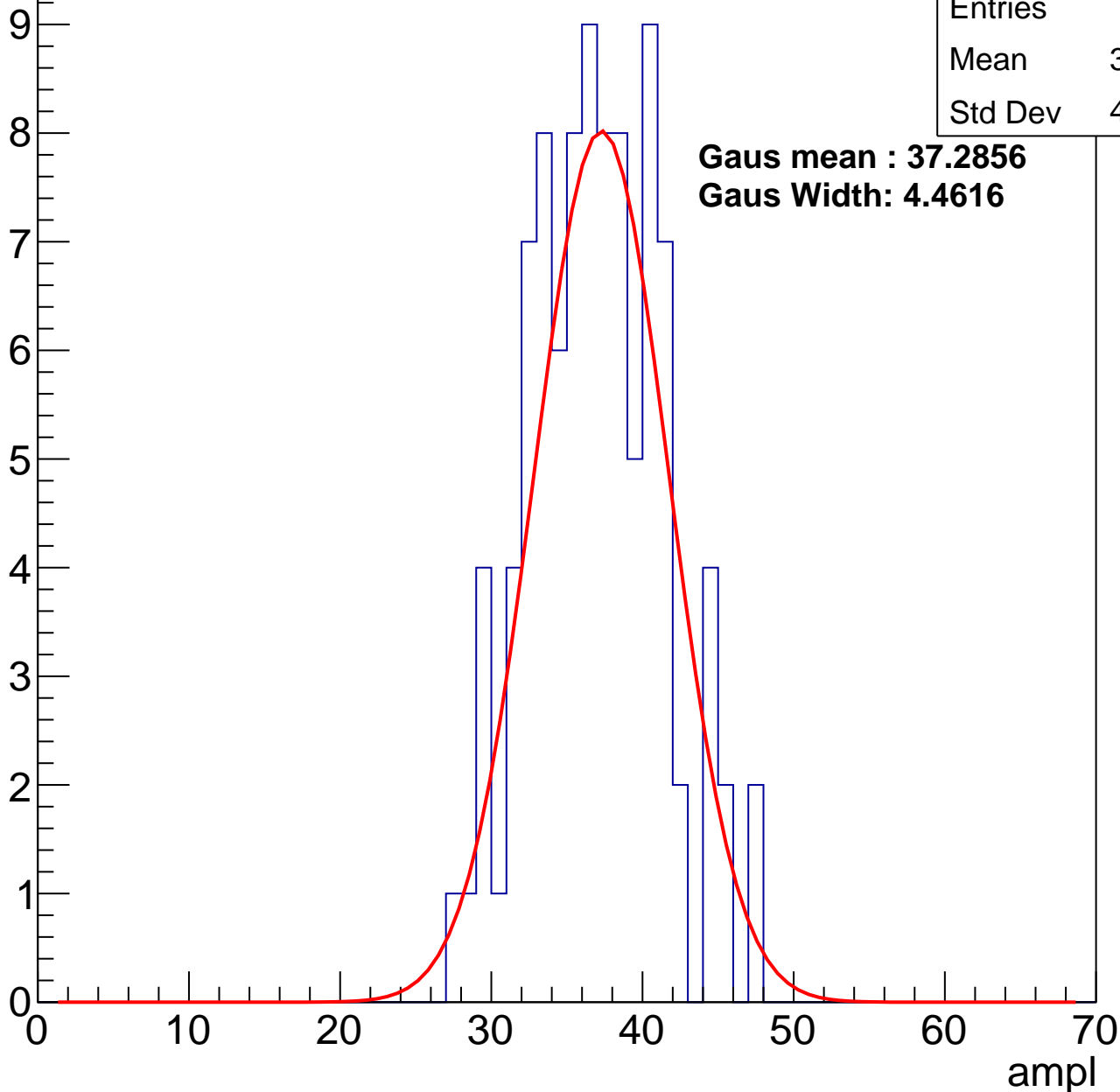
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	96
Mean	36.53
Std Dev	4.332

**Gaus mean : 37.2856**

**Gaus Width: 4.4616**



# B1L102S, U25-ch114, adc2

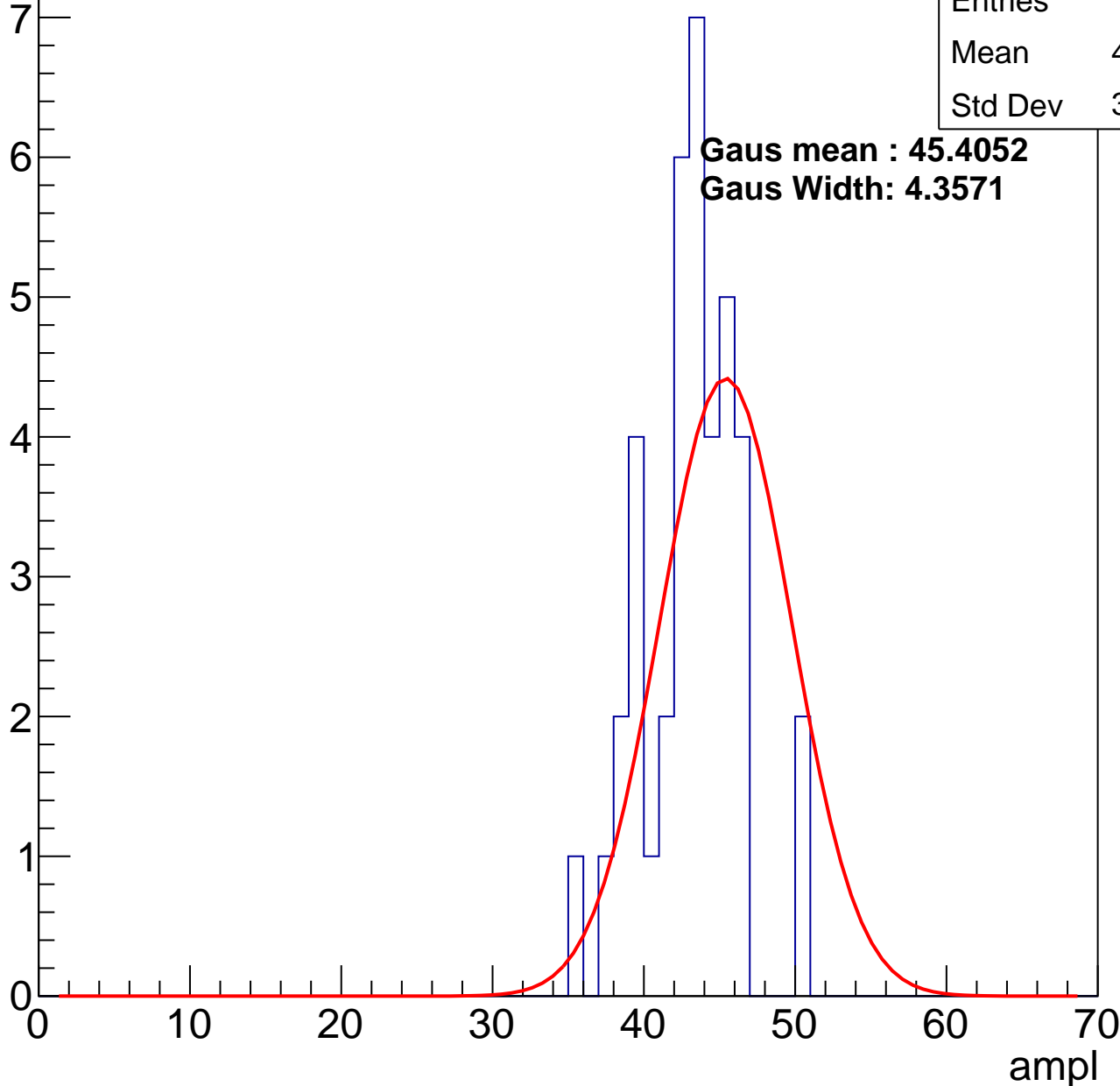
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	39
Mean	42.67
Std Dev	3.165

**Gaus mean : 45.4052**

**Gaus Width: 4.3571**

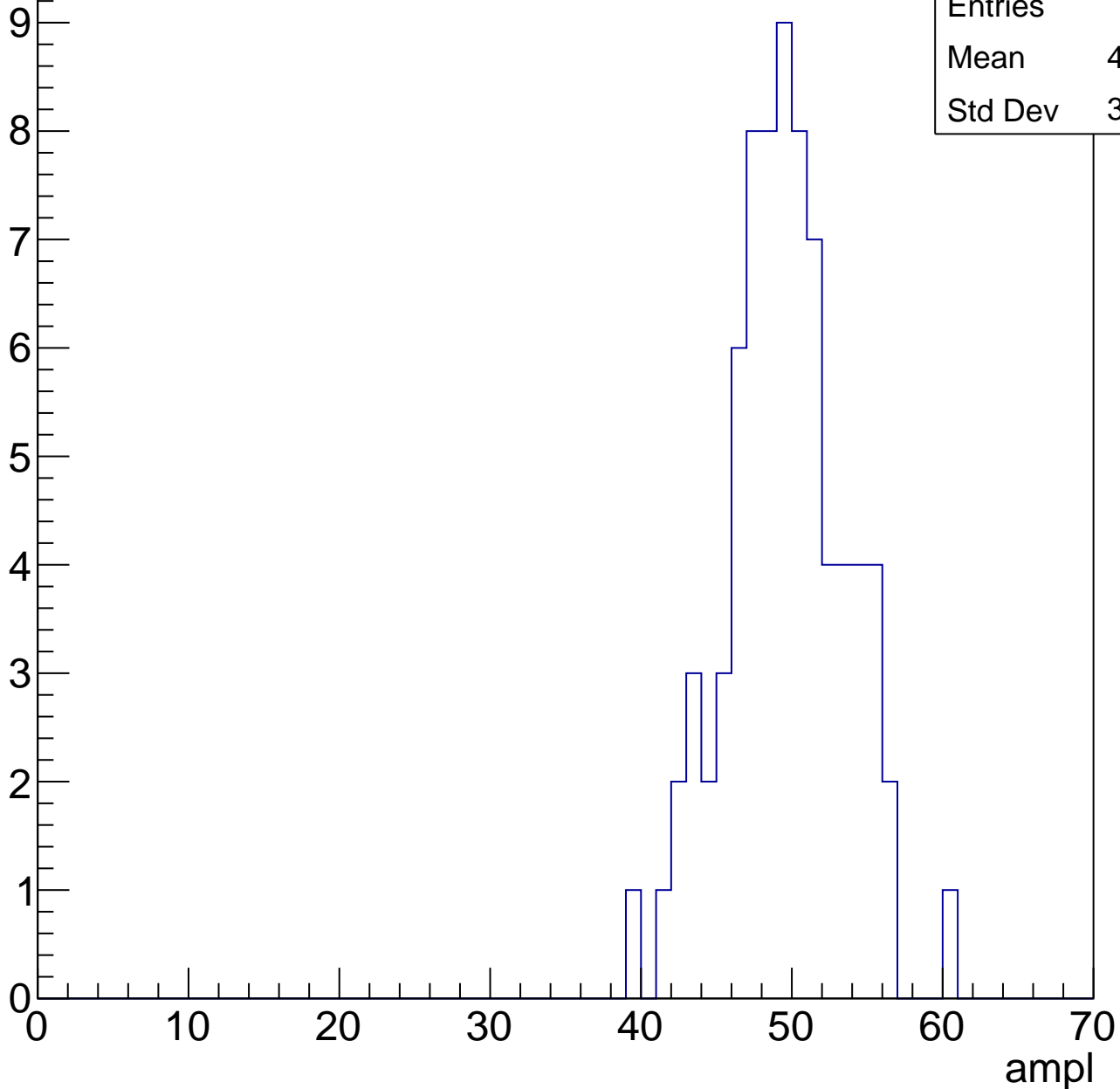


# B1L102S, U25-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.06
Std Dev	3.882

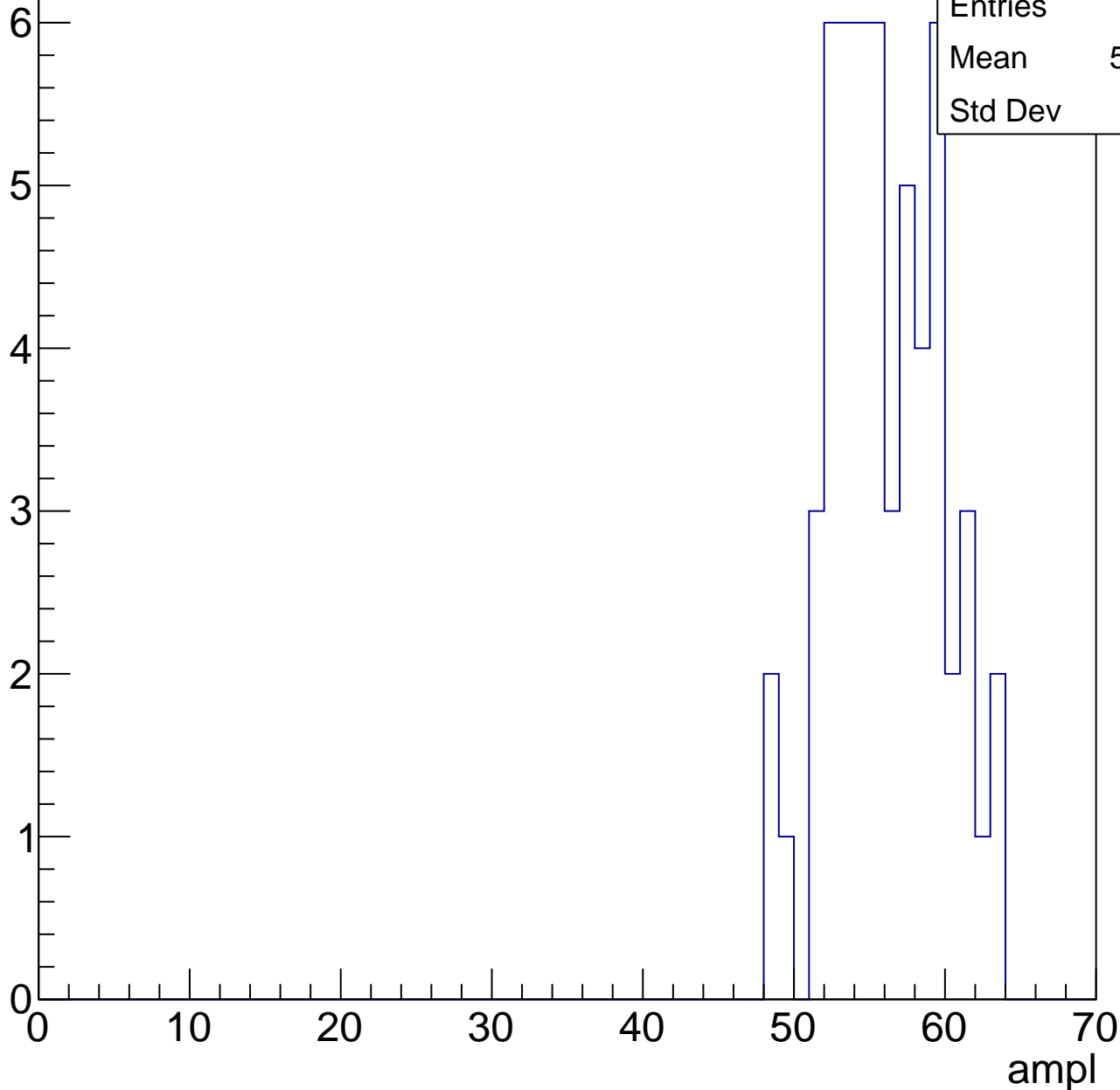


# B1L102S, U25-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.57
Std Dev	3.63

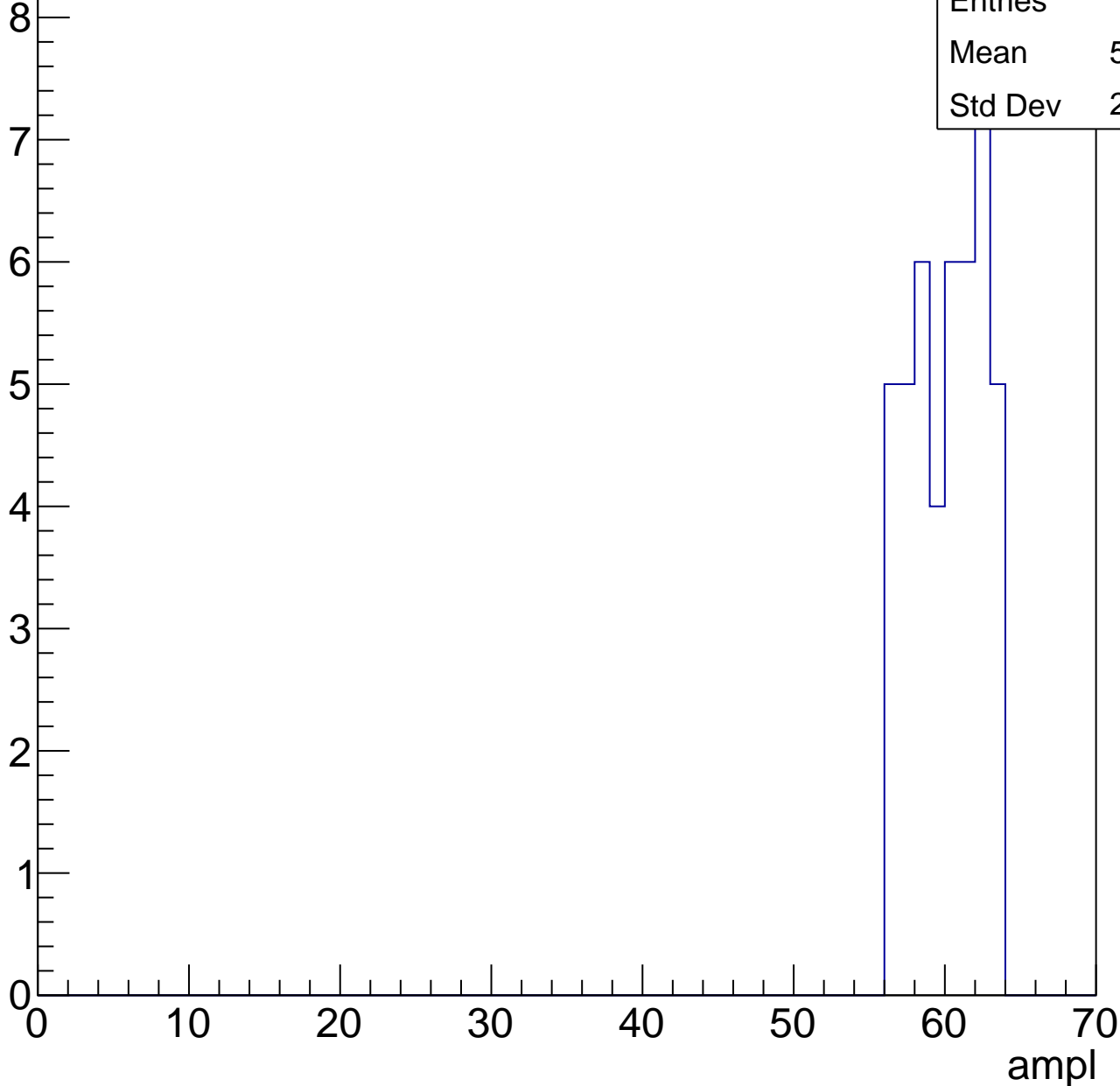


# B1L102S, U25-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

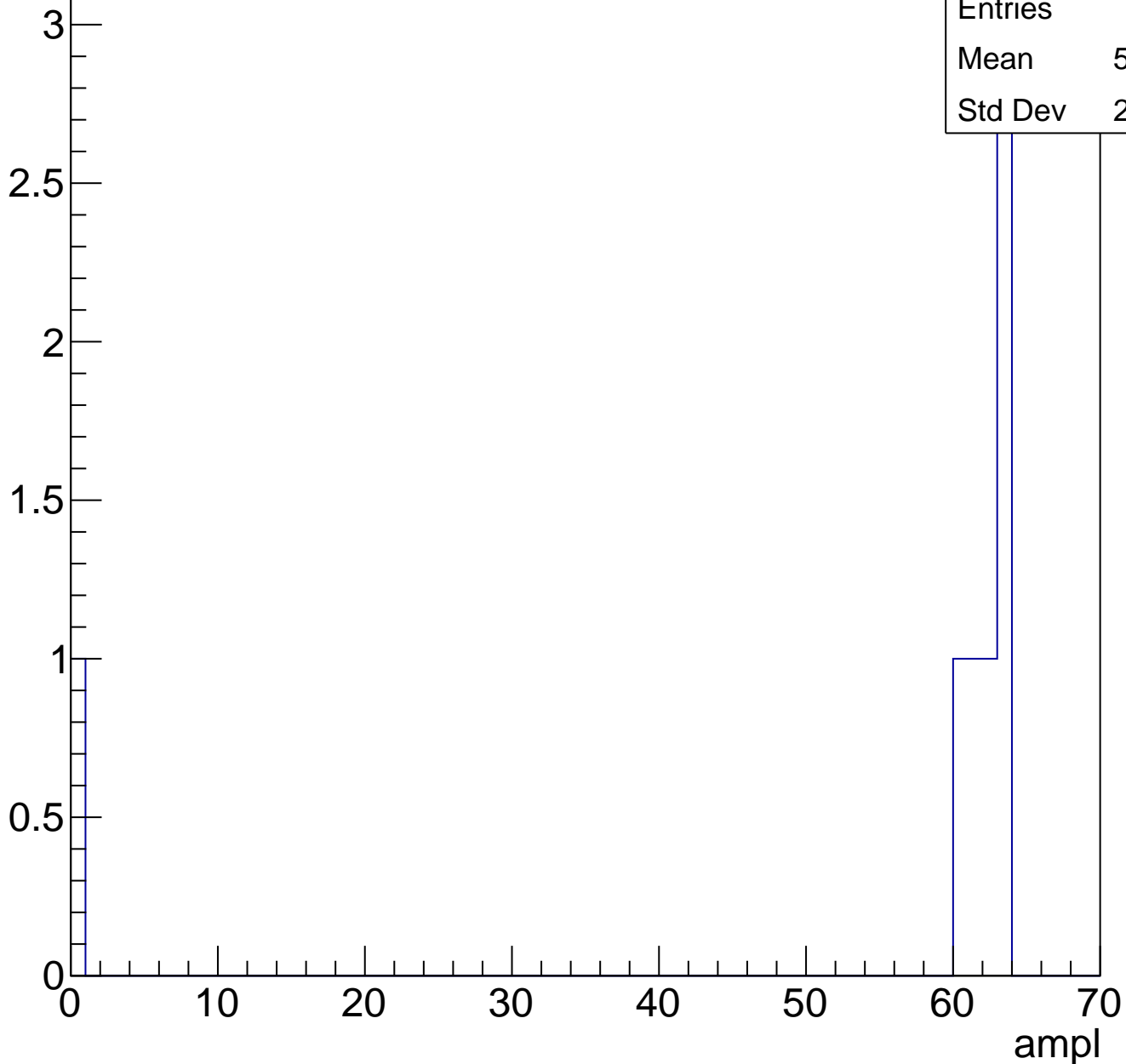
Entries	45
Mean	59.69
Std Dev	2.269



# B1L102S, U25-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

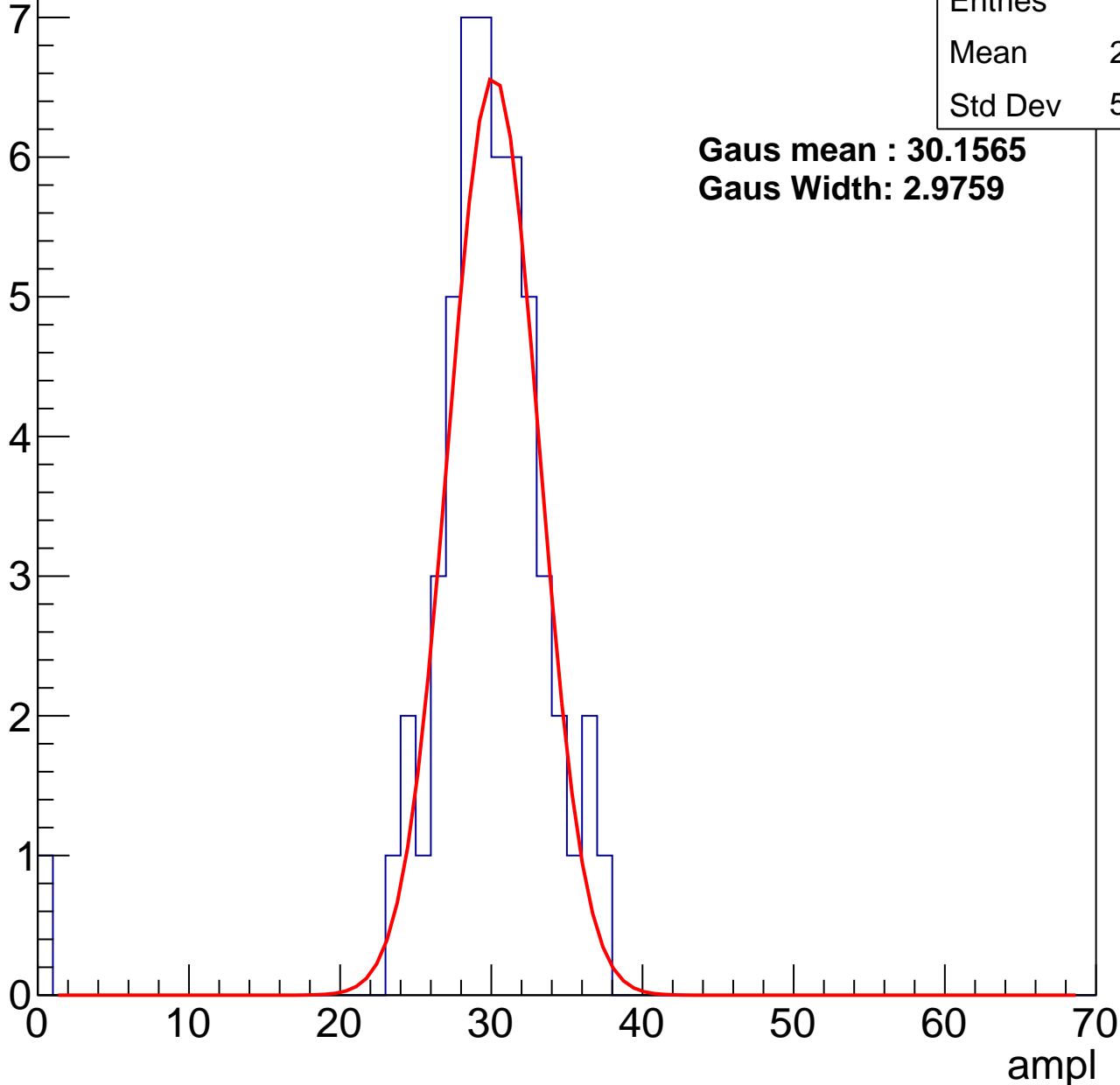
# B1L102S, U25-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	29.15
Std Dev	5.078

**Gaus mean : 30.1565**  
**Gaus Width: 2.9759**



# B1L102S, U25-ch115, adc1

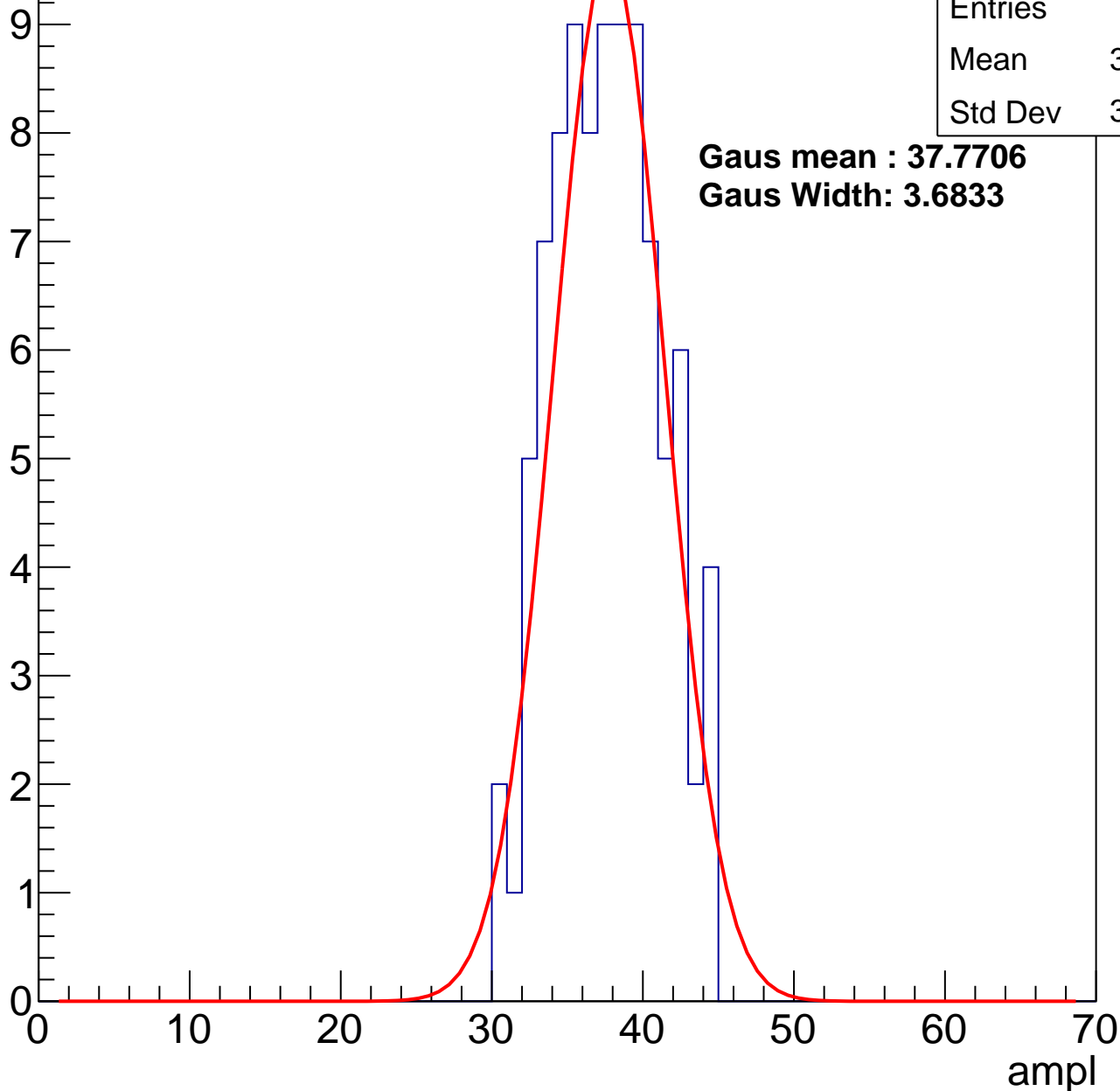
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	91
Mean	37.16
Std Dev	3.462

**Gaus mean : 37.7706**

**Gaus Width: 3.6833**



# B1L102S, U25-ch115, adc2

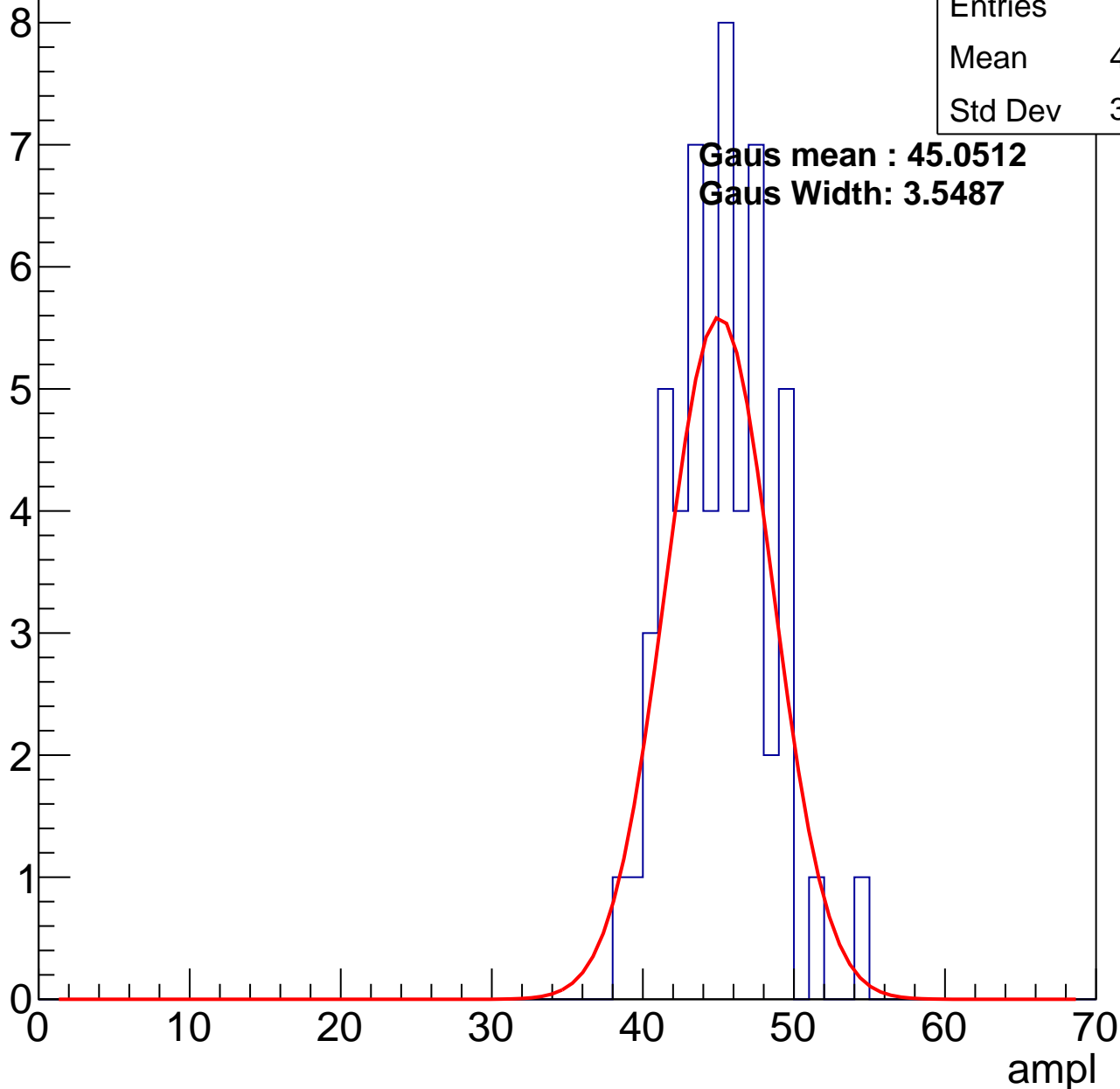
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	44.64
Std Dev	3.216

Gaus mean : 45.0512

Gaus Width: 3.5487



# B1L102S, U25-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

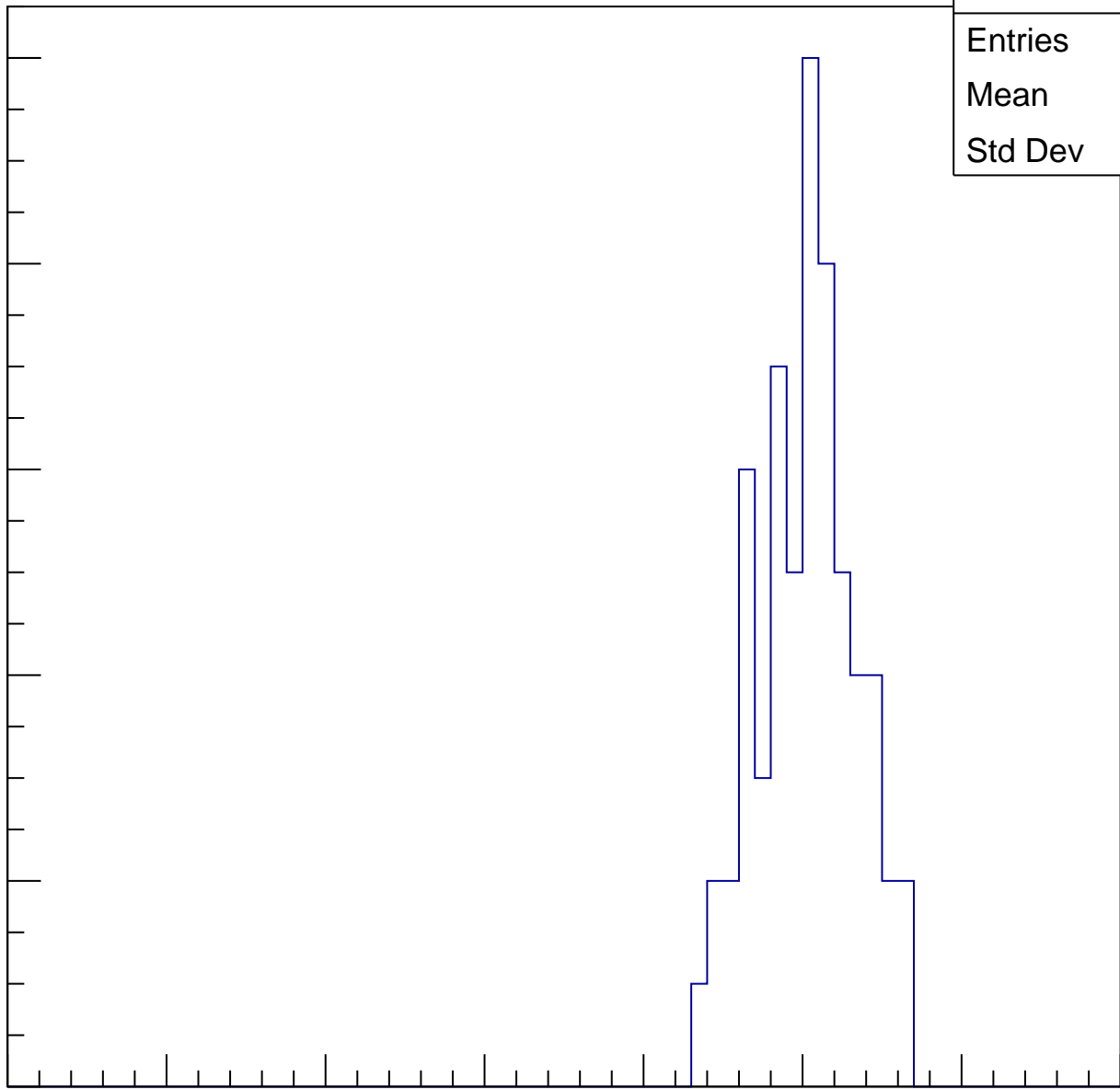
Entries	61
Mean	49.79
Std Dev	3.068

Entry

10  
8  
6  
4  
2  
0

ampl

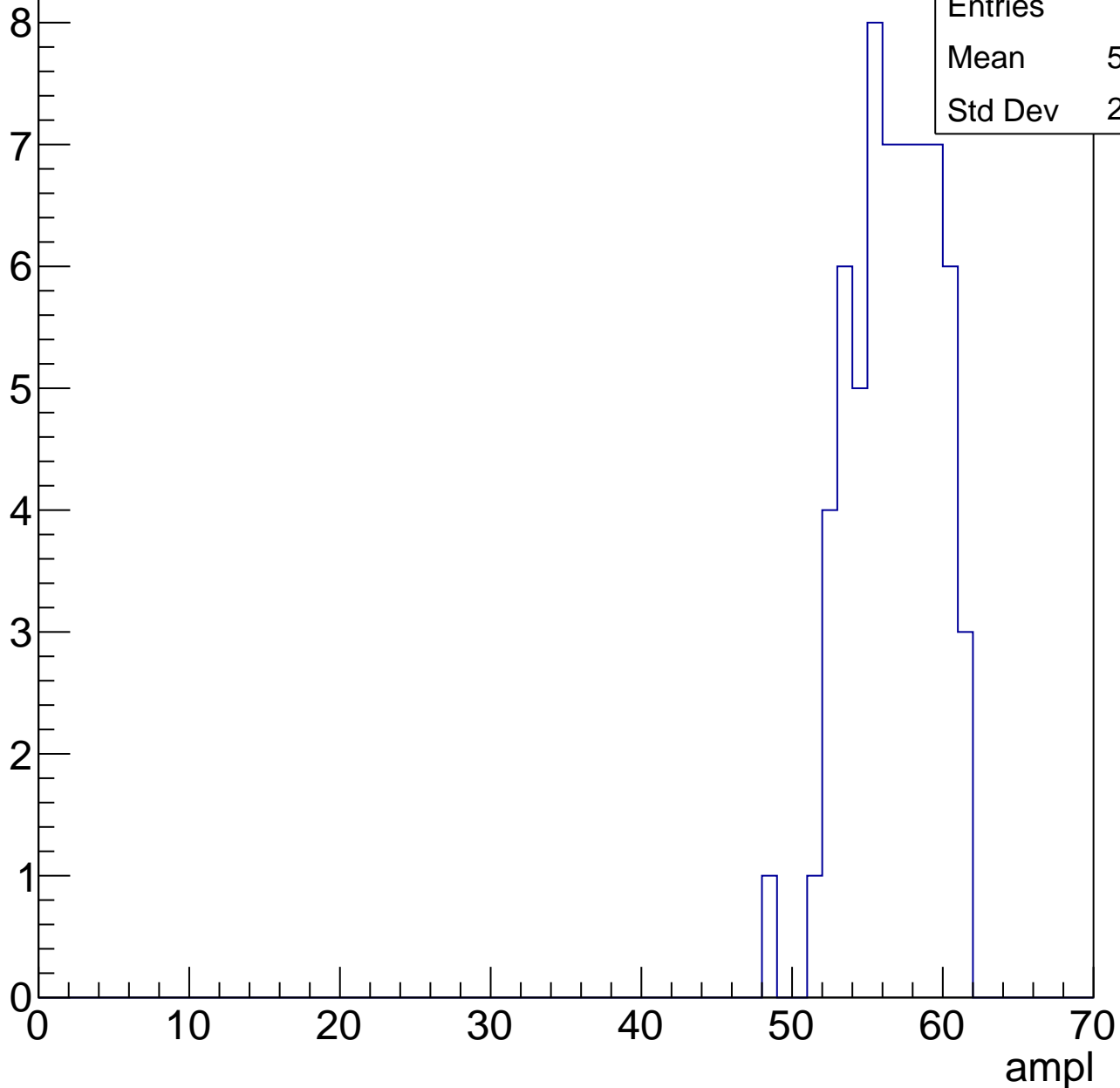
0 10 20 30 40 50 60 70



# B1L102S, U25-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	62
Mean	56.26
Std Dev	2.839

# B1L102S, U25-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	42
Mean	59.21
Std Dev	9.553

Entry

10

8

6

4

2

0

0

10

20

30

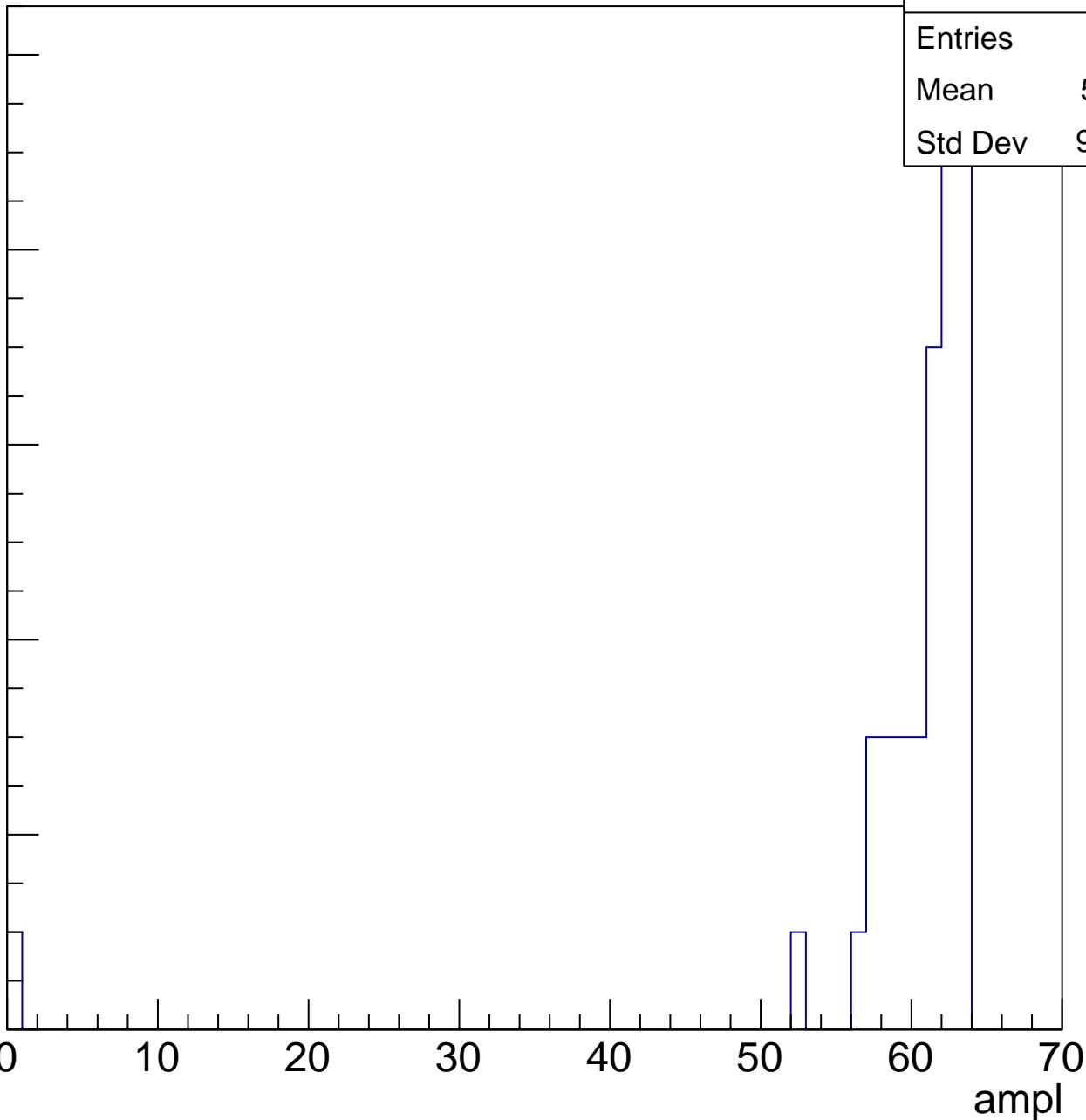
40

50

60

70

ampl



# B1L102S, U25-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch116, adc0

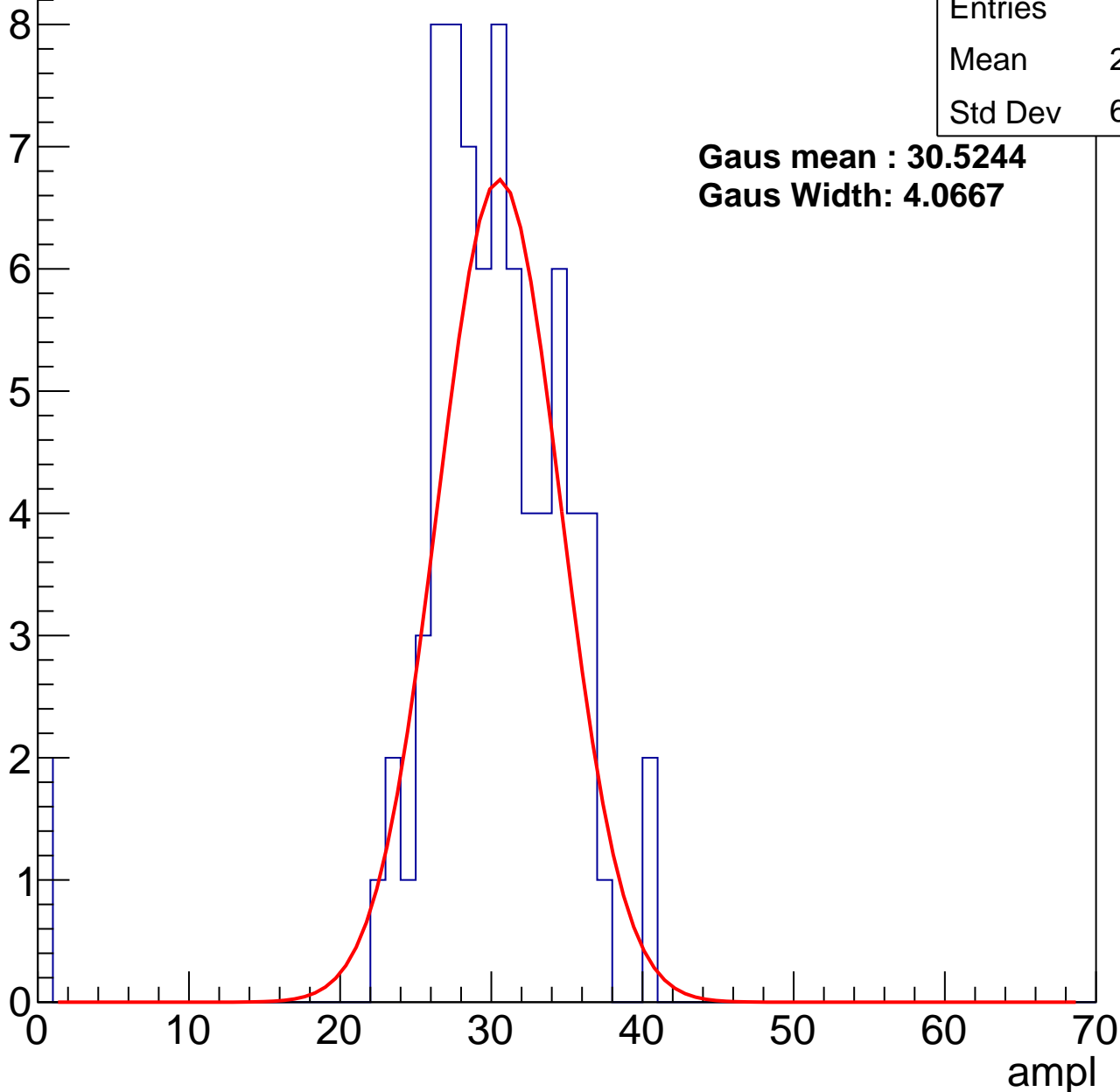
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.25
Std Dev	6.148

**Gaus mean : 30.5244**

**Gaus Width: 4.0667**



# B1L102S, U25-ch116, adc1

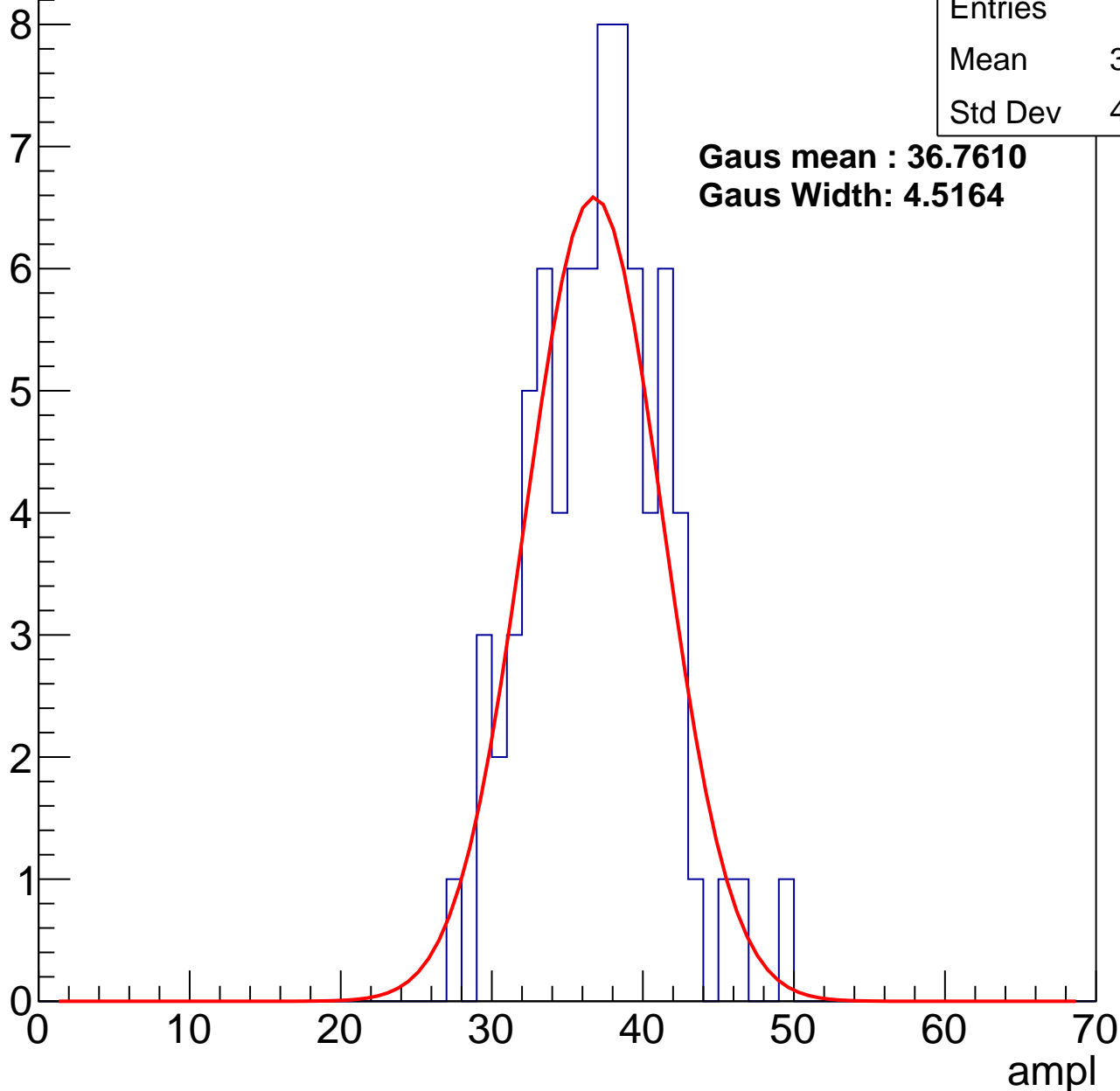
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.55
Std Dev	4.228

**Gaus mean : 36.7610**

**Gaus Width: 4.5164**



# B1L102S, U25-ch116, adc2

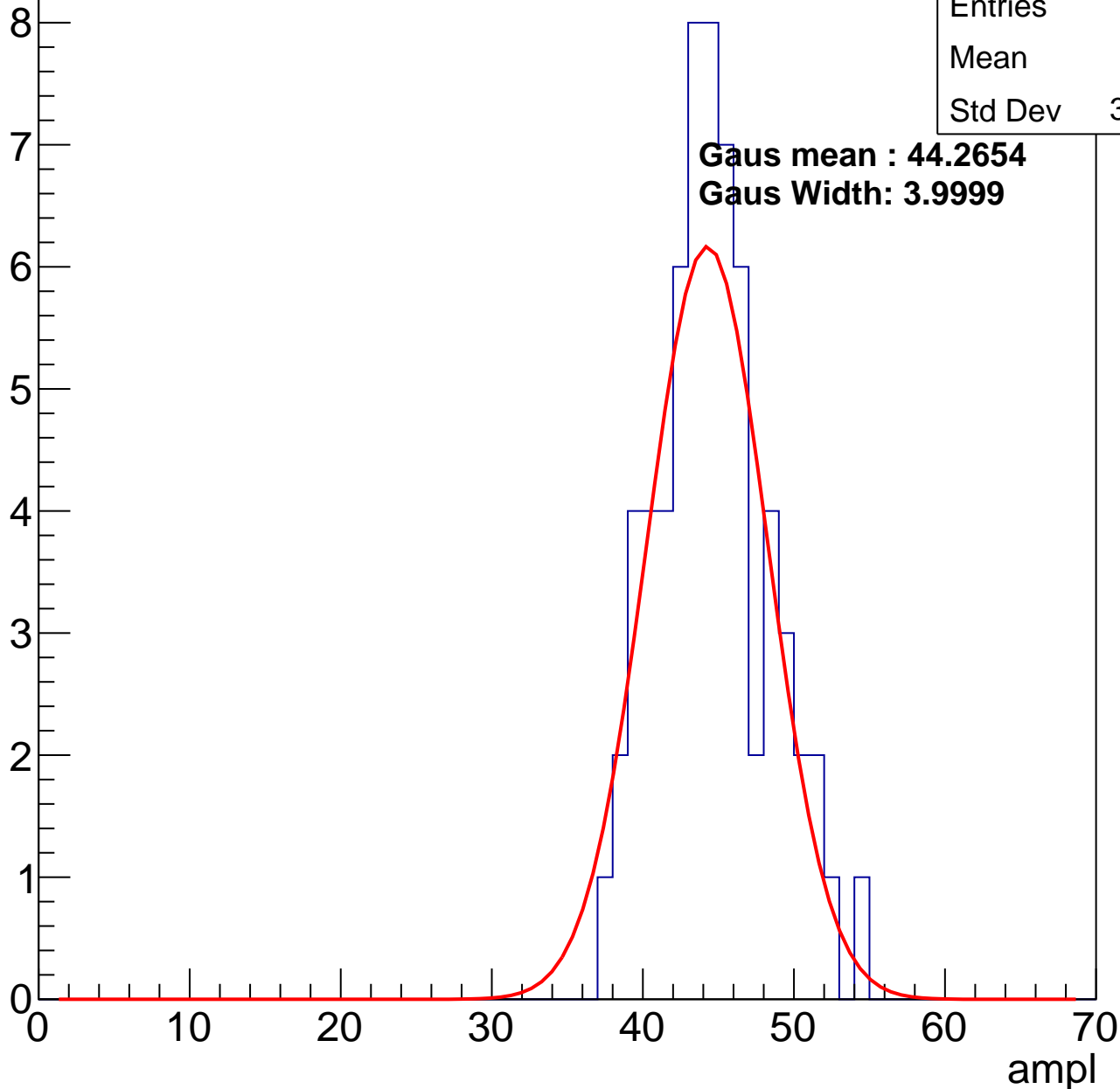
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.2
Std Dev	3.664

**Gaus mean : 44.2654**

**Gaus Width: 3.9999**

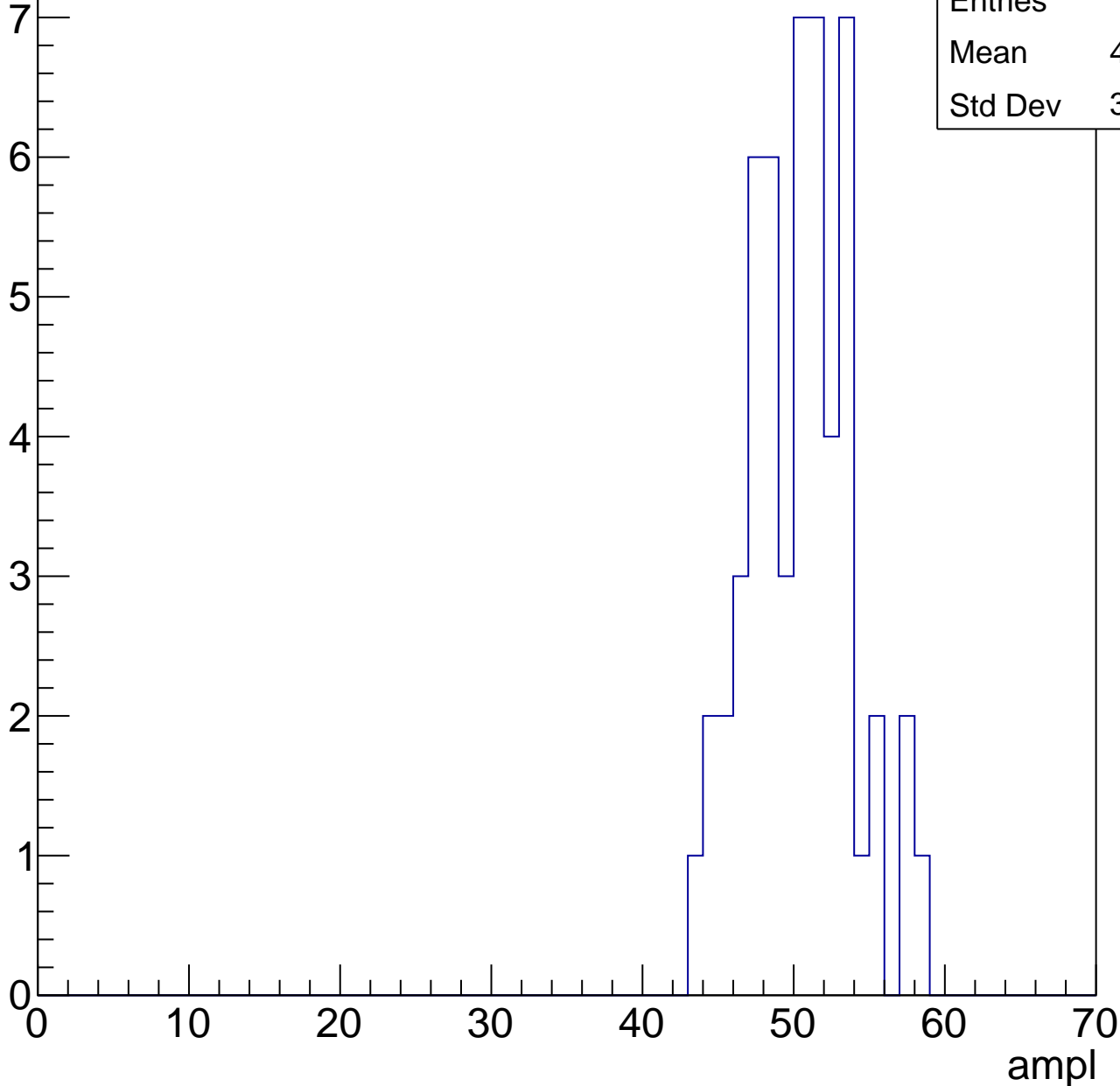


# B1L102S, U25-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	49.96
Std Dev	3.372

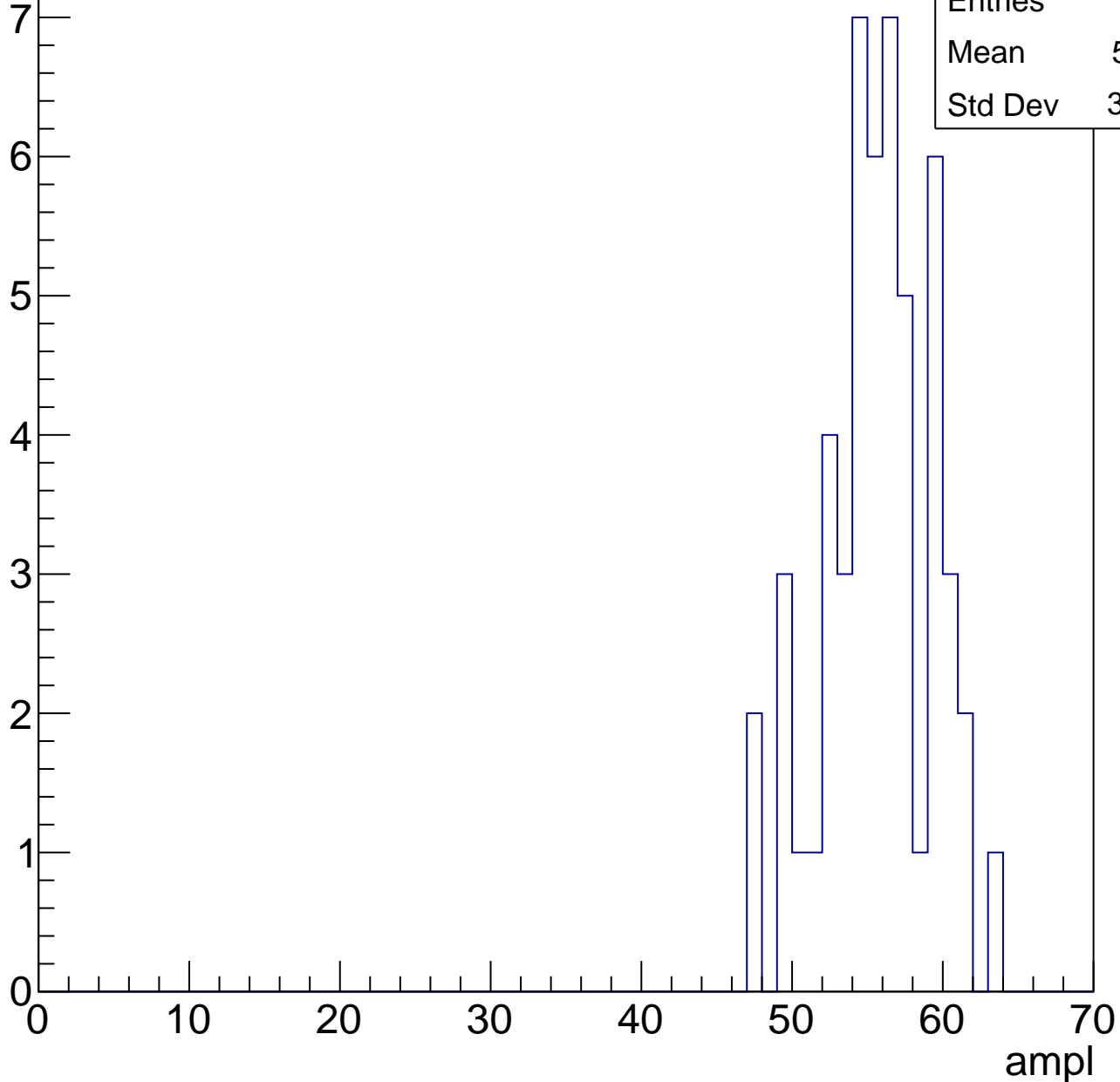


# B1L102S, U25-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.21
Std Dev	3.597

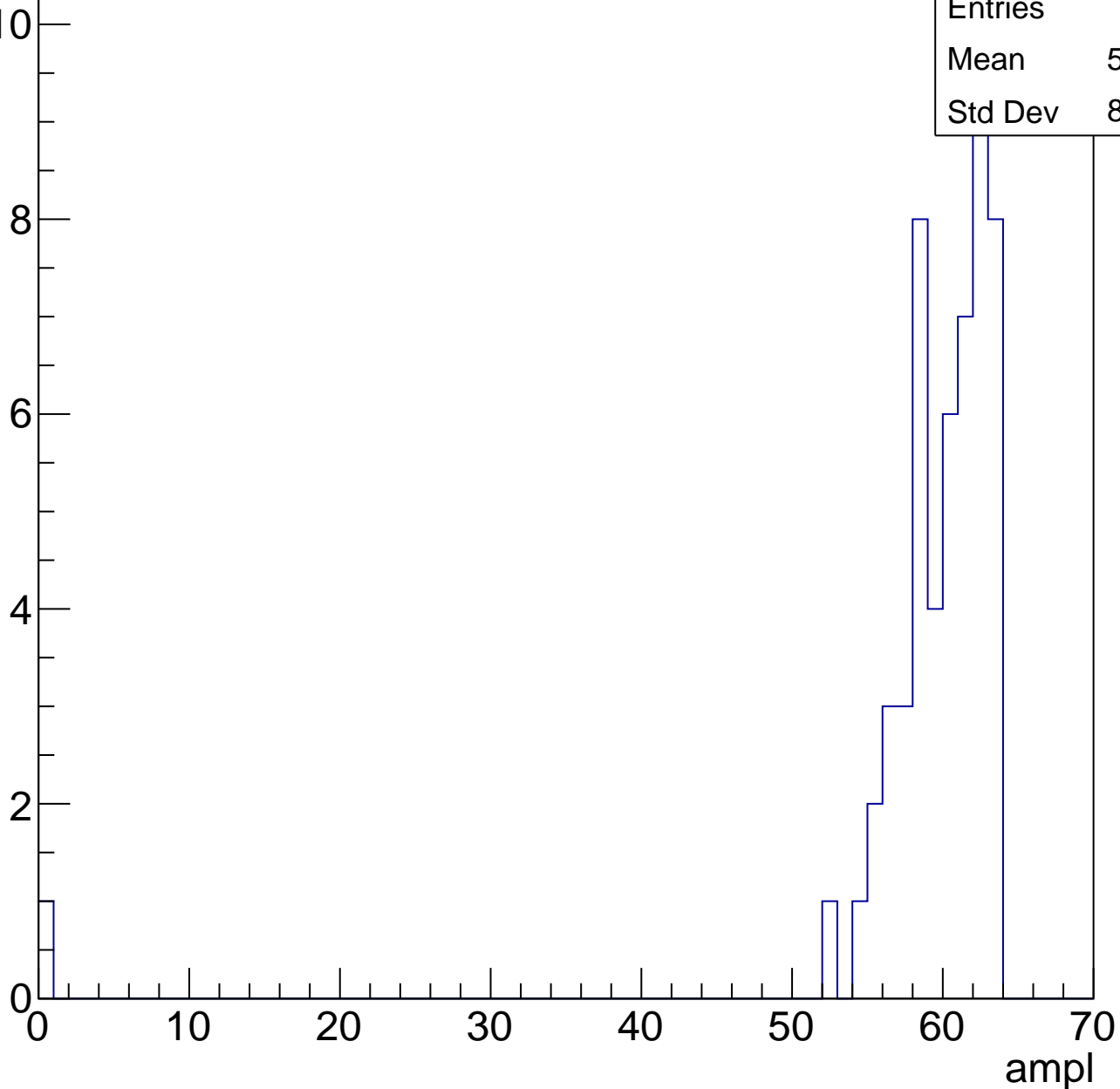


# B1L102S, U25-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.63
Std Dev	8.475



# B1L102S, U25-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch117, adc0

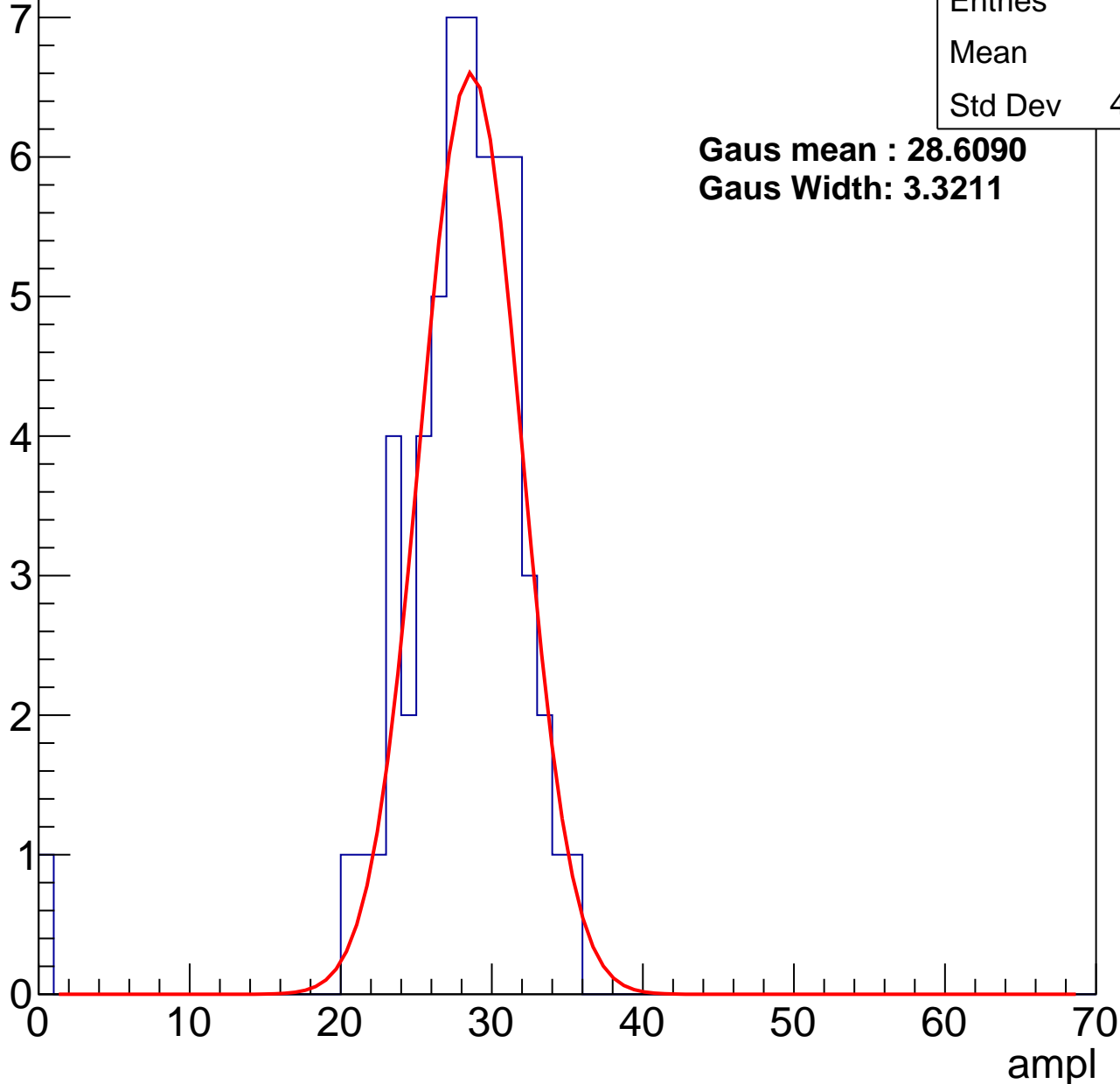
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.4
Std Dev	4.867

**Gaus mean : 28.6090**

**Gaus Width: 3.3211**



# B1L102S, U25-ch117, adc1

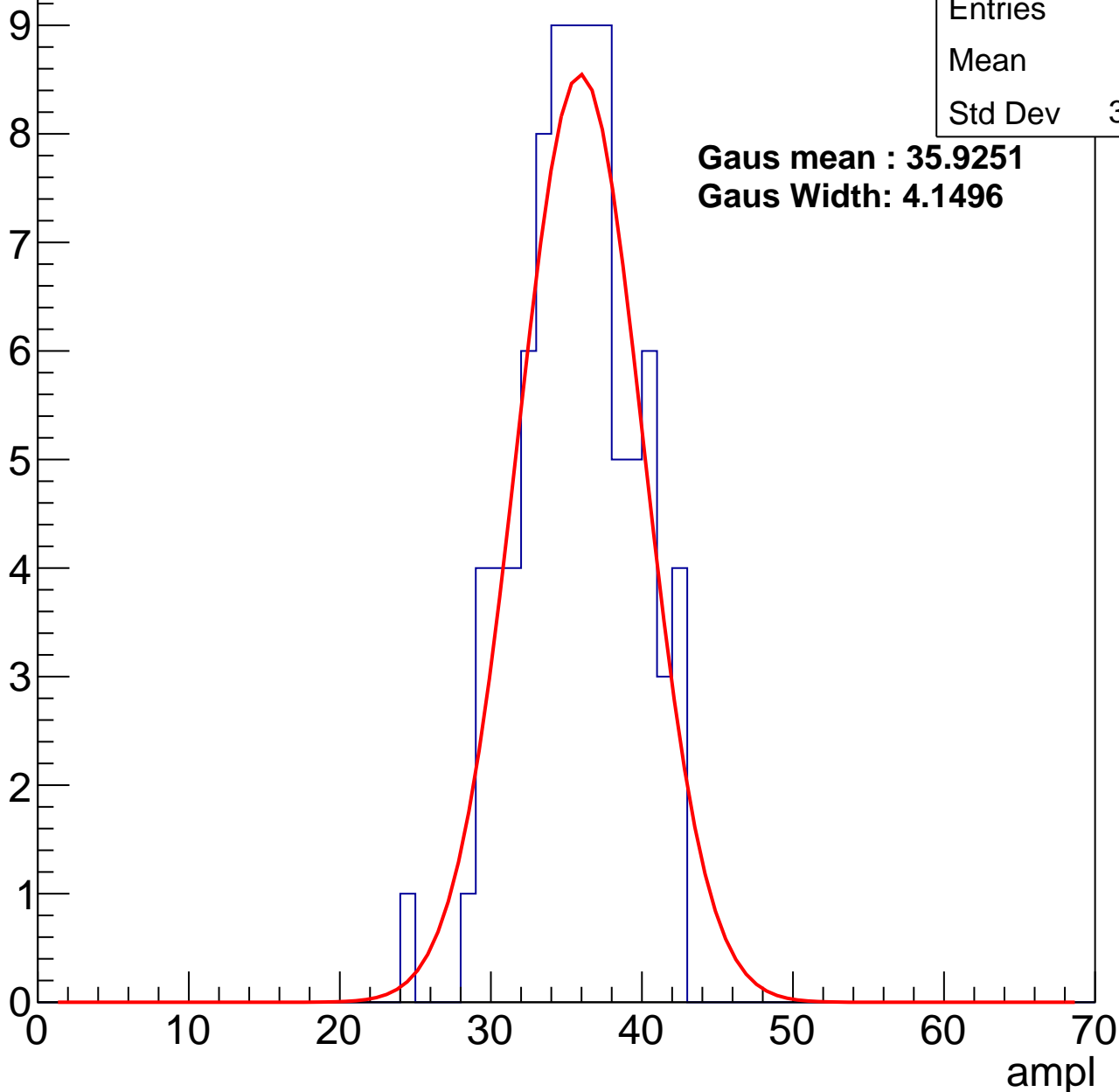
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.2
Std Dev	3.704

**Gaus mean : 35.9251**

**Gaus Width: 4.1496**



# B1L102S, U25-ch117, adc2

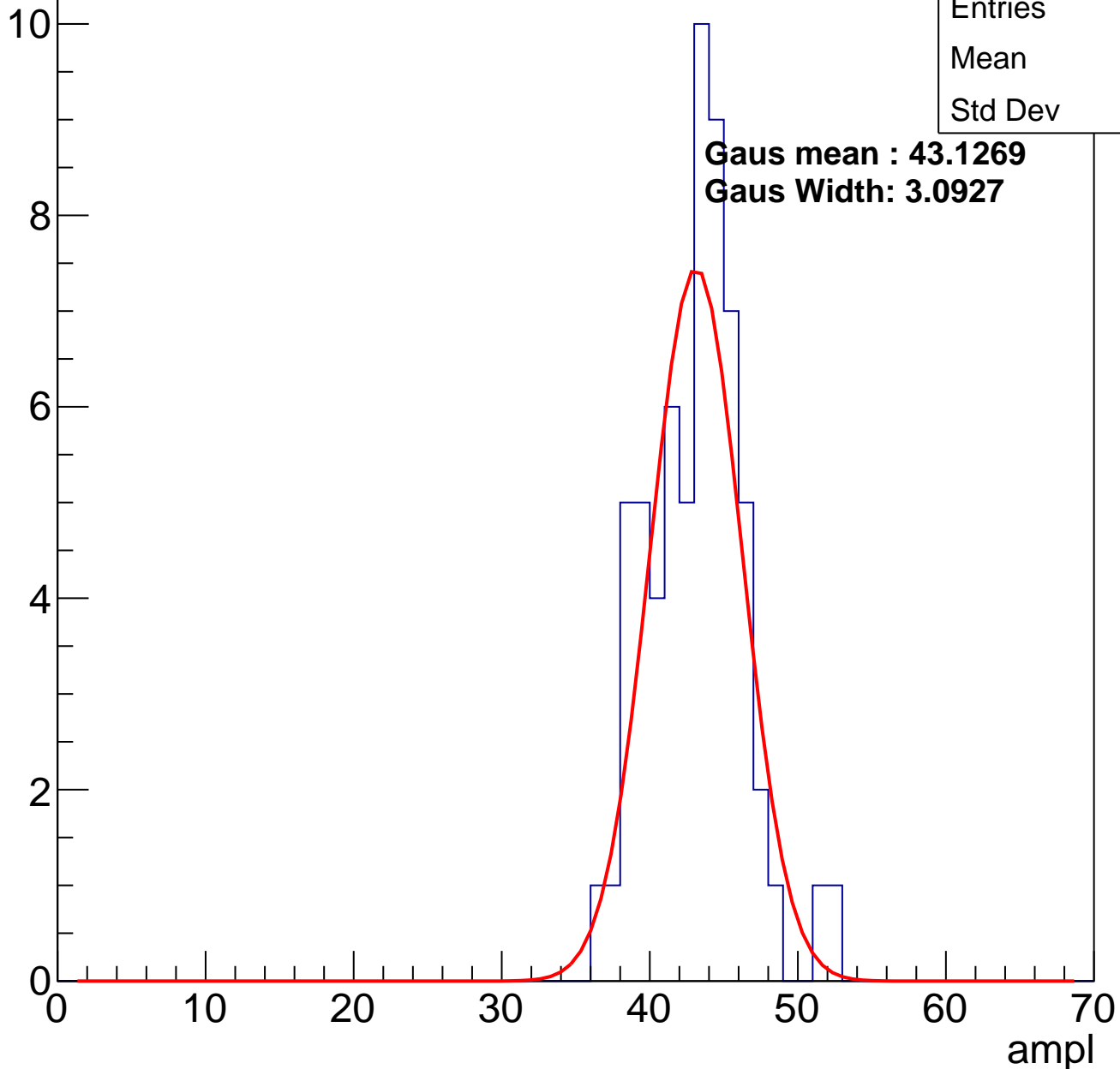
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	63
Mean	42.7
Std Dev	3.17

**Gaus mean : 43.1269**

**Gaus Width: 3.0927**

Entry

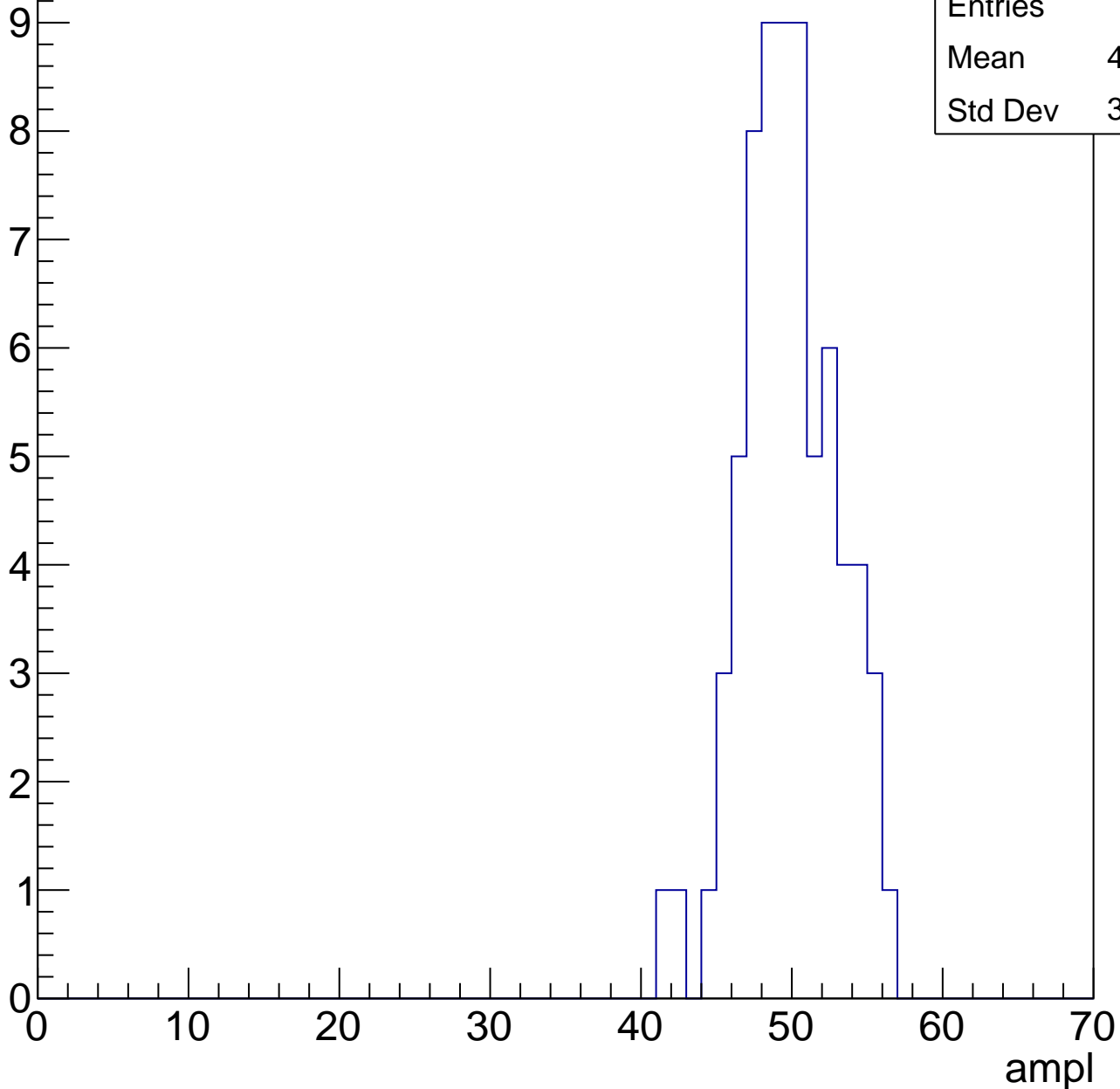


# B1L102S, U25-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.38
Std Dev	3.107

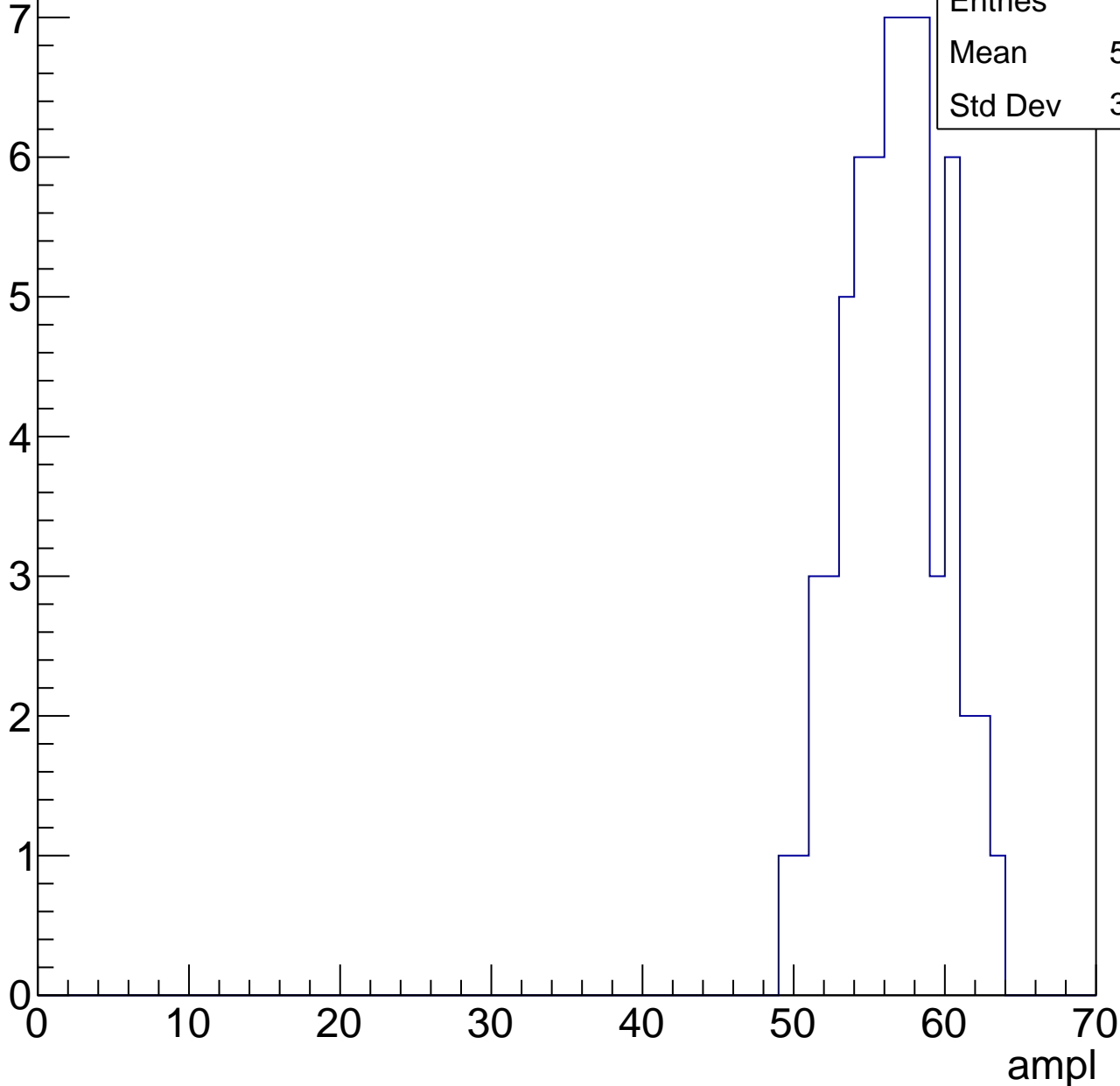


# B1L102S, U25-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.17
Std Dev	3.189

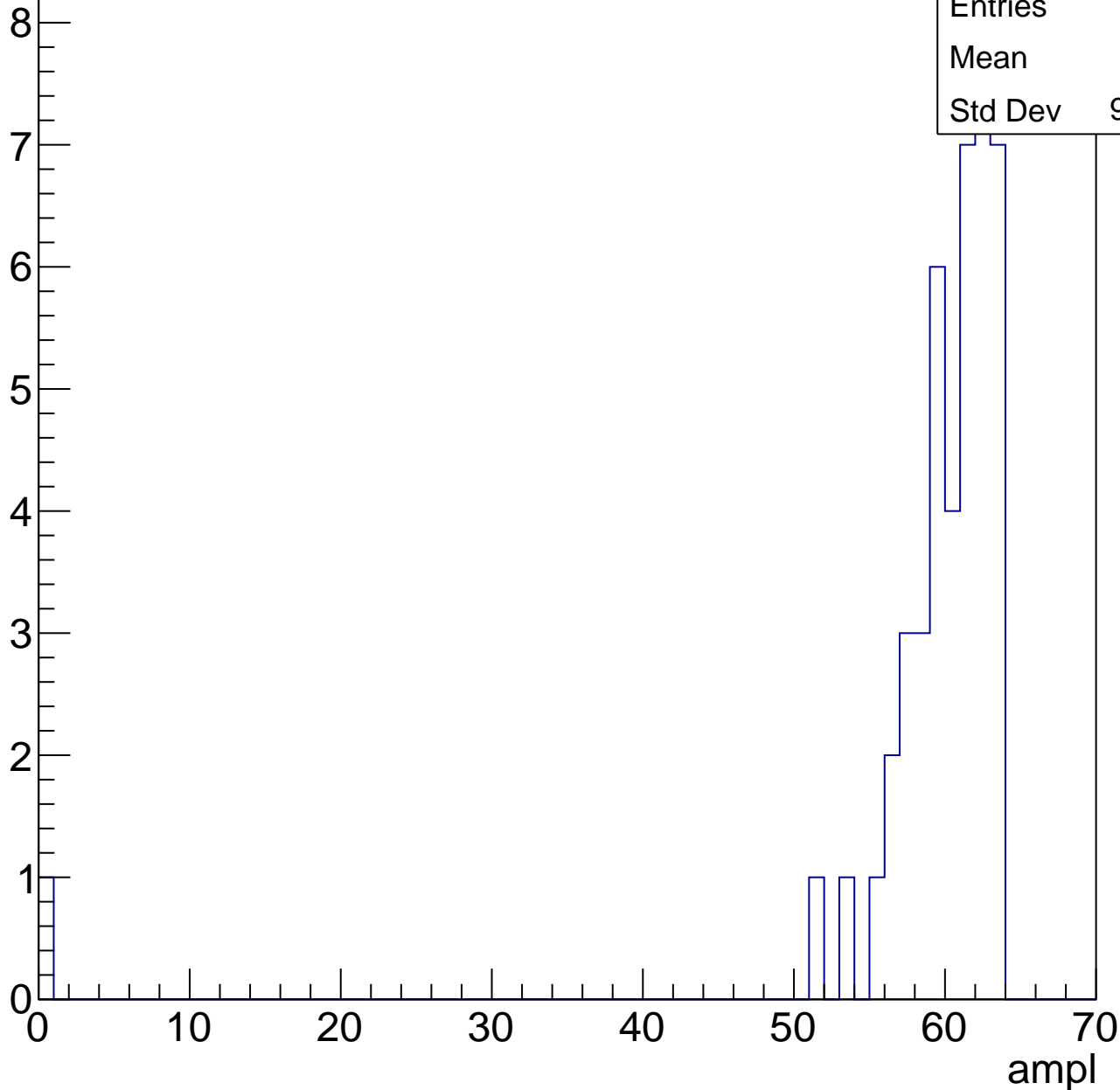


# B1L102S, U25-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

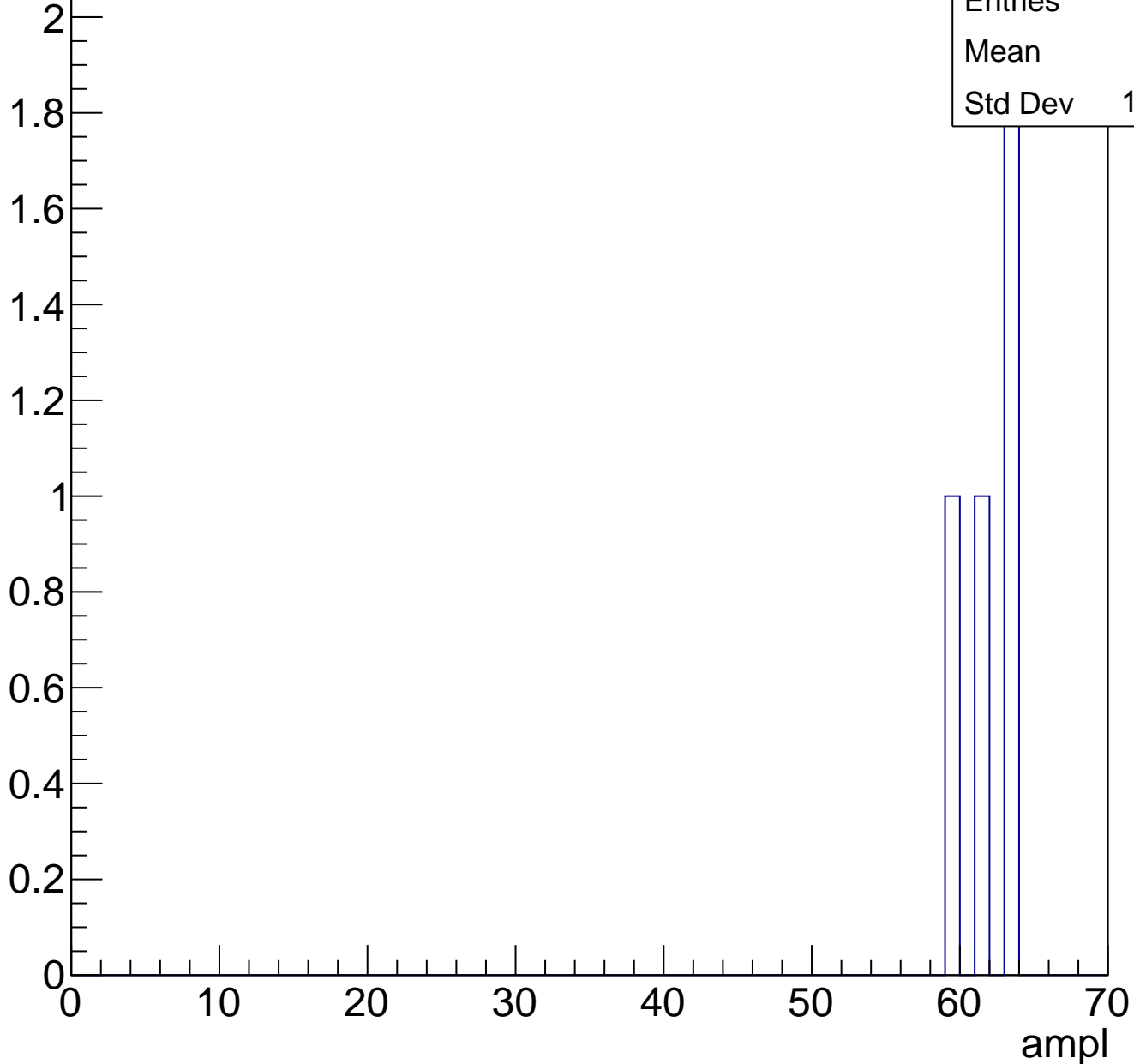
Entries	44
Mean	58.5
Std Dev	9.336



# B1L102S, U25-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch118, adc0

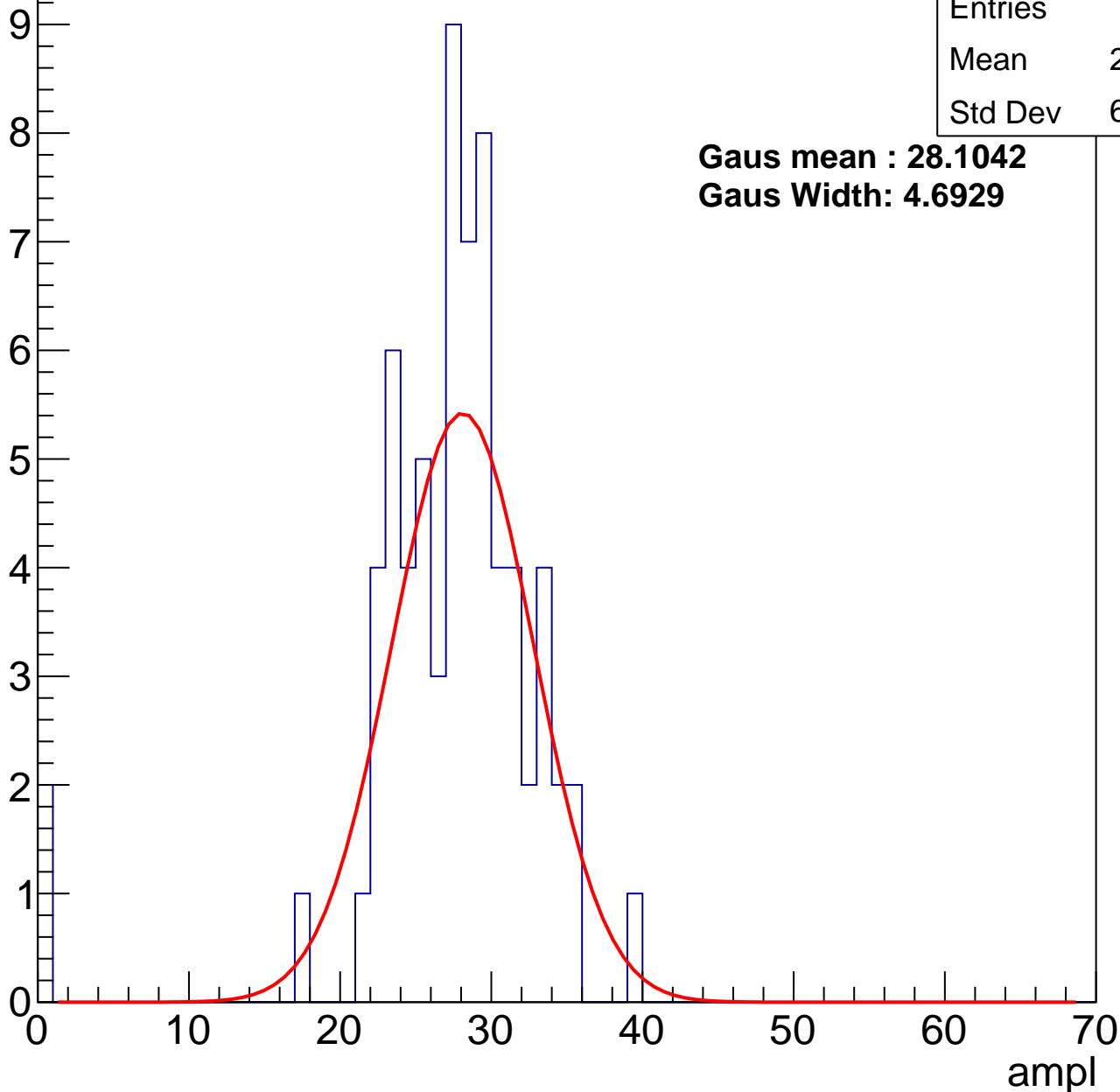
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	69
Mean	26.83
Std Dev	6.079

**Gaus mean : 28.1042**

**Gaus Width: 4.6929**



# B1L102S, U25-ch118, adc1

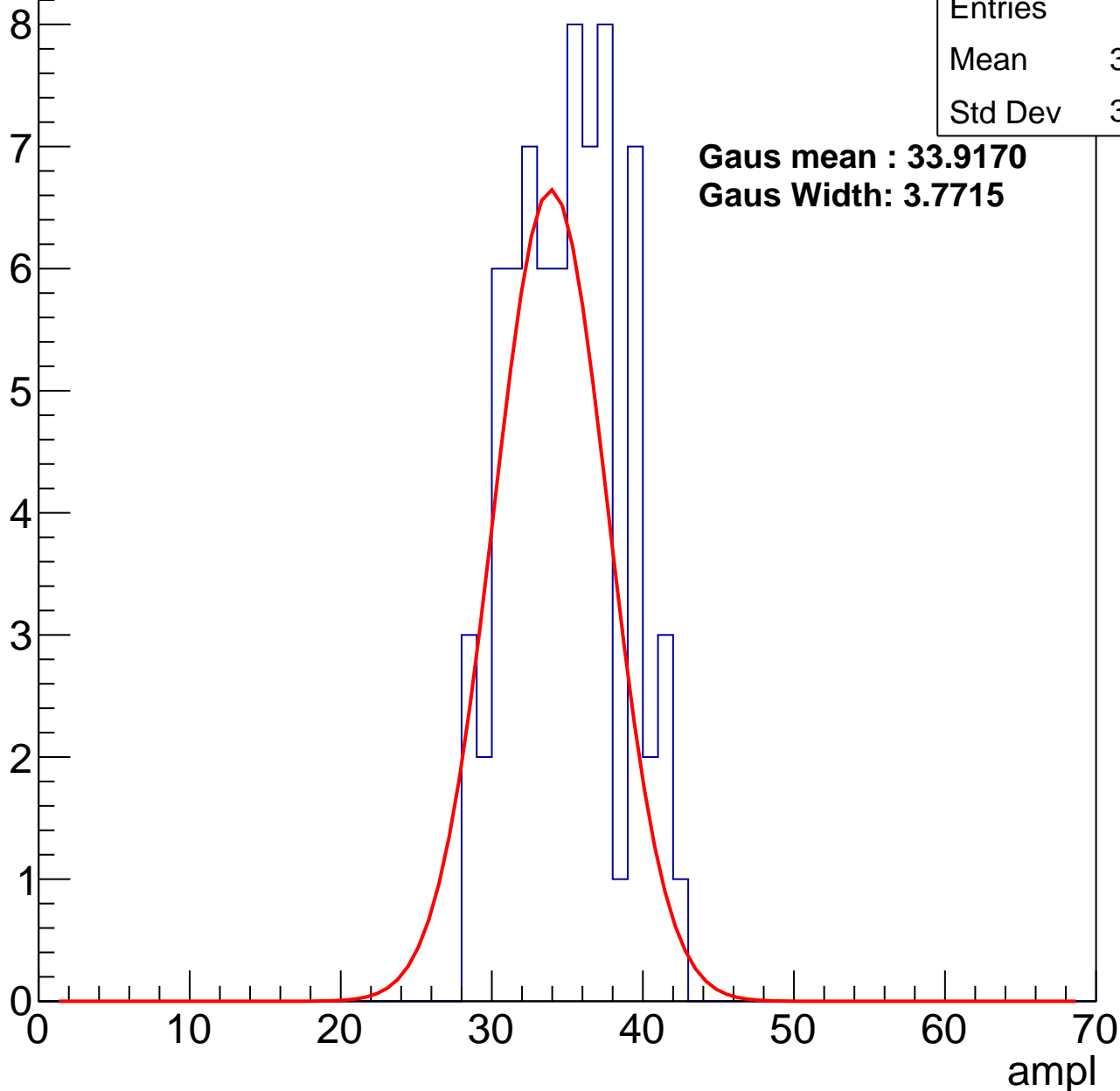
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.49
Std Dev	3.523

**Gaus mean : 33.9170**

**Gaus Width: 3.7715**



# B1L102S, U25-ch118, adc2

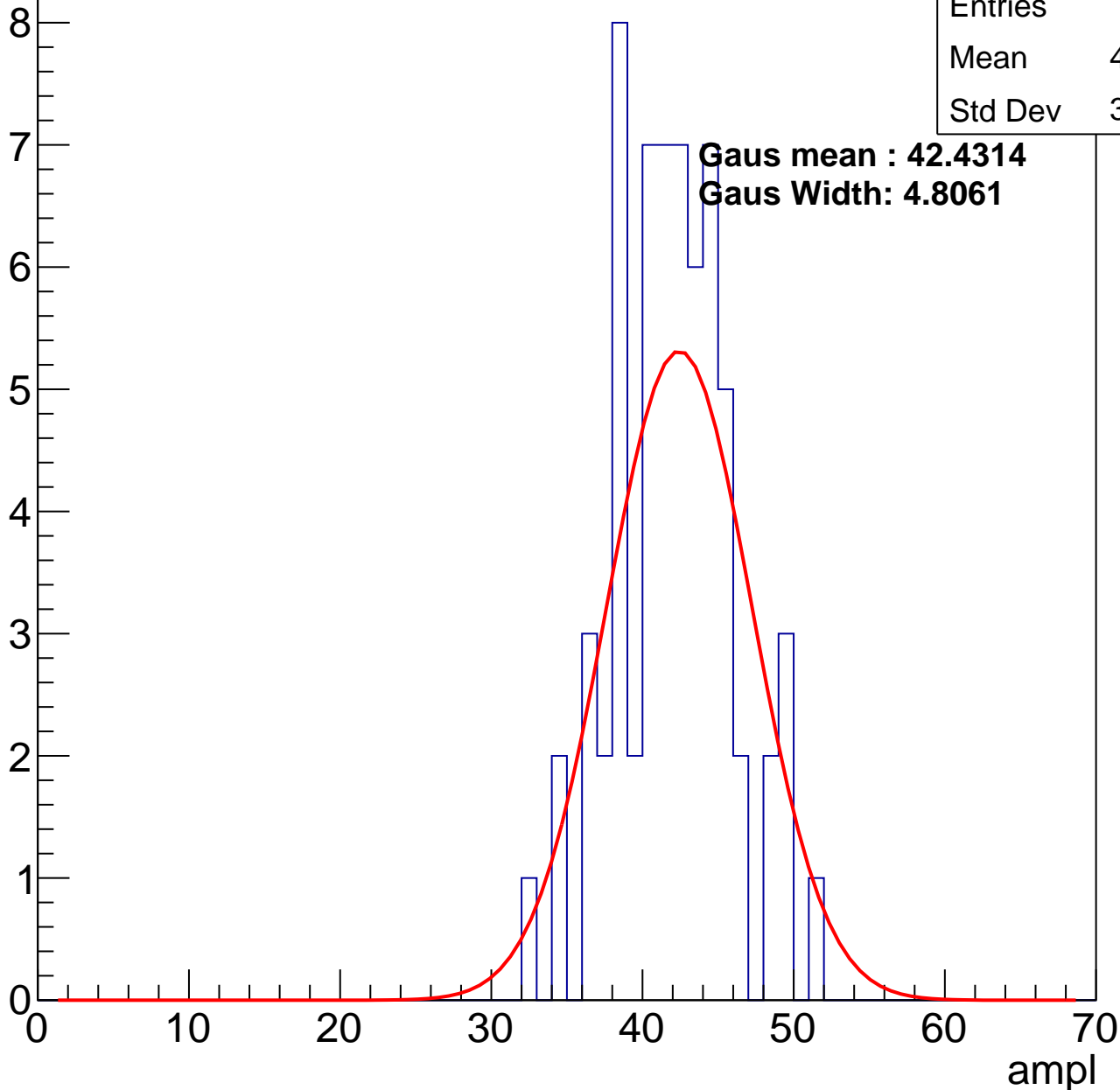
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.57
Std Dev	3.855

Gaus mean : 42.4314

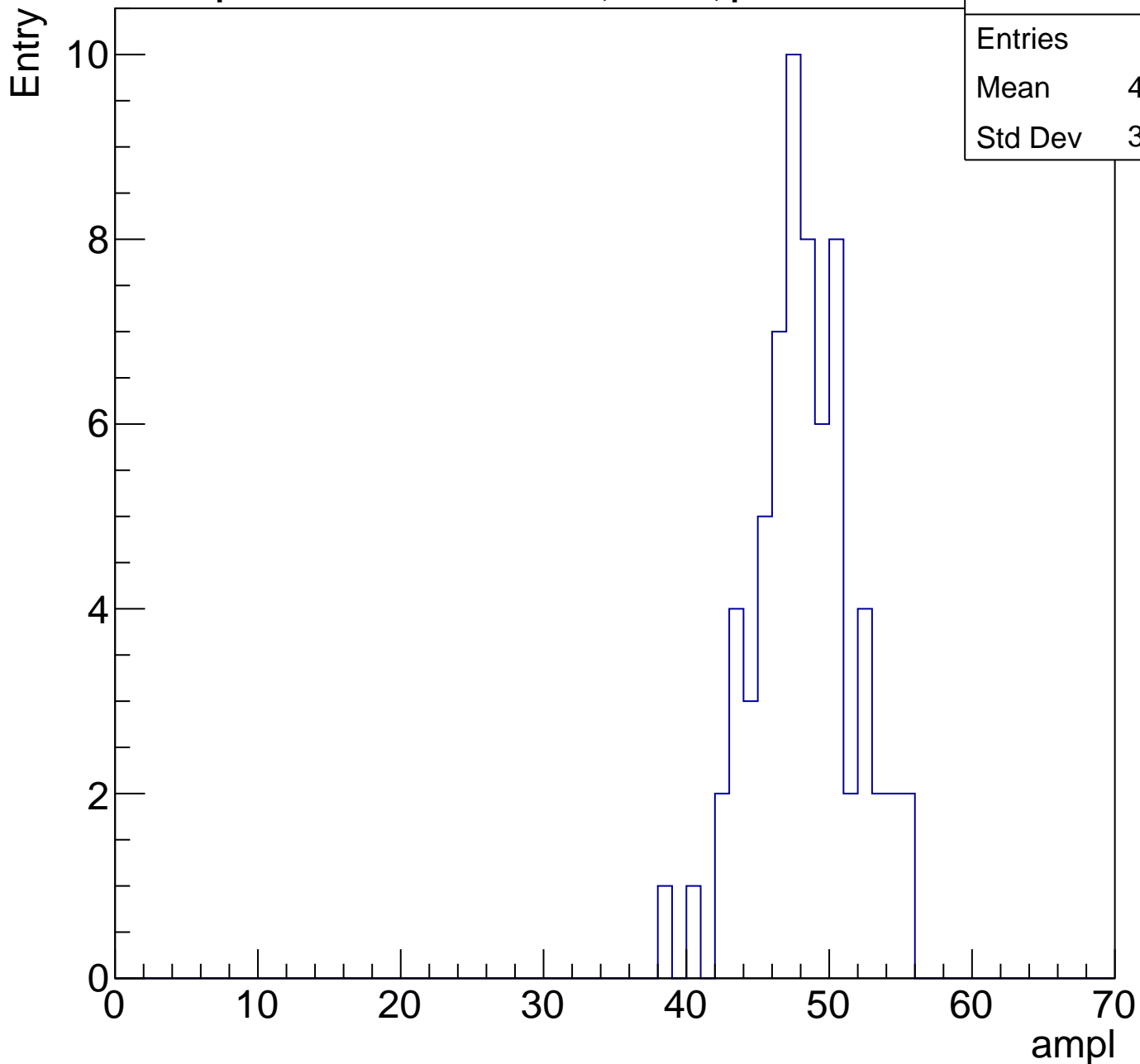
Gaus Width: 4.8061



# B1L102S, U25-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	67
Mean	47.69
Std Dev	3.456

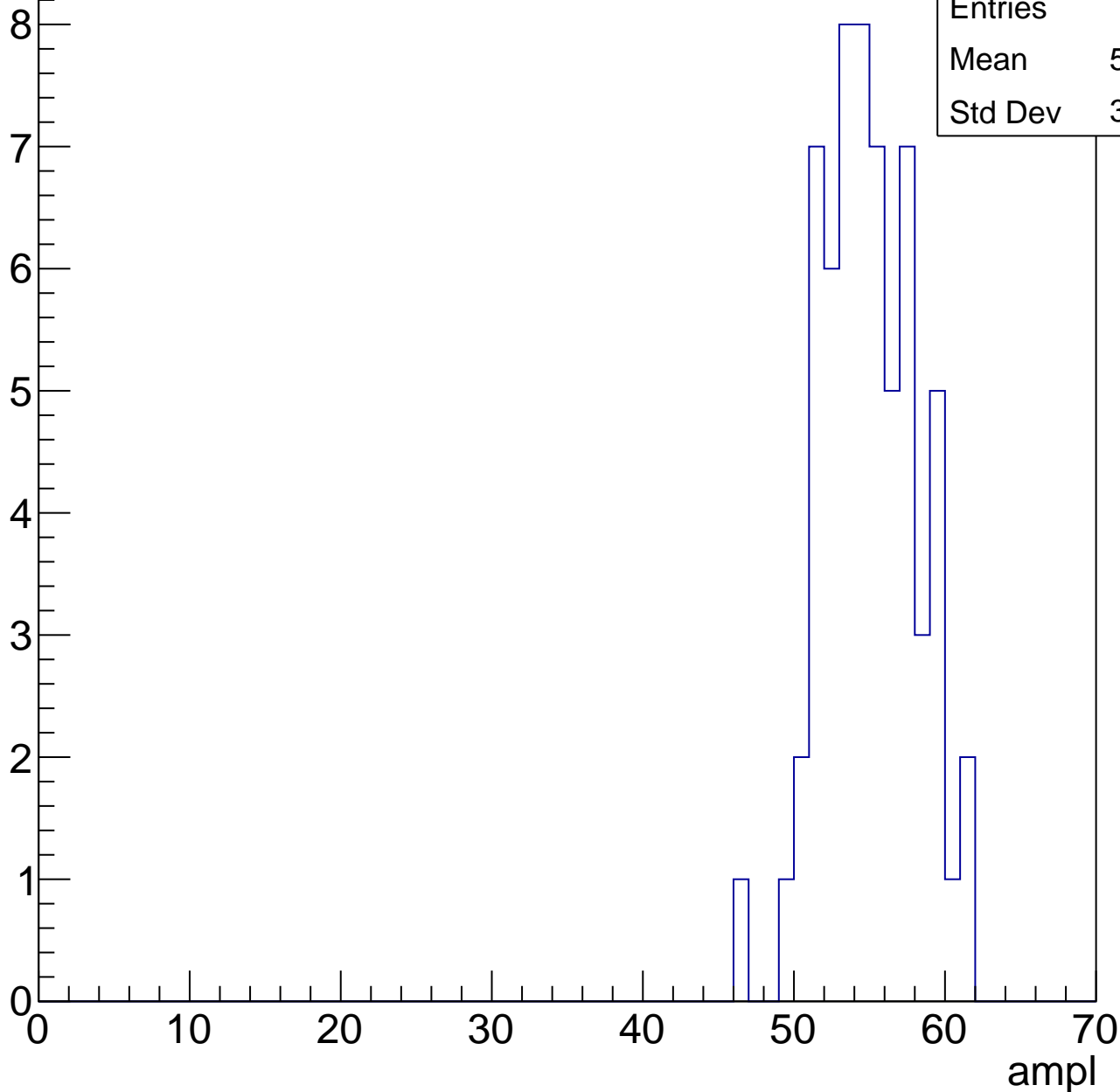


# B1L102S, U25-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.52
Std Dev	3.075

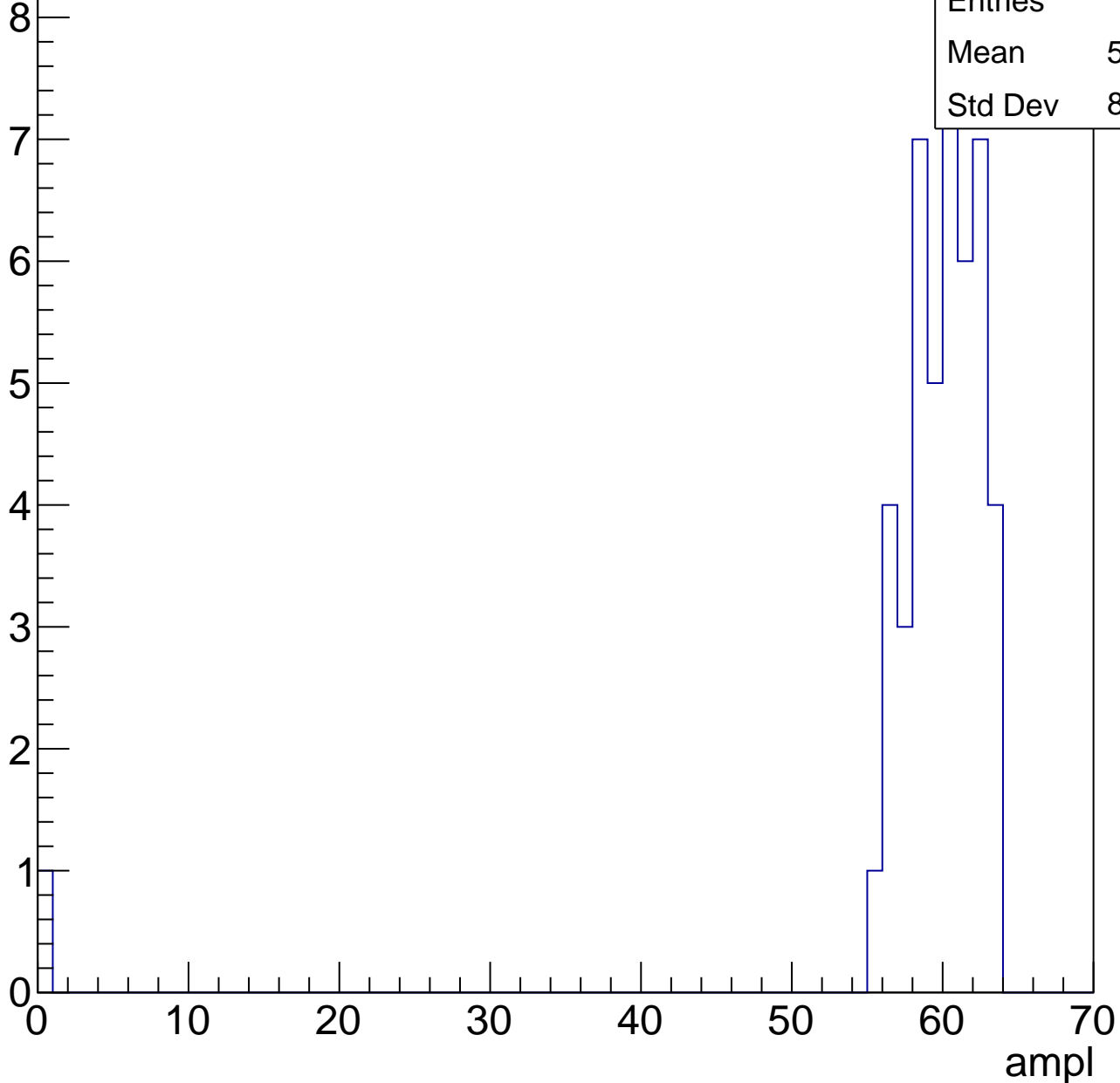


# B1L102S, U25-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.33
Std Dev	8.957

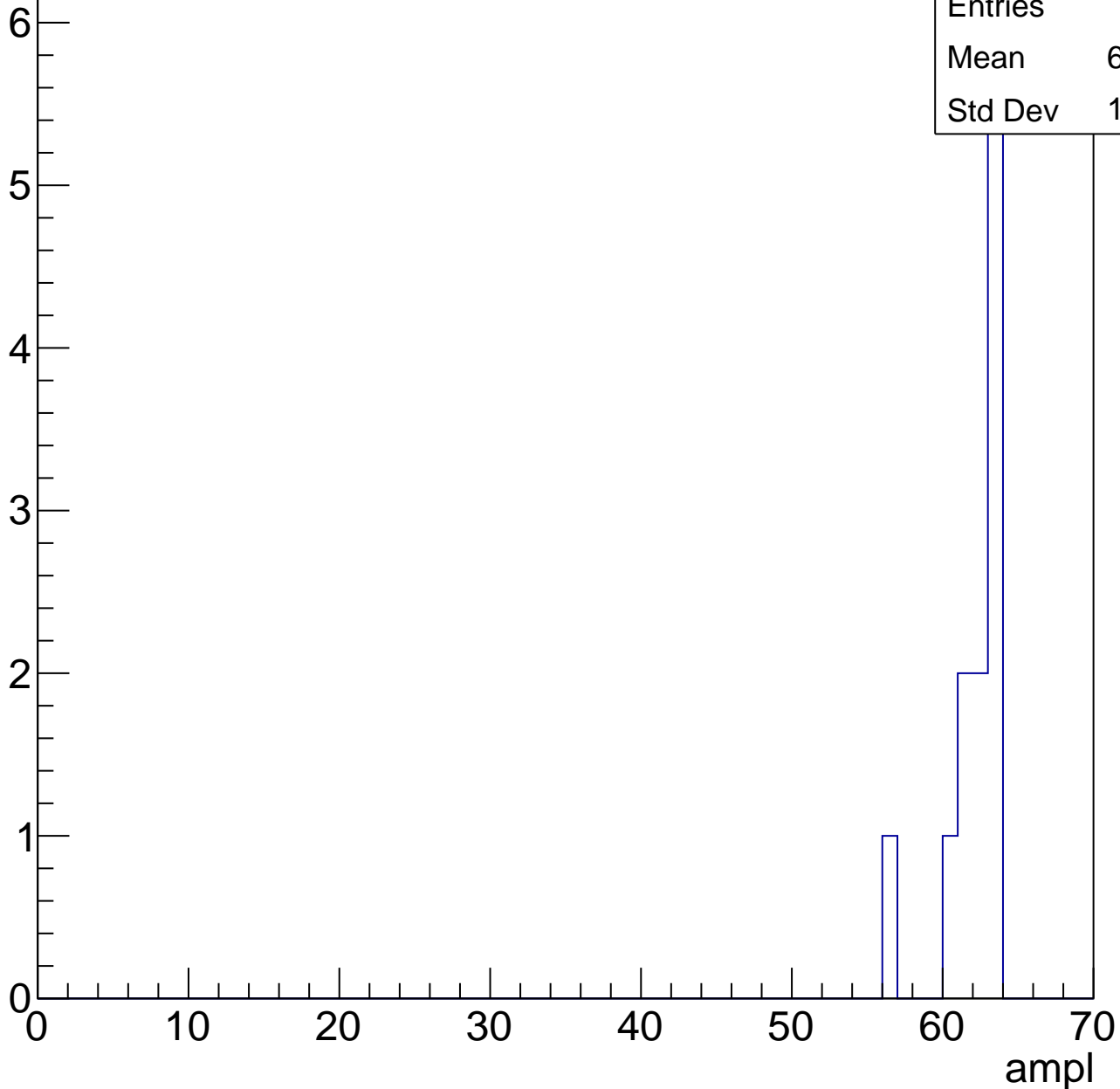


# B1L102S, U25-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.67
Std Dev	1.972

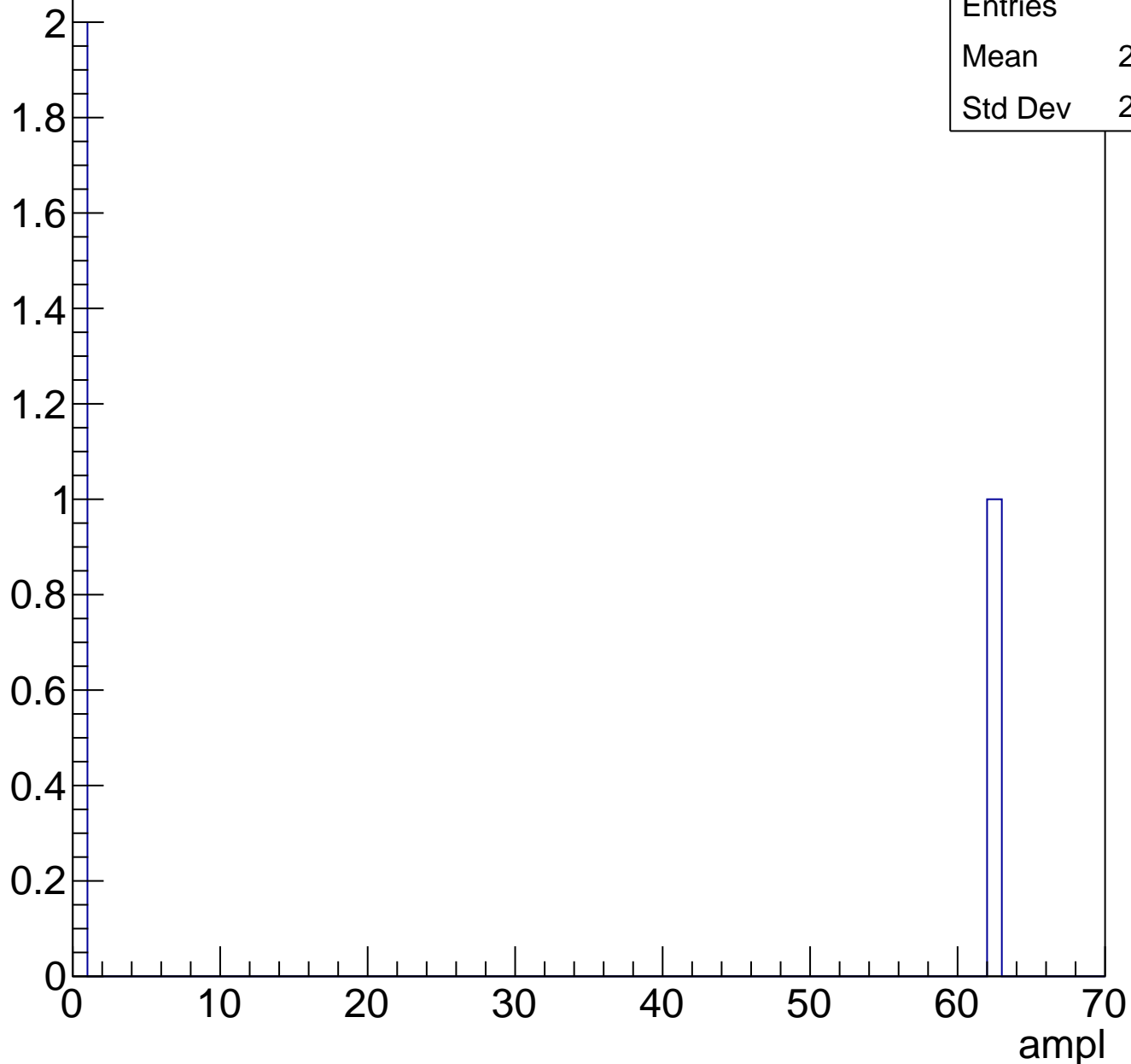




# B1L102S, U25-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	81
Mean	29.31
Std Dev	4.713

**Gaus mean : 30.1736**

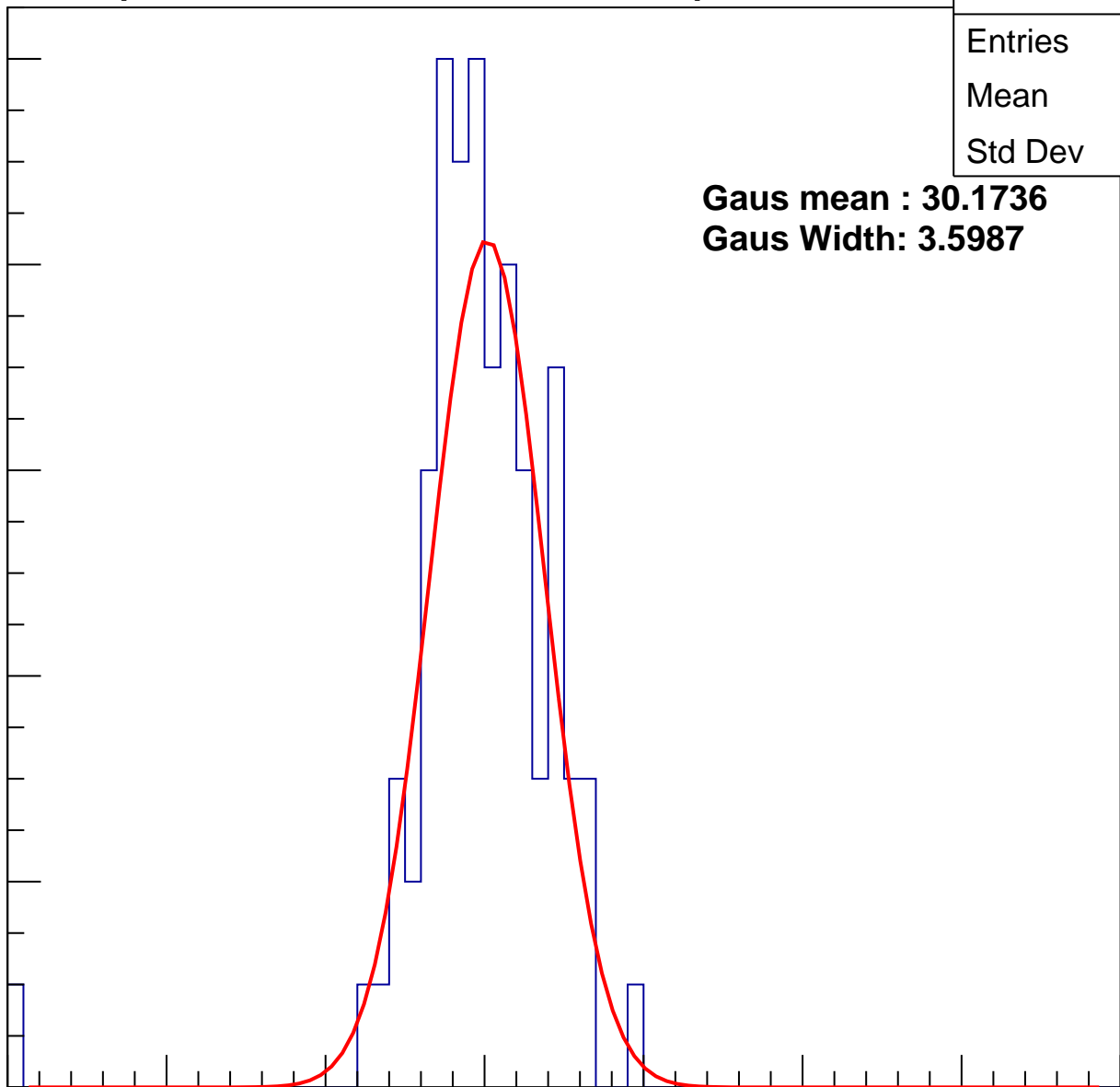
**Gaus Width: 3.5987**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch119, adc1

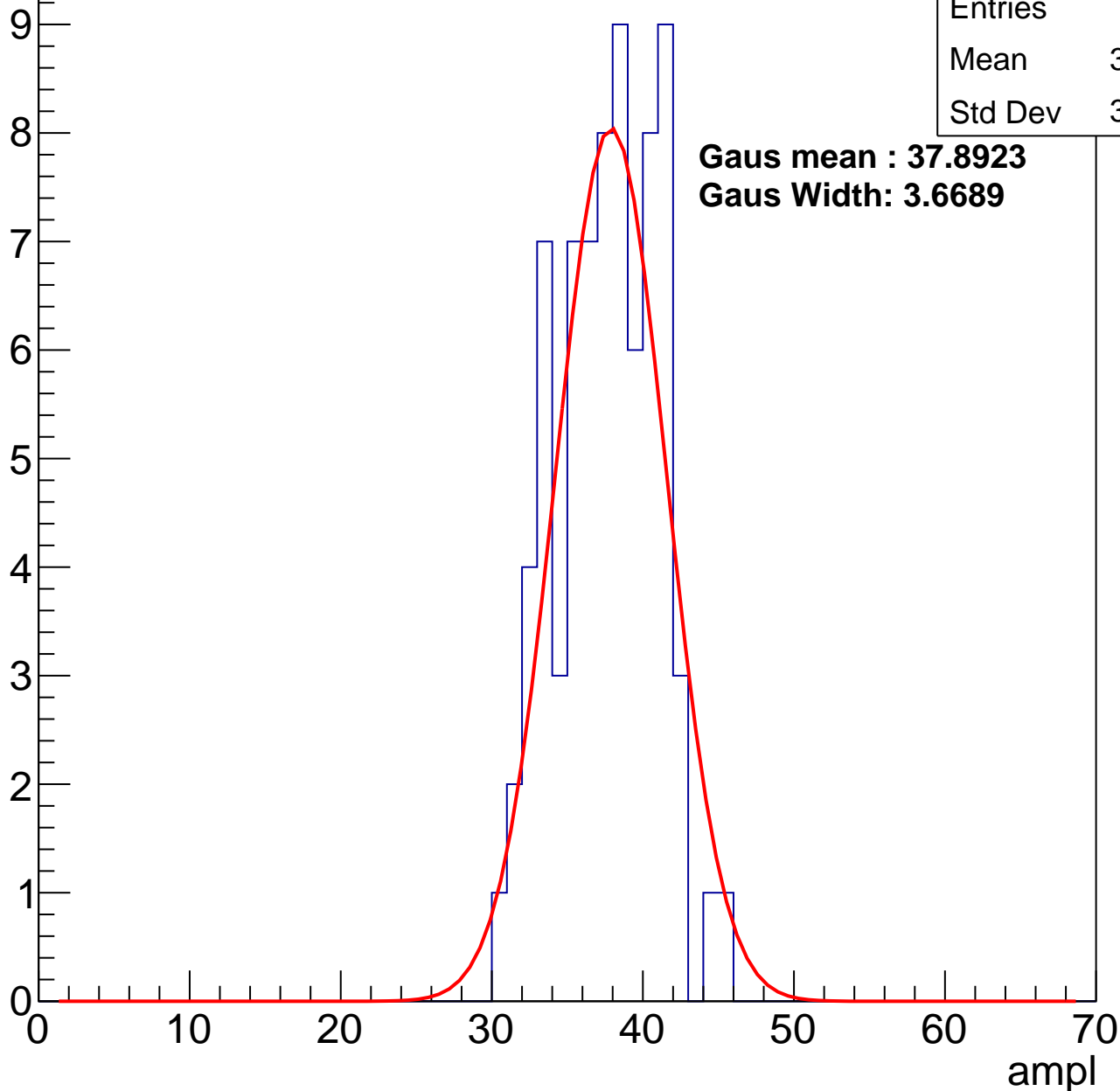
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.18
Std Dev	3.304

**Gaus mean : 37.8923**

**Gaus Width: 3.6689**



# B1L102S, U25-ch119, adc2

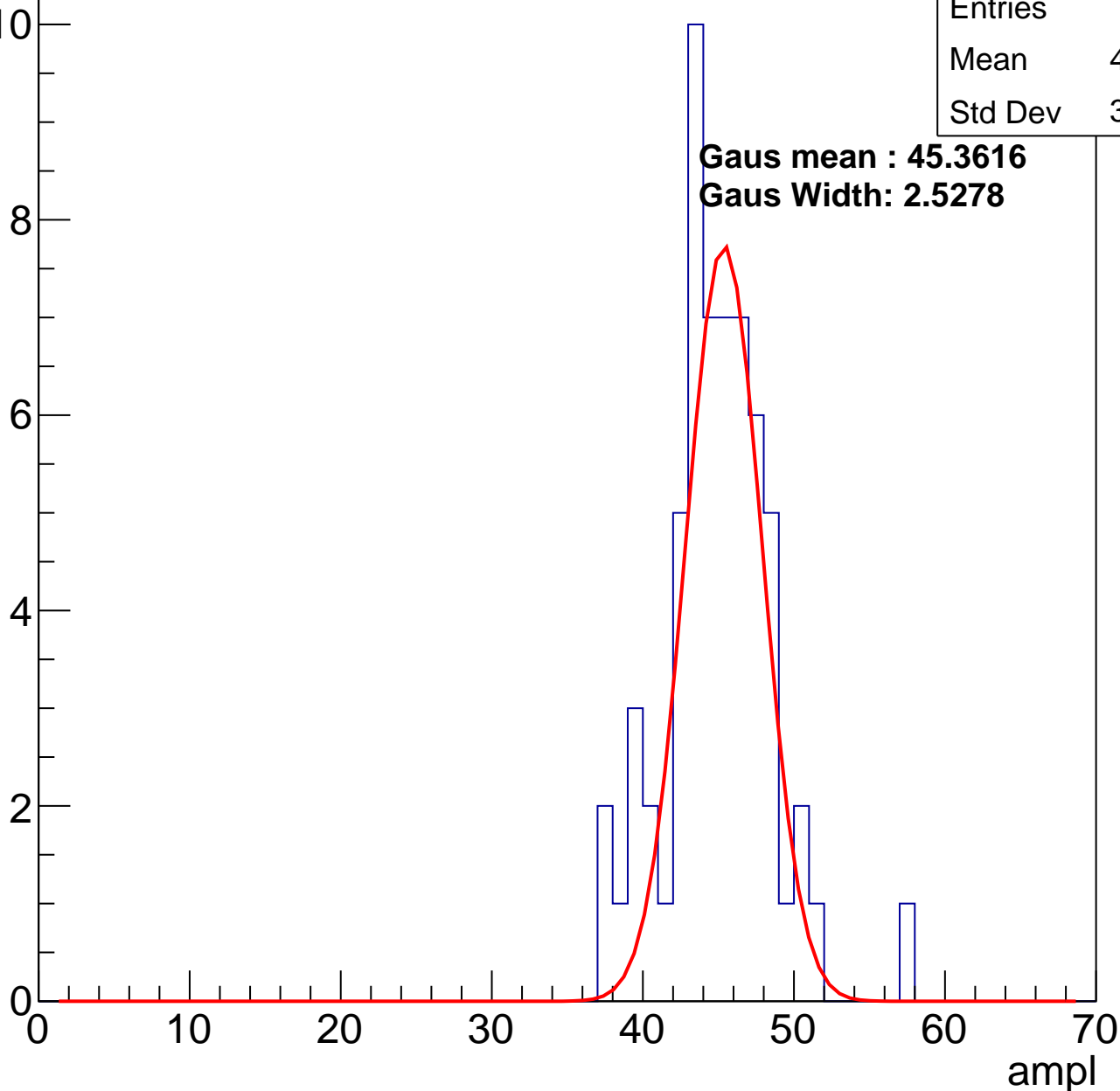
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	61
Mean	44.49
Std Dev	3.505

**Gaus mean : 45.3616**

**Gaus Width: 2.5278**

Entry

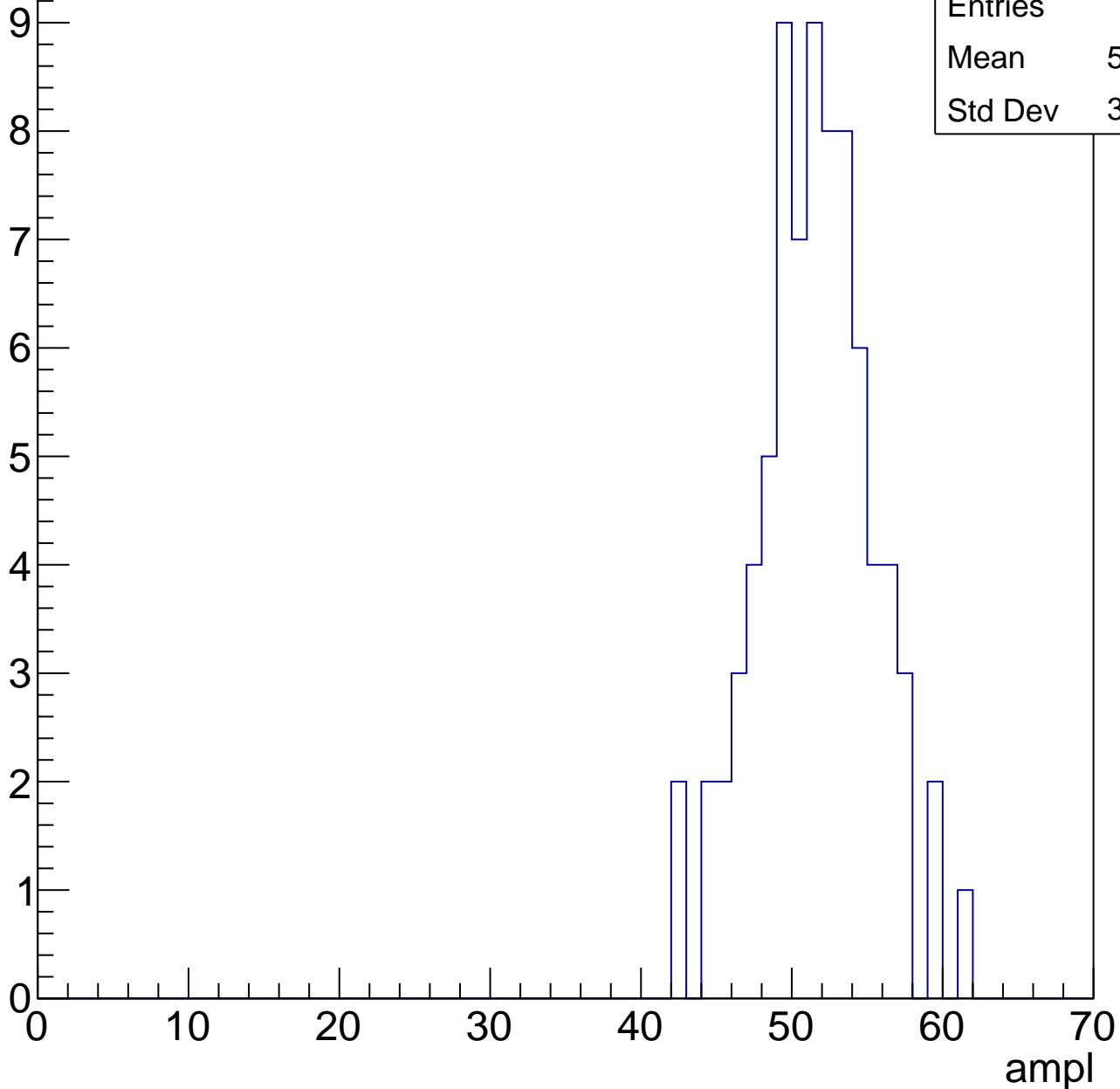


# B1L102S, U25-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	51.09
Std Dev	3.819

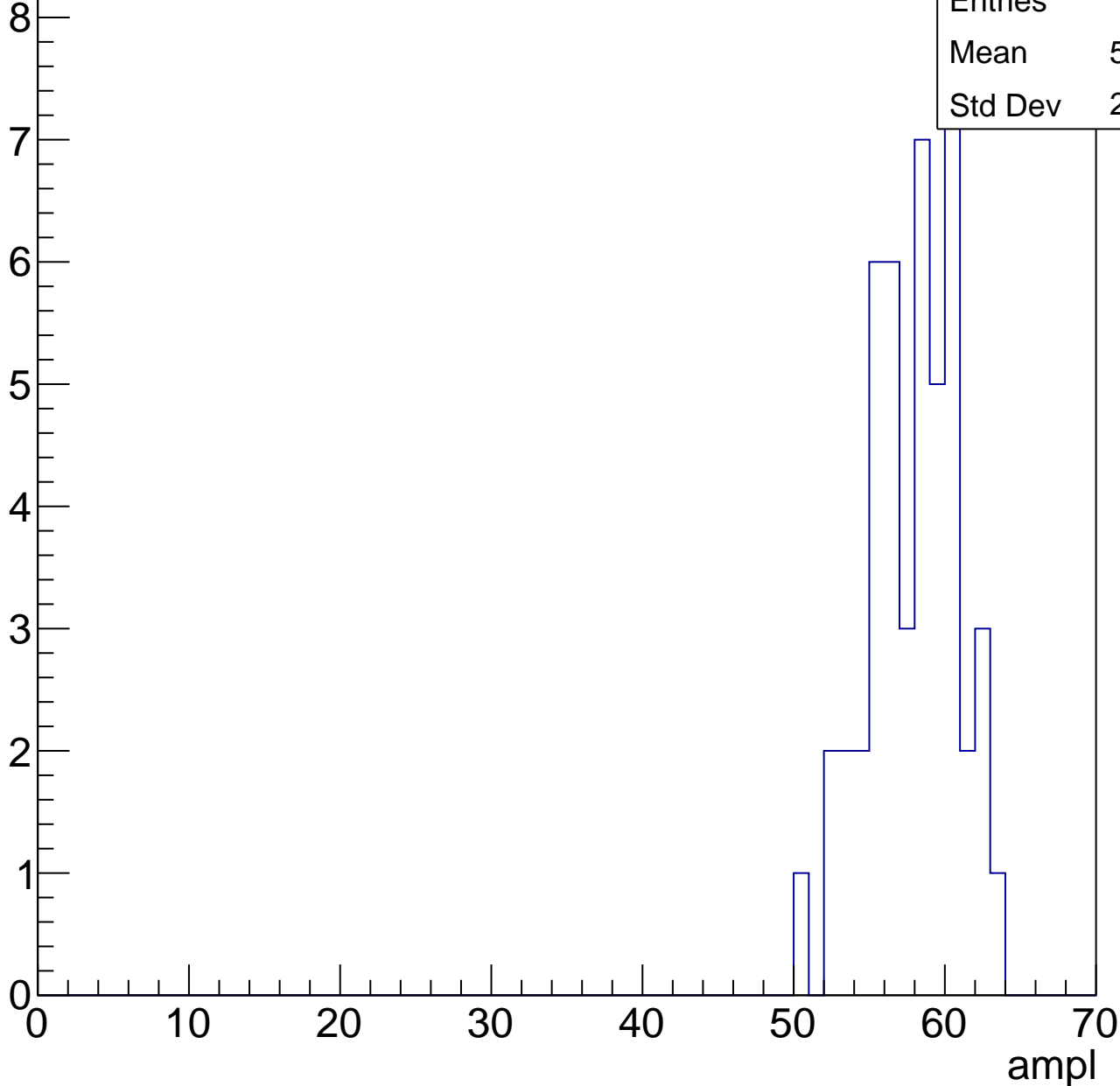


# B1L102S, U25-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.44
Std Dev	2.929

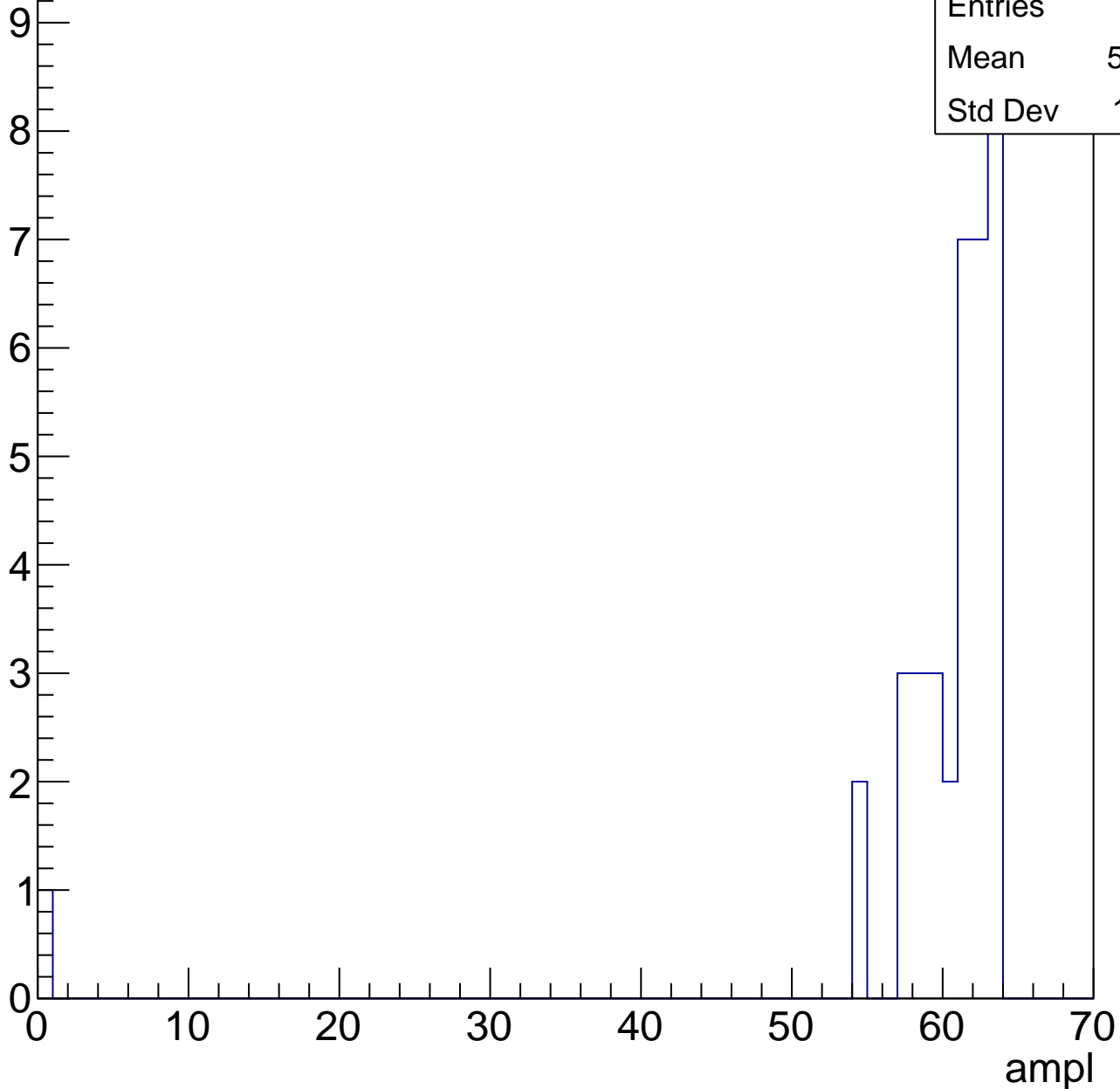


# B1L102S, U25-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.86
Std Dev	10.11



# B1L102S, U25-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



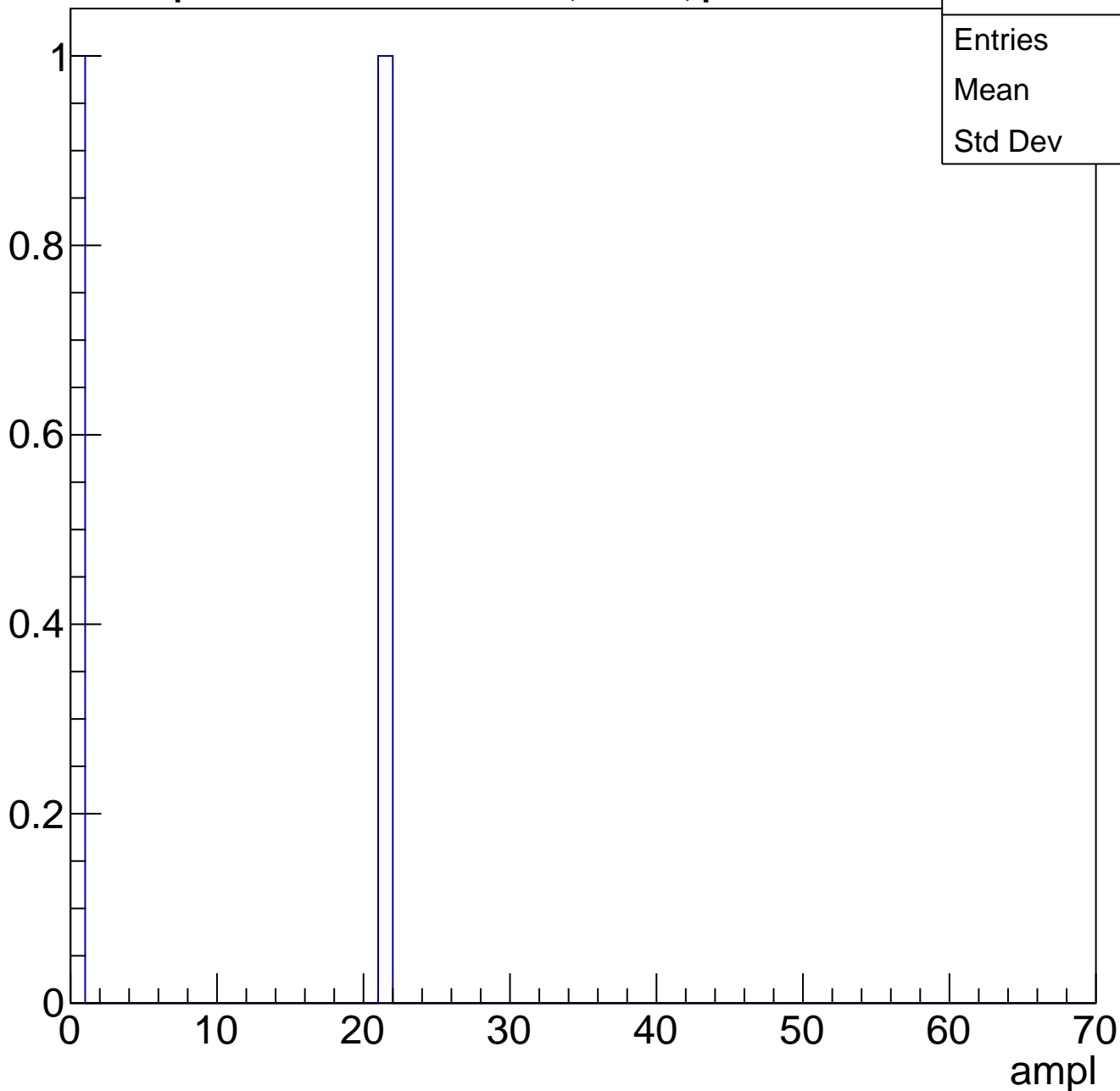
Entries	0
Mean	0
Std Dev	0



# B1L102S, U25-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch120, adc0

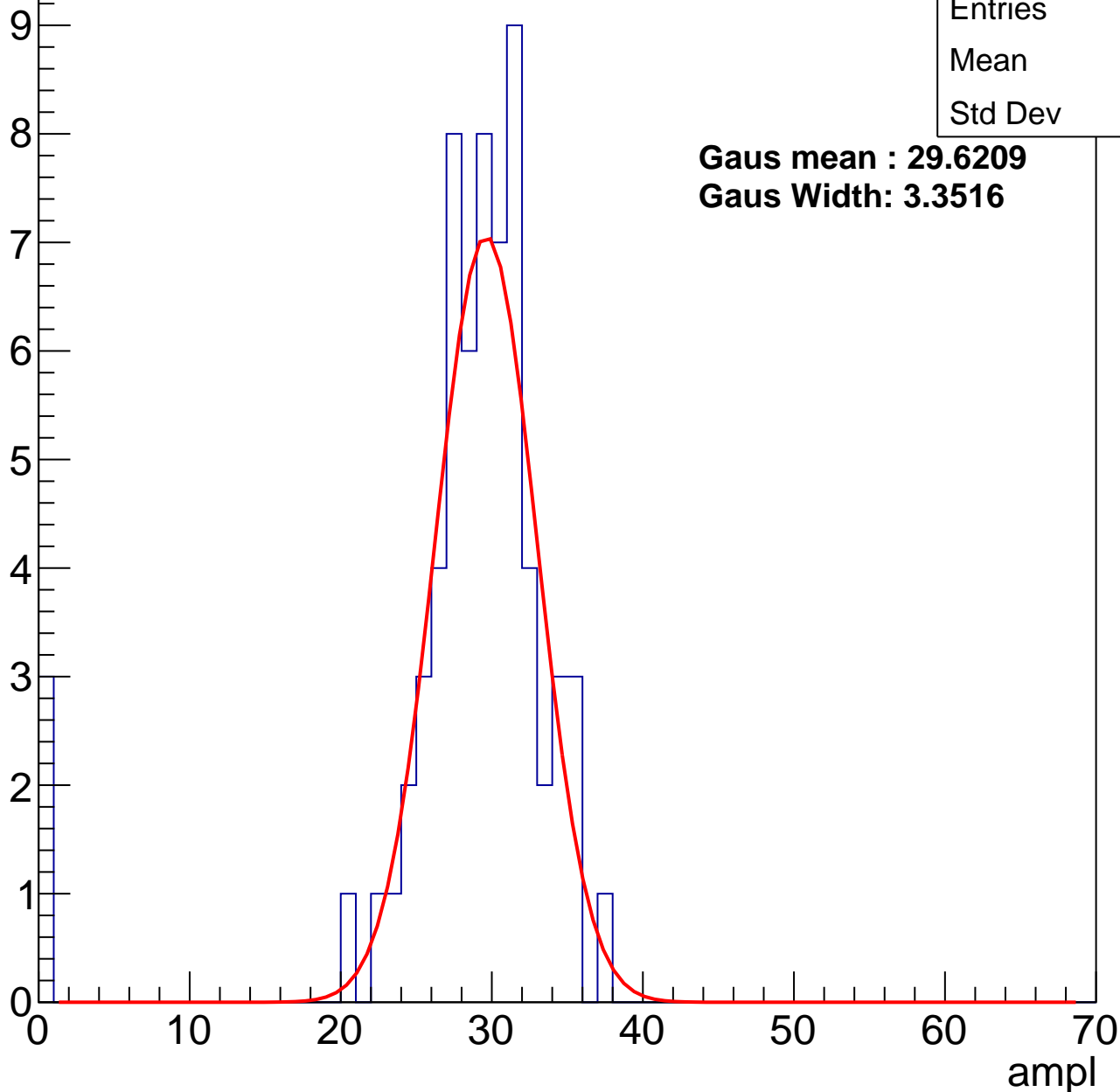
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.8
Std Dev	6.88

**Gaus mean : 29.6209**

**Gaus Width: 3.3516**



# B1L102S, U25-ch120, adc1

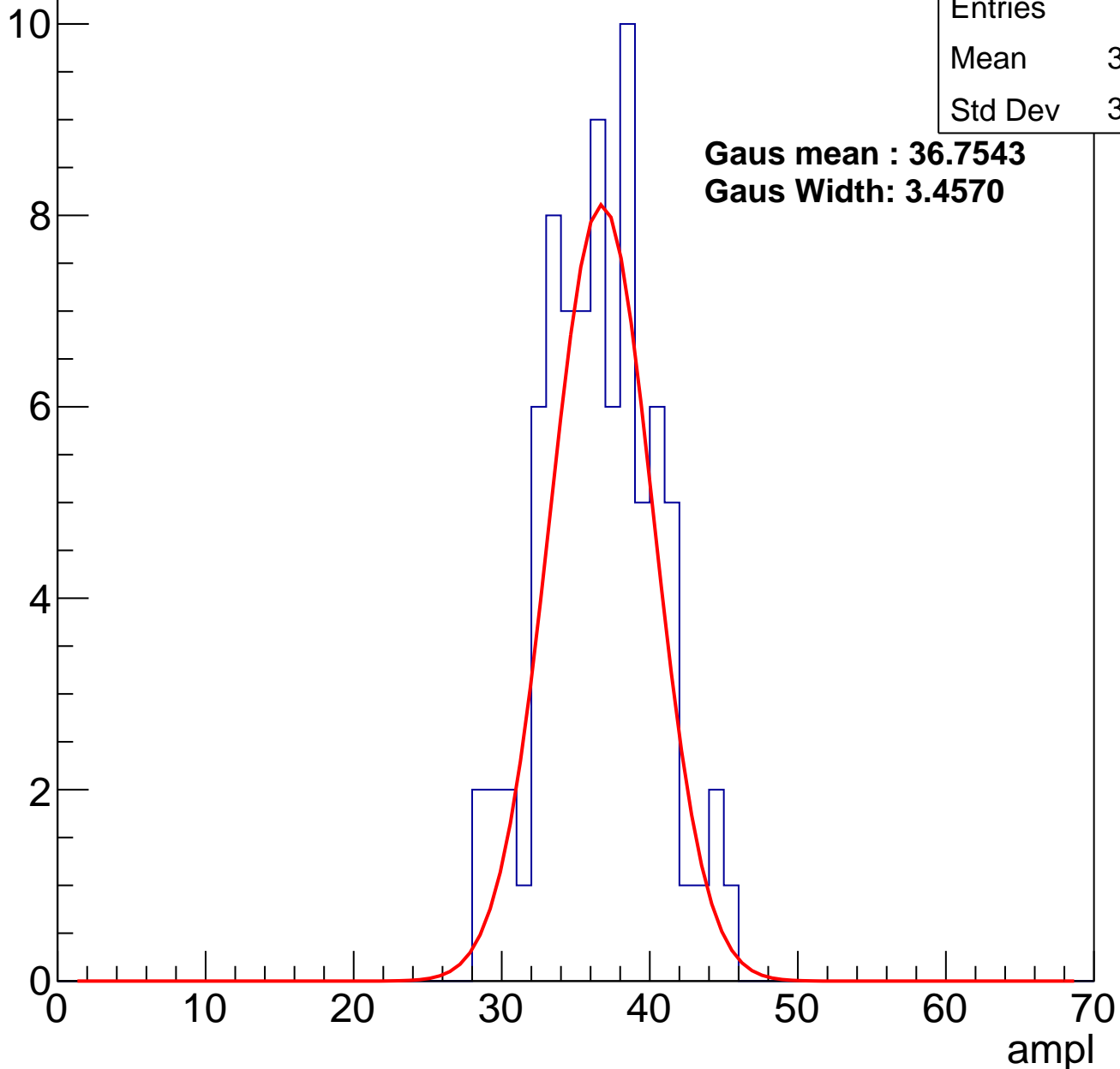
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	81
Mean	36.15
Std Dev	3.735

**Gaus mean : 36.7543**

**Gaus Width: 3.4570**

Entry



# B1L102S, U25-ch120, adc2

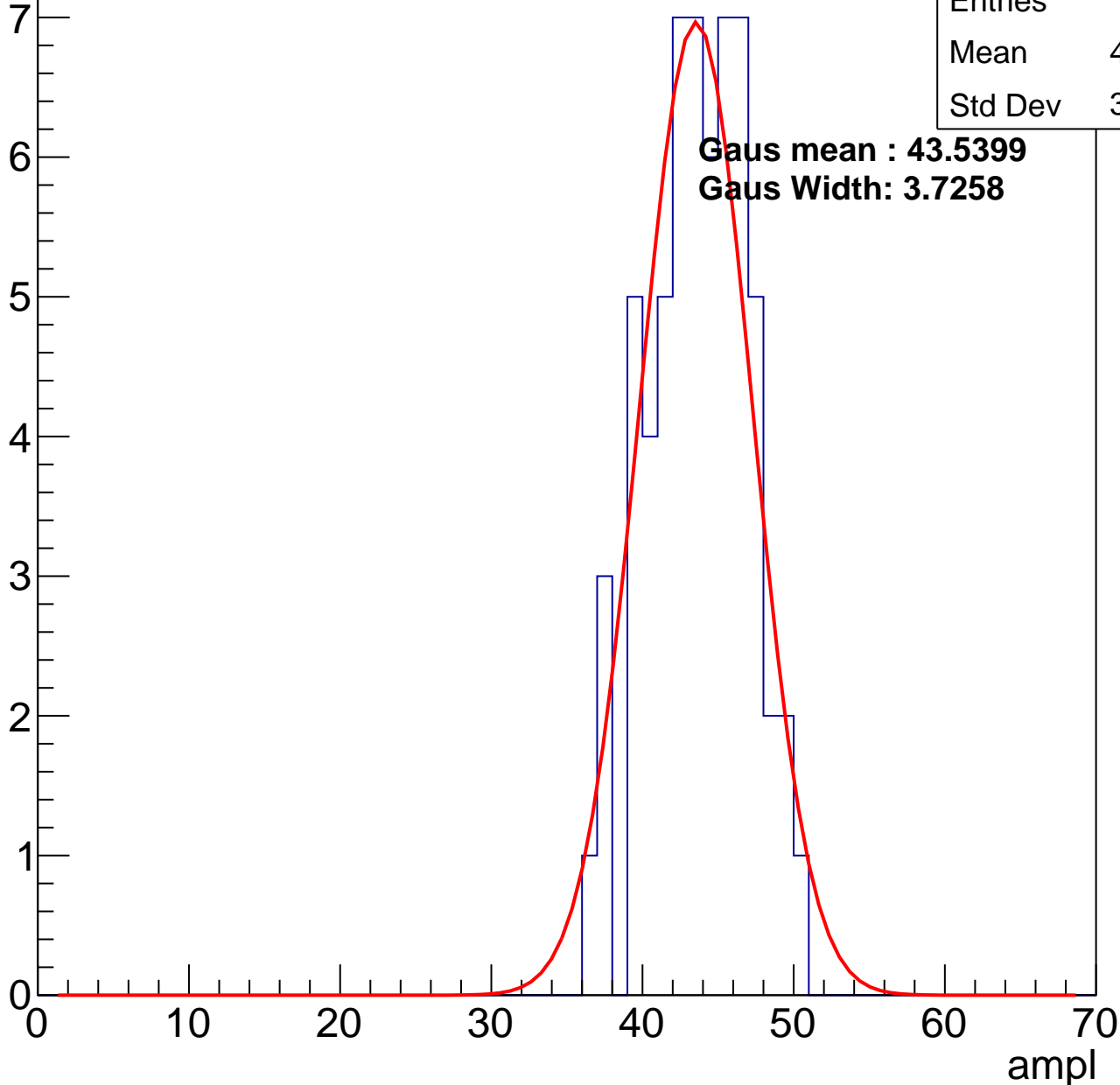
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.26
Std Dev	3.223

**Gaus mean : 43.5399**

**Gaus Width: 3.7258**

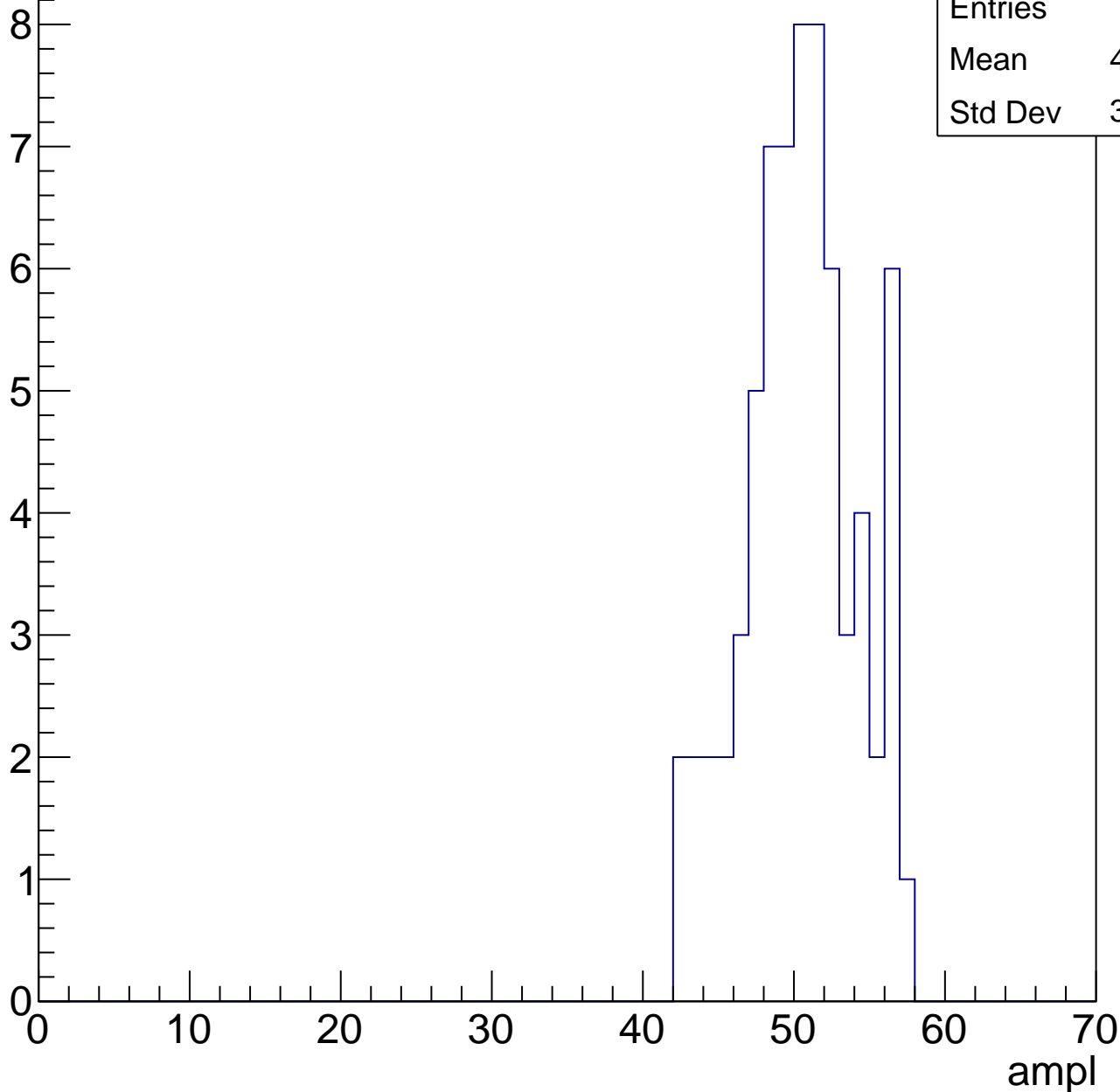


# B1L102S, U25-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

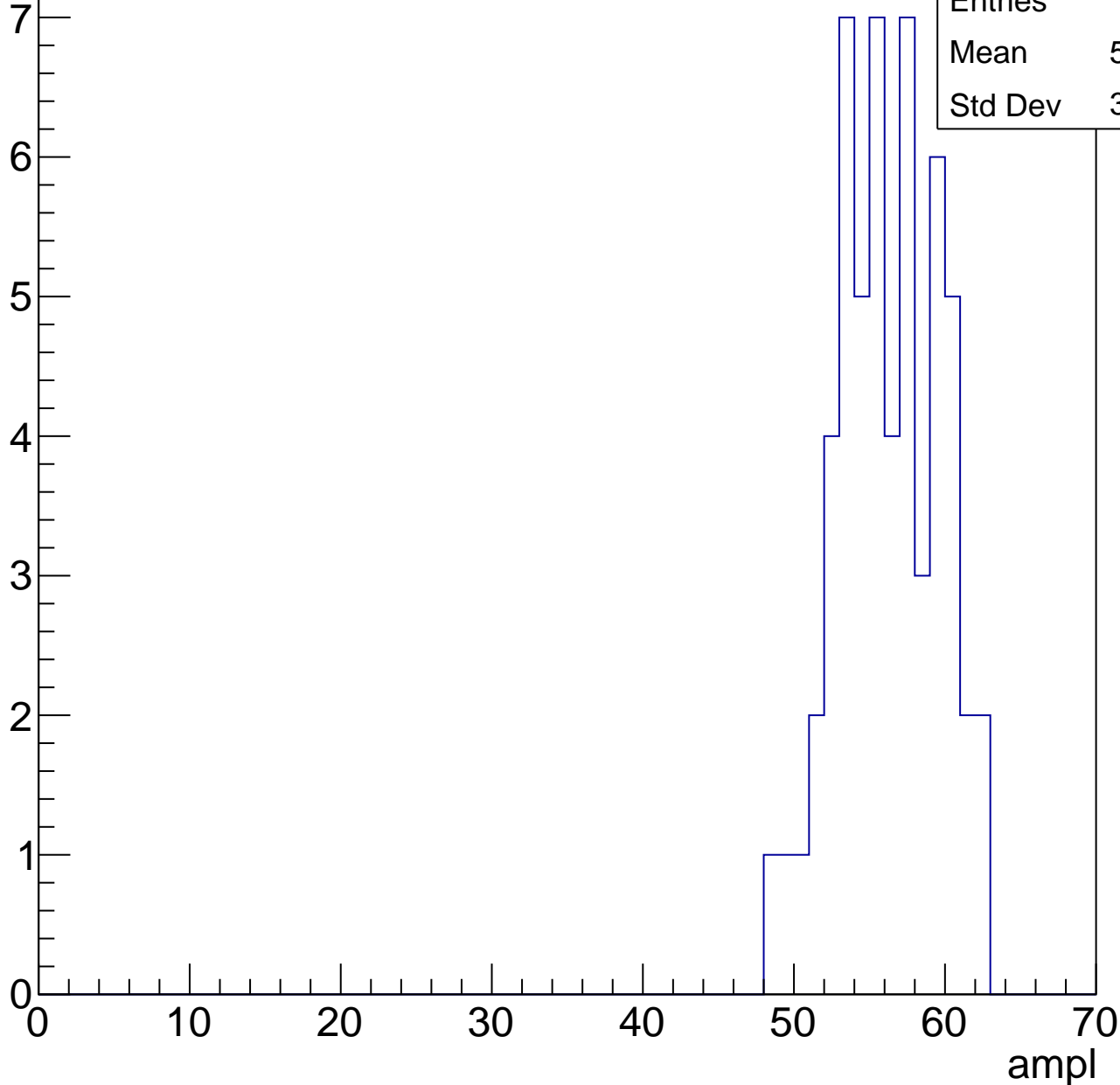
Entries	68
Mean	49.97
Std Dev	3.666



# B1L102S, U25-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



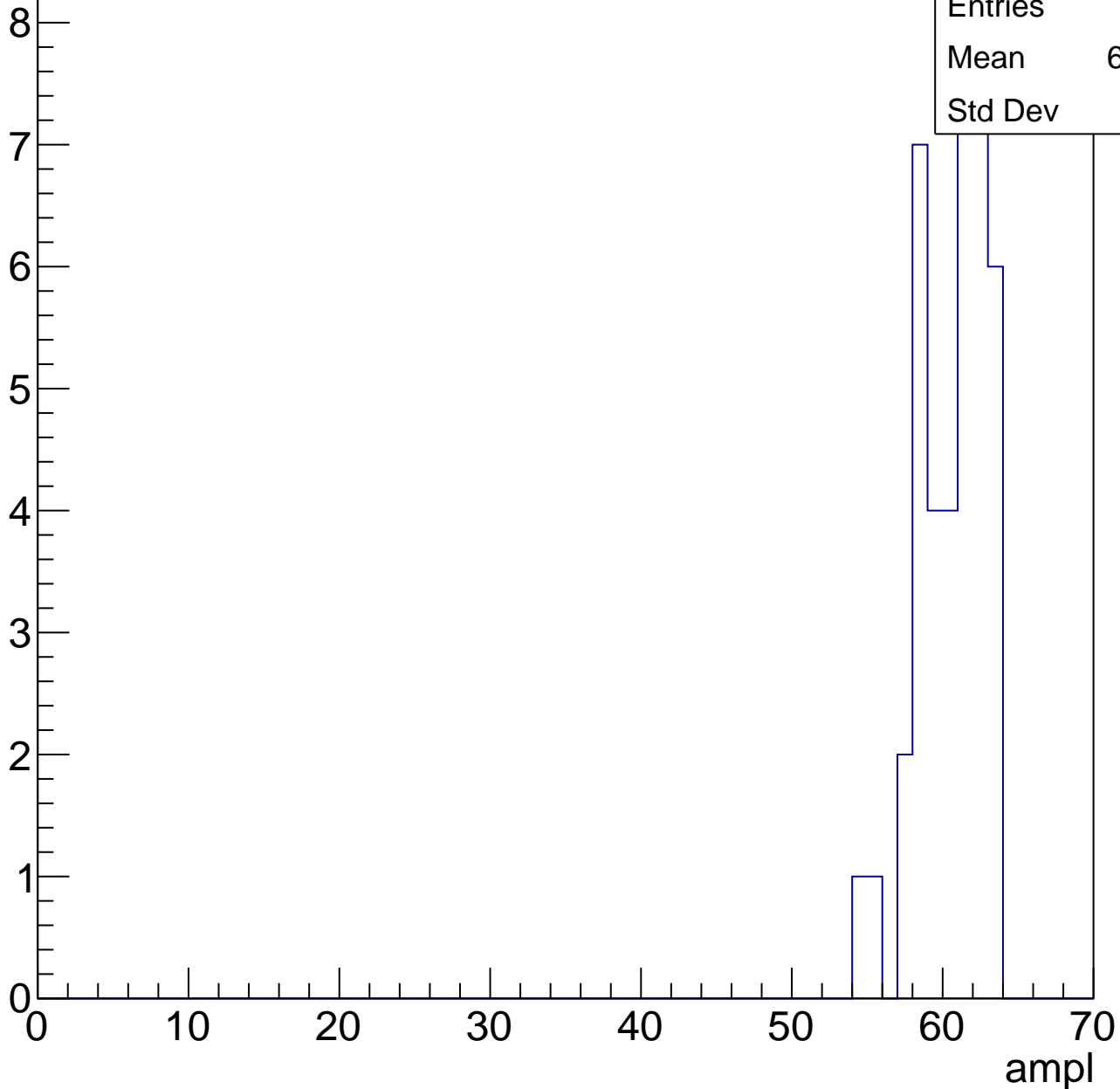
Entries	57
Mean	55.79
Std Dev	3.313

# B1L102S, U25-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

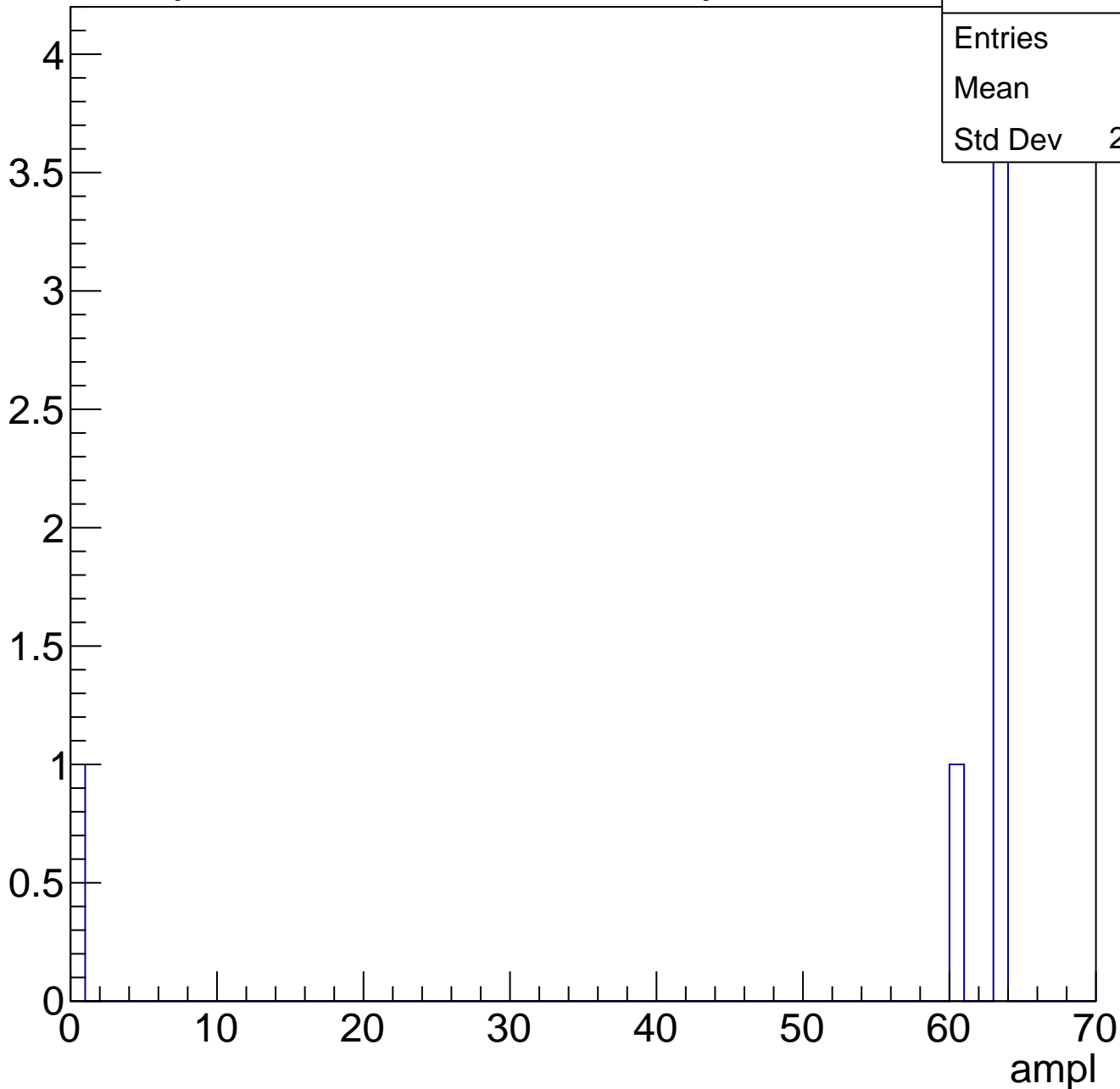
Entries	41
Mean	60.17
Std Dev	2.23



# B1L102S, U25-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	6
Mean	52
Std Dev	23.28



# B1L102S, U25-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch121, adc0

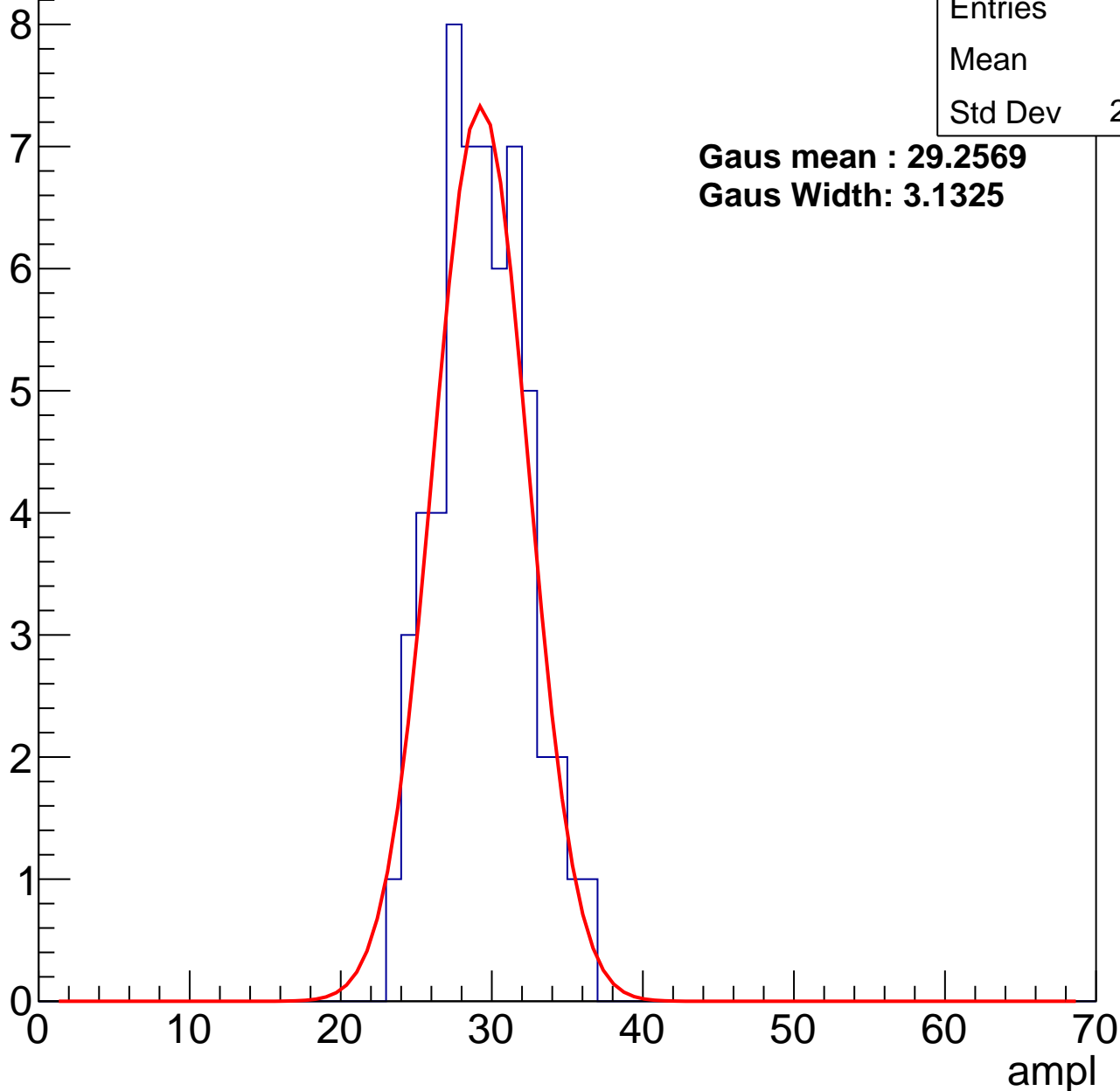
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.9
Std Dev	2.923

**Gaus mean : 29.2569**

**Gaus Width: 3.1325**



# B1L102S, U25-ch121, adc1

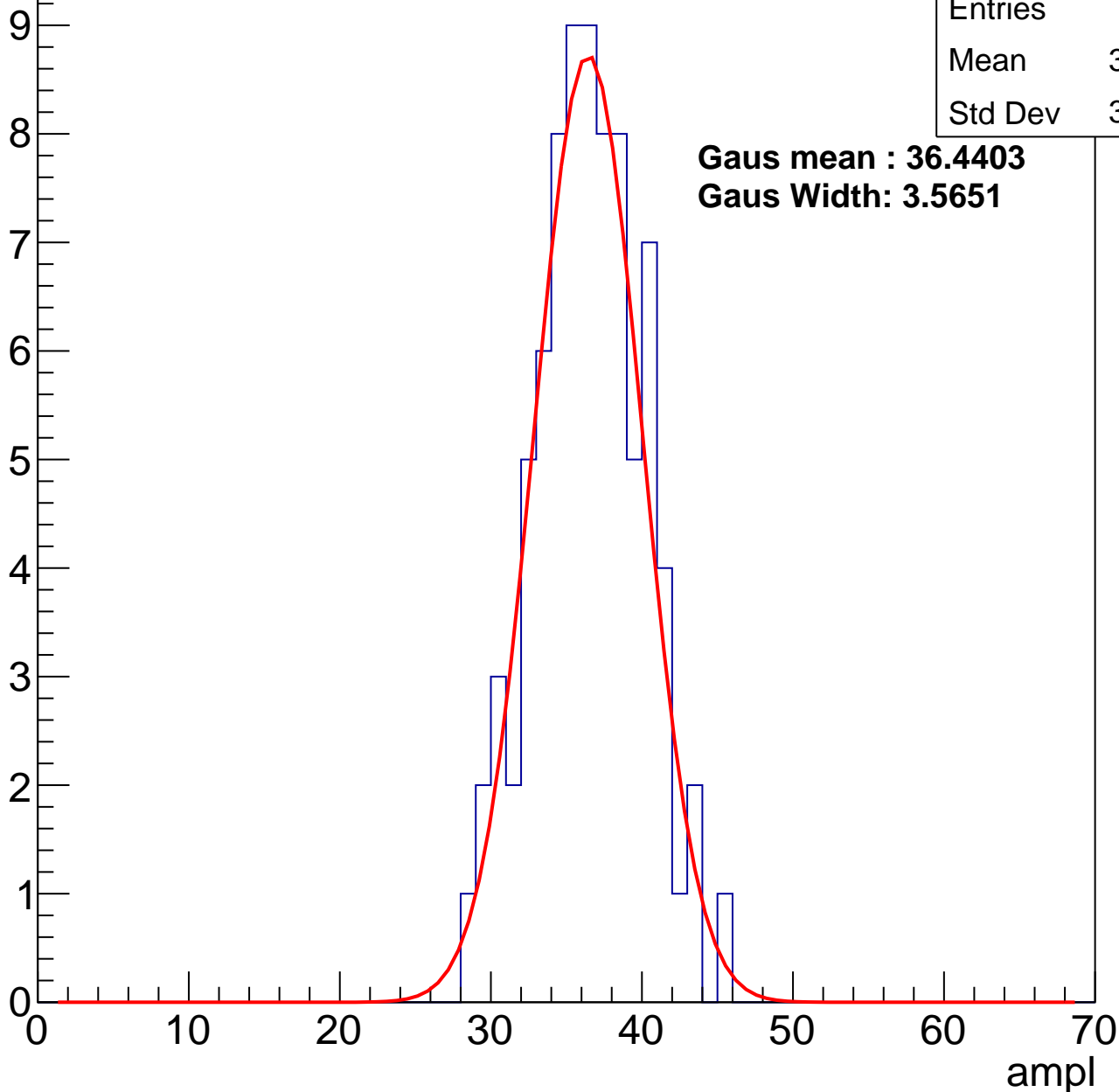
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.04
Std Dev	3.526

**Gaus mean : 36.4403**

**Gaus Width: 3.5651**



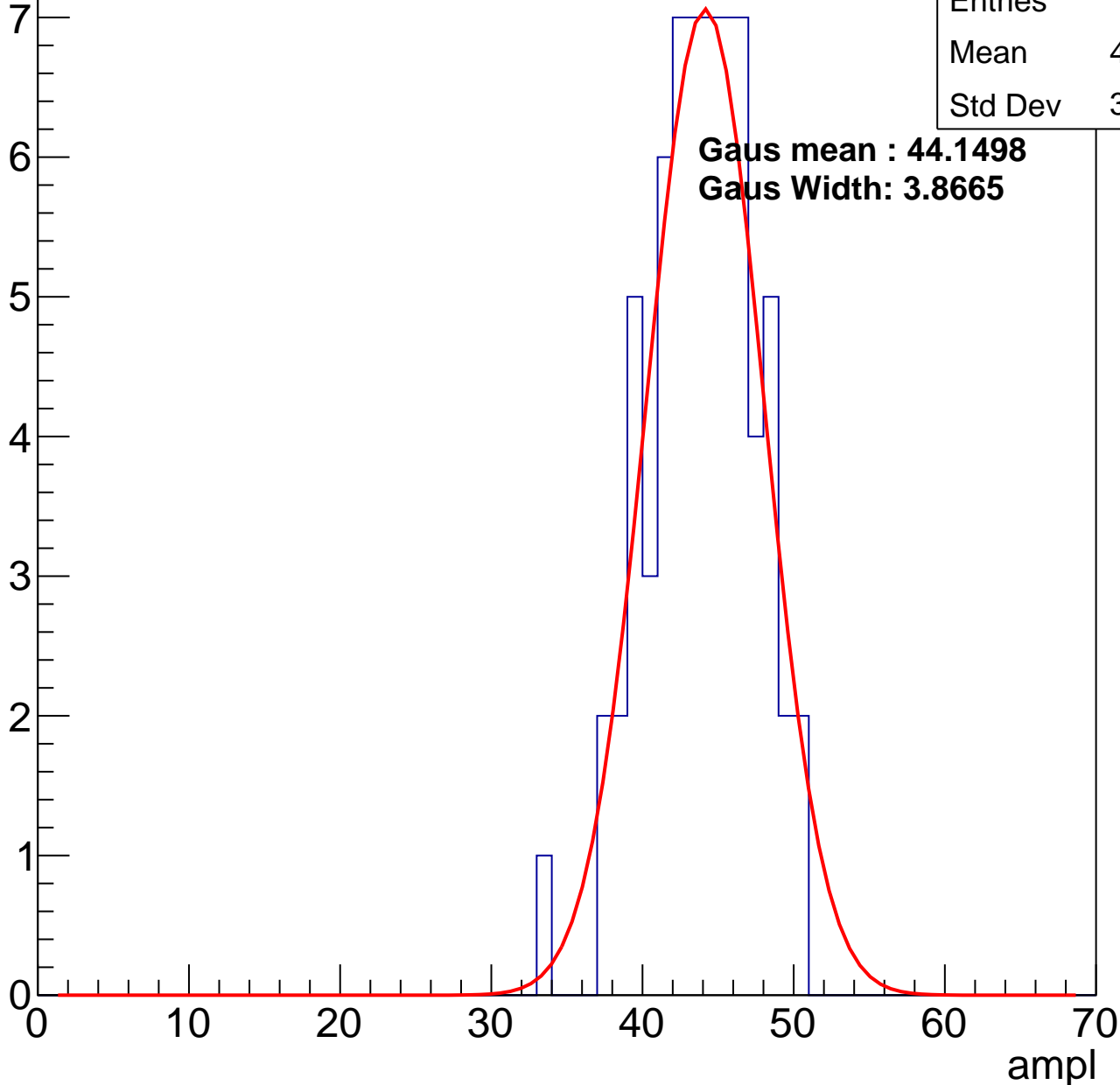
# B1L102S, U25-ch121, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.43
Std Dev	3.465

**Gaus mean : 44.1498**  
**Gaus Width: 3.8665**

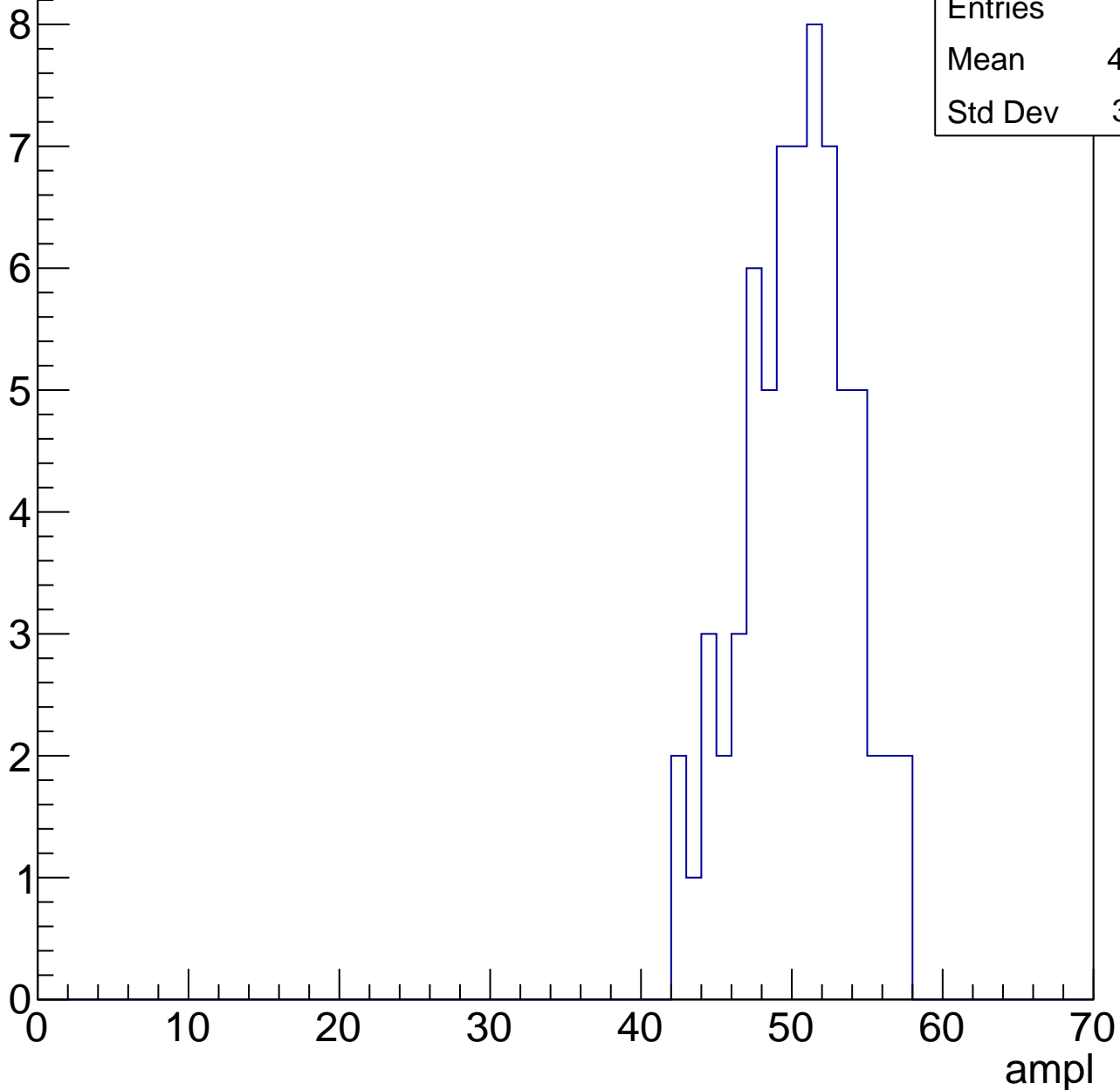


# B1L102S, U25-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.93
Std Dev	3.551

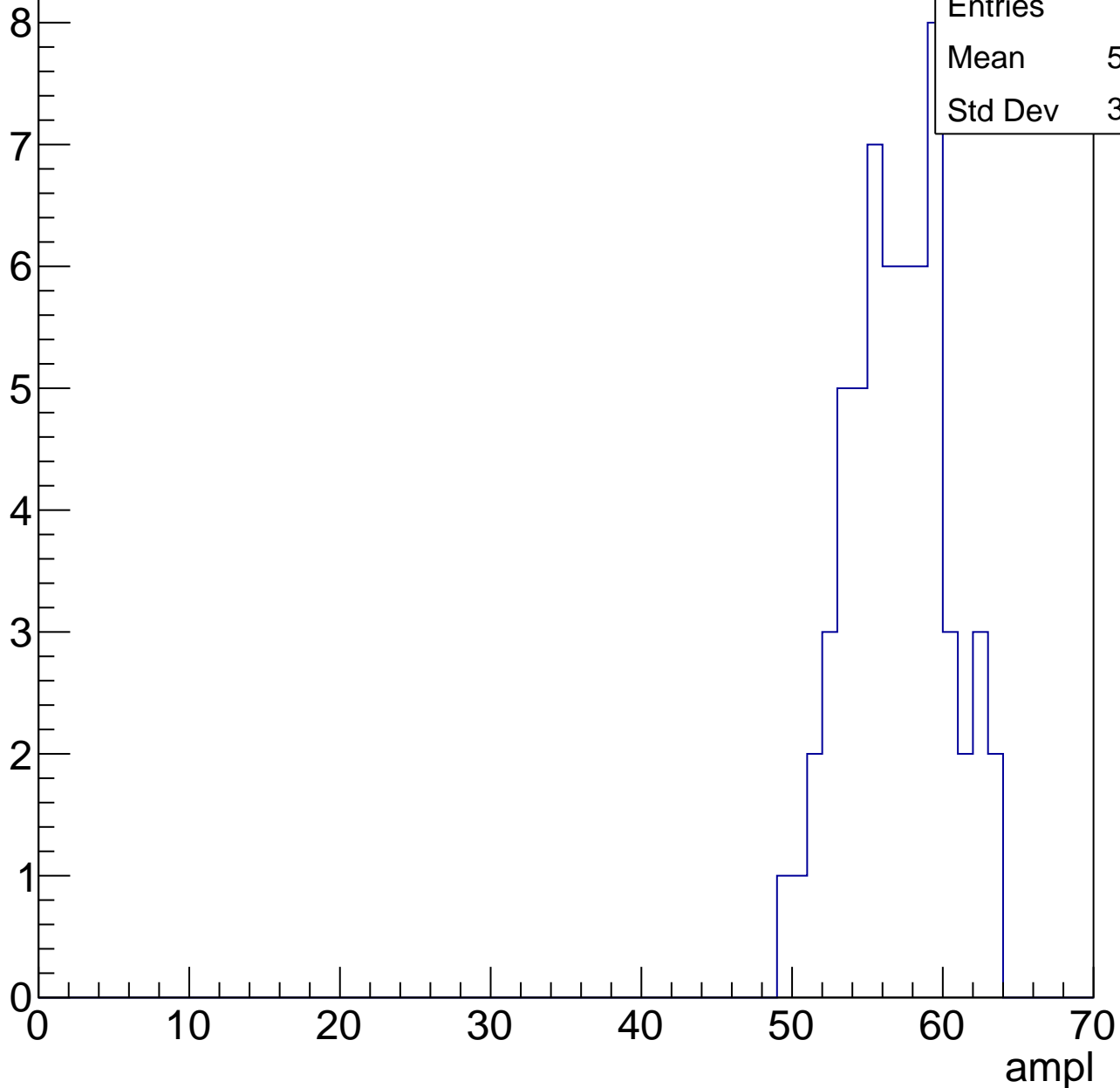


# B1L102S, U25-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

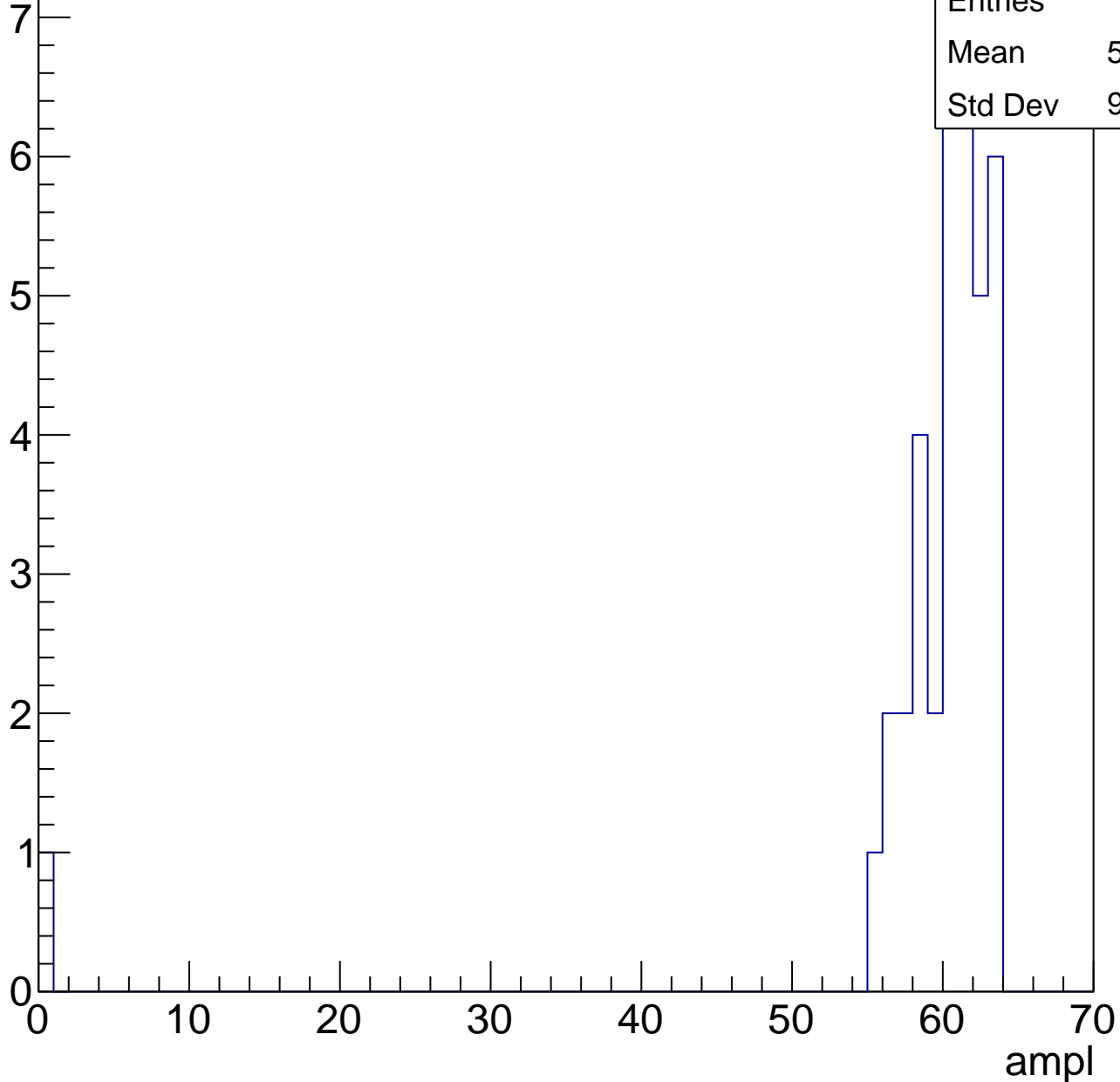
Entries	60
Mean	56.48
Std Dev	3.284



# B1L102S, U25-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

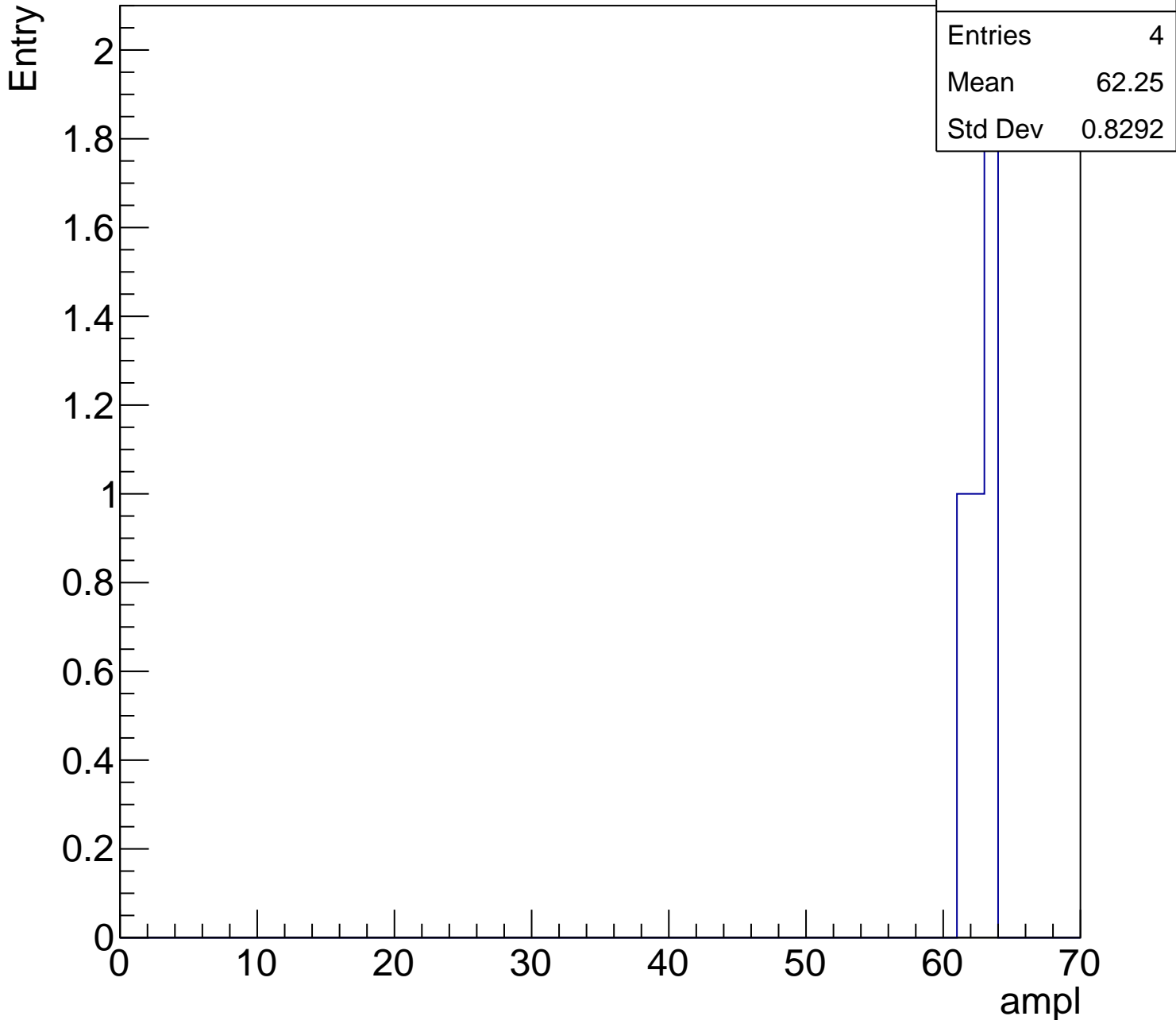
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl

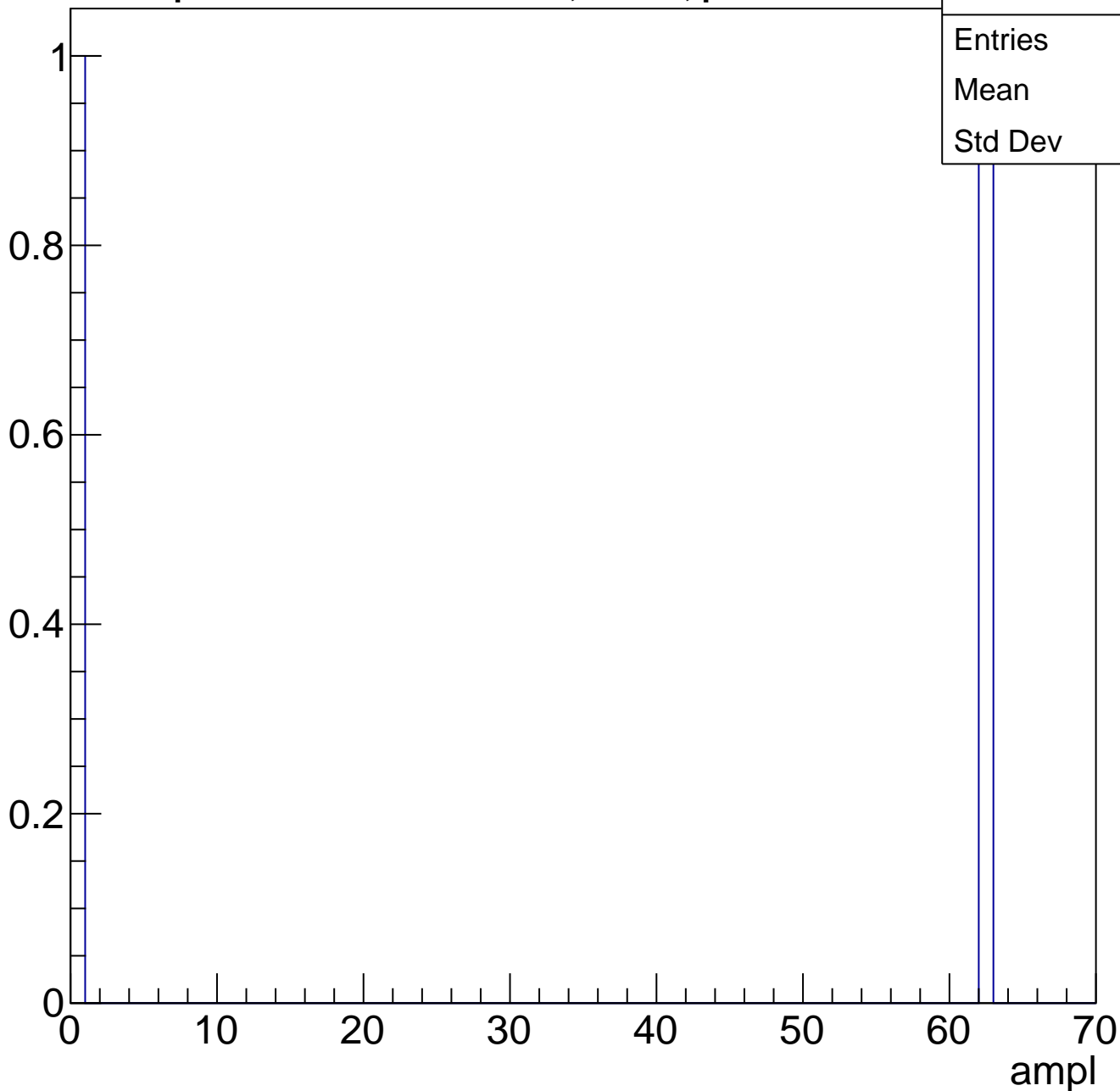




# B1L102S, U25-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	86
Mean	26.88
Std Dev	4.975

**Gaus mean : 28.6534**

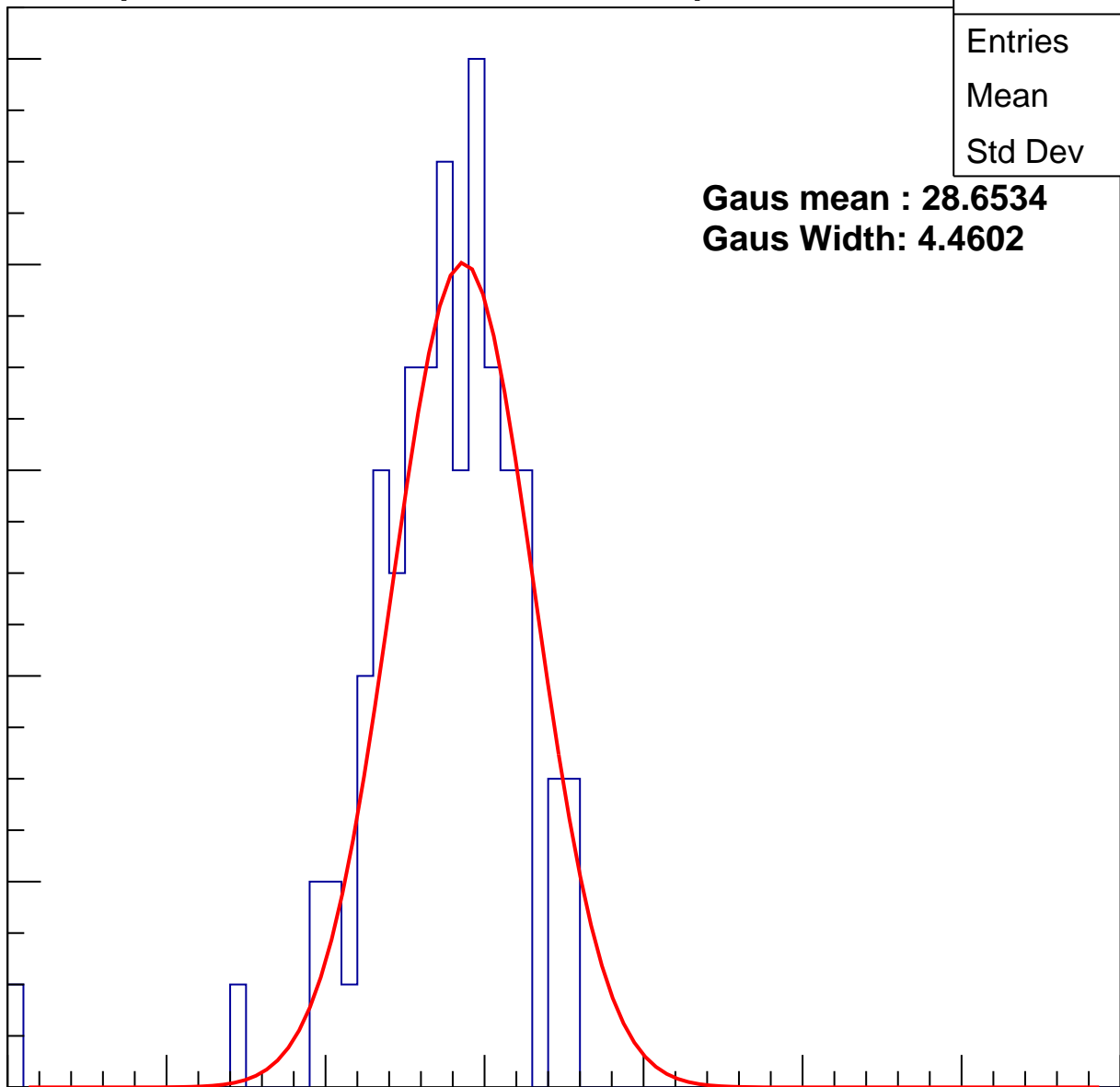
**Gaus Width: 4.4602**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch122, adc1

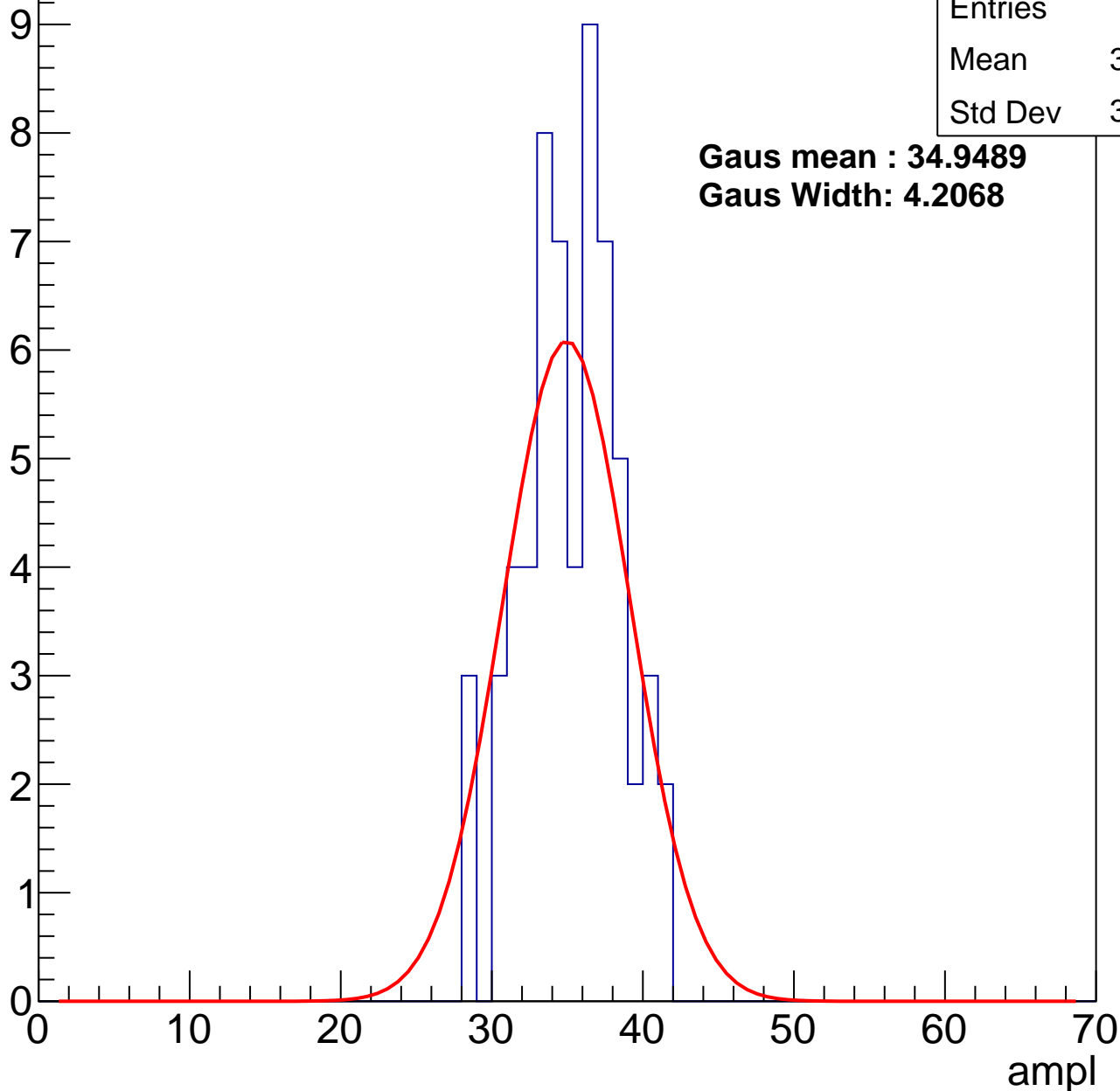
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.77
Std Dev	3.185

**Gaus mean : 34.9489**

**Gaus Width: 4.2068**



# B1L102S, U25-ch122, adc2

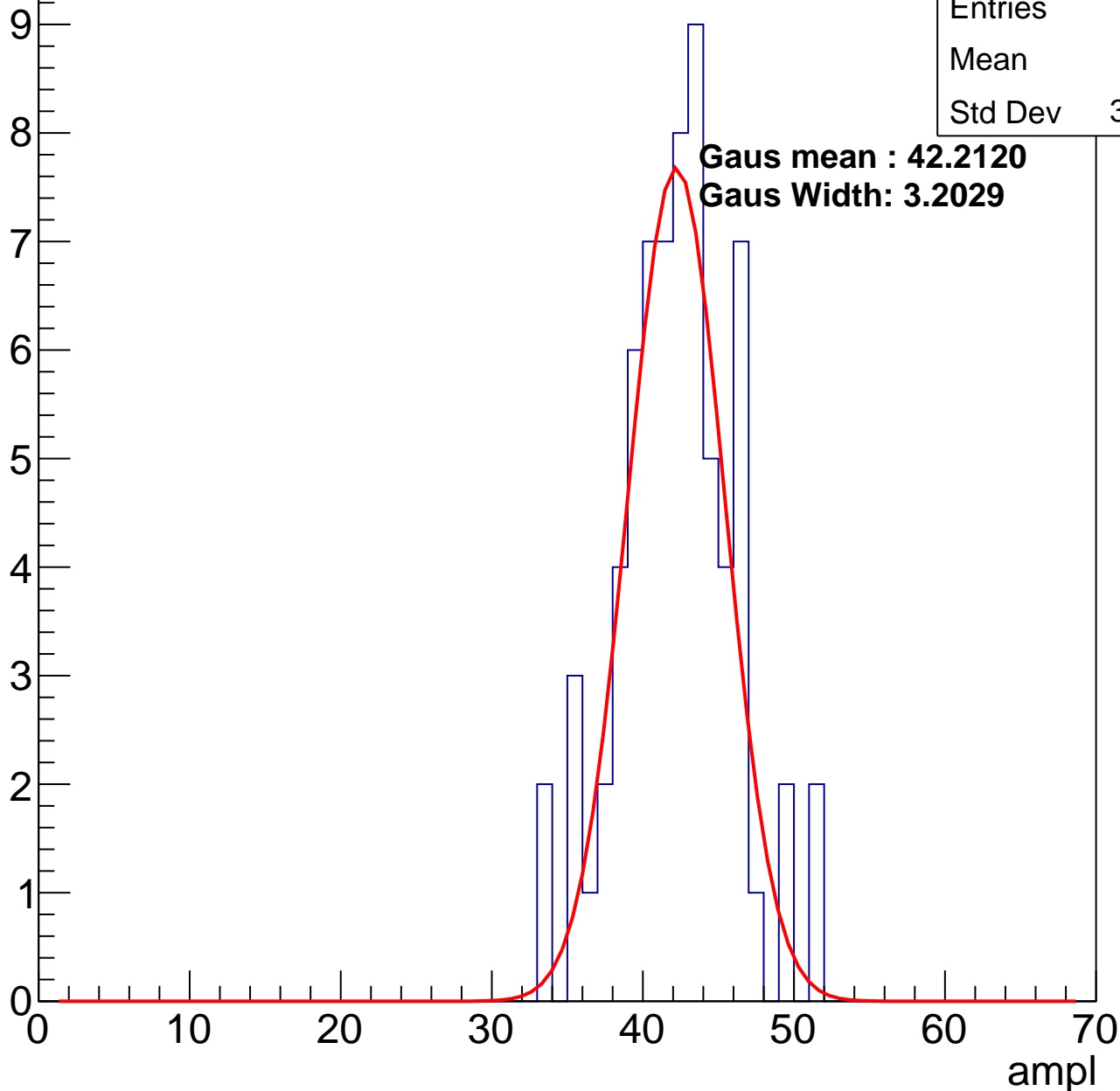
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.8
Std Dev	3.793

**Gaus mean : 42.2120**

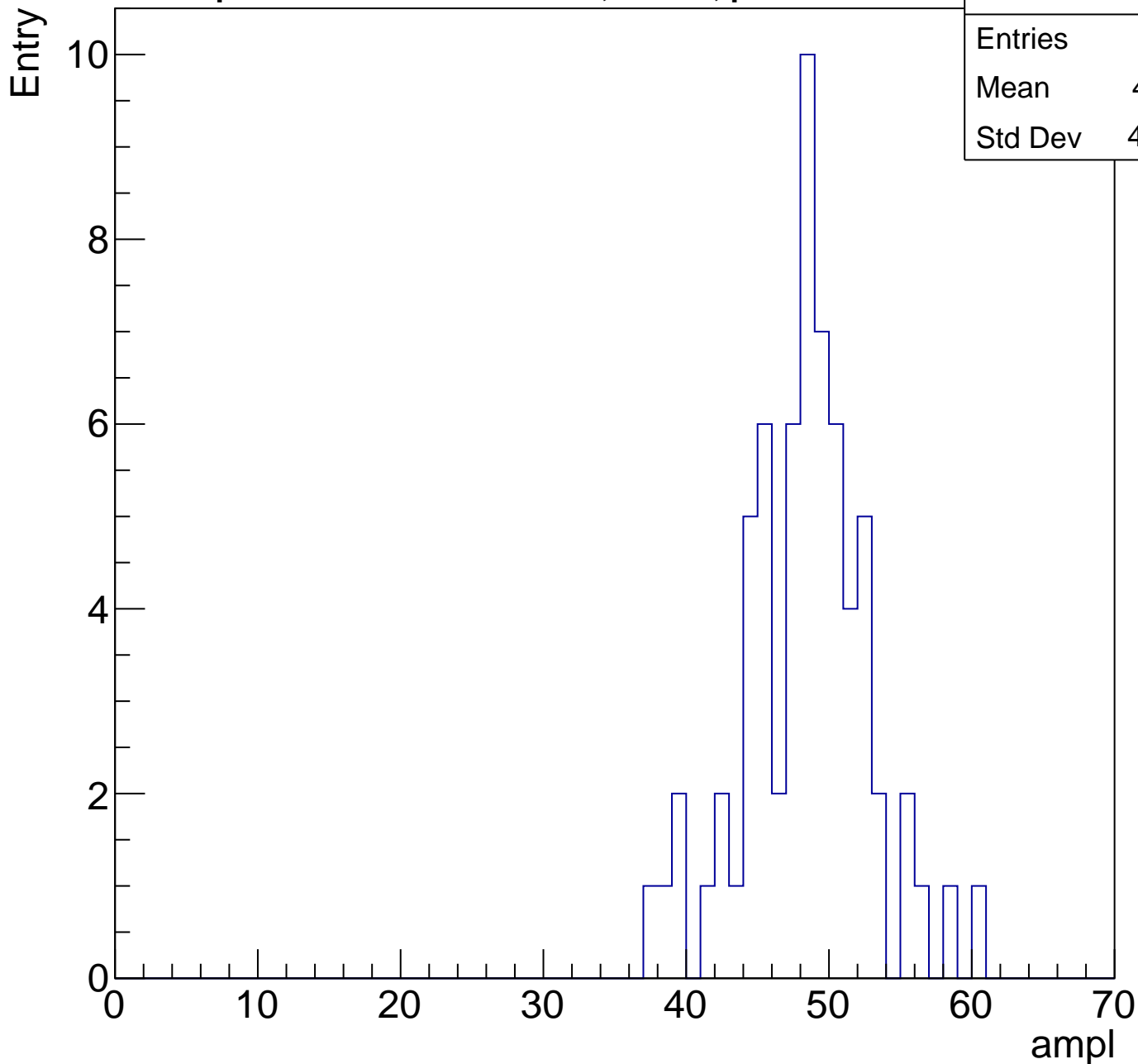
**Gaus Width: 3.2029**



# B1L102S, U25-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	66
Mean	47.91
Std Dev	4.403

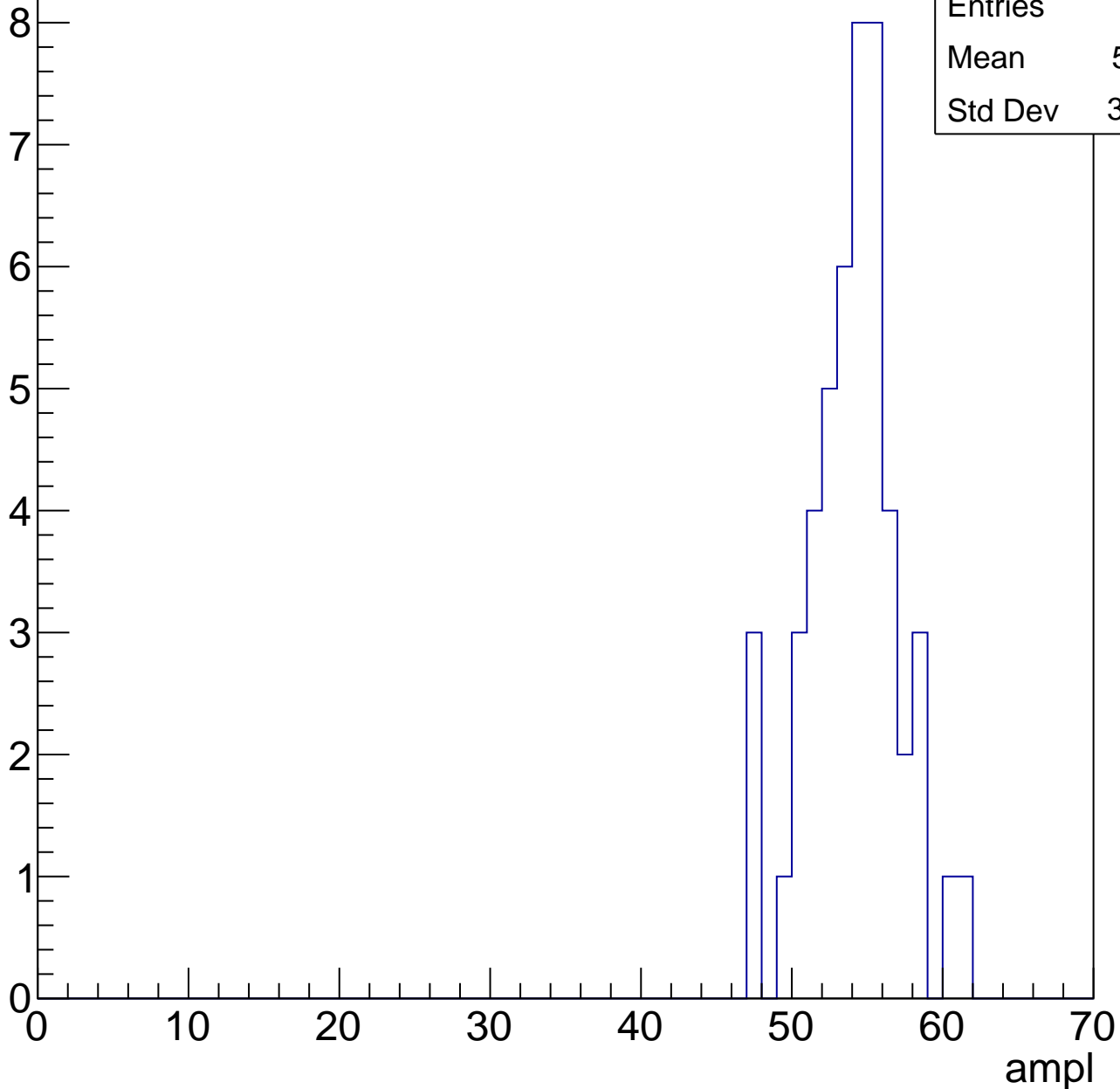


# B1L102S, U25-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.61
Std Dev	3.029

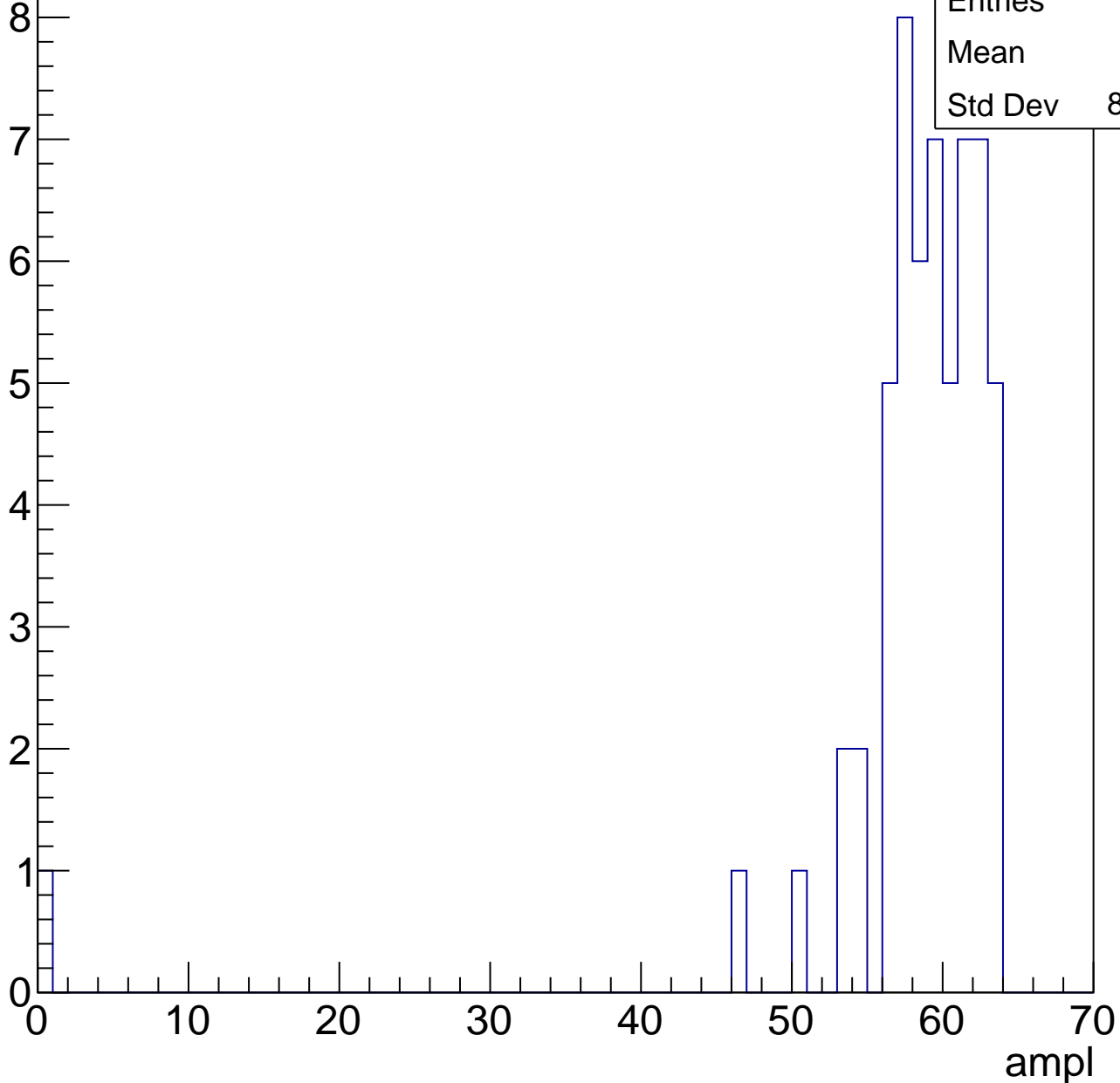


# B1L102S, U25-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.6
Std Dev	8.377

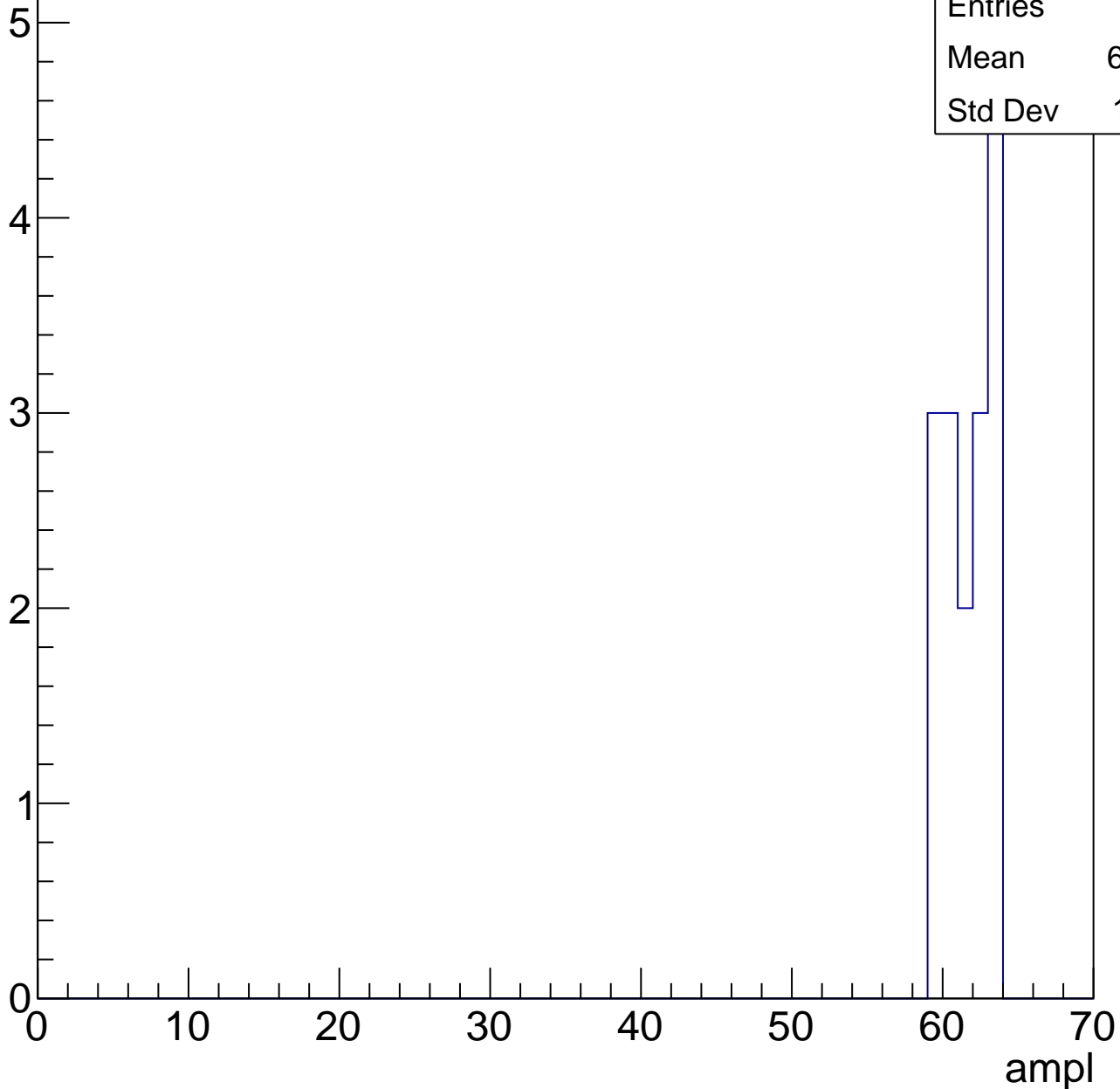


# B1L102S, U25-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.25
Std Dev	1.521





# B1L102S, U25-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch123, adc0

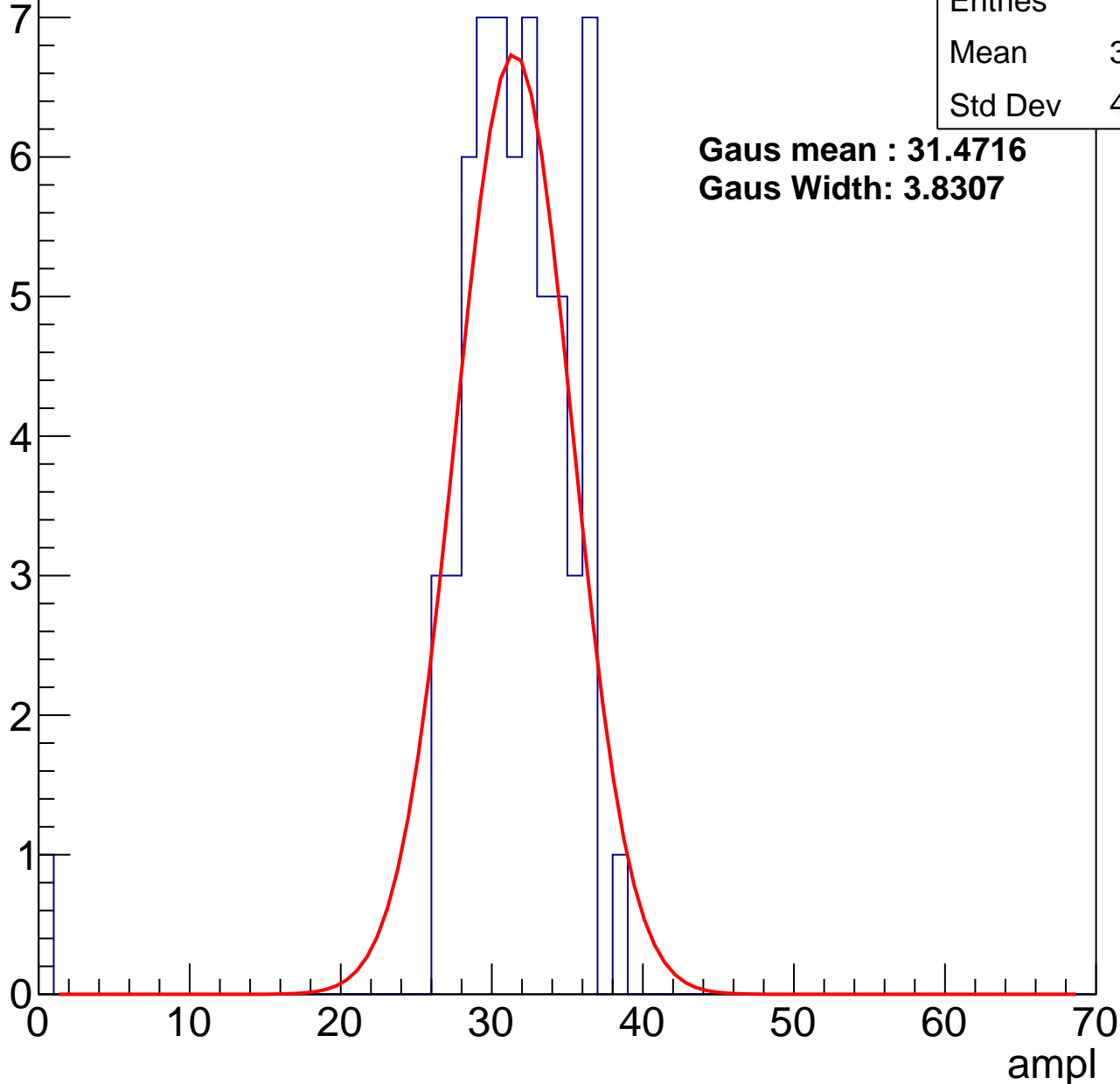
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.82
Std Dev	4.984

**Gaus mean : 31.4716**

**Gaus Width: 3.8307**



# B1L102S, U25-ch123, adc1

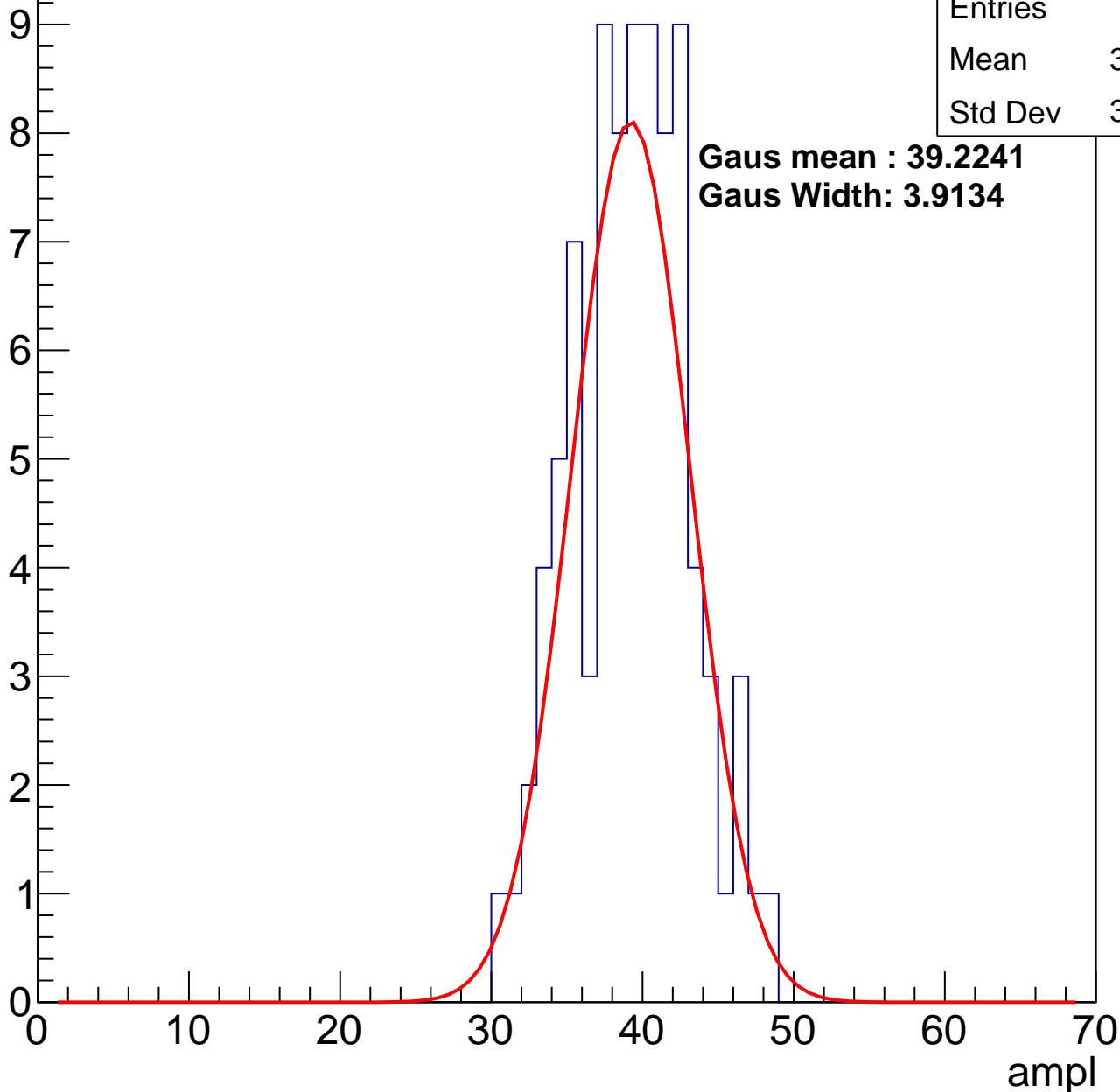
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	88
Mean	38.82
Std Dev	3.815

**Gaus mean : 39.2241**

**Gaus Width: 3.9134**



# B1L102S, U25-ch123, adc2

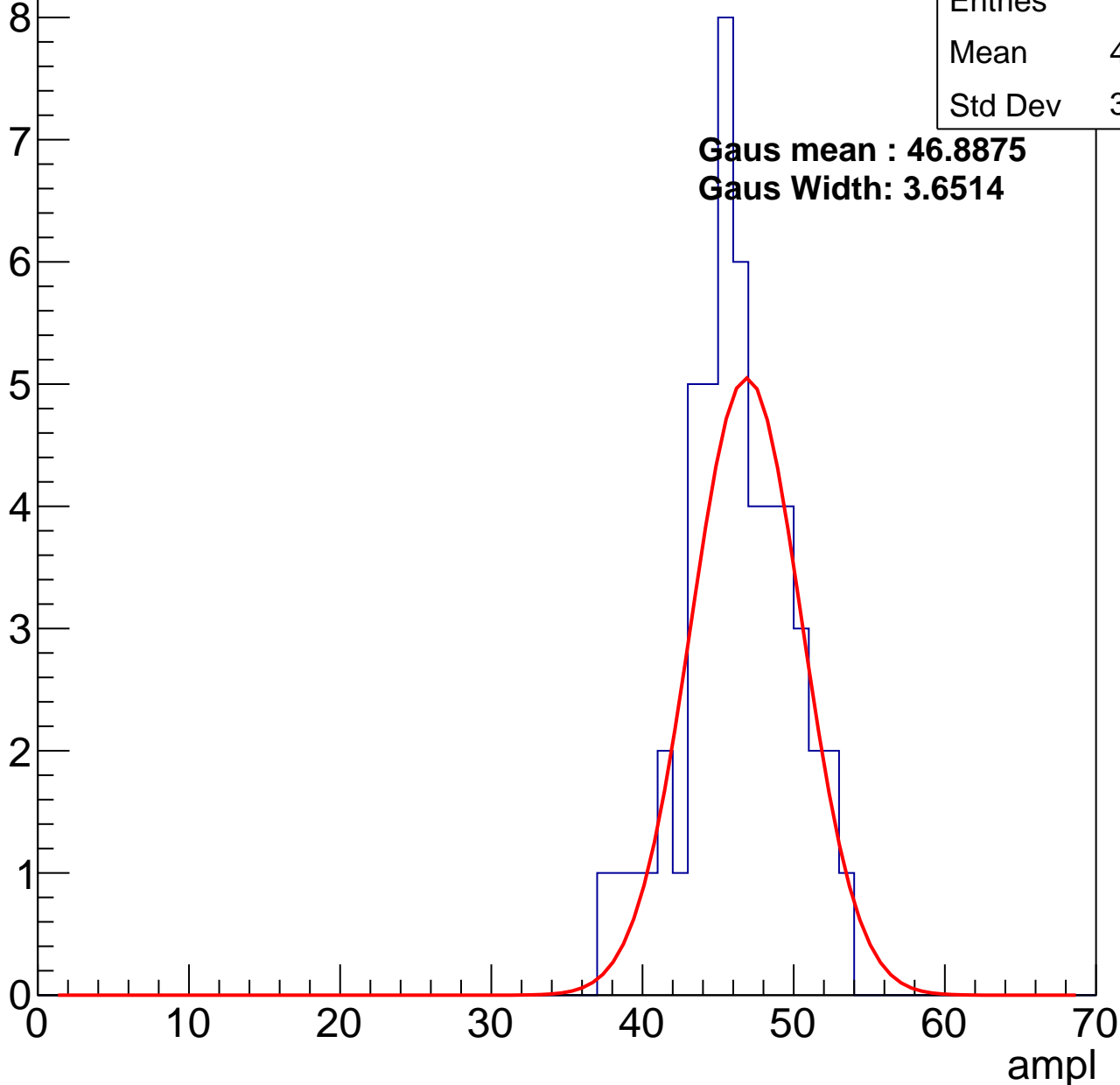
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	51
Mean	45.76
Std Dev	3.546

**Gaus mean : 46.8875**

**Gaus Width: 3.6514**

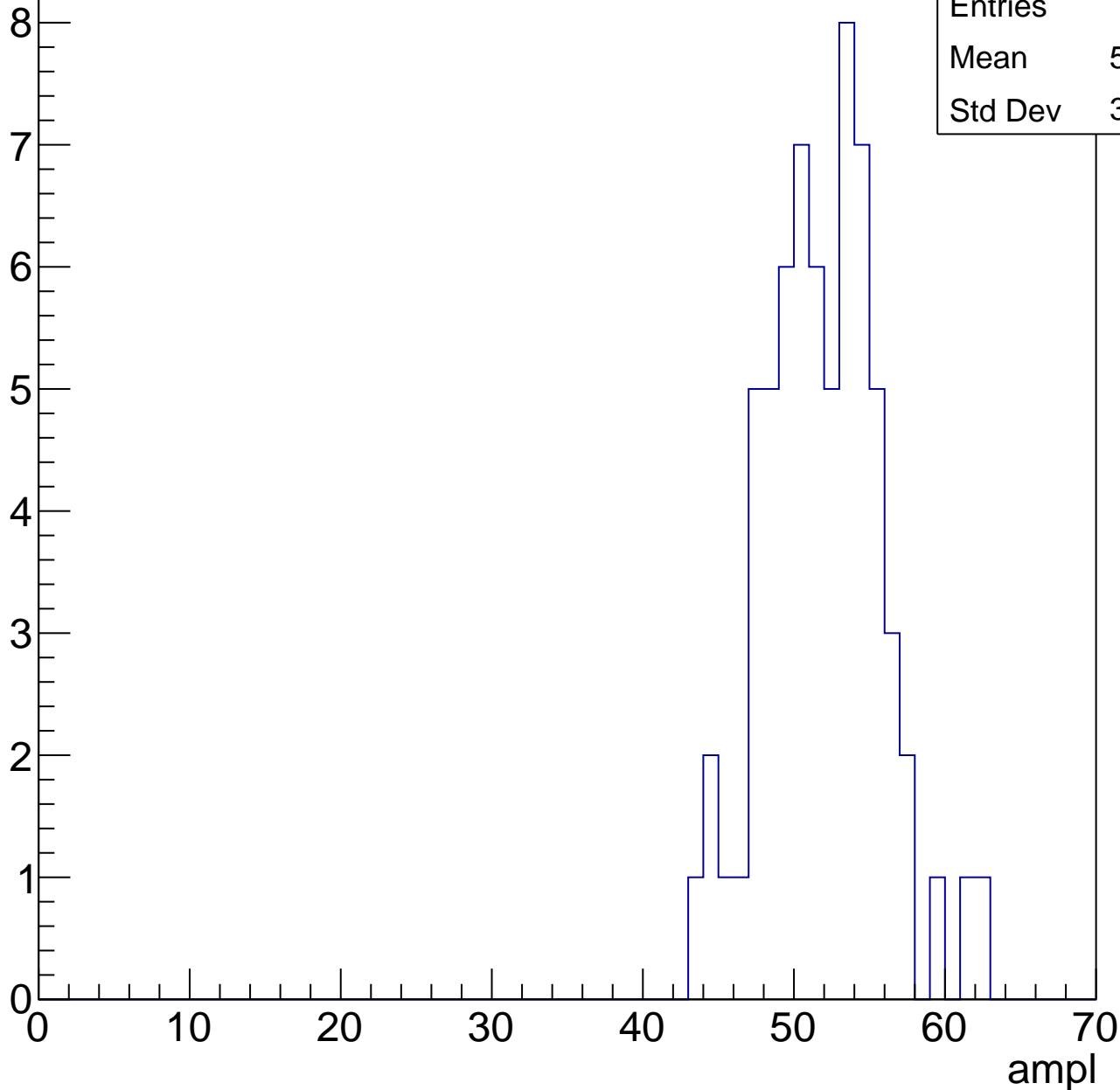


# B1L102S, U25-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	51.46
Std Dev	3.818

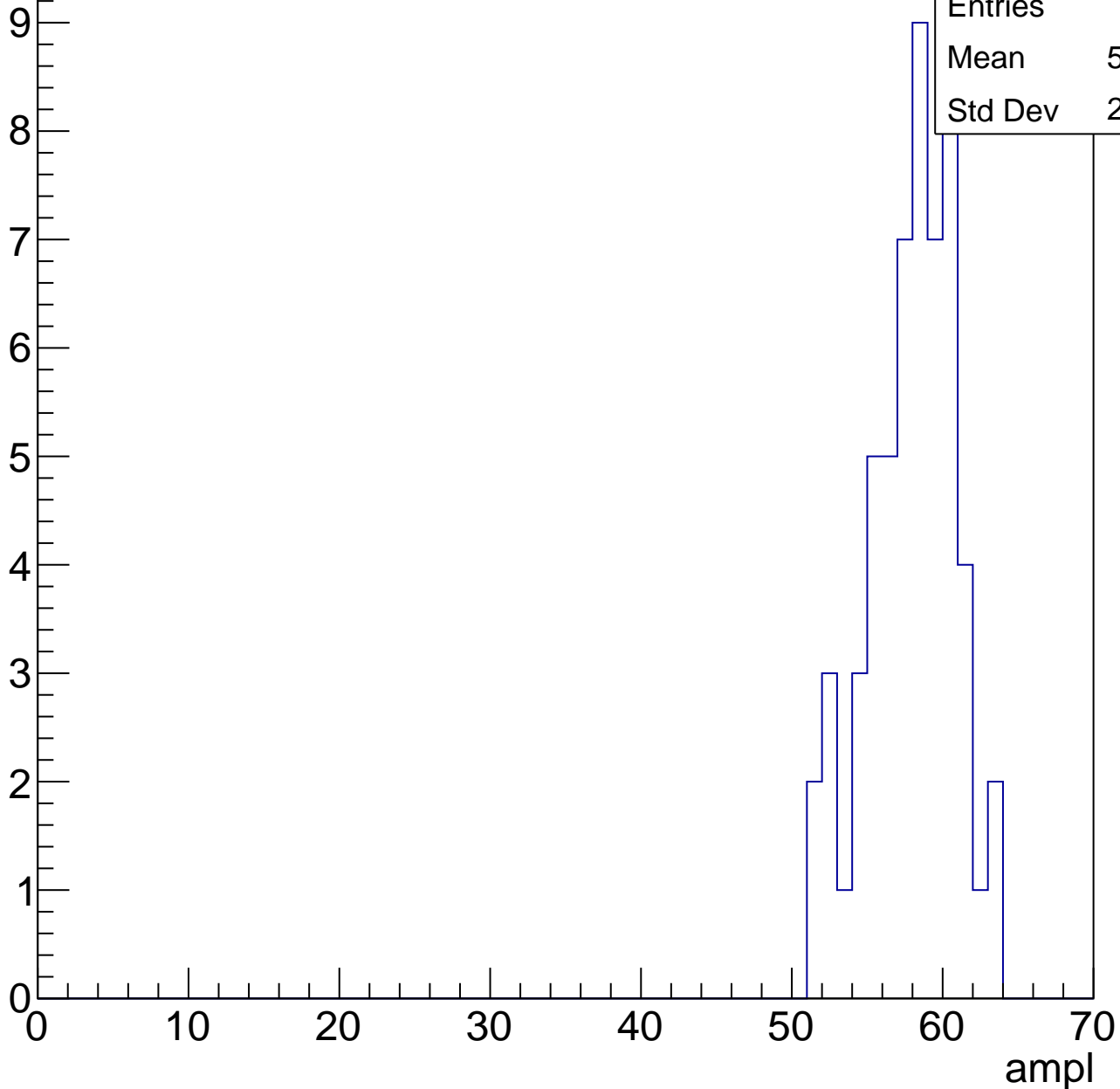


# B1L102S, U25-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	57
Mean	57.44
Std Dev	2.884

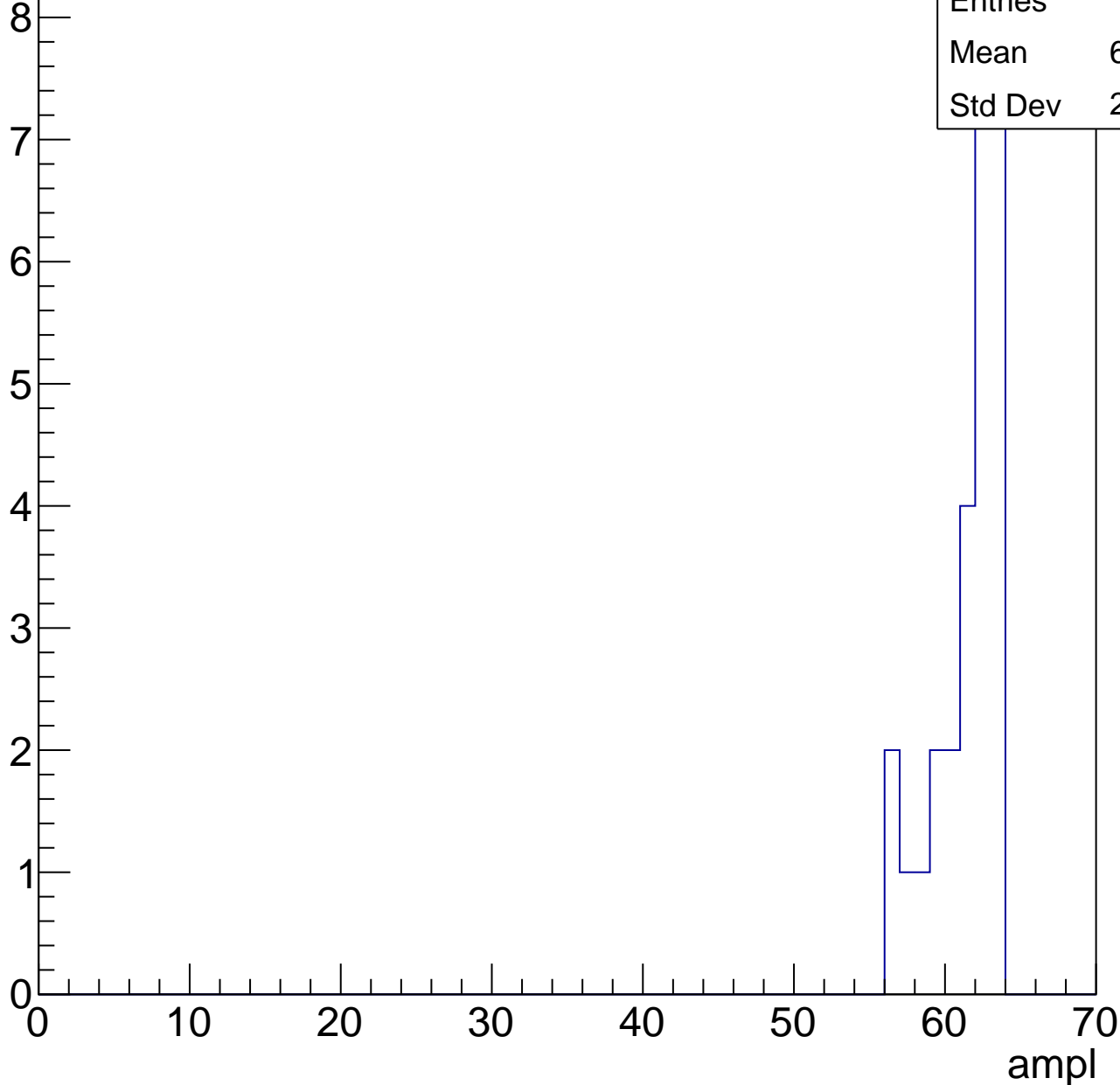


# B1L102S, U25-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

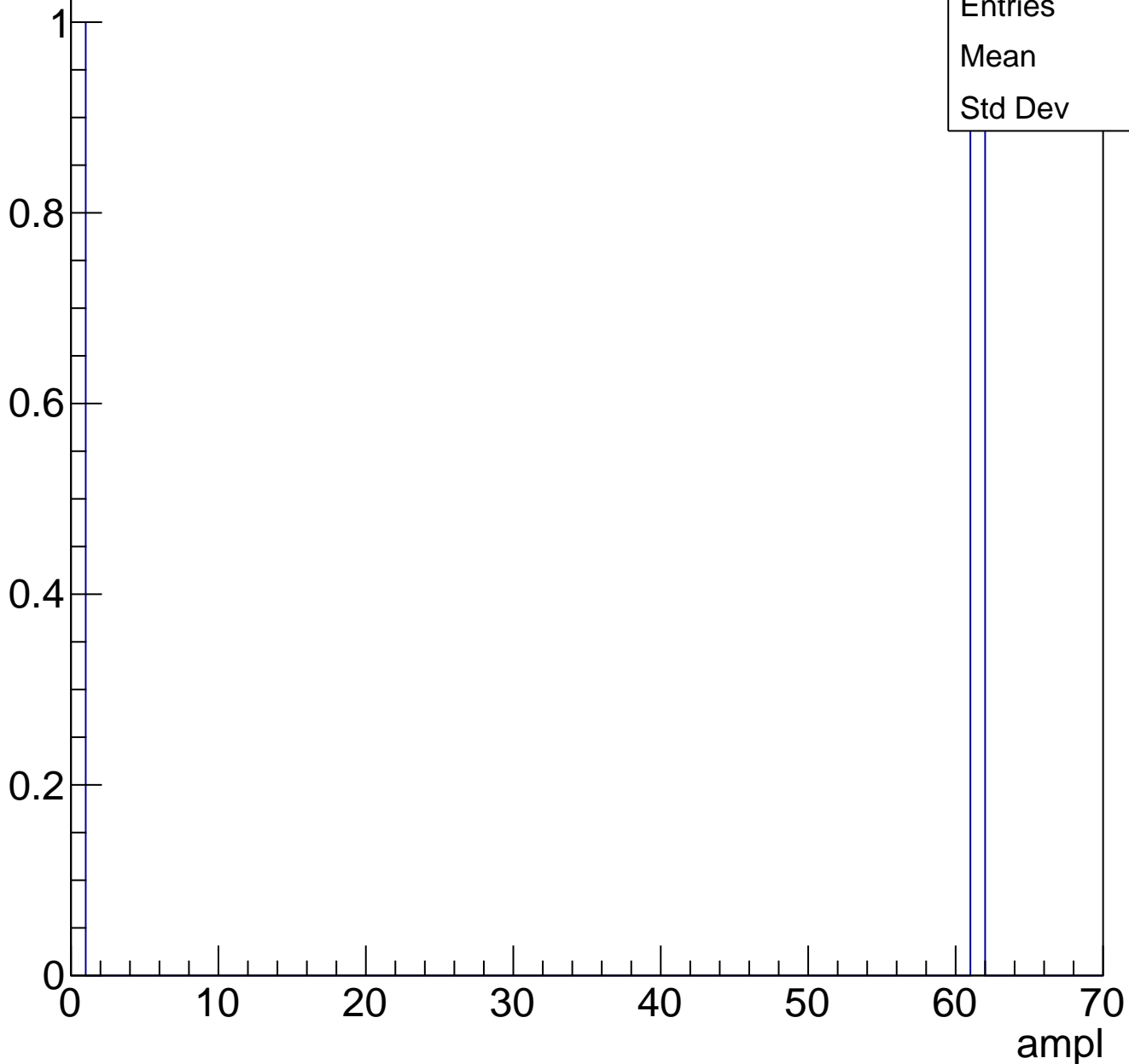
Entries	28
Mean	61.04
Std Dev	2.113



# B1L102S, U25-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

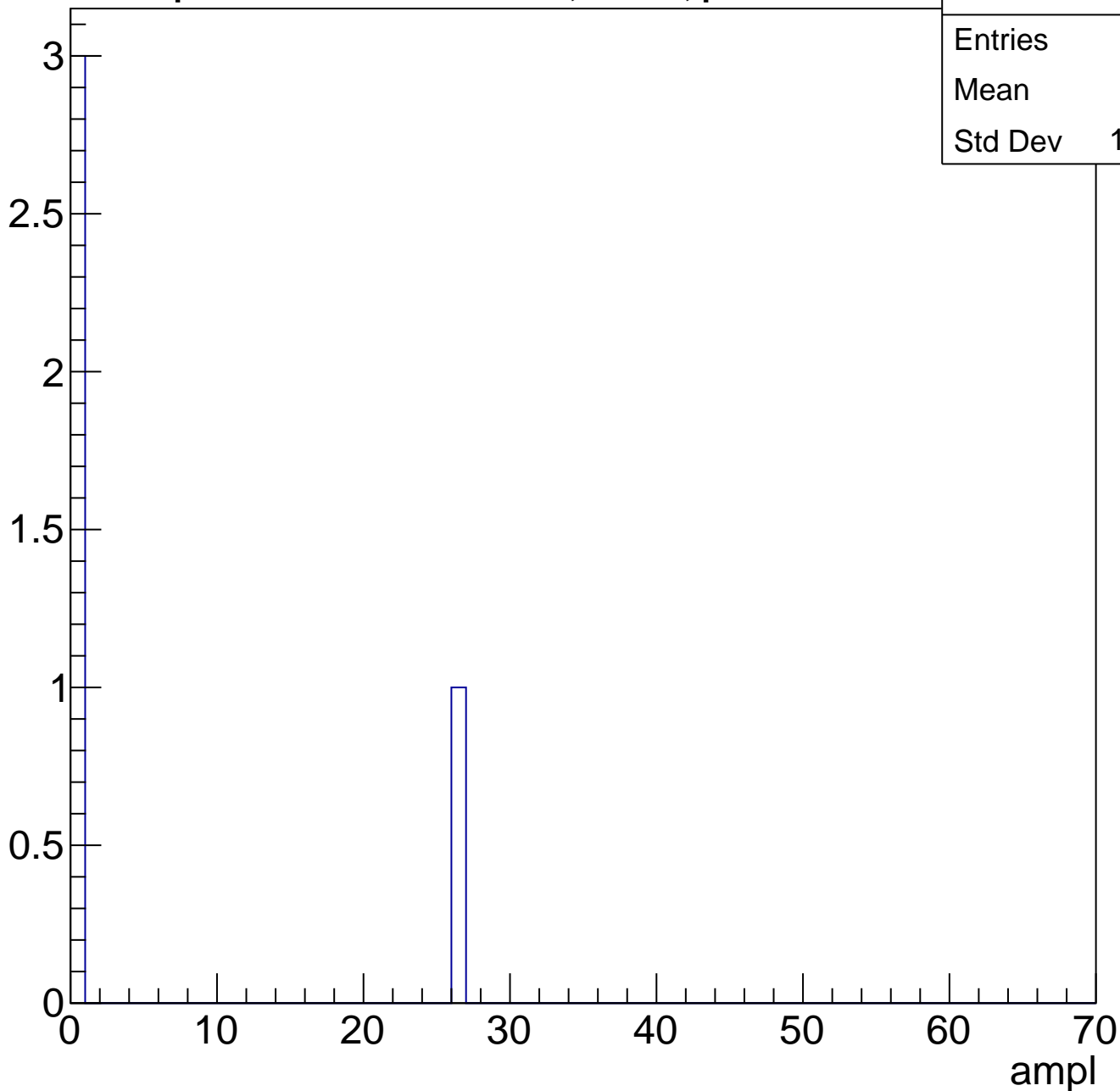




# B1L102S, U25-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch124, adc0

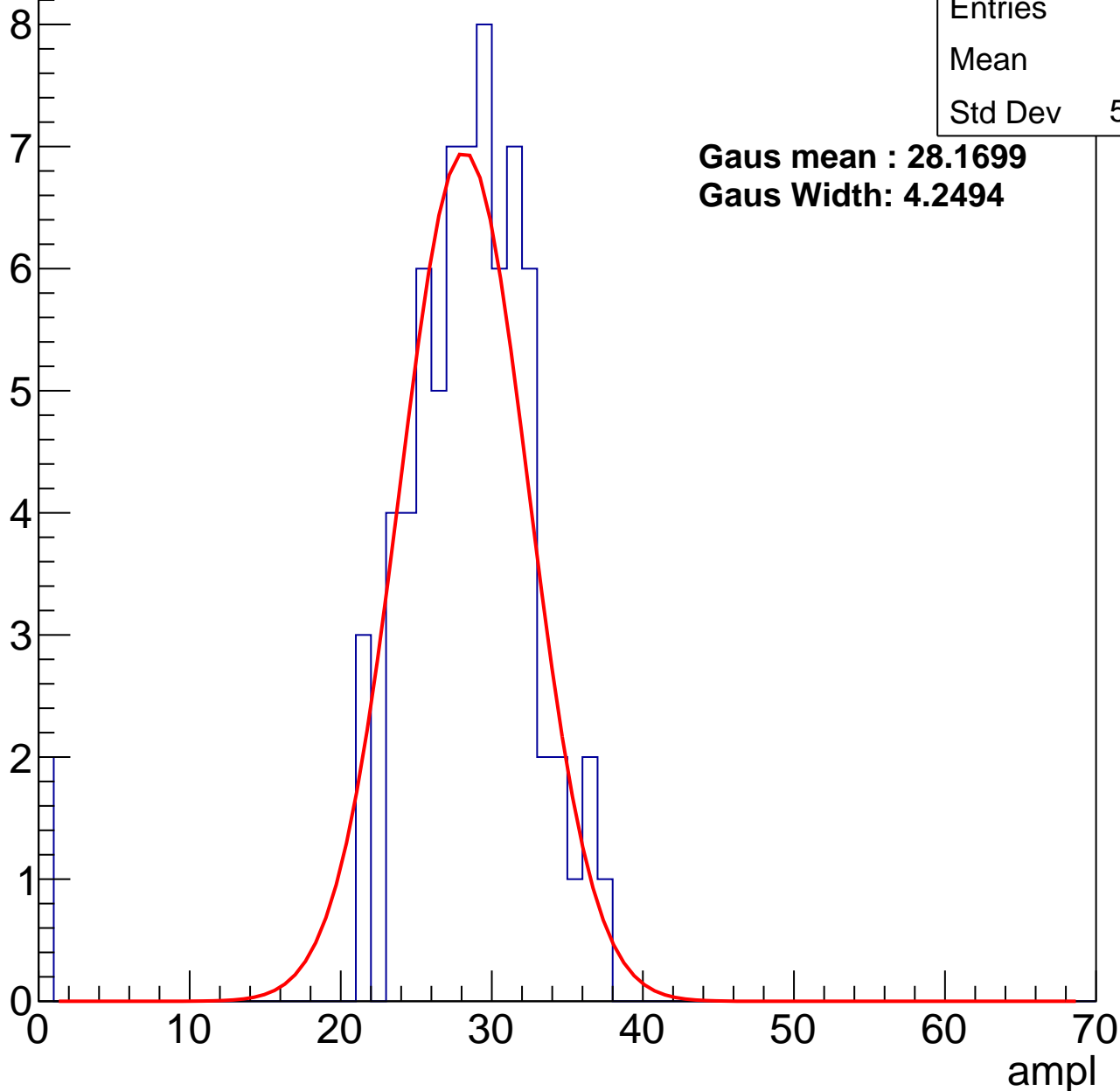
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.6
Std Dev	5.875

**Gaus mean : 28.1699**

**Gaus Width: 4.2494**



# B1L102S, U25-ch124, adc1

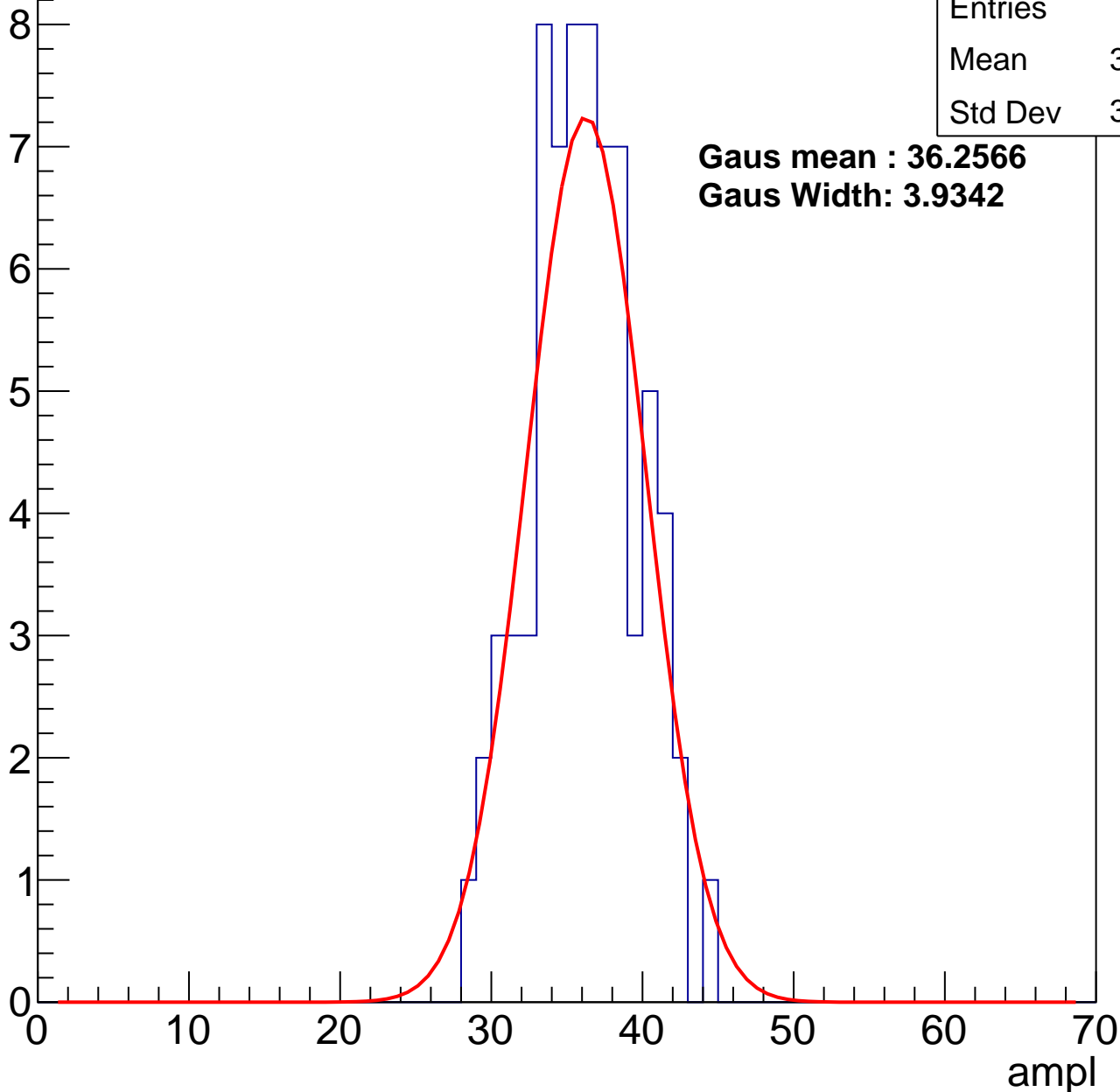
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.68
Std Dev	3.479

**Gaus mean : 36.2566**

**Gaus Width: 3.9342**



# B1L102S, U25-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	43.15
Std Dev	4.095

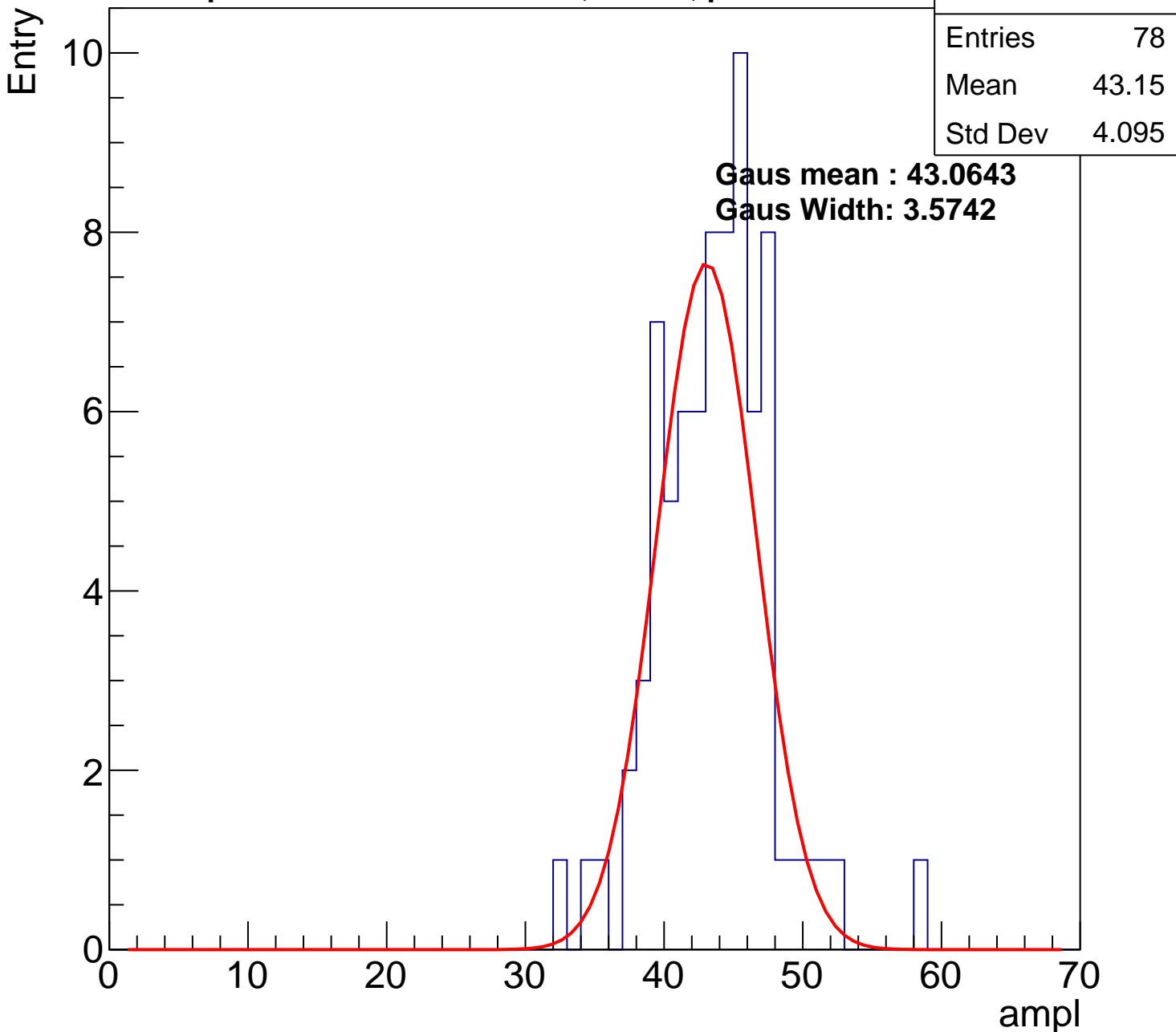
**Gaus mean : 43.0643**

**Gaus Width: 3.5742**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

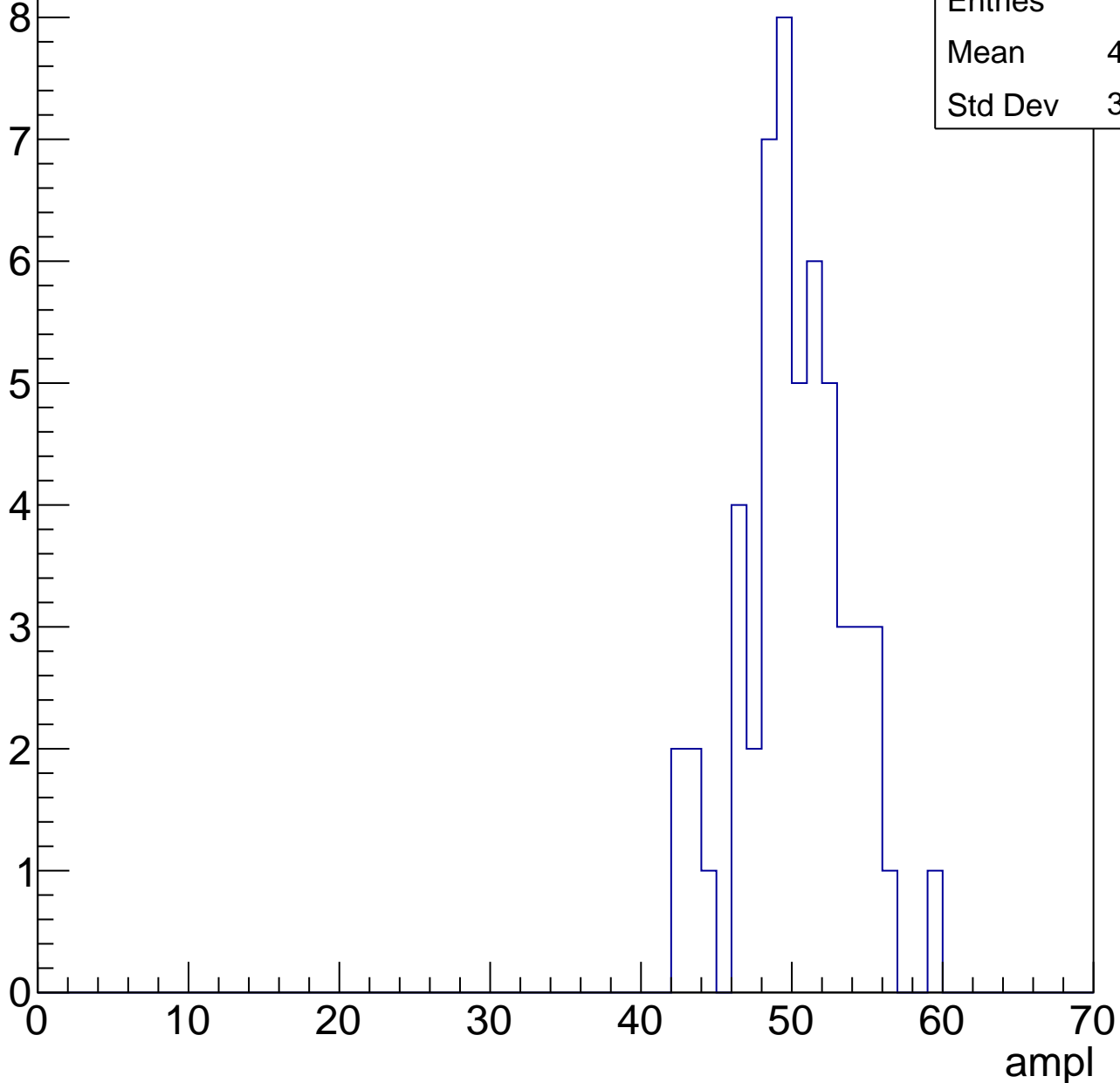


# B1L102S, U25-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	49.75
Std Dev	3.555

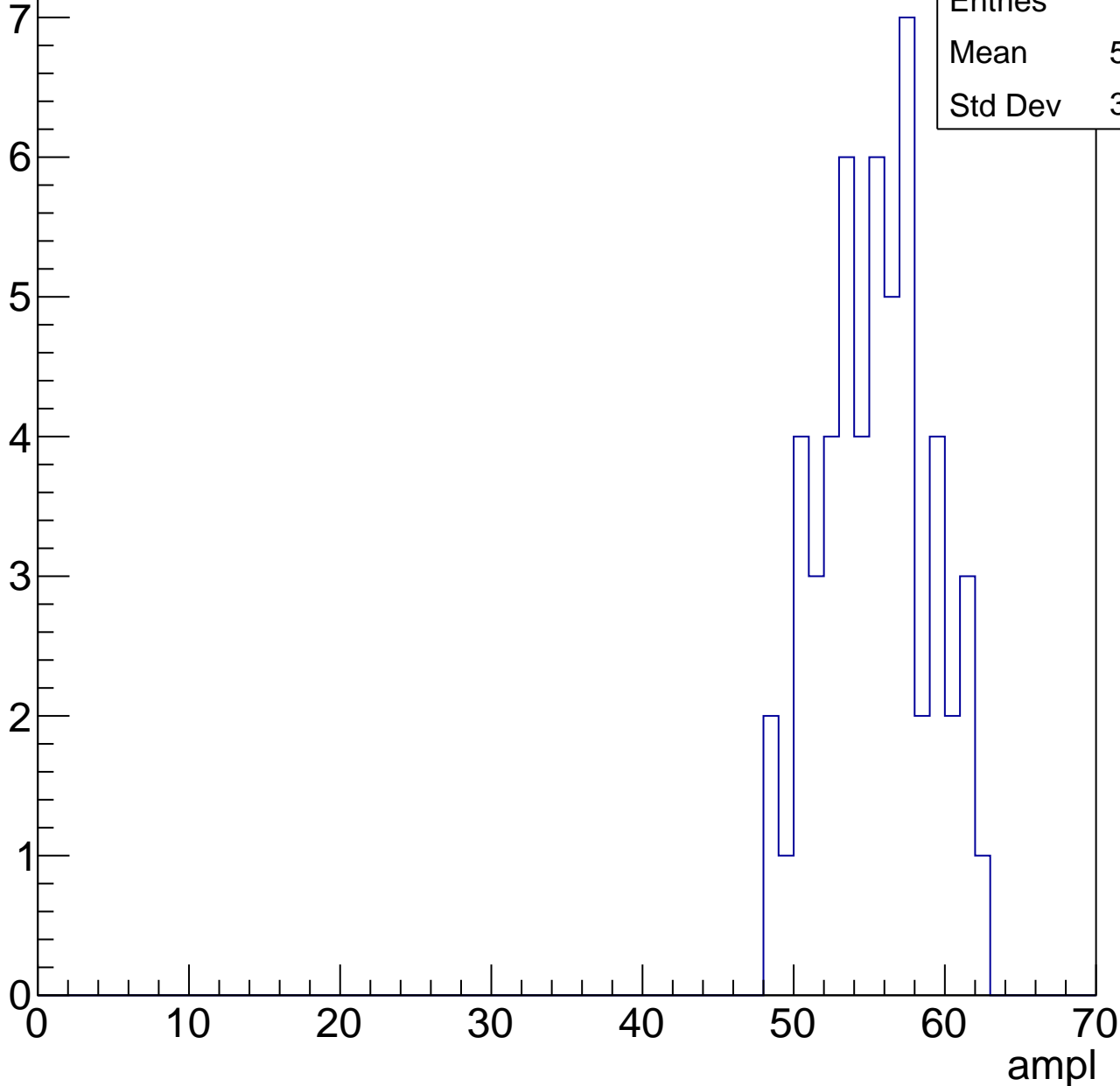


# B1L102S, U25-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.93
Std Dev	3.516

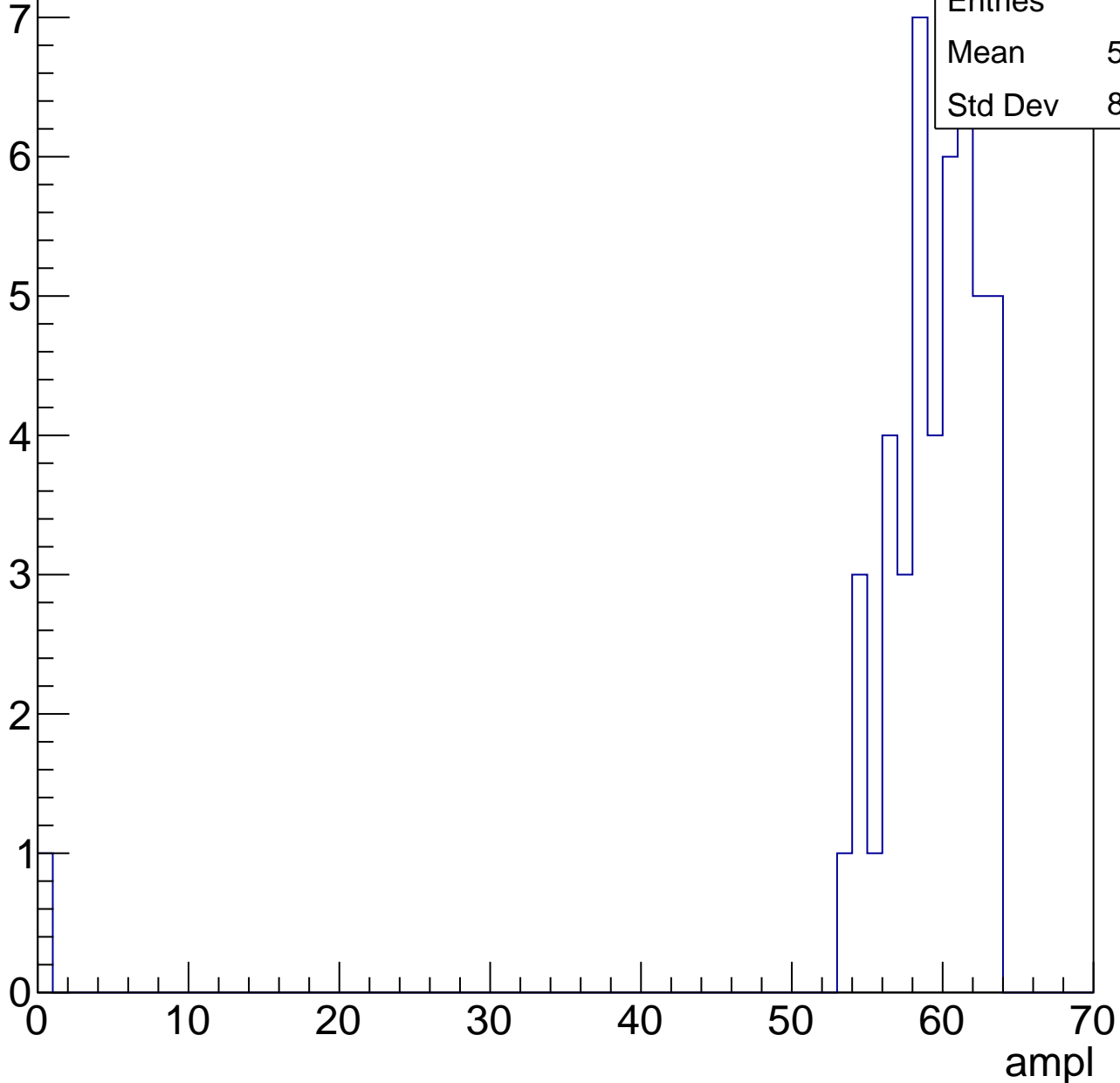


# B1L102S, U25-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.85
Std Dev	8.944

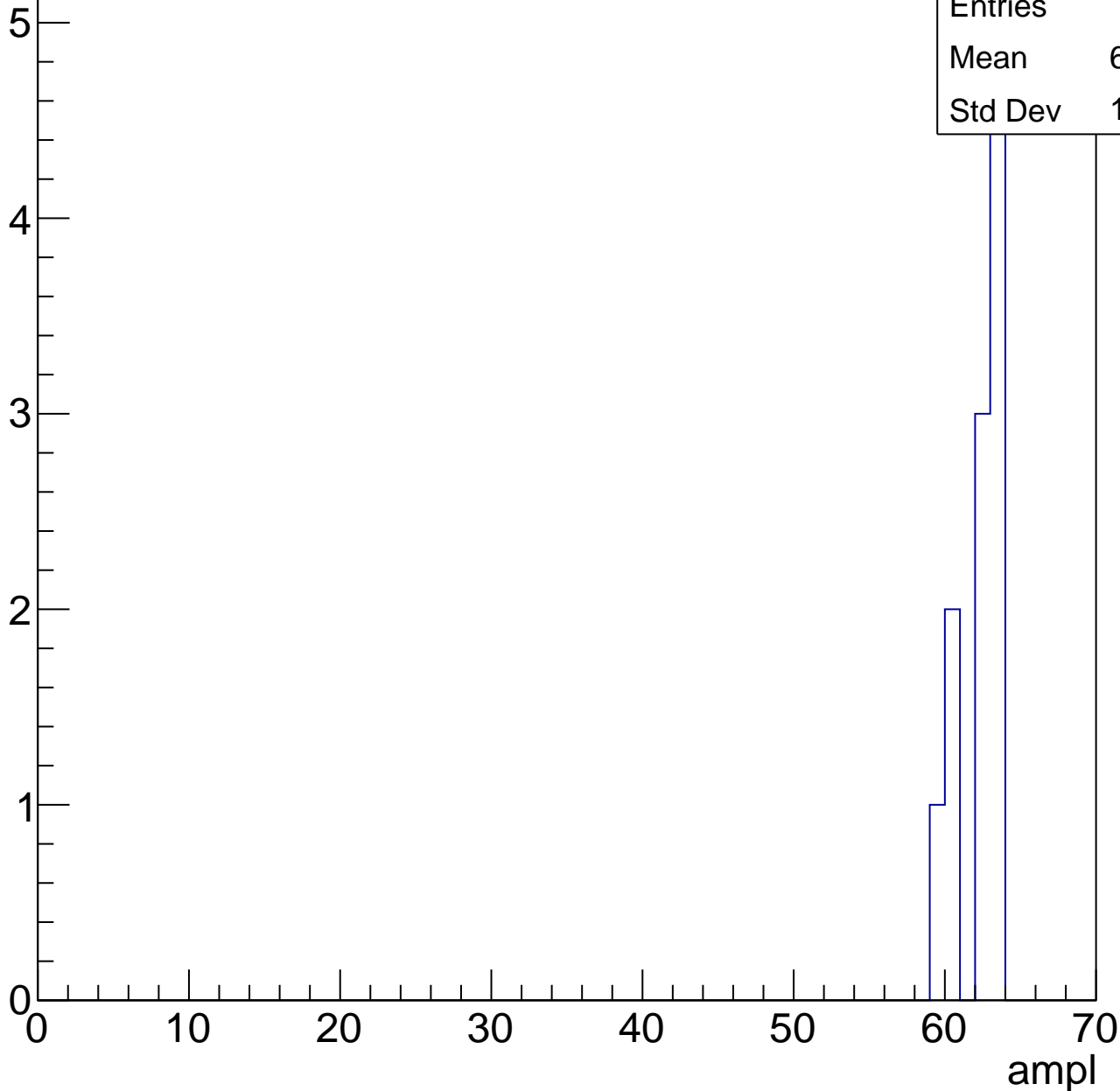


# B1L102S, U25-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.82
Std Dev	1.402





# B1L102S, U25-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch125, adc0

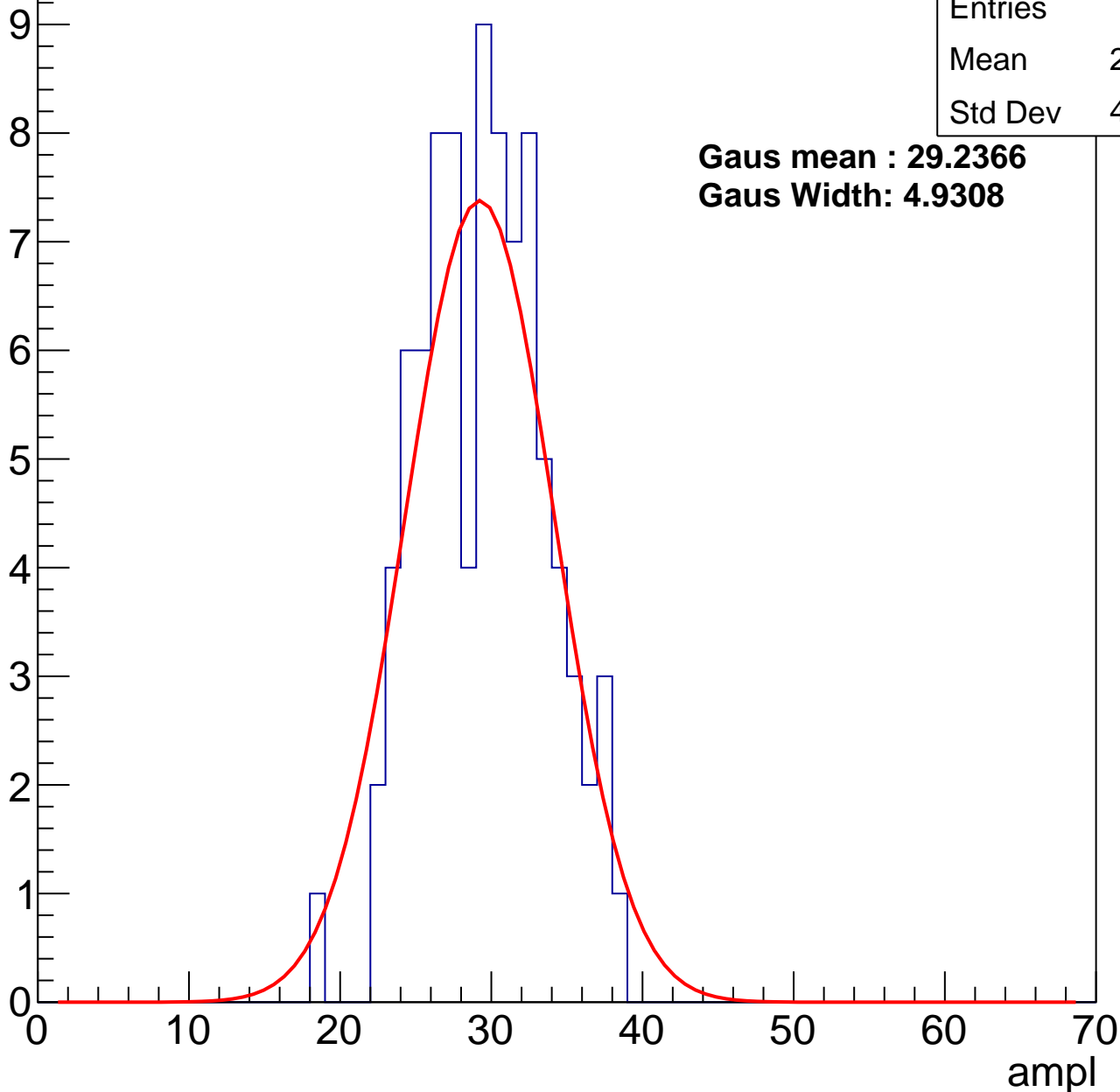
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	89
Mean	29.04
Std Dev	4.083

**Gaus mean : 29.2366**

**Gaus Width: 4.9308**



# B1L102S, U25-ch125, adc1

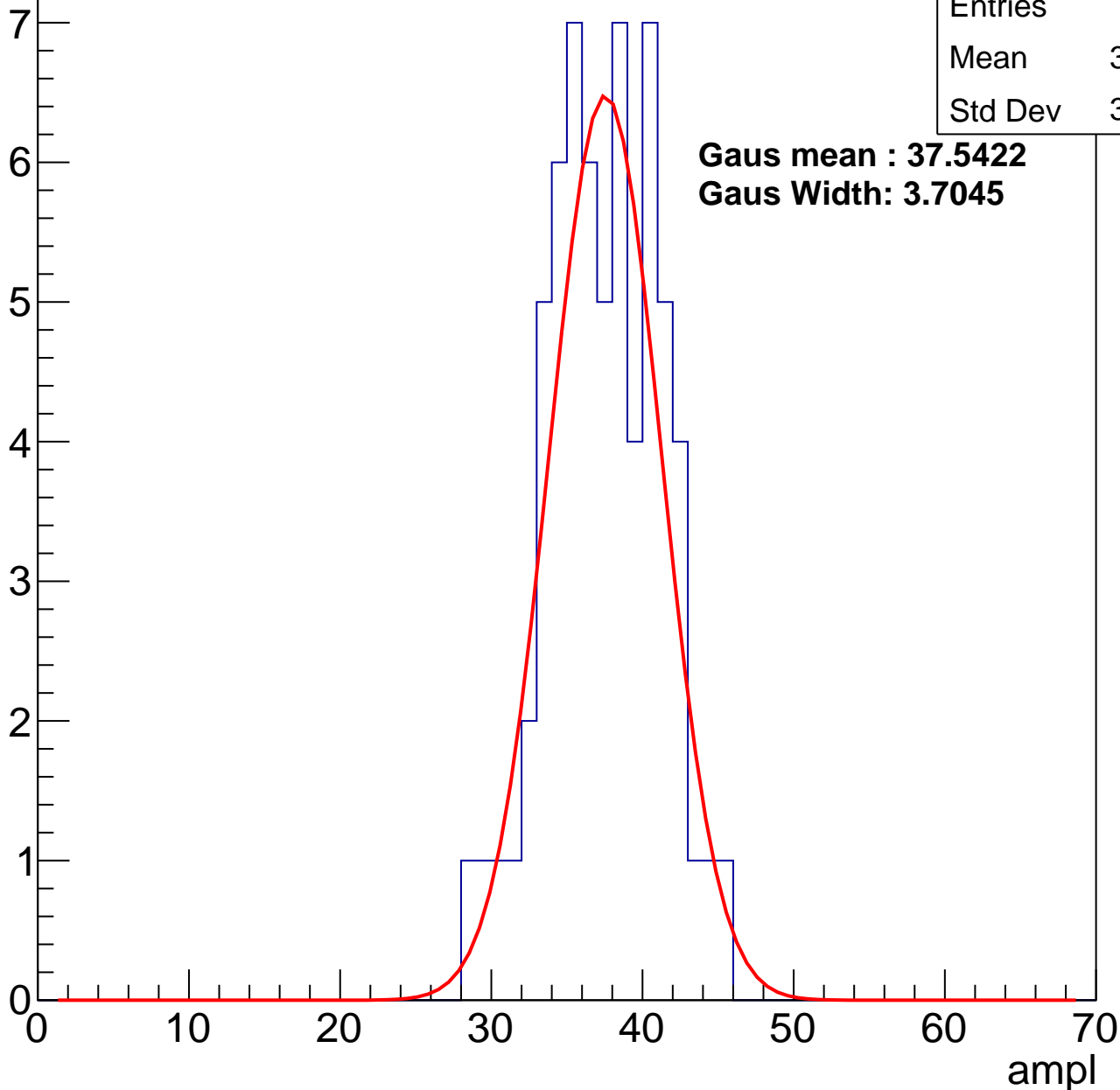
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.98
Std Dev	3.644

**Gaus mean : 37.5422**

**Gaus Width: 3.7045**



# B1L102S, U25-ch125, adc2

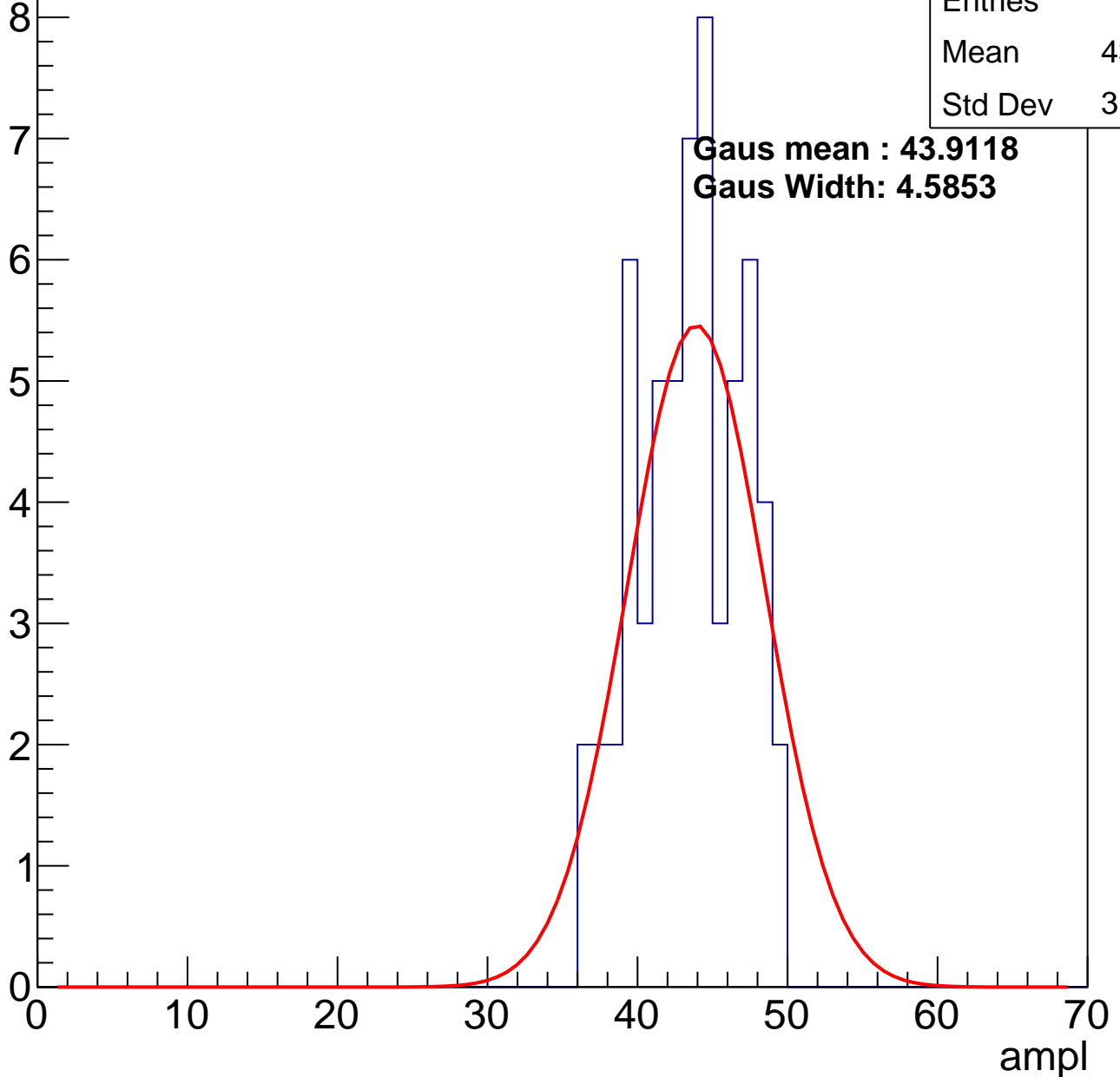
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.02
Std Dev	3.418

**Gaus mean : 43.9118**

**Gaus Width: 4.5853**

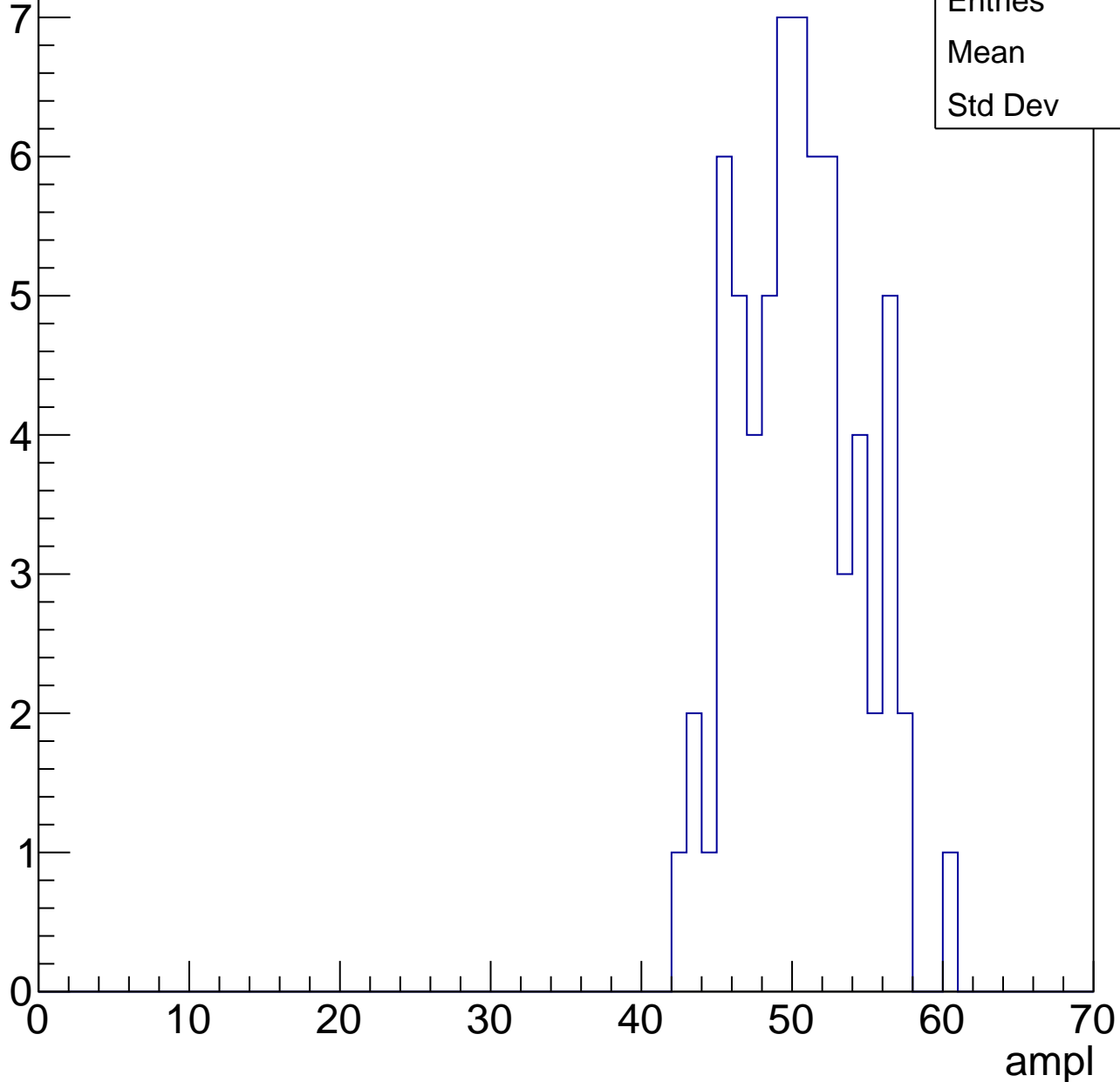


# B1L102S, U25-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	50
Std Dev	3.94

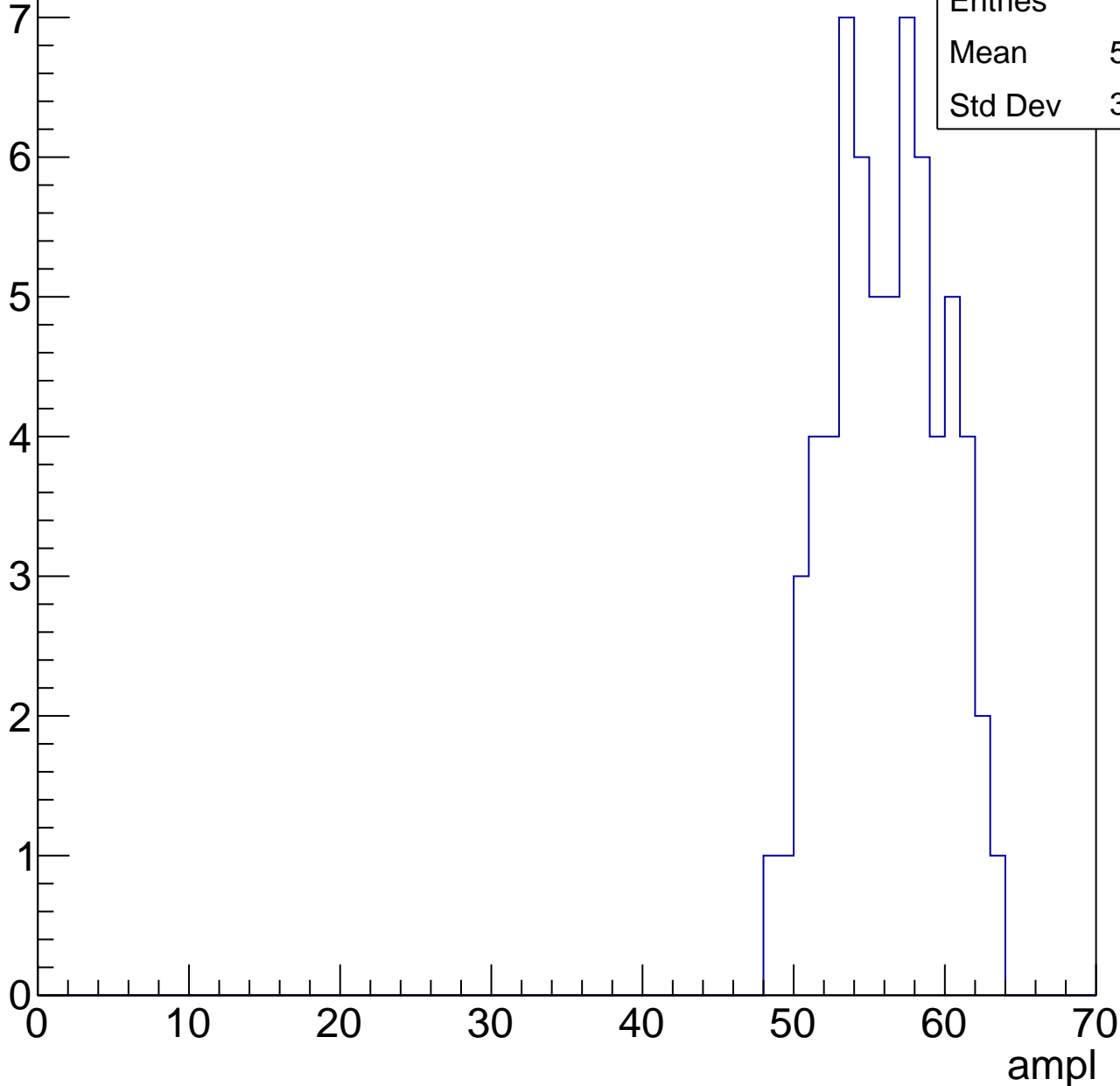


# B1L102S, U25-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.74
Std Dev	3.596

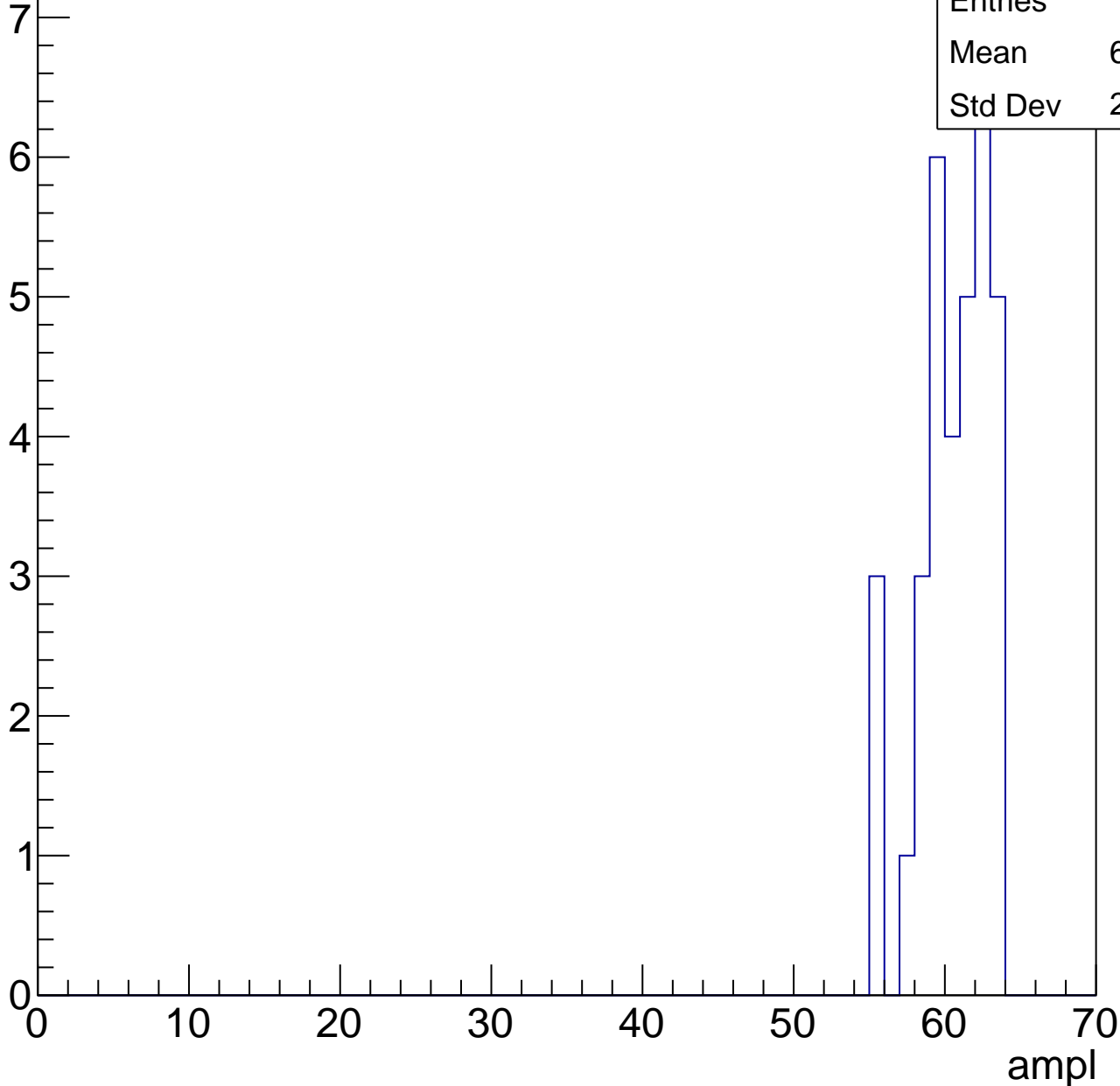


# B1L102S, U25-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

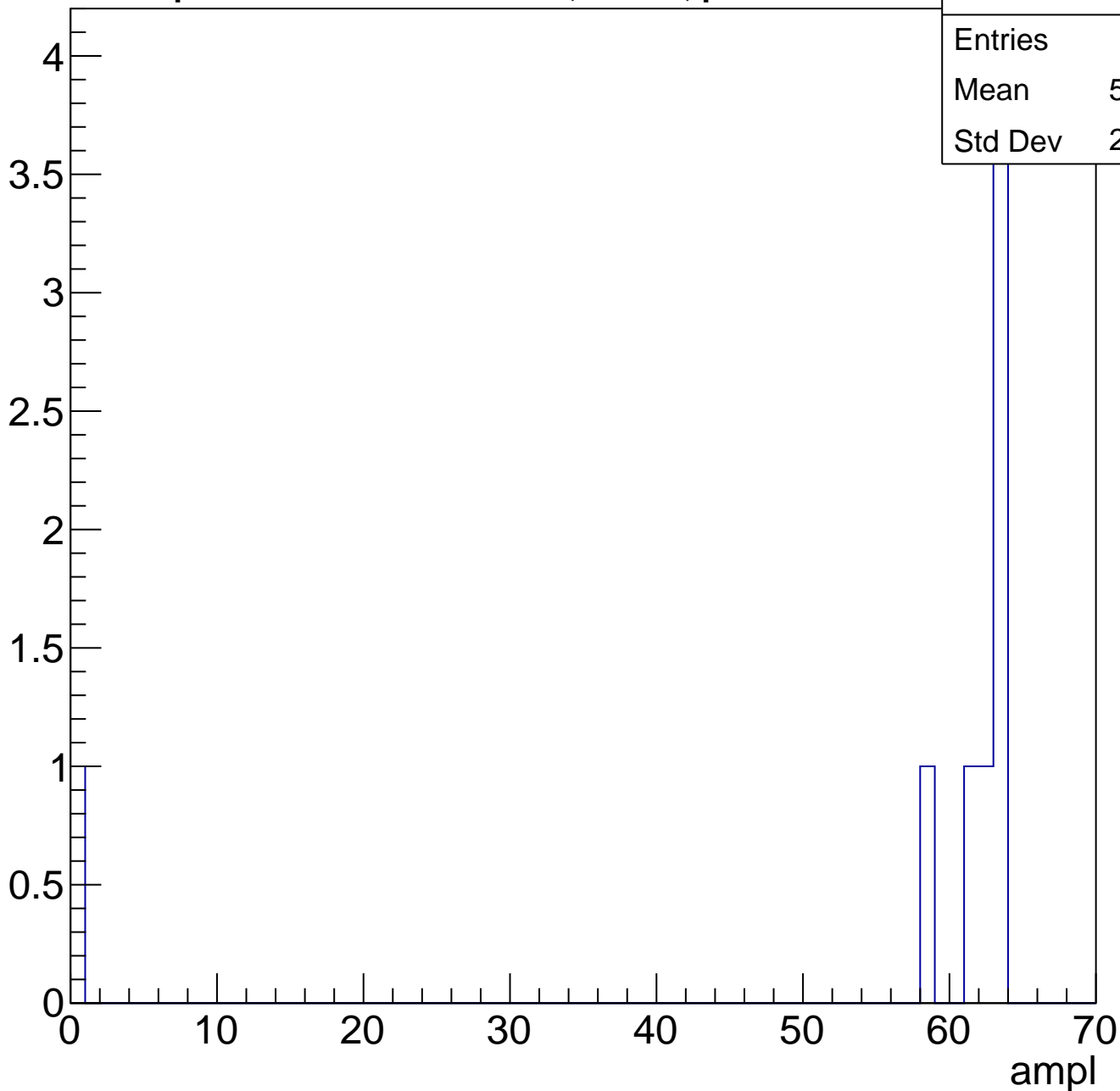
Entries	34
Mean	60.12
Std Dev	2.298



# B1L102S, U25-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry





# B1L102S, U25-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch126, adc0

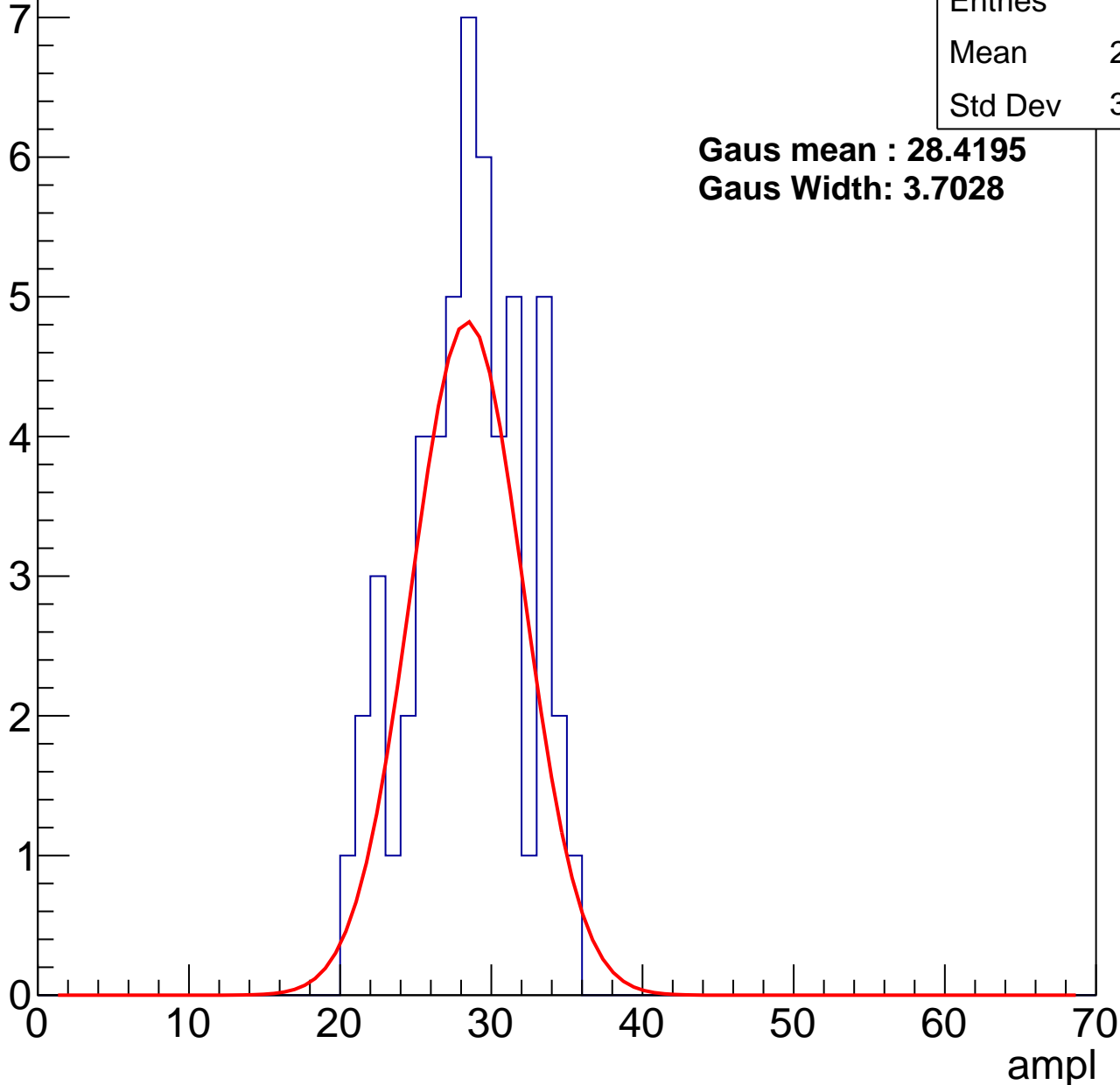
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	53
Mean	27.98
Std Dev	3.668

**Gaus mean : 28.4195**

**Gaus Width: 3.7028**



# B1L102S, U25-ch126, adc1

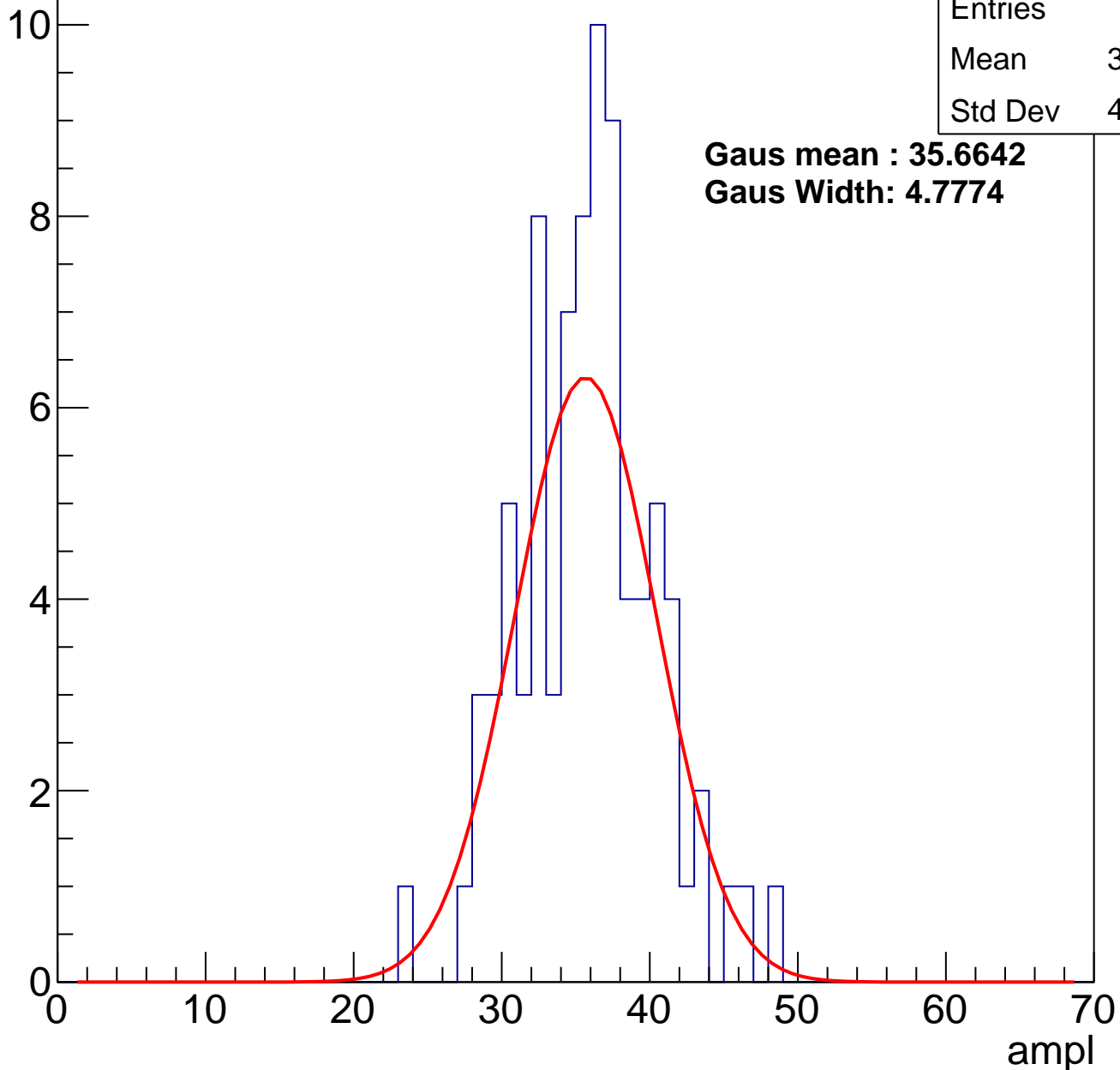
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	84
Mean	35.35
Std Dev	4.497

**Gaus mean : 35.6642**

**Gaus Width: 4.7774**

Entry



# B1L102S, U25-ch126, adc2

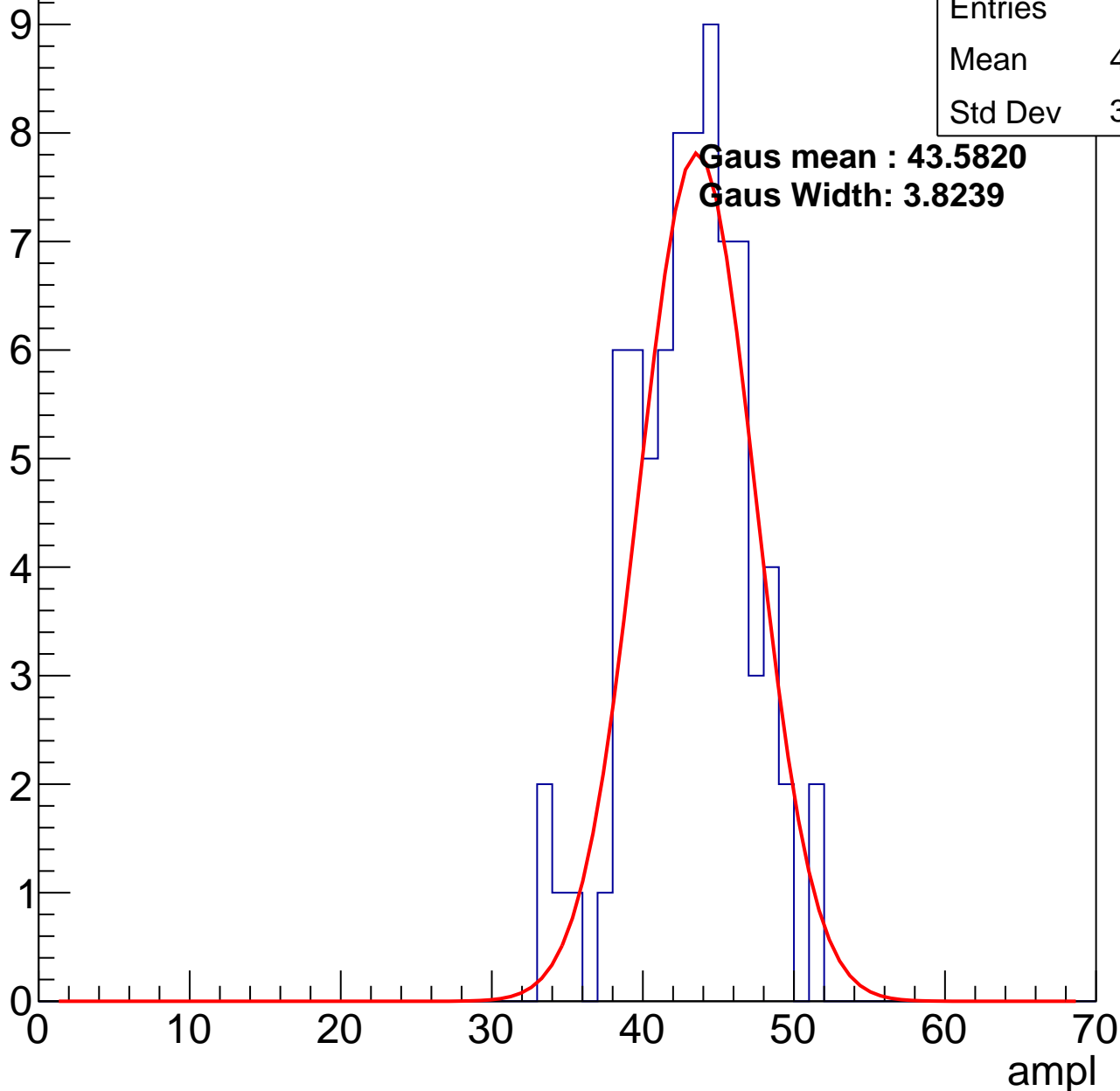
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.64
Std Dev	3.826

**Gaus mean : 43.5820**

**Gaus Width: 3.8239**

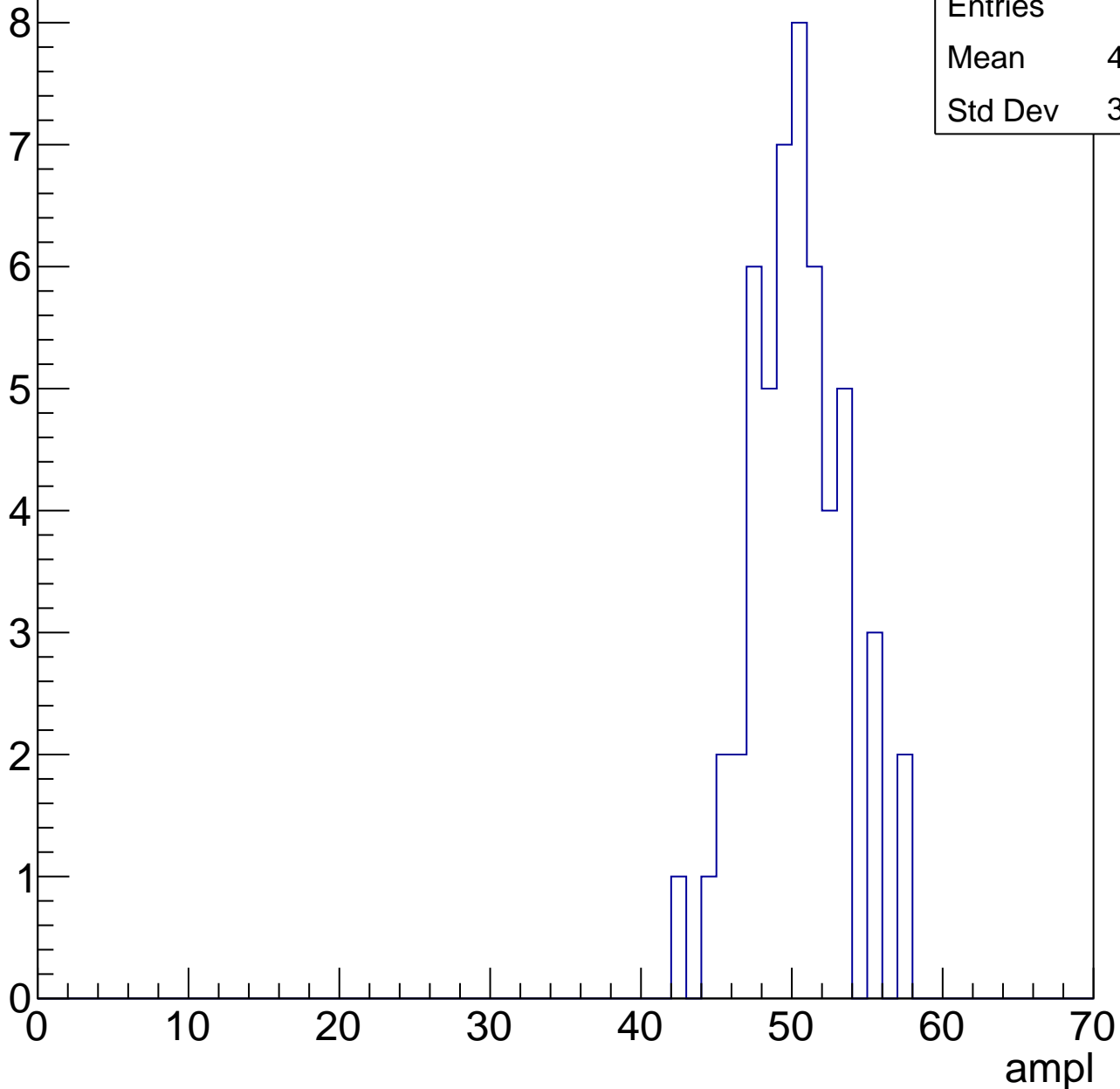


# B1L102S, U25-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.83
Std Dev	3.105

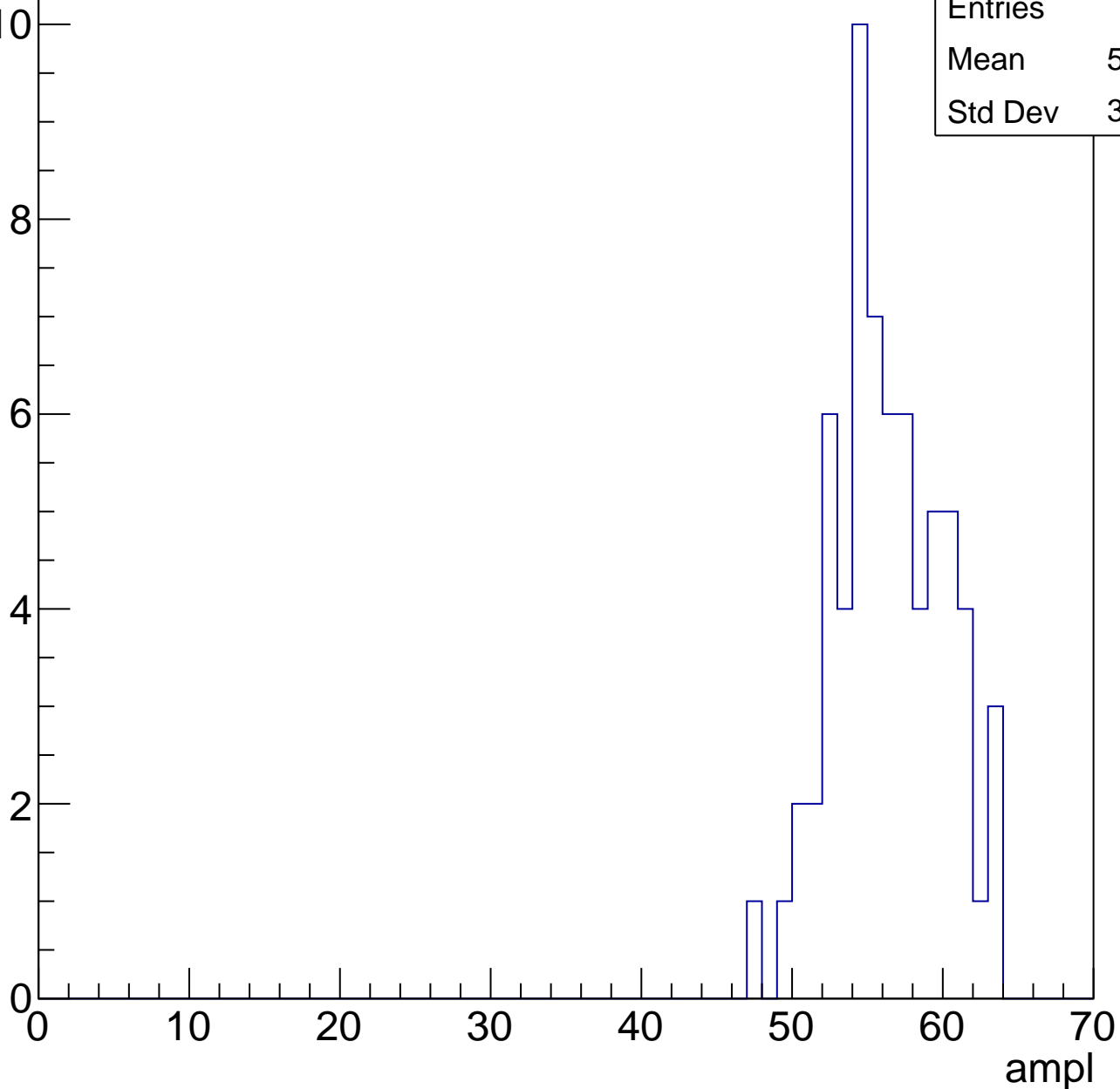


# B1L102S, U25-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	67
Mean	55.93
Std Dev	3.584

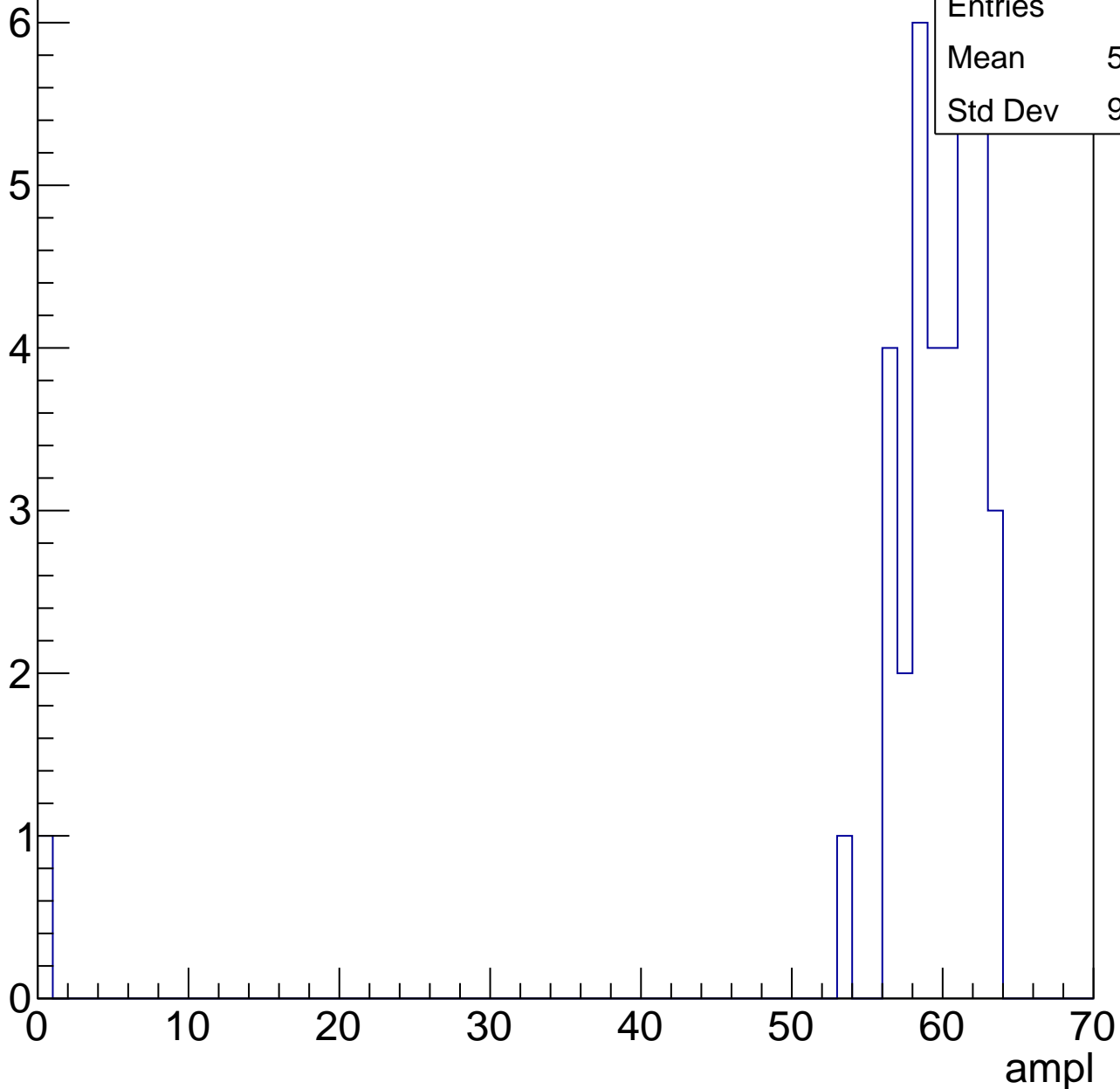


# B1L102S, U25-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

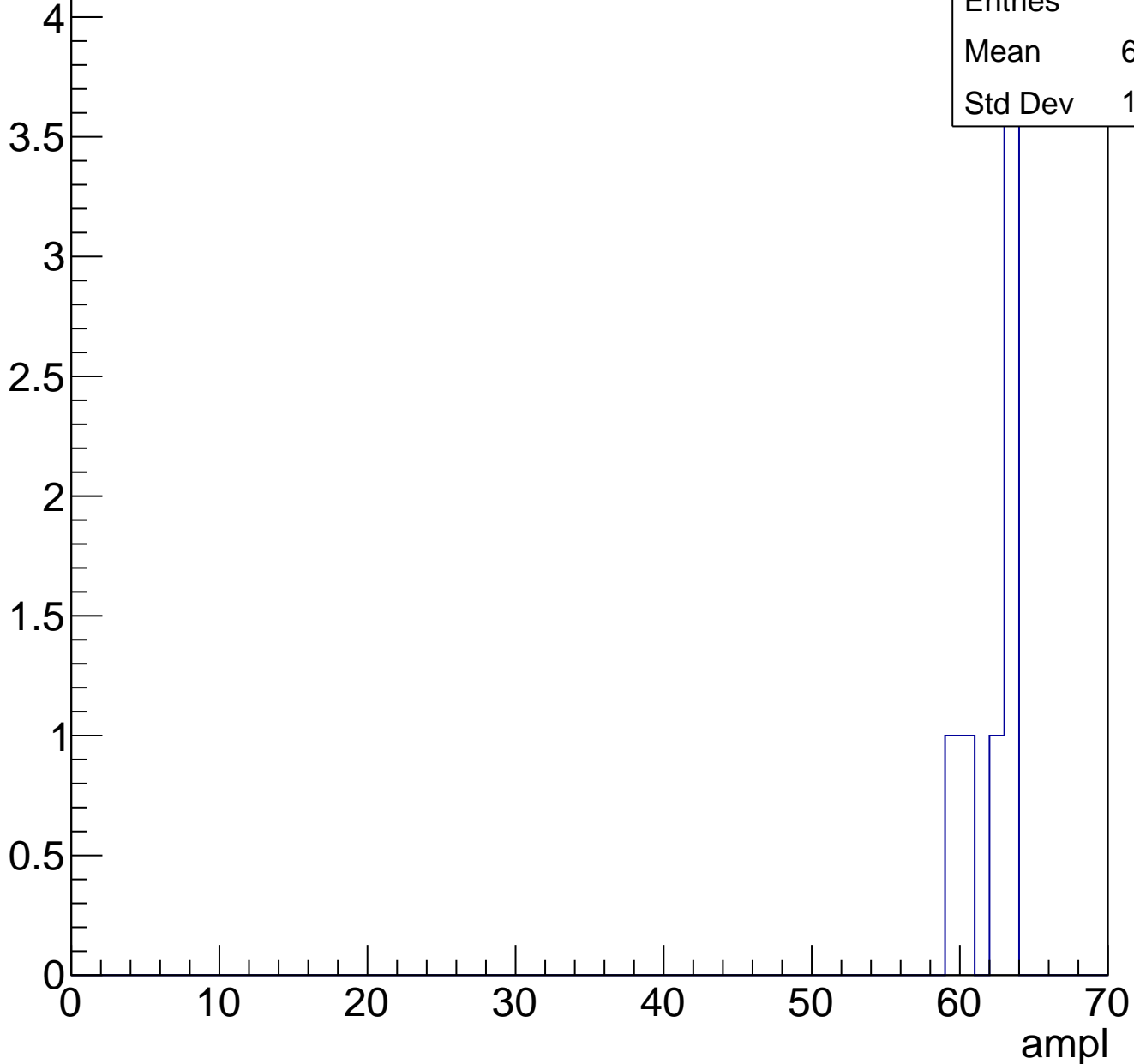
Entries	37
Mean	57.89
Std Dev	9.934



# B1L102S, U25-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

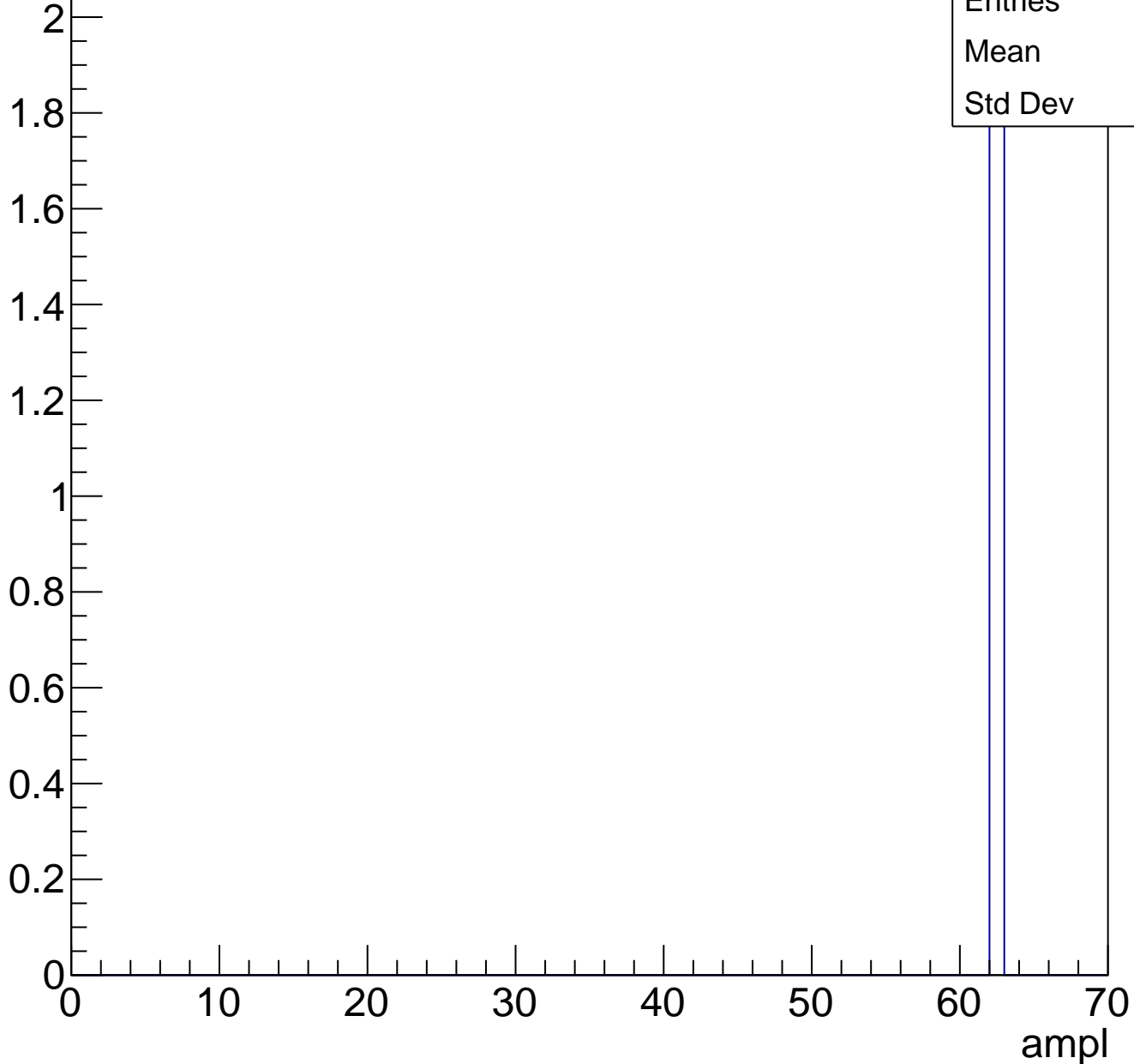




# B1L102S, U25-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



# B1L102S, U25-ch127, adc0

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	73
Mean	28.53
Std Dev	4.789

**Gaus mean : 29.6712**

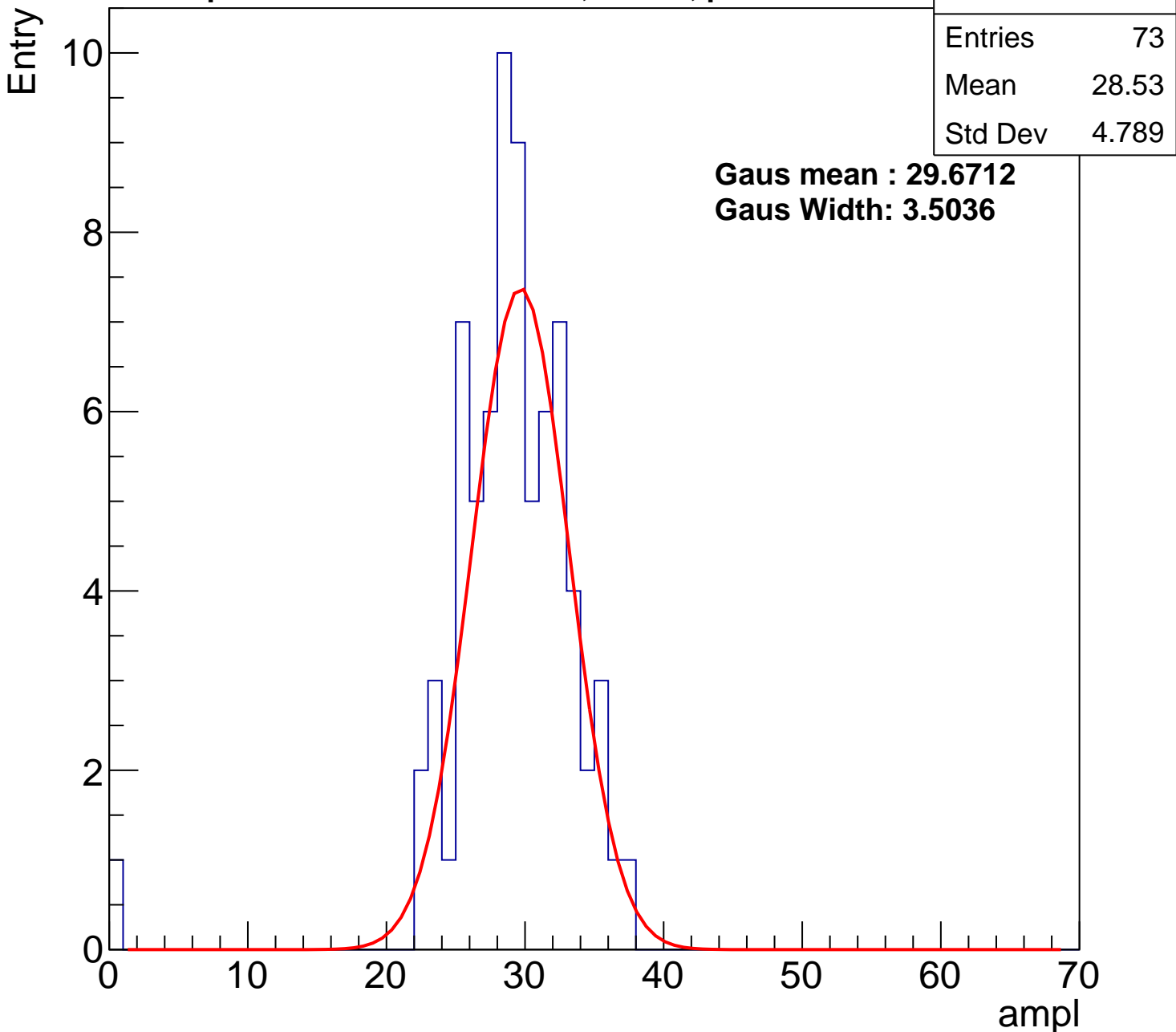
**Gaus Width: 3.5036**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch127, adc1

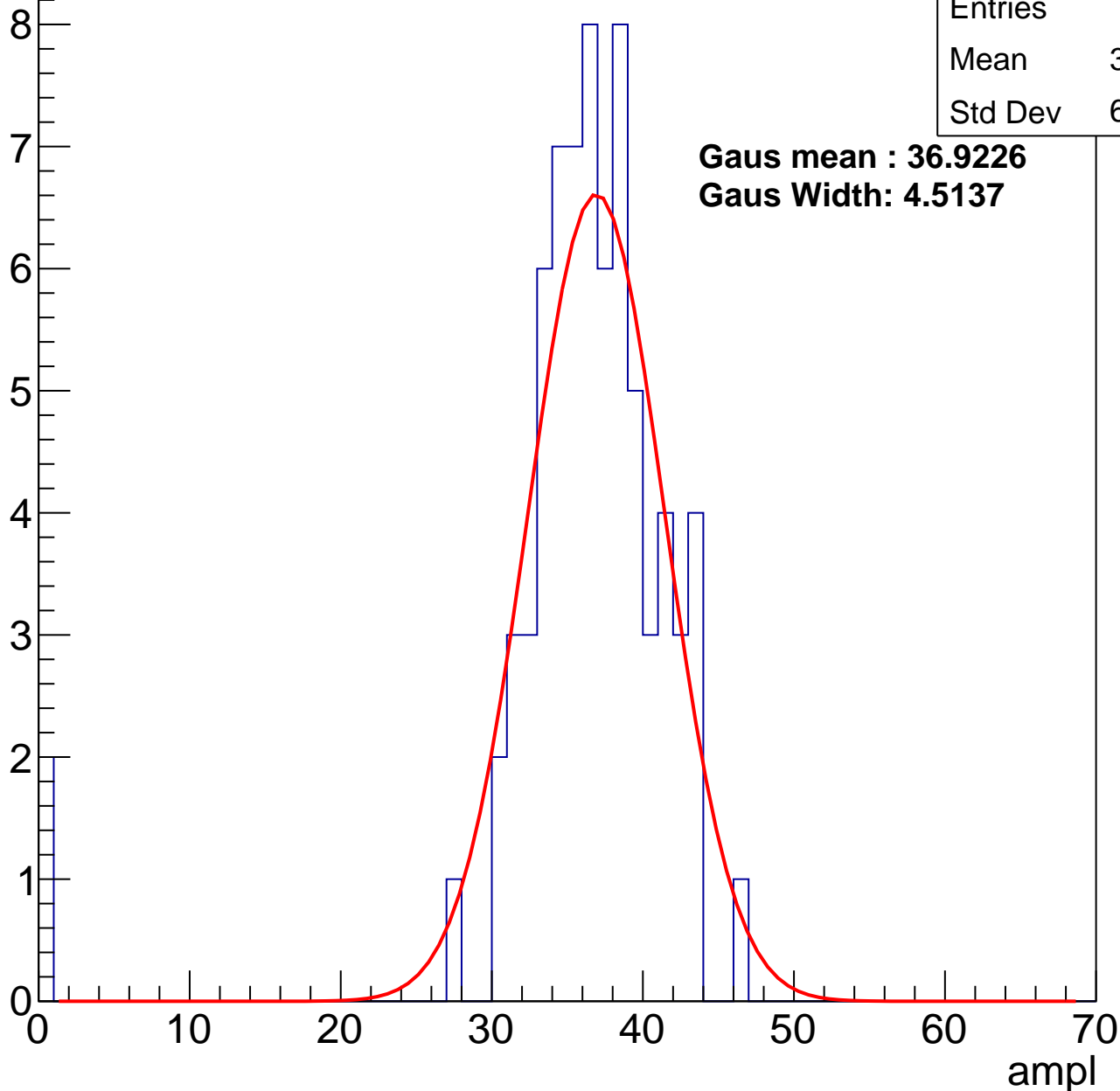
calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.53
Std Dev	6.999

**Gaus mean : 36.9226**

**Gaus Width: 4.5137**



# B1L102S, U25-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entries	63
Mean	43.29
Std Dev	3.848

**Gaus mean : 43.3208**

**Gaus Width: 4.1095**

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

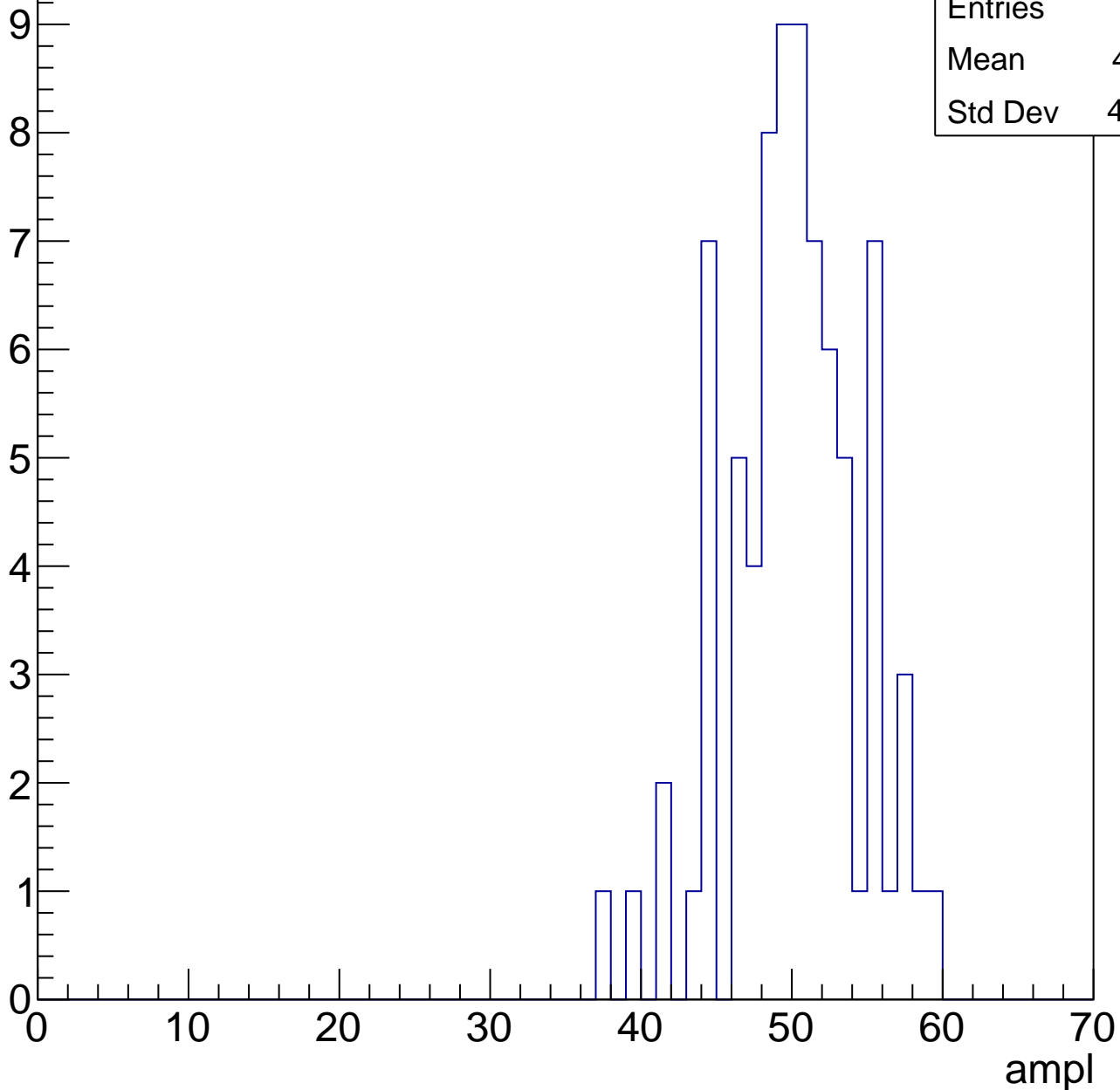
70

# B1L102S, U25-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	79
Mean	49.61
Std Dev	4.347

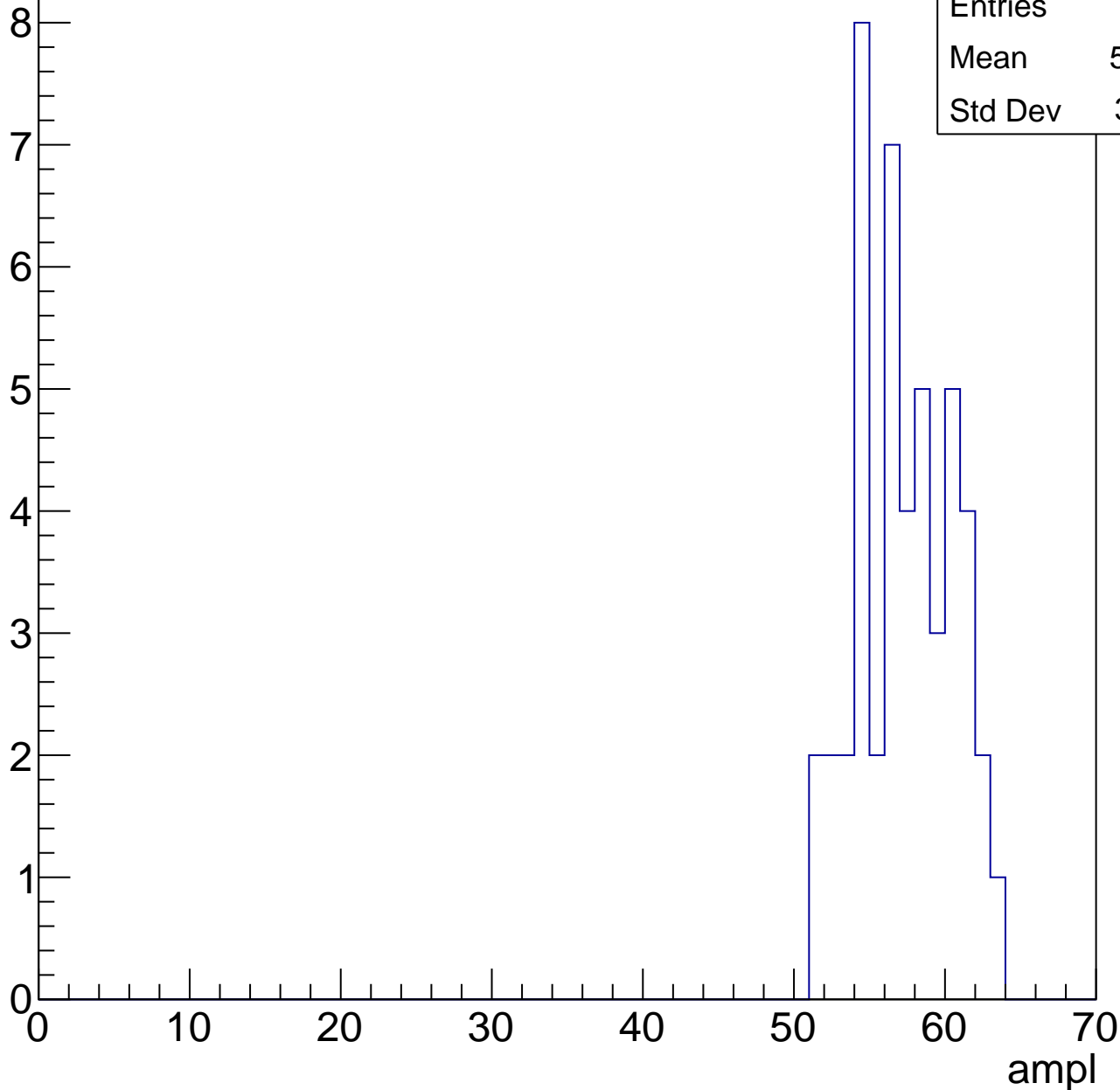


# B1L102S, U25-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.85
Std Dev	3.101

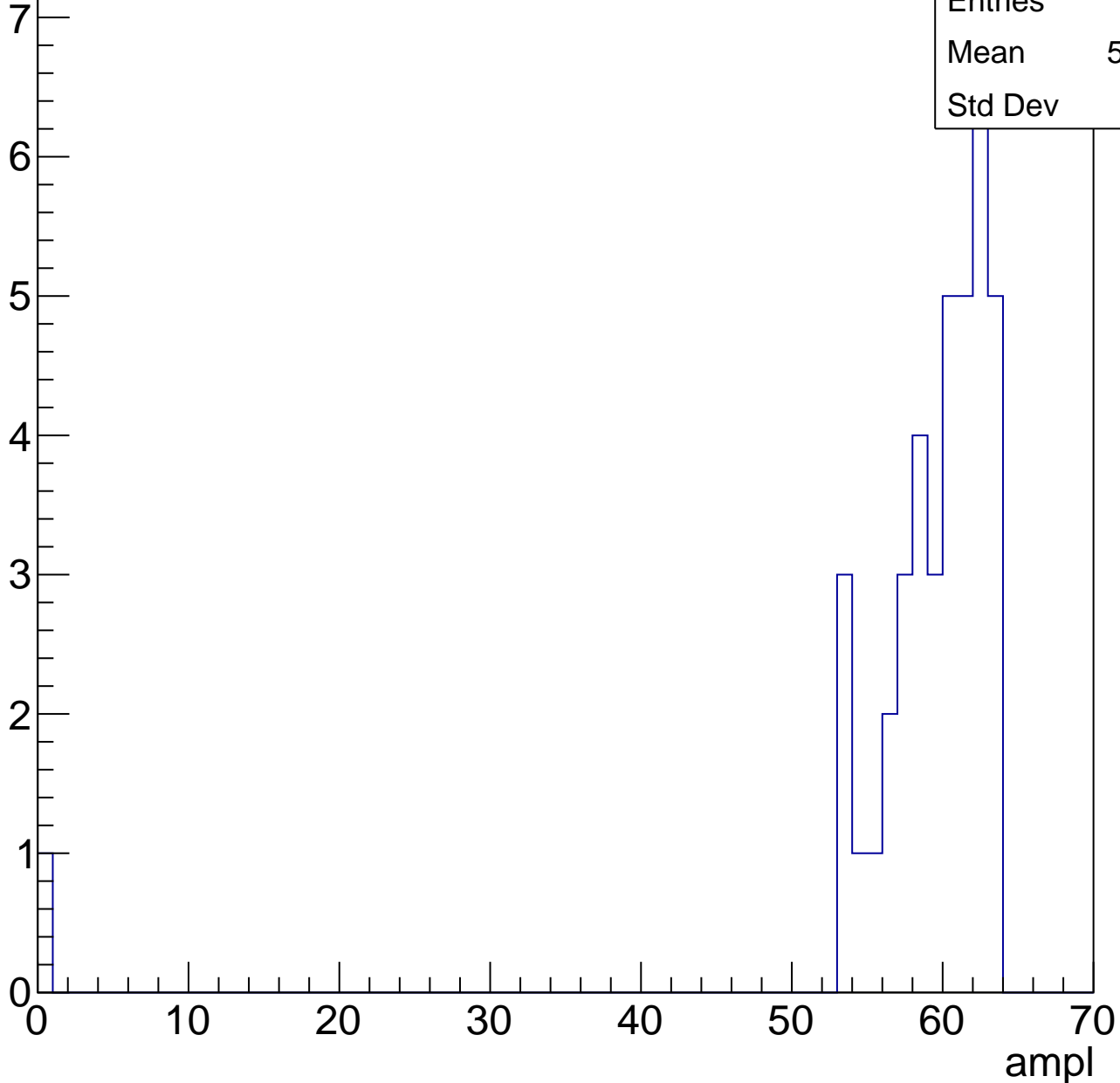


# B1L102S, U25-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

Entries	40
Mean	57.85
Std Dev	9.72



# B1L102S, U25-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	61.33
Std Dev	1.764

ampl

0 10 20 30 40 50 60 70



# B1L102S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L102S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#11, port A2

Entry

