

B1L104S, U20-ch0

calib_packv5_033123_0516.root, FC#4, port A1

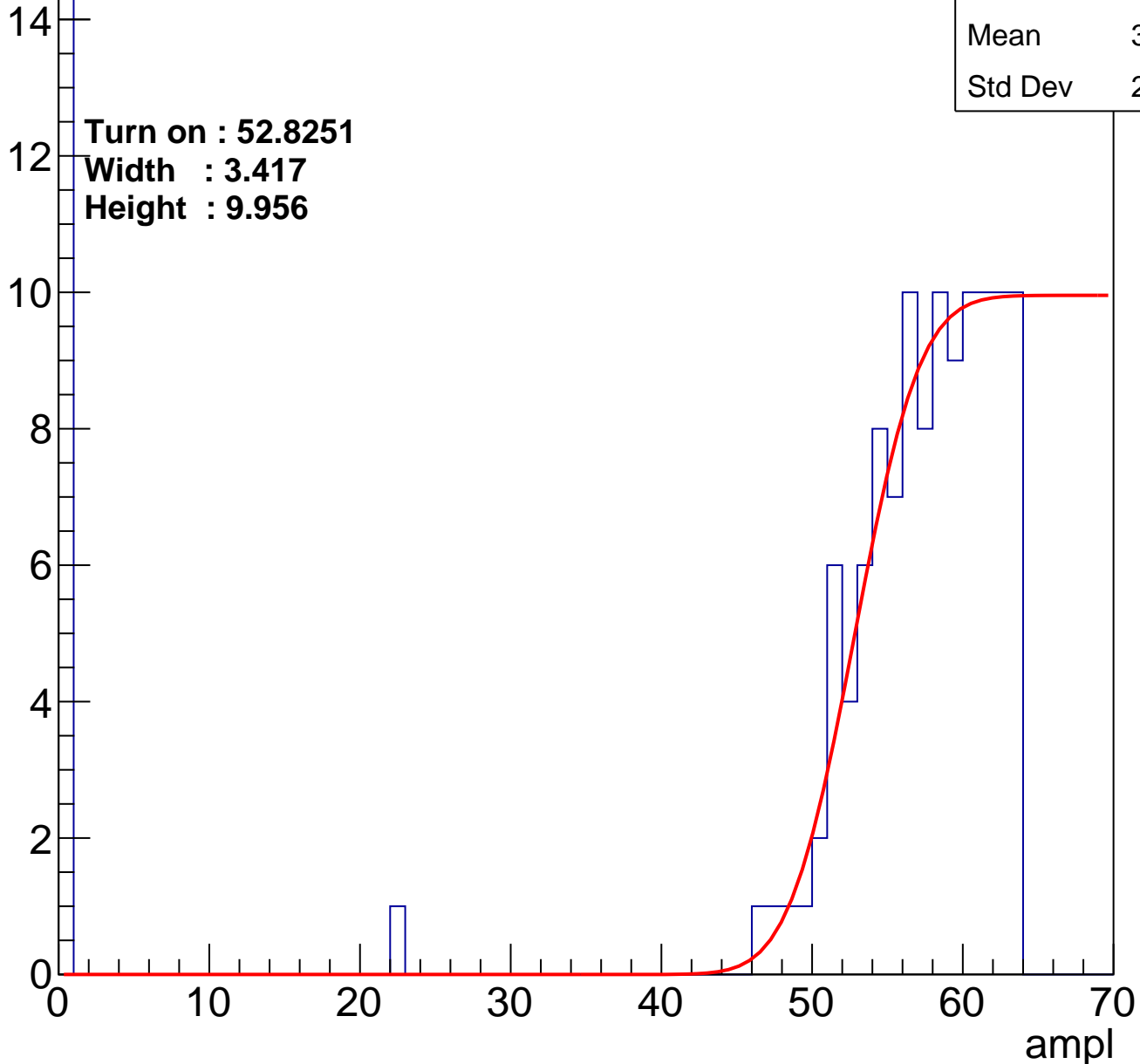
Entries	207
Mean	31.63
Std Dev	28.56

Turn on : 52.8251

Width : 3.417

Height : 9.956

Entry



B1L104S, U20-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	34.04
Std Dev	28.62

Turn on : 54.5866

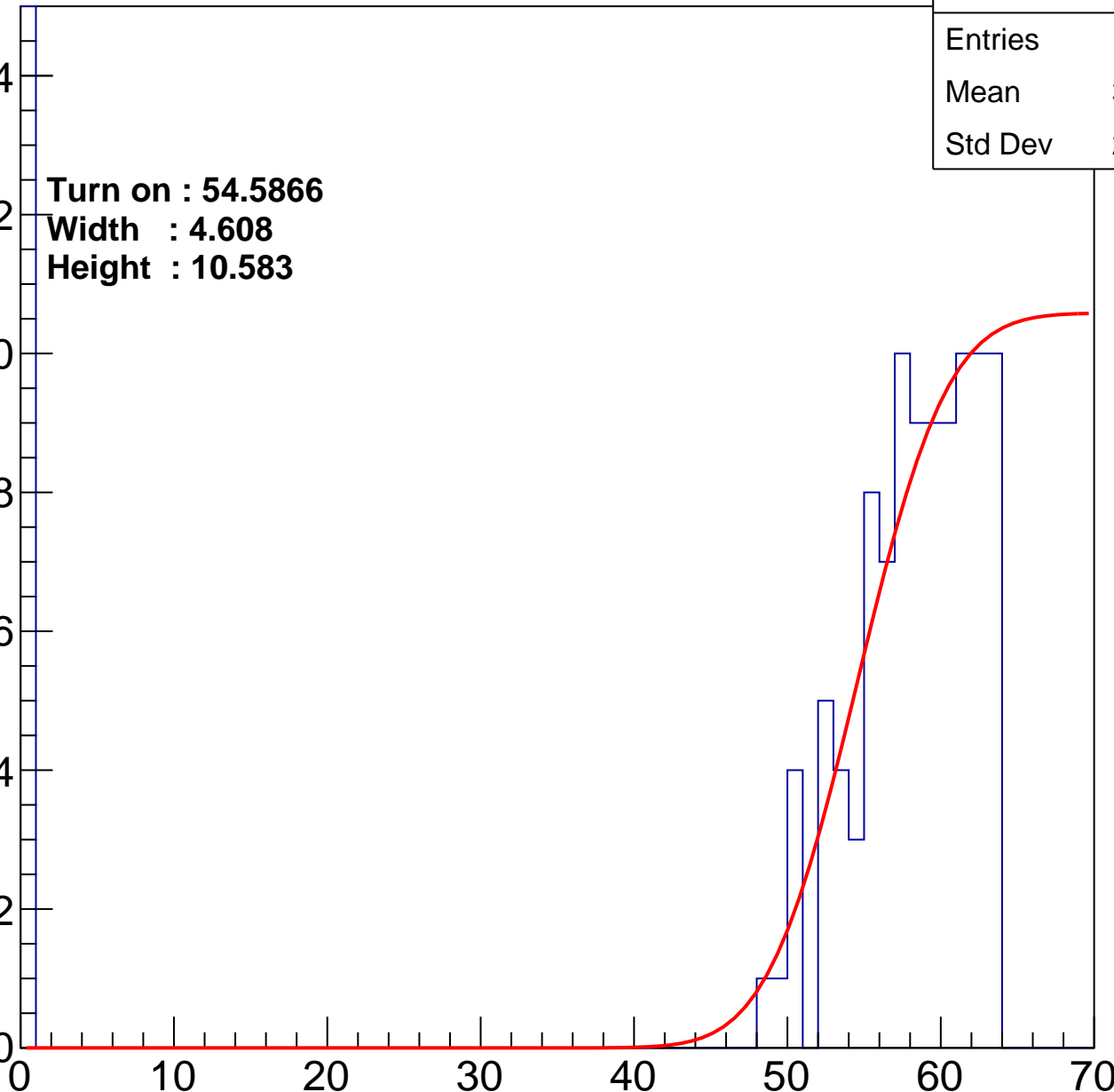
Width : 4.608

Height : 10.583

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	31.23
Std Dev	28.71

Turn on : 53.0302

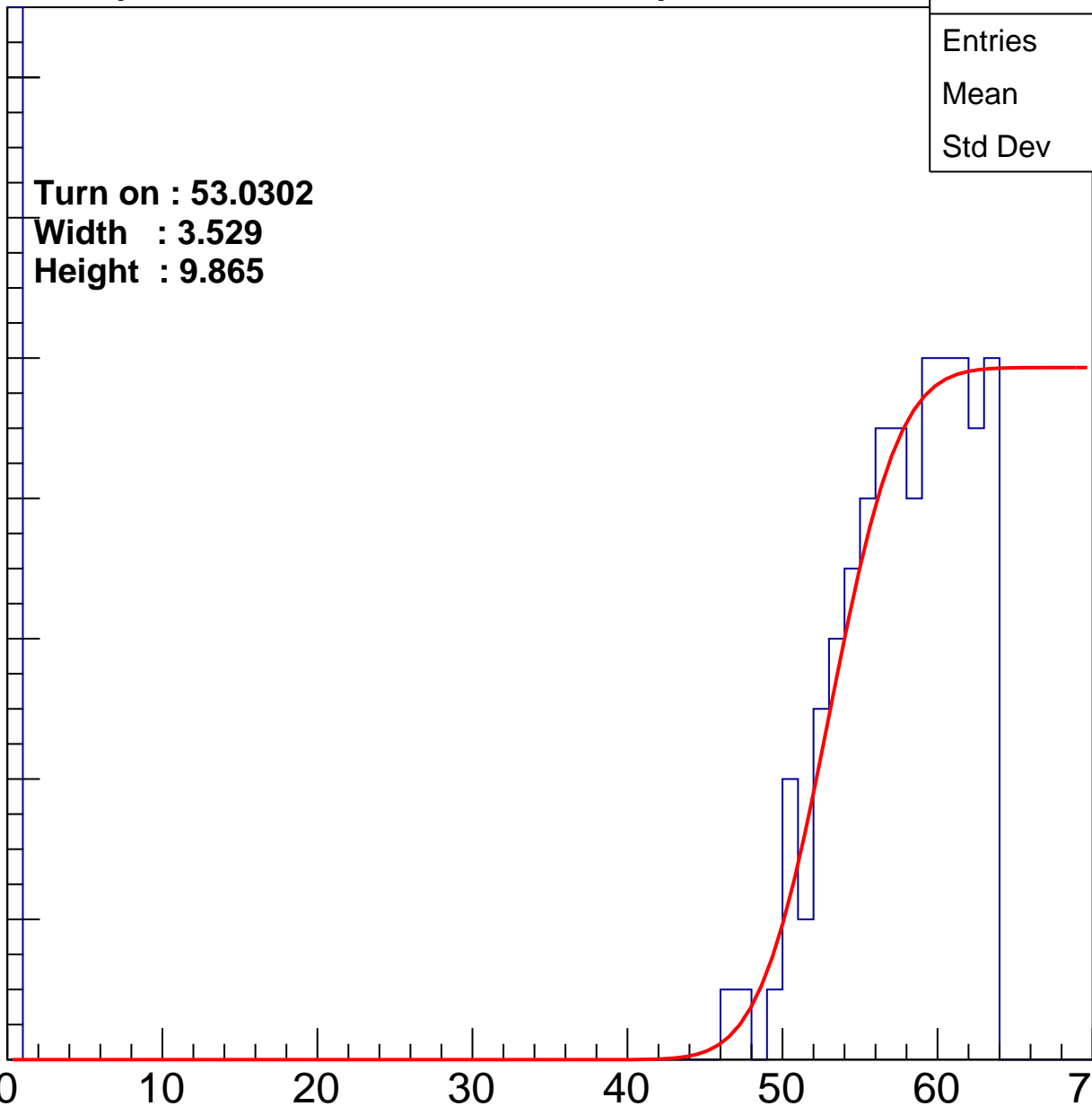
Width : 3.529

Height : 9.865

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.42
Std Dev	29.08

Turn on : 54.2168

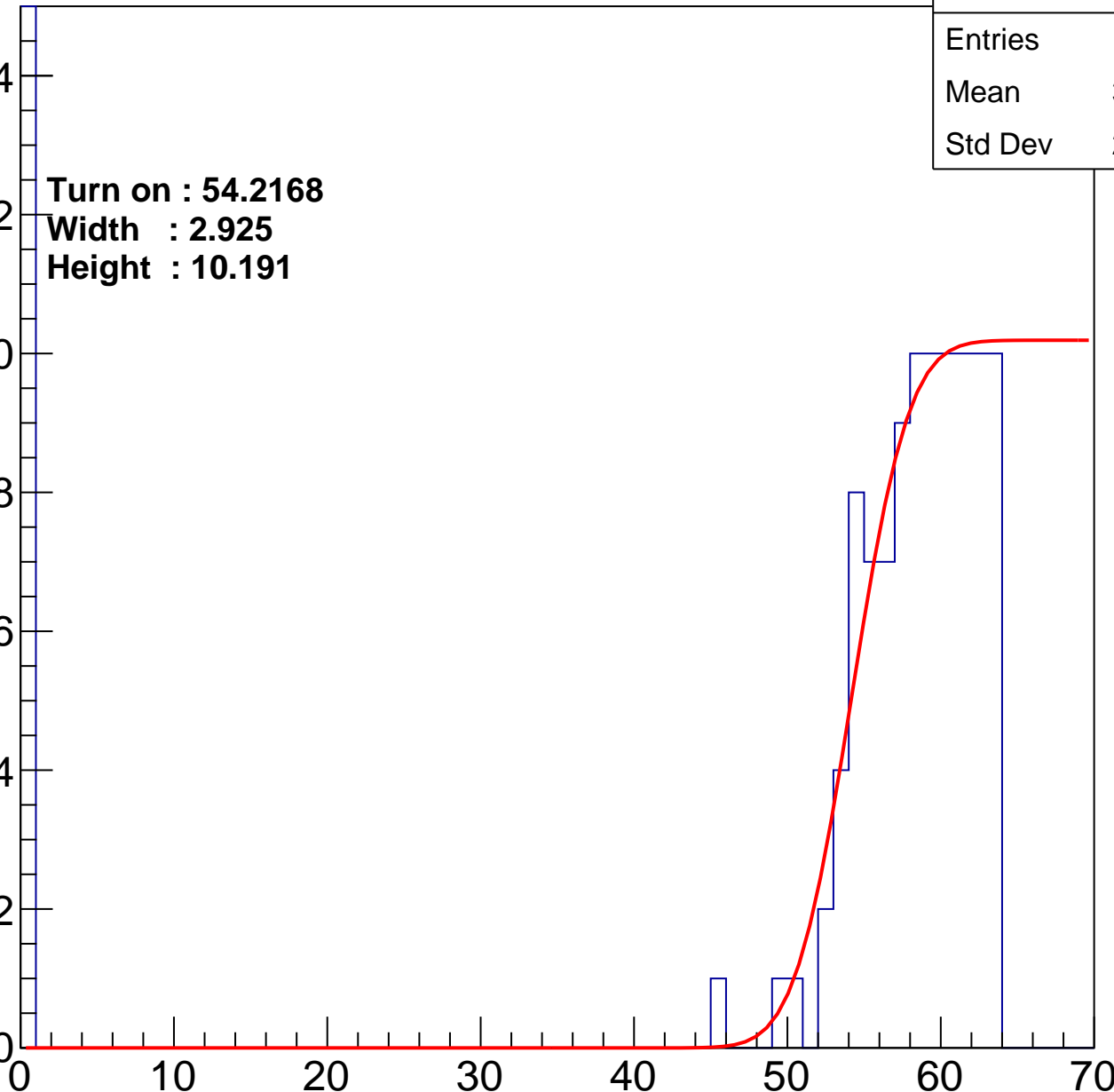
Width : 2.925

Height : 10.191

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.19
Std Dev	28.74

Turn on : 53.6823

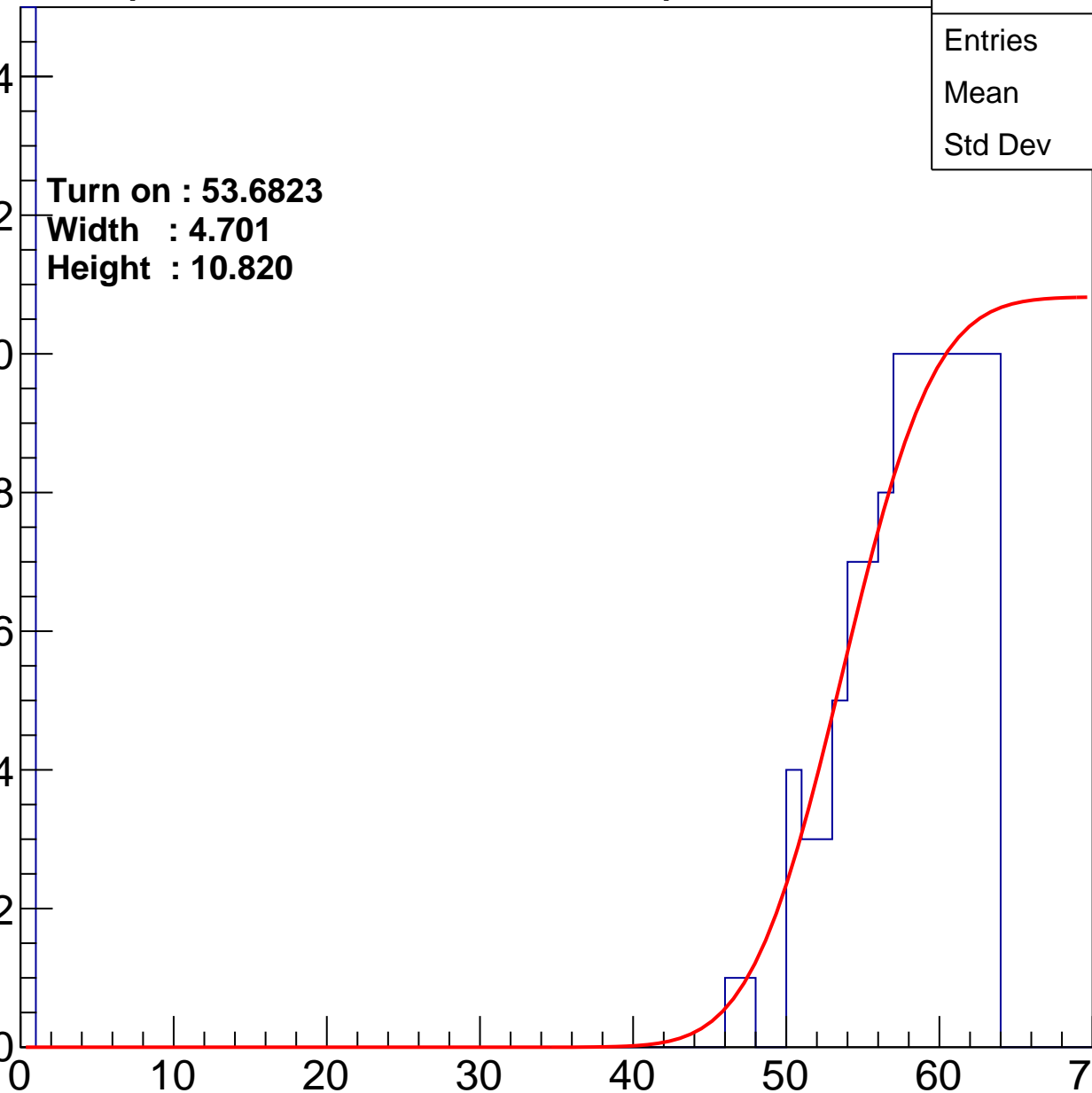
Width : 4.701

Height : 10.820

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	37.09
Std Dev	27.48

Turn on : 52.1880

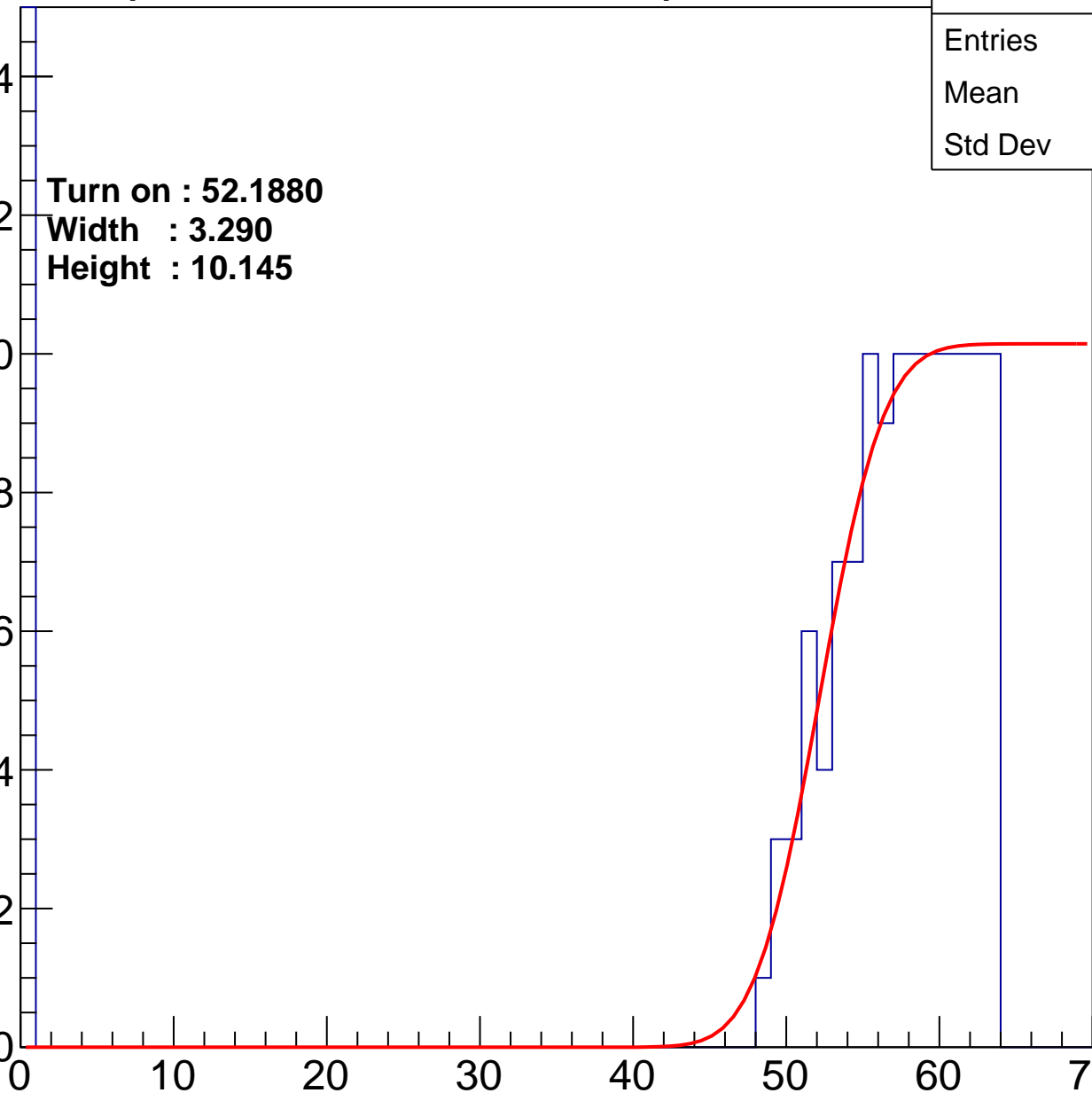
Width : 3.290

Height : 10.145

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	30.93
Std Dev	29.18

Turn on : 55.7735

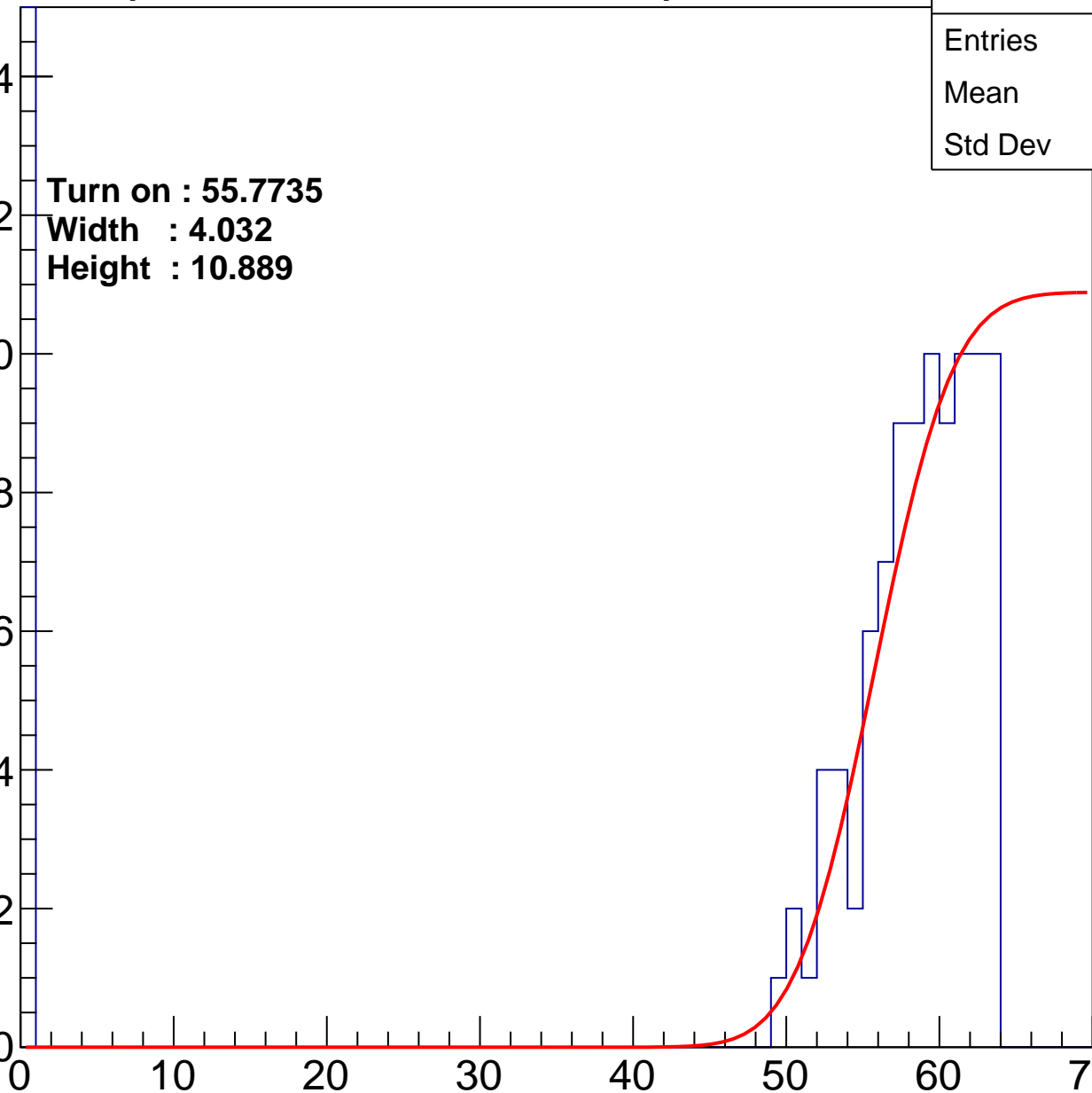
Width : 4.032

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.49
Std Dev	28.39

Turn on : 53.6589

Width : 3.444

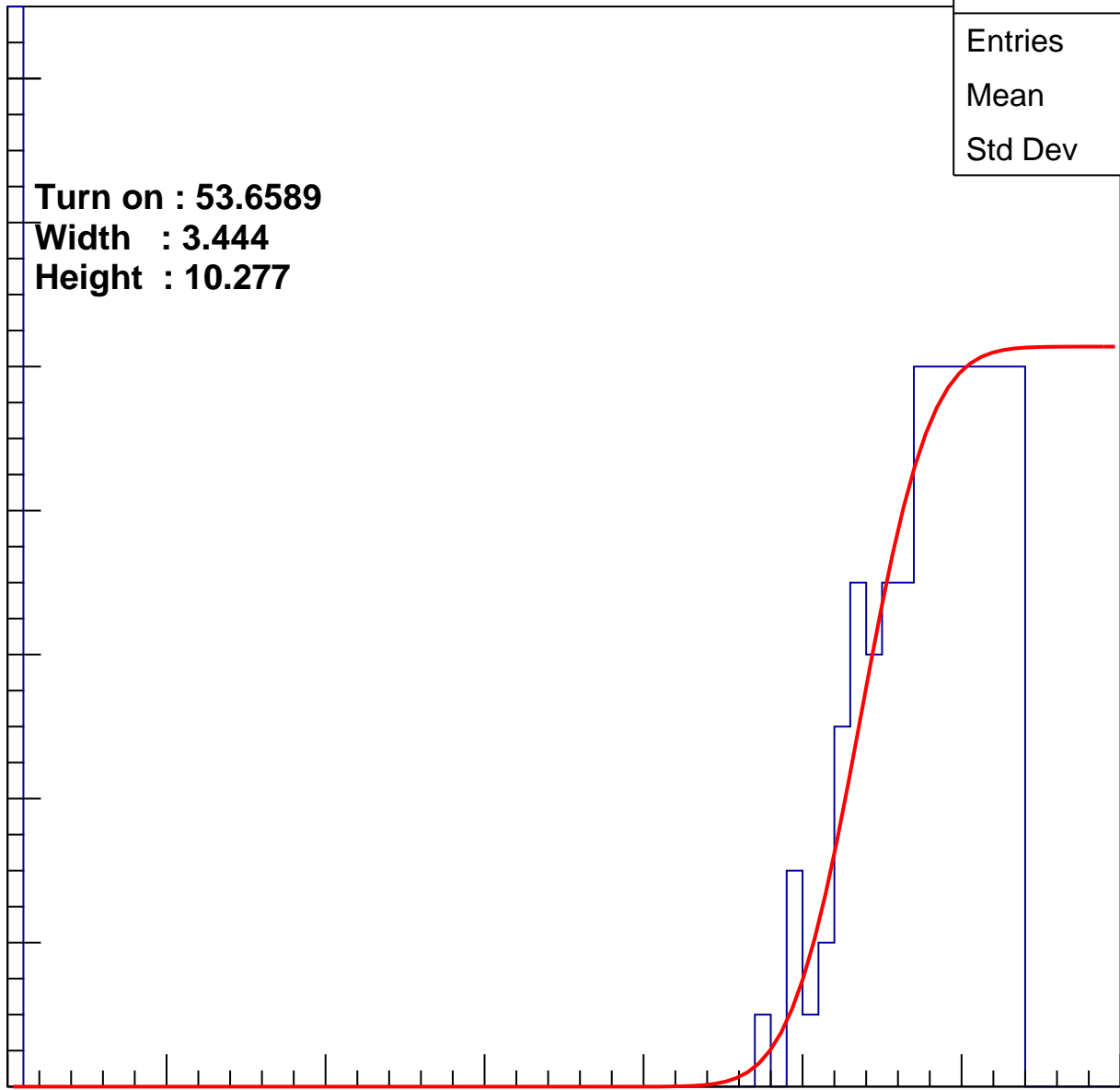
Height : 10.277

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U20-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	27.9
Std Dev	29.35

Turn on : 55.4784

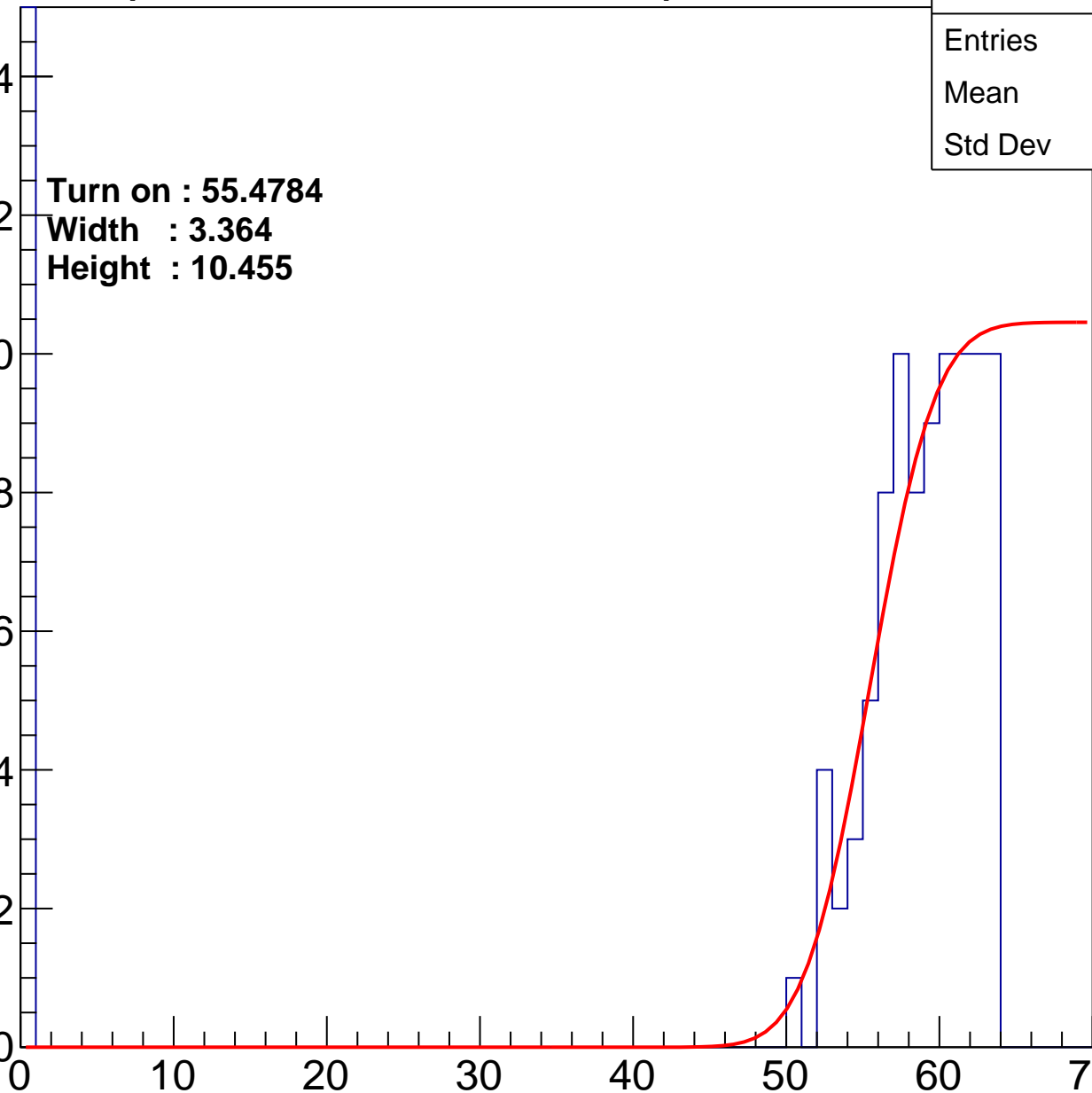
Width : 3.364

Height : 10.455

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch9

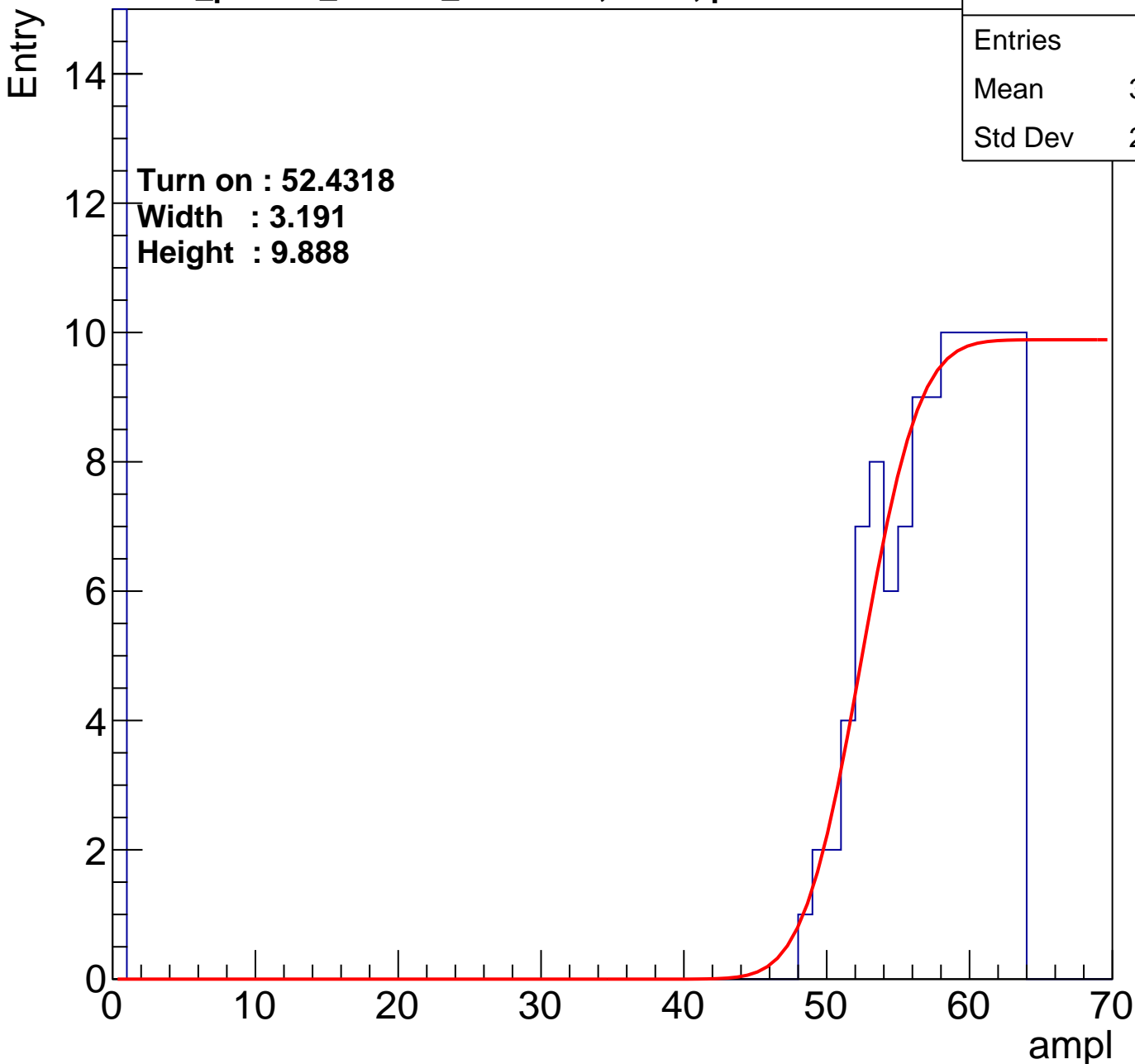
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	36.63
Std Dev	27.72

Turn on : 52.4318

Width : 3.191

Height : 9.888



B1L104S, U20-ch10

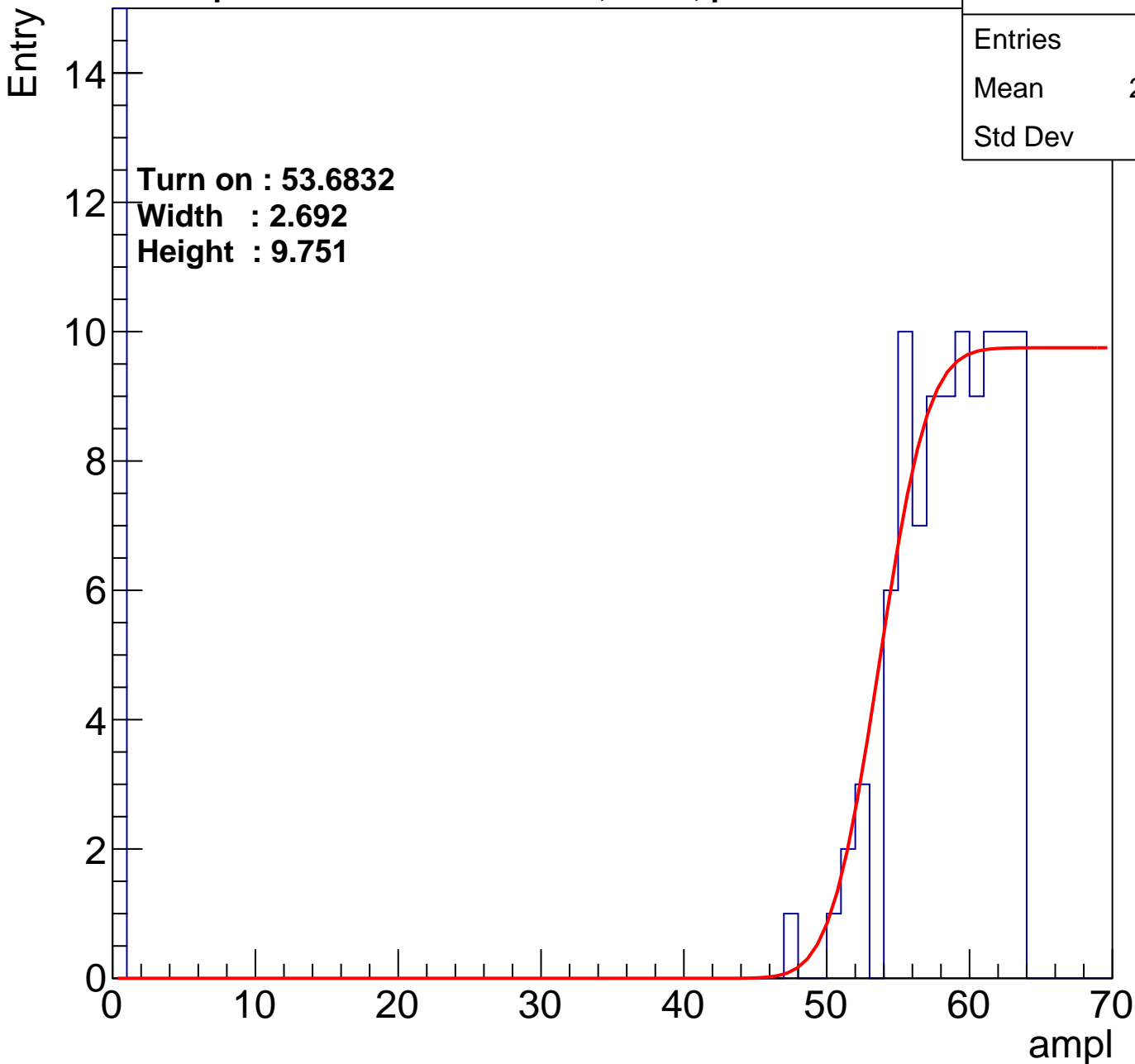
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	28.52
Std Dev	29.2

Turn on : 53.6832

Width : 2.692

Height : 9.751



B1L104S, U20-ch11

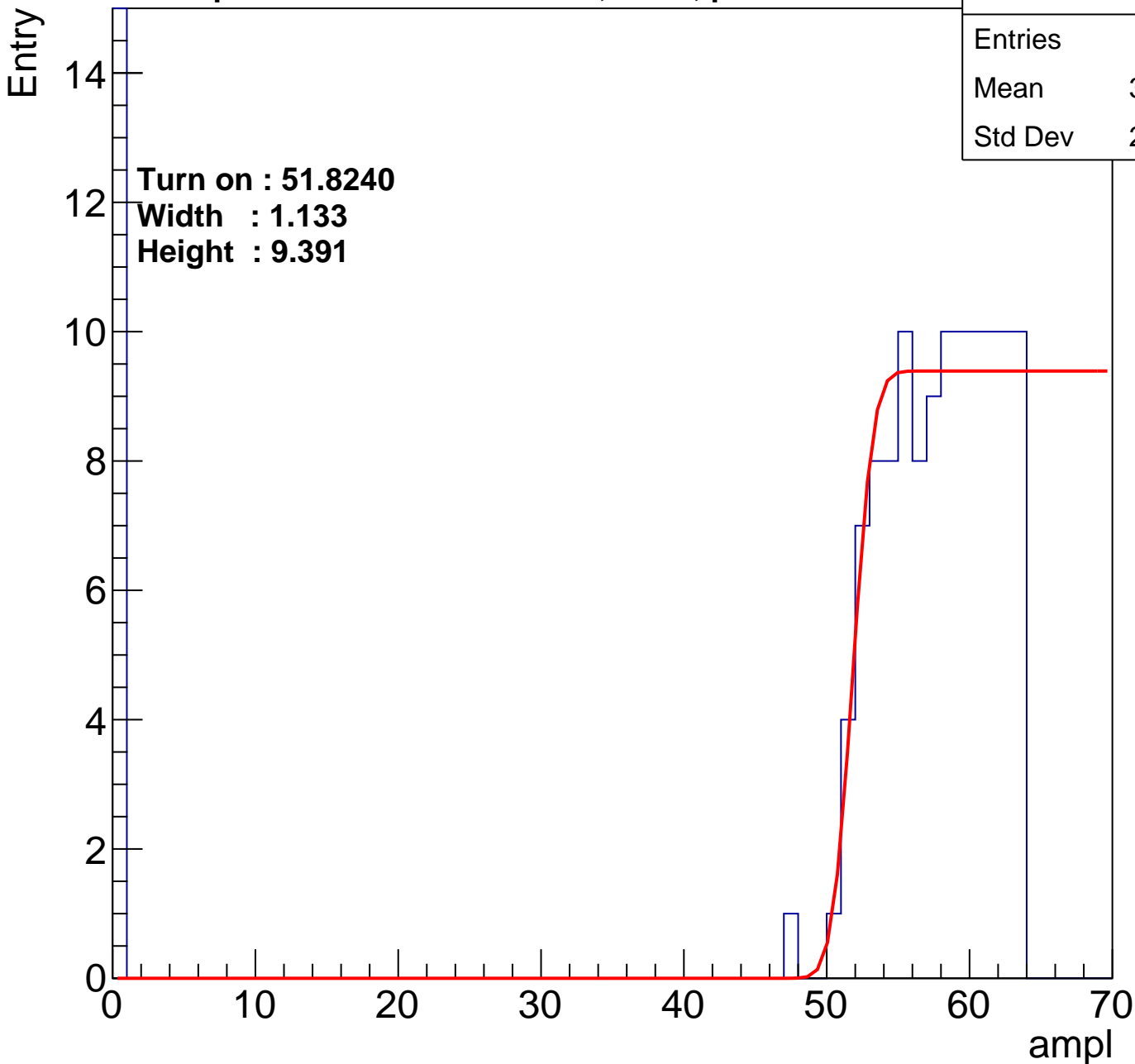
calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	39.65
Std Dev	26.73

Turn on : 51.8240

Width : 1.133

Height : 9.391



B1L104S, U20-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	31.49
Std Dev	29.36

Turn on : 55.7314

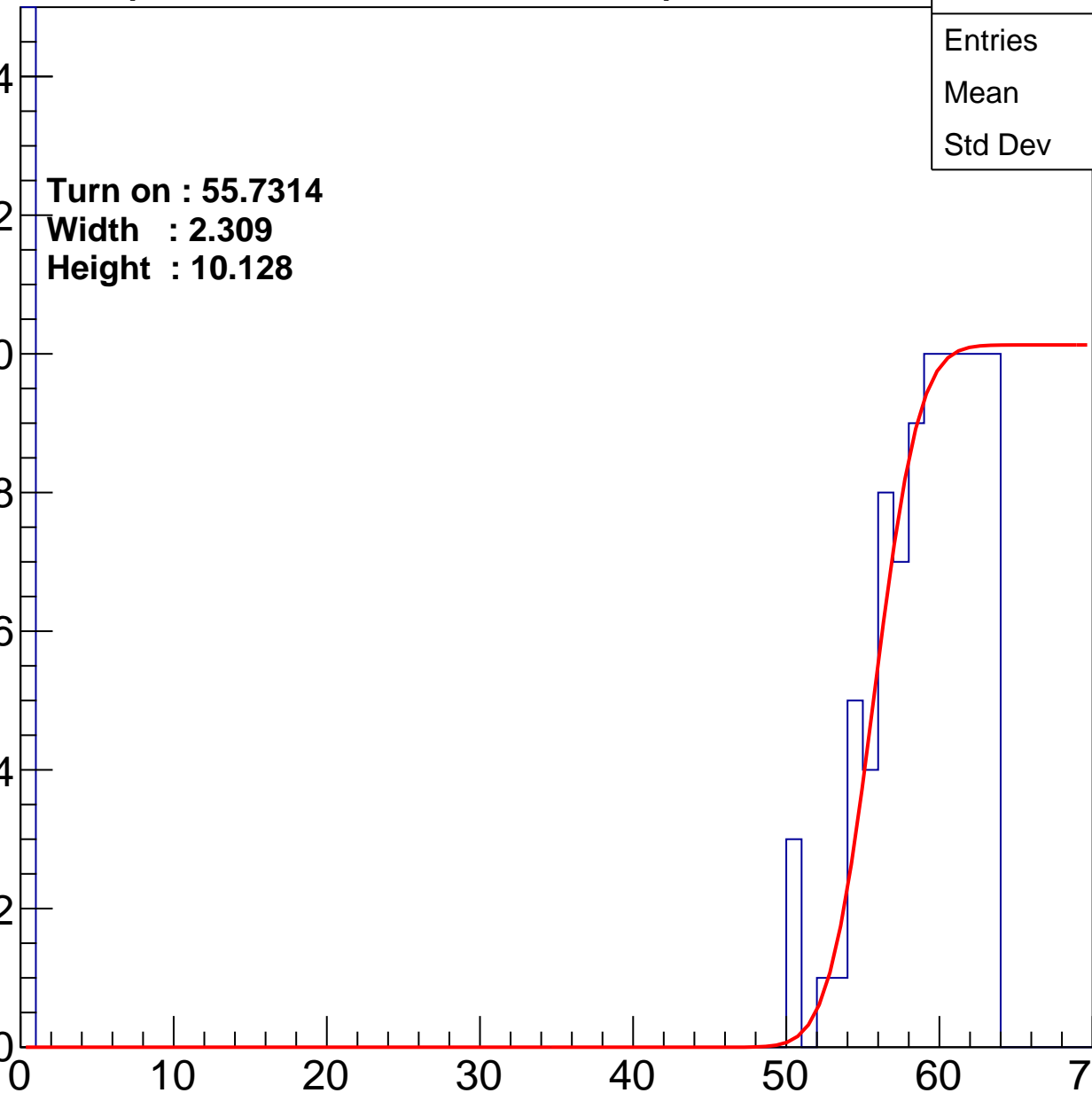
Width : 2.309

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	31.92
Std Dev	29.1

Turn on : 54.2177

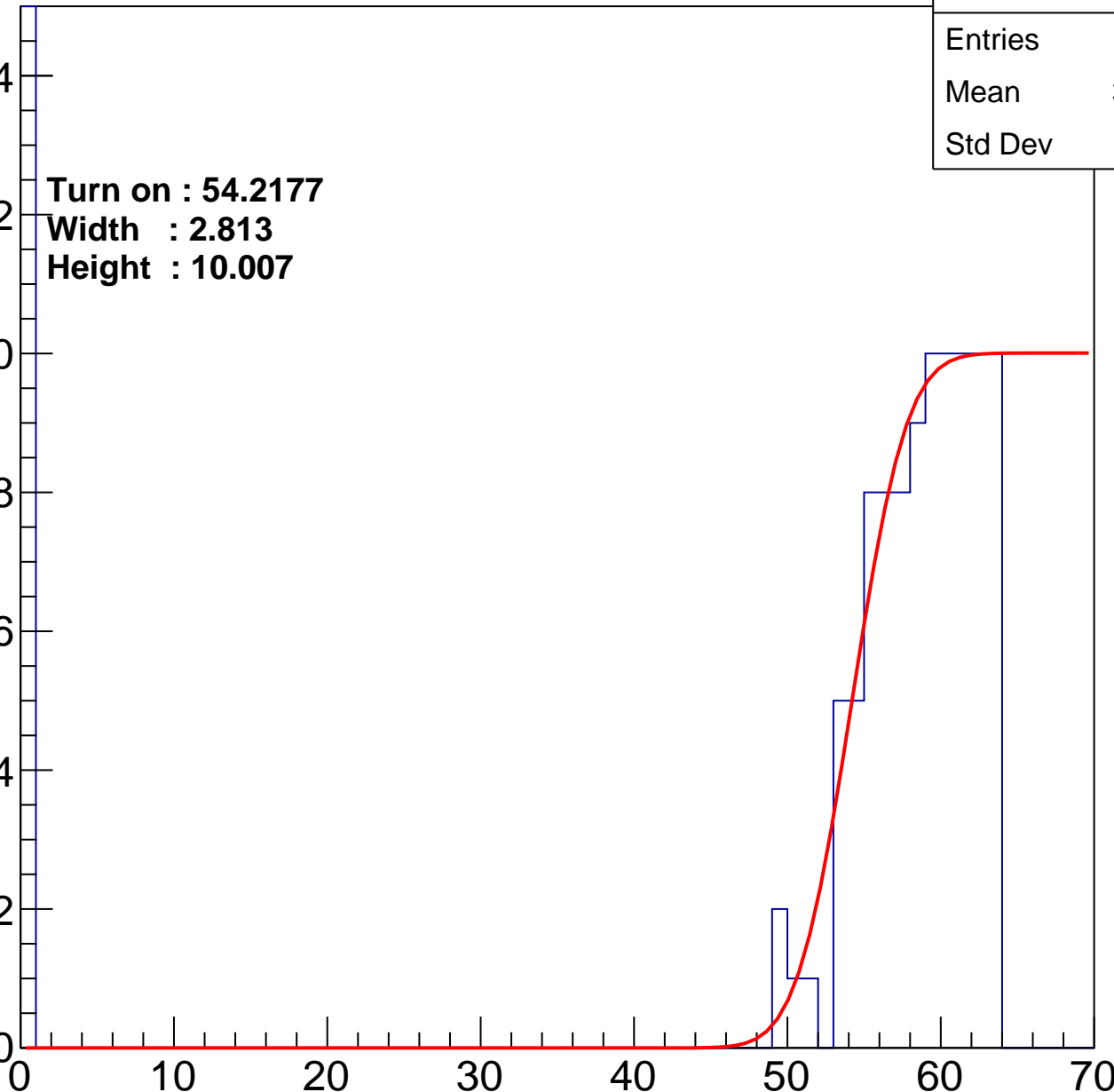
Width : 2.813

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	35.71
Std Dev	27.46

Turn on : 50.8715

Width : 2.910

Height : 10.045

Entry

14

12

10

8

6

4

2

0

0

10

20

30

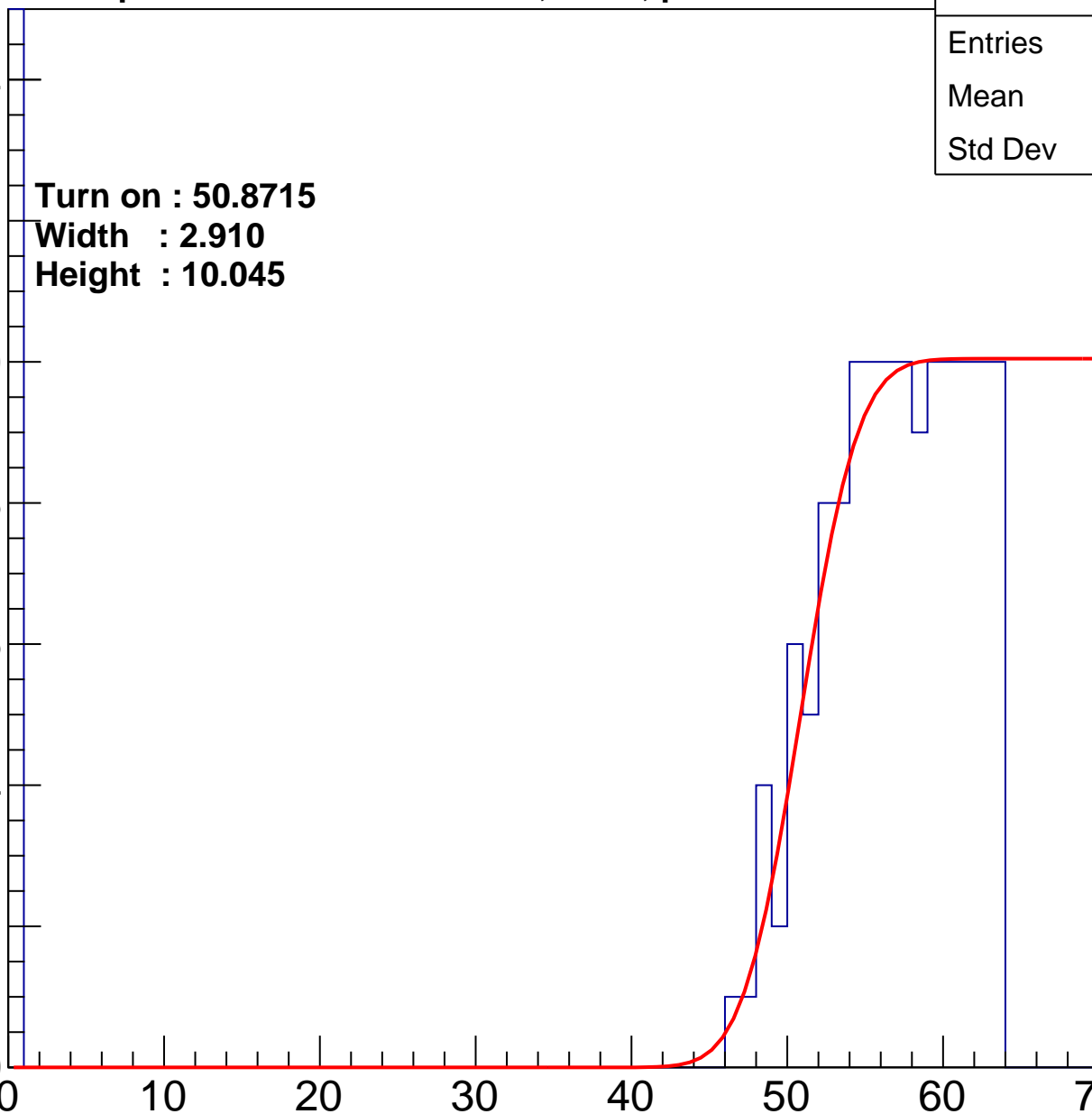
40

50

60

70

ampl



B1L104S, U20-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	33.46
Std Dev	29.23

Turn on : 55.7229

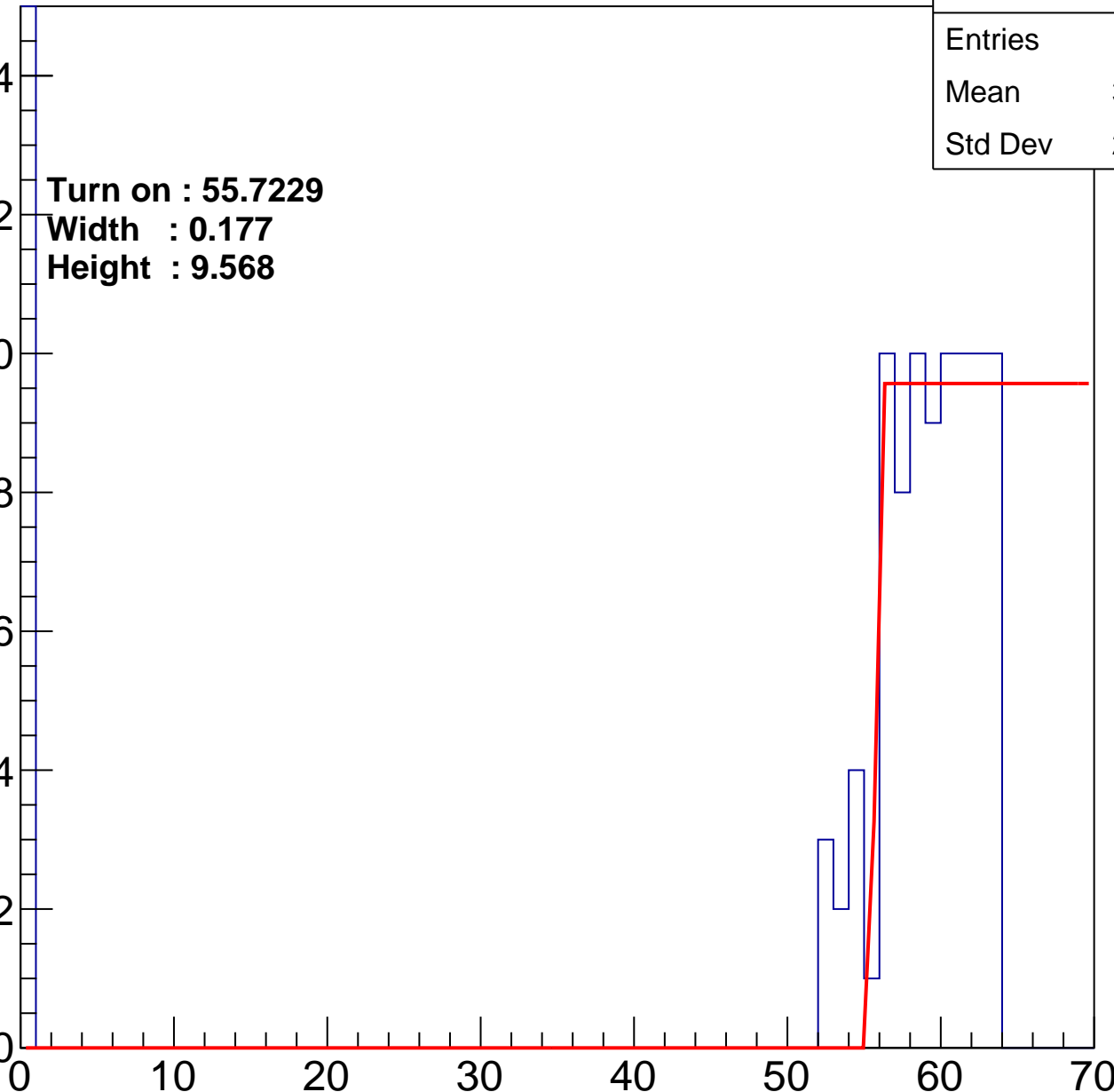
Width : 0.177

Height : 9.568

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch16

calib_packv5_033123_0516.root, FC#4, port A1

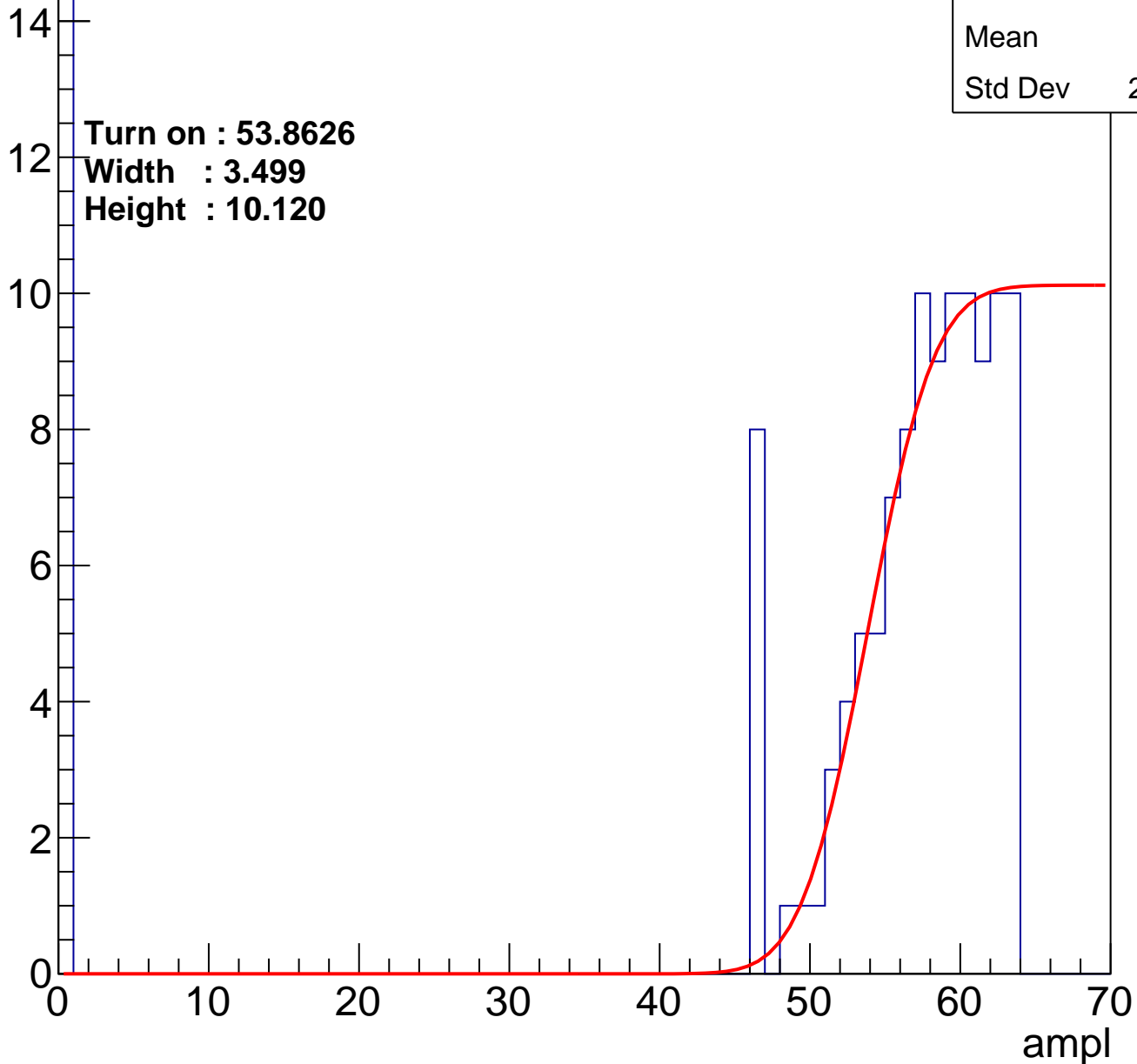
Entry

Entries	204
Mean	31
Std Dev	28.59

Turn on : 53.8626

Width : 3.499

Height : 10.120



B1L104S, U20-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	32.84
Std Dev	28.99

Turn on : 55.0435

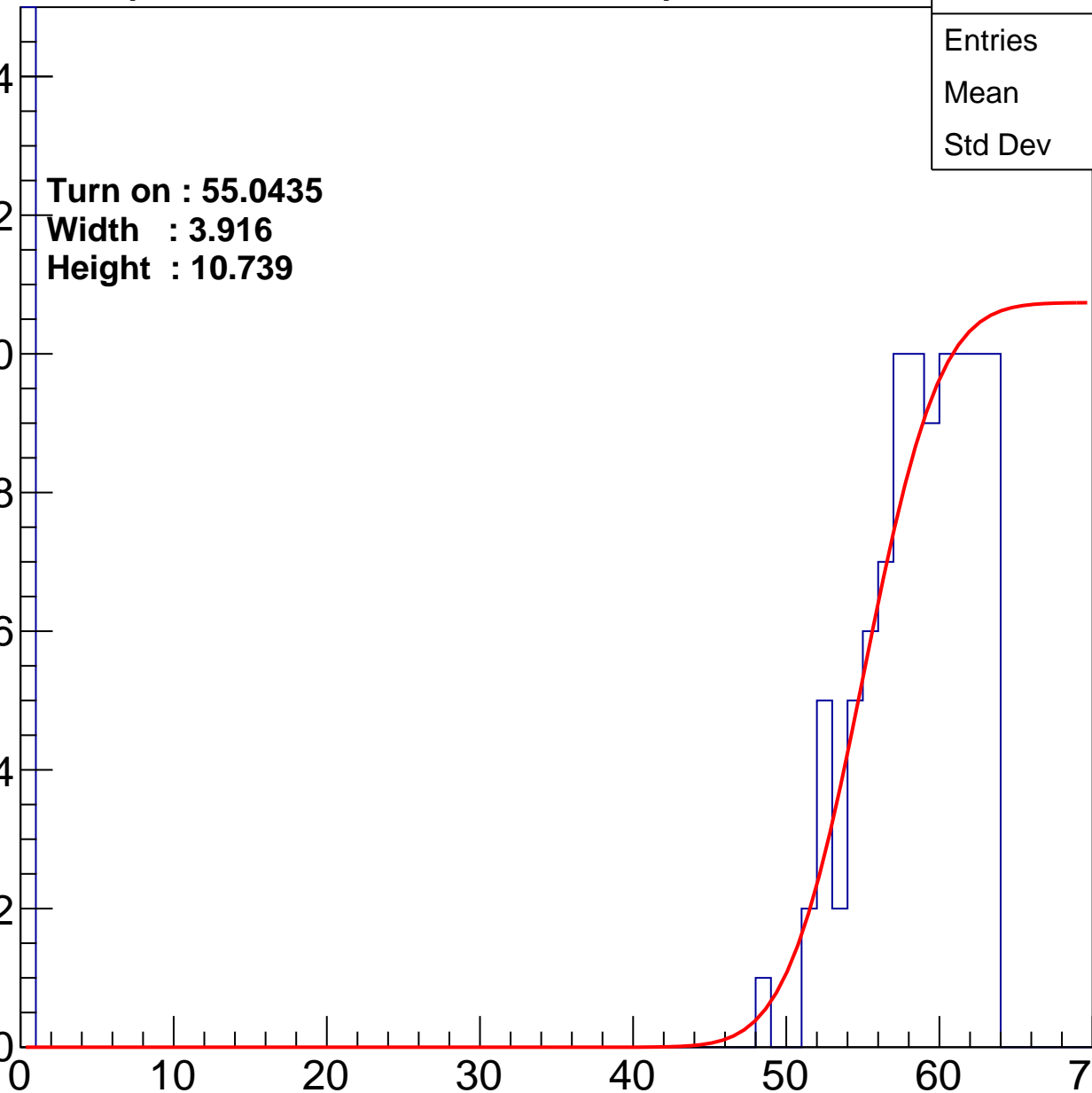
Width : 3.916

Height : 10.739

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	36.61
Std Dev	27.71

Turn on : 53.1982

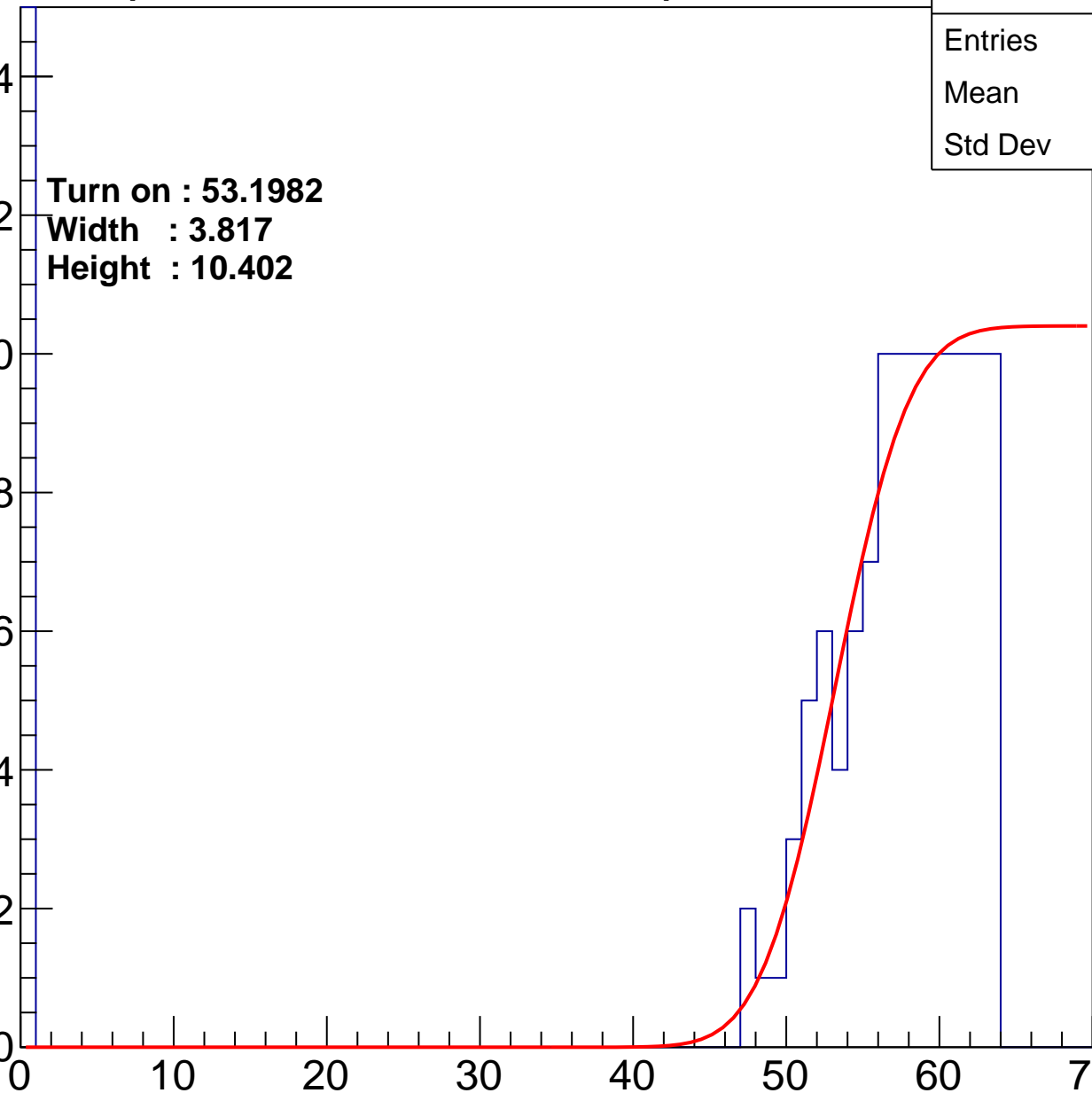
Width : 3.817

Height : 10.402

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	39.46
Std Dev	26.66

Turn on : 52.4883

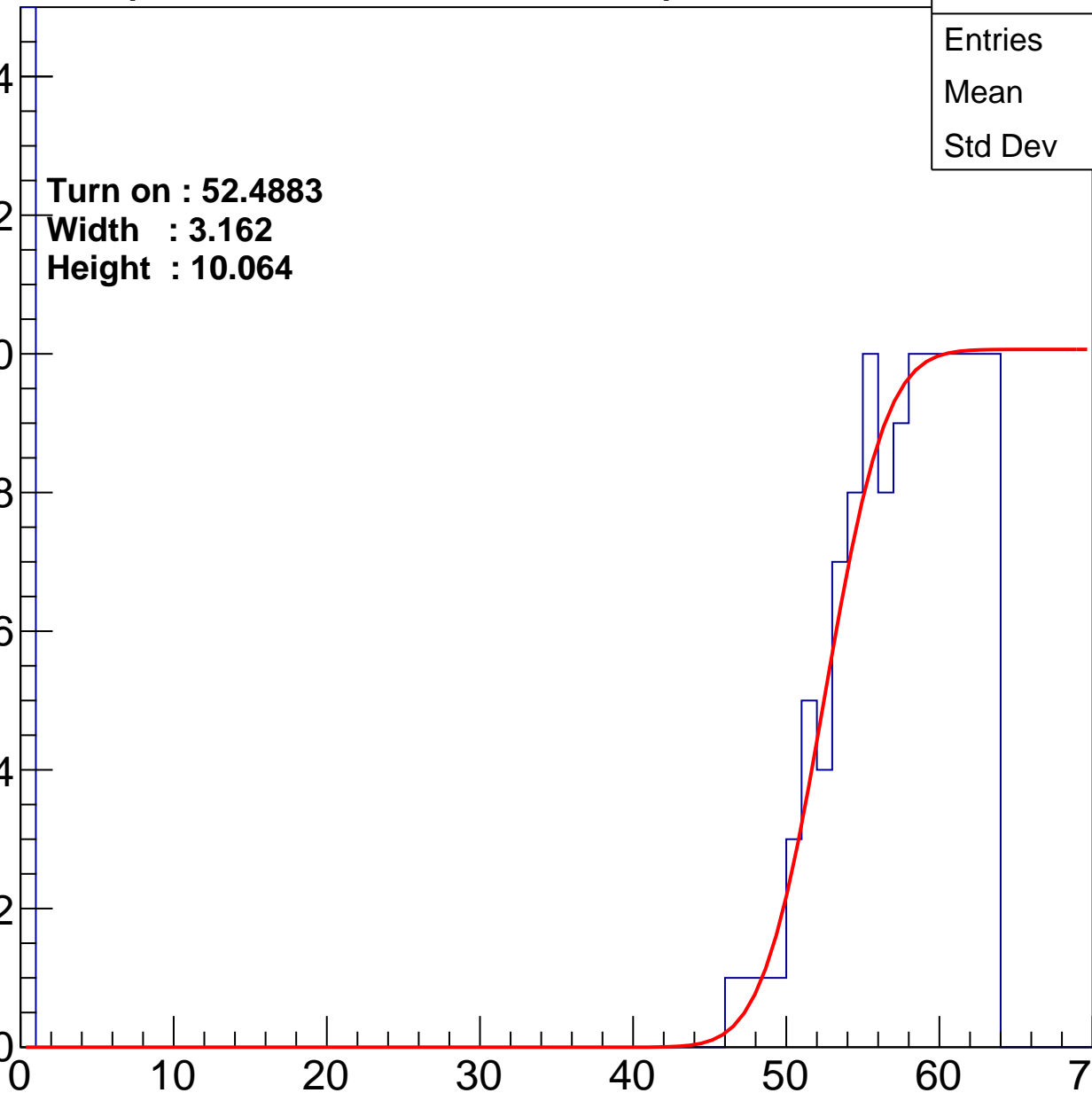
Width : 3.162

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.3
Std Dev	28.3

Turn on : 54.2926

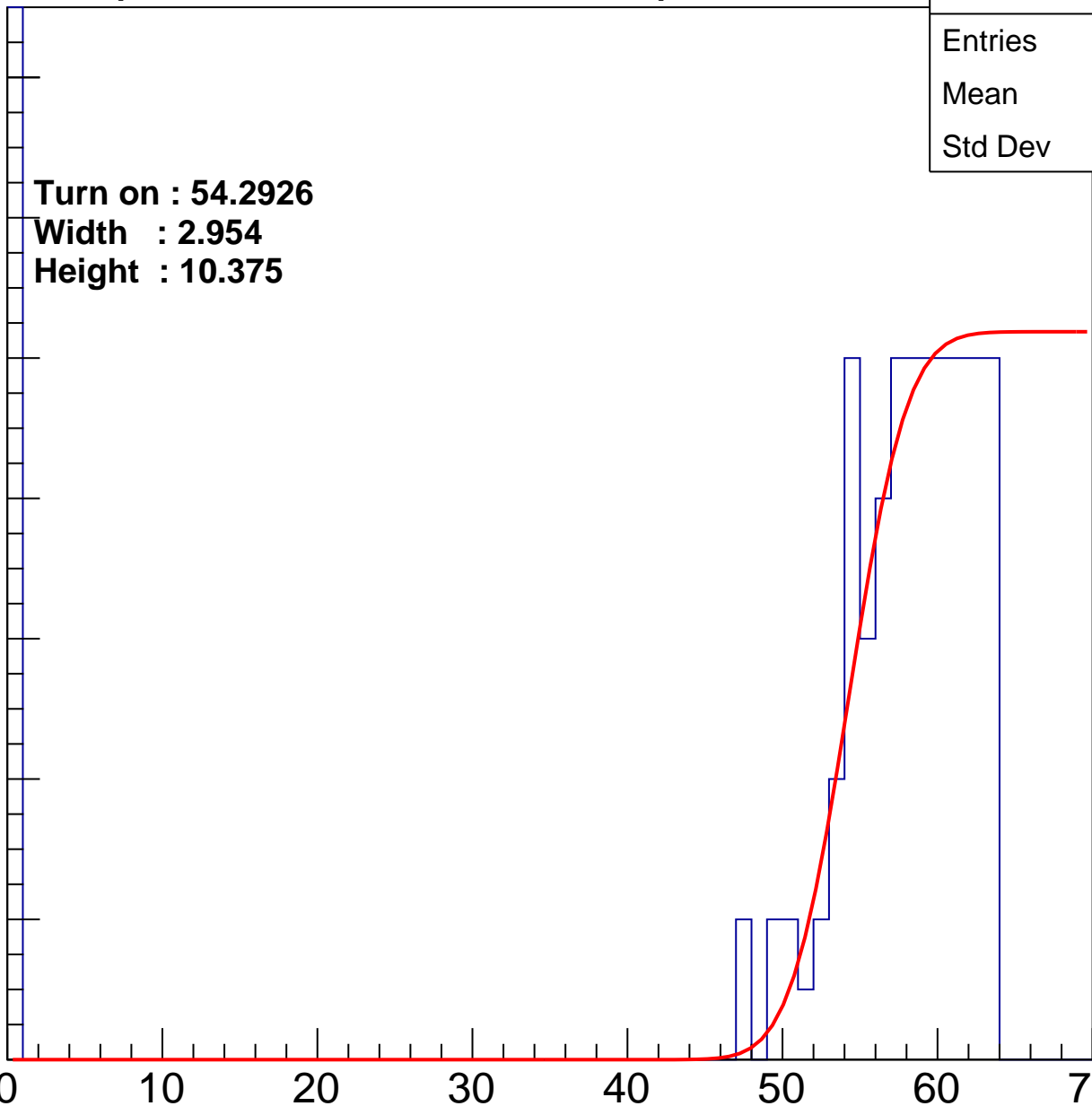
Width : 2.954

Height : 10.375

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	34.34
Std Dev	28.45

Turn on : 53.4847

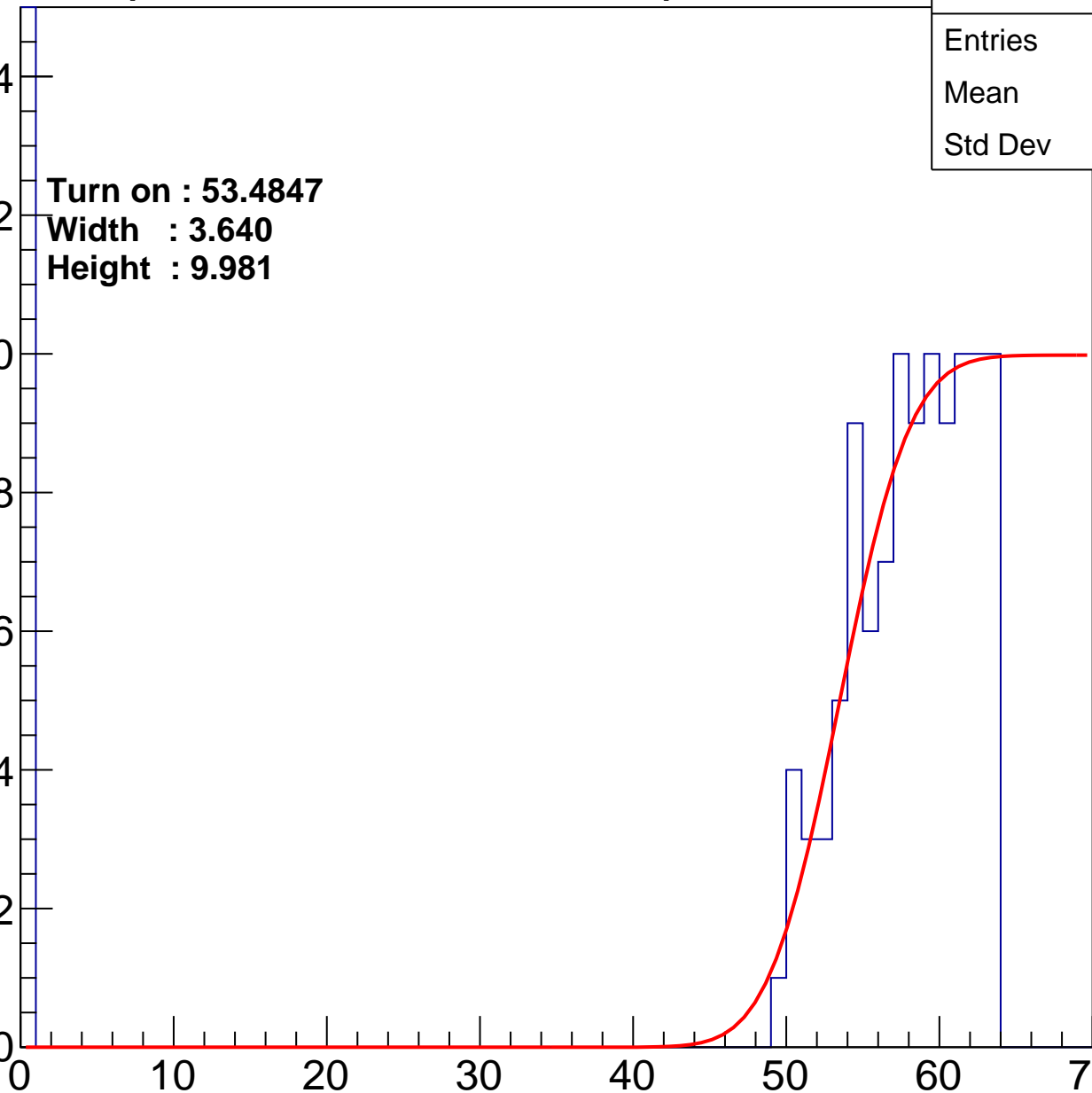
Width : 3.640

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	38.41
Std Dev	27.02

Turn on : 52.0520

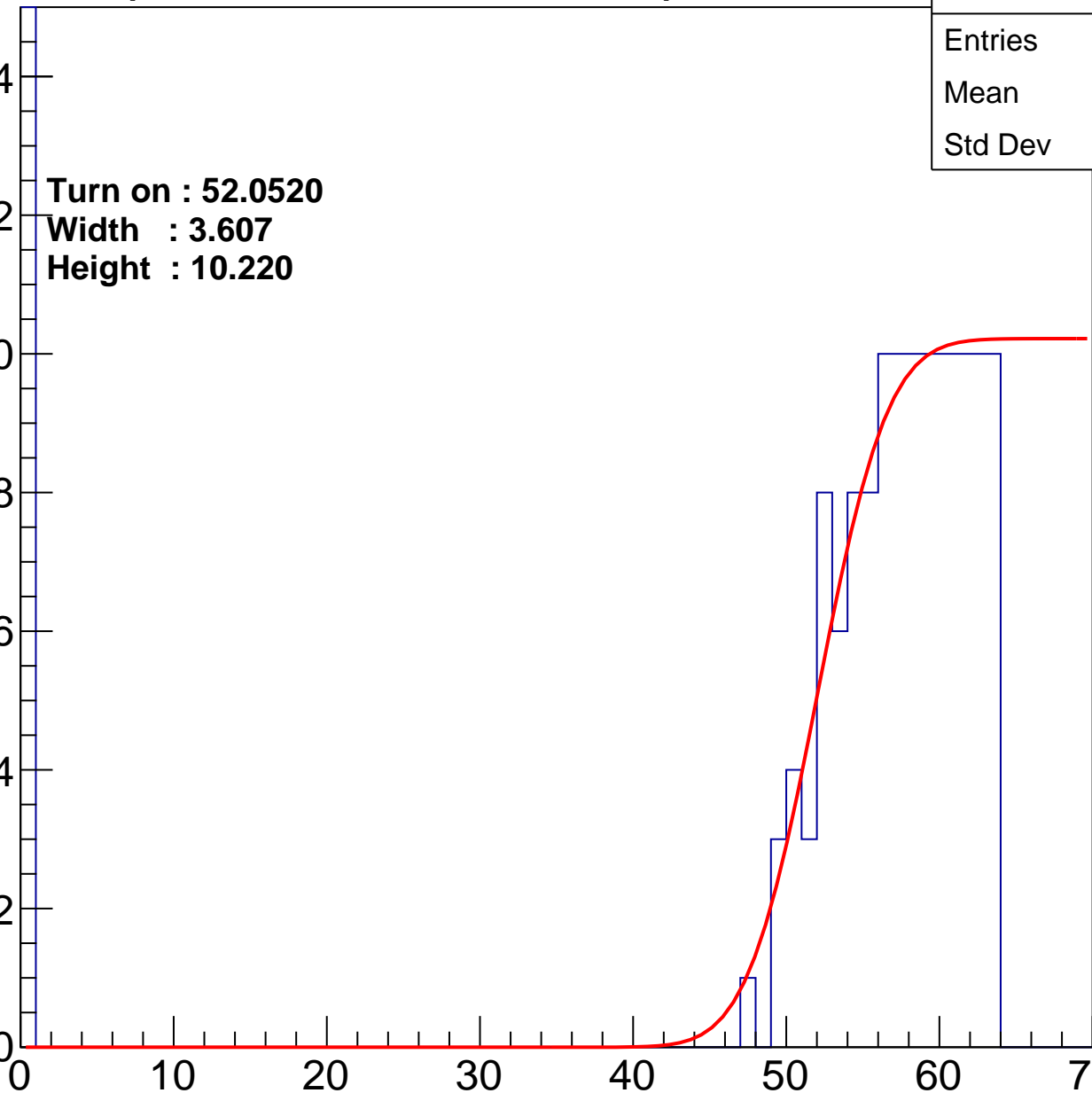
Width : 3.607

Height : 10.220

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.49
Std Dev	28.25

Turn on : 53.2745

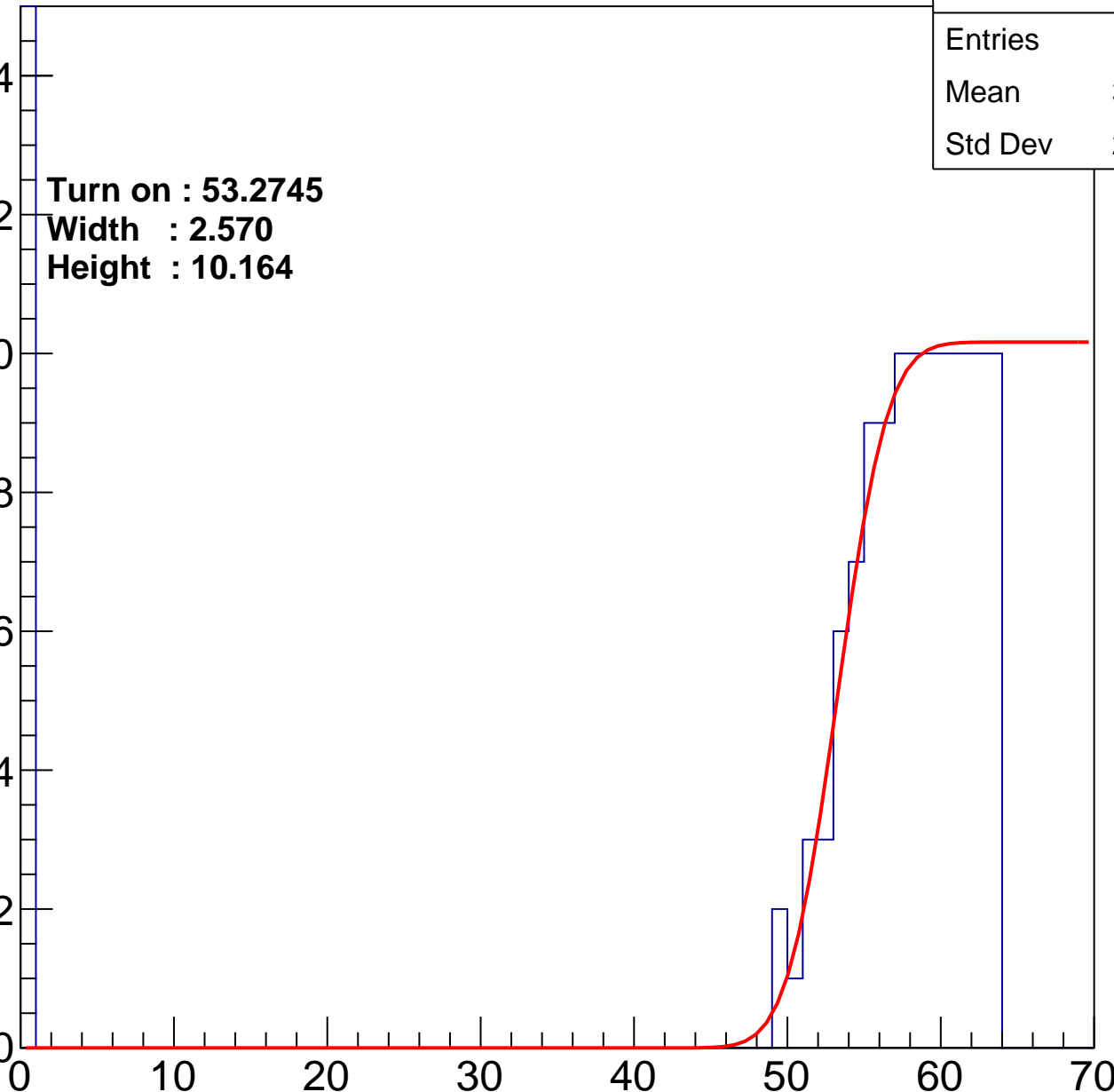
Width : 2.570

Height : 10.164

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	36.03
Std Dev	27.47

Turn on : 50.5486

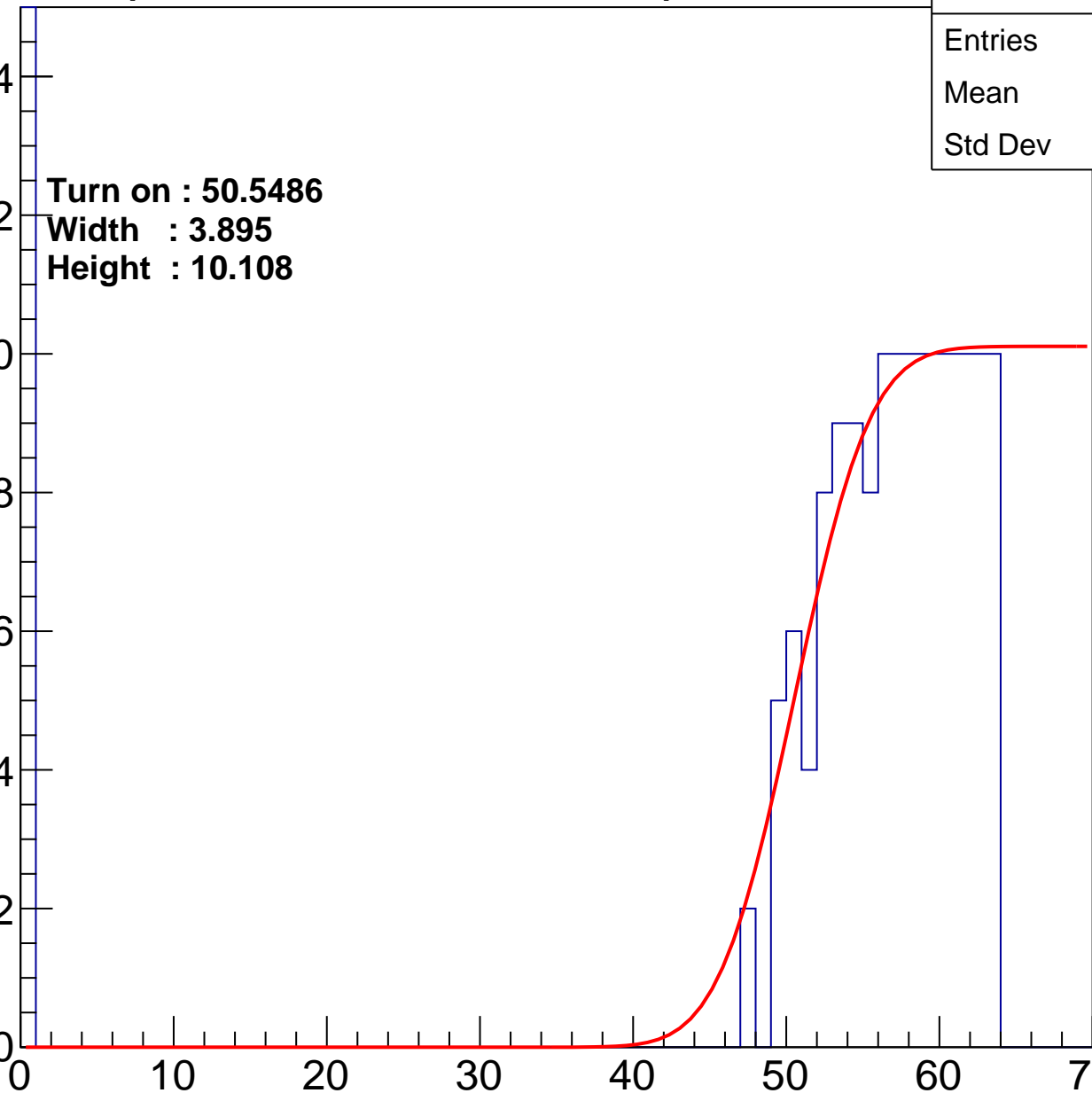
Width : 3.895

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	34.9
Std Dev	28.68

Turn on : 54.5795

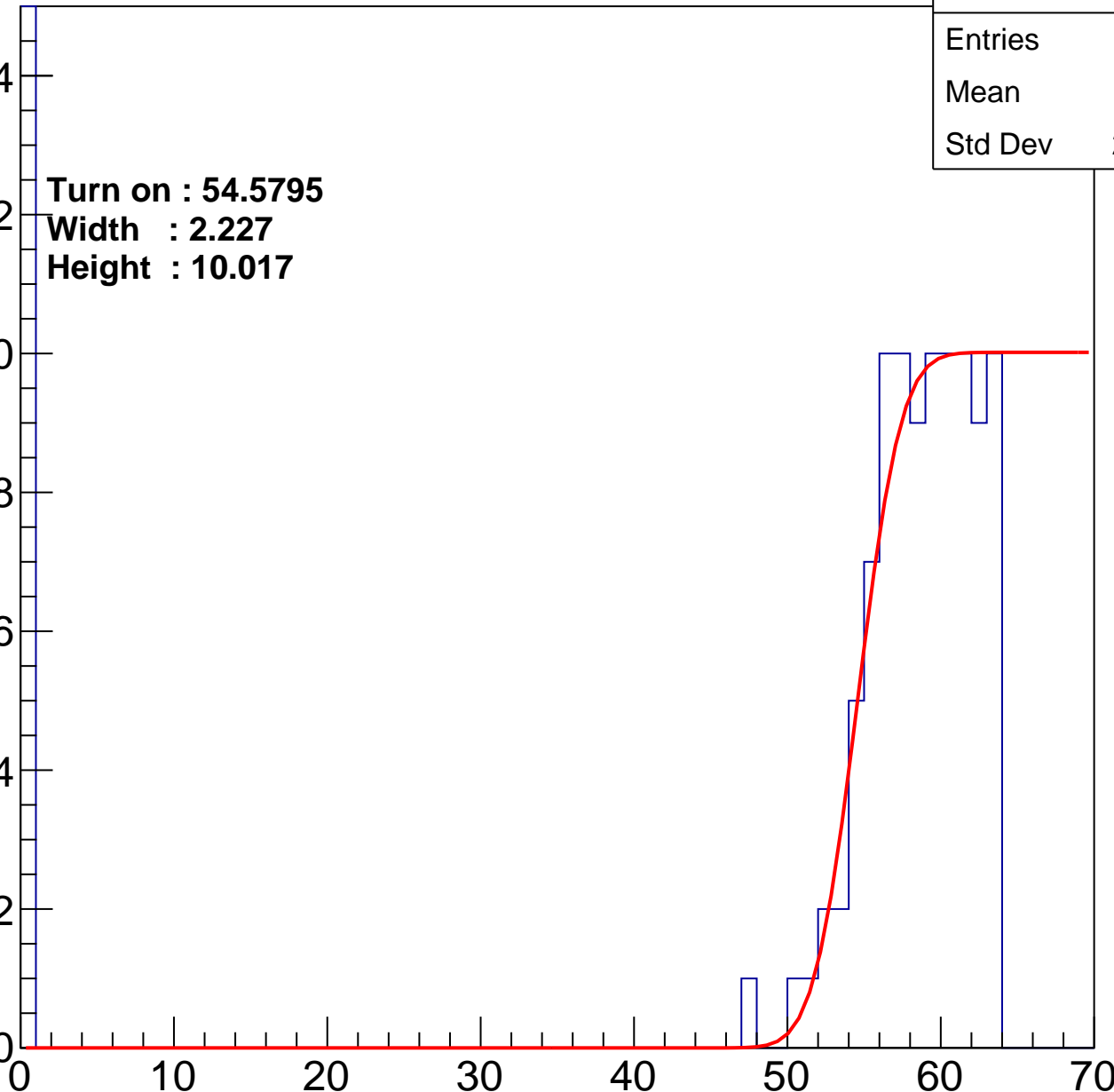
Width : 2.227

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	35.81
Std Dev	27.77

Turn on : 51.7422

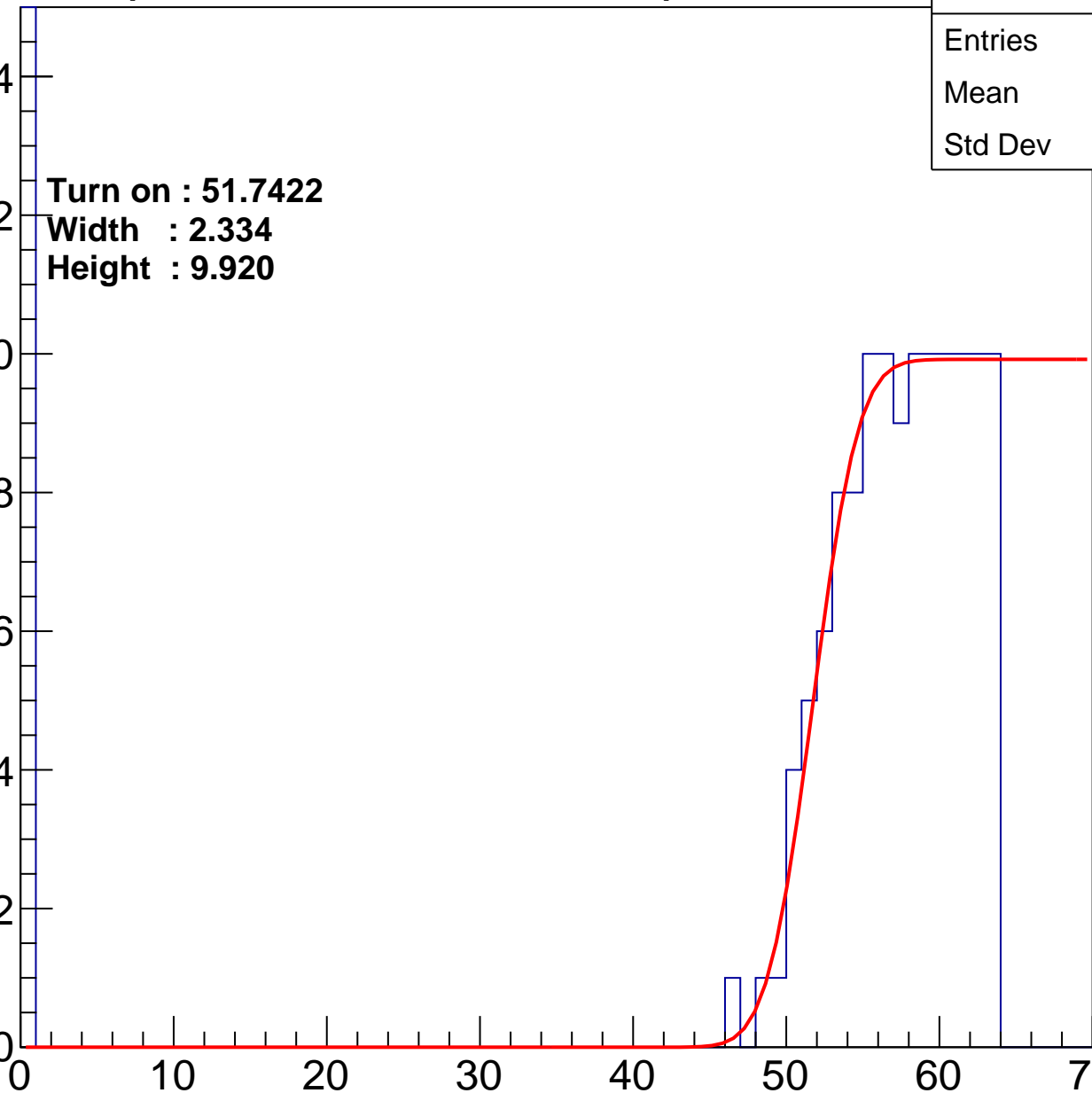
Width : 2.334

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	32.09
Std Dev	28.83

Turn on : 53.4322

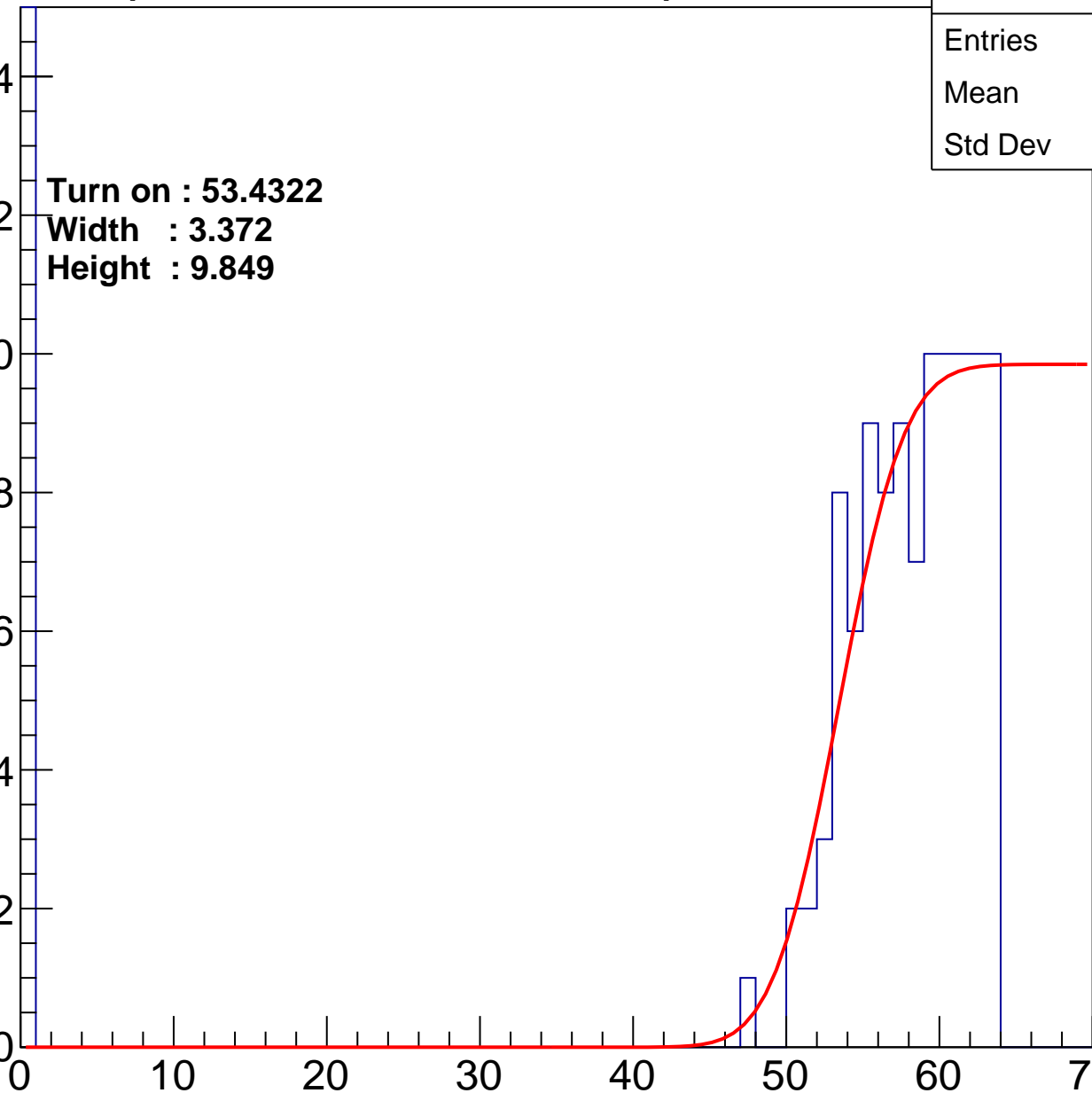
Width : 3.372

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	31.35
Std Dev	28.64

Turn on : 52.9521

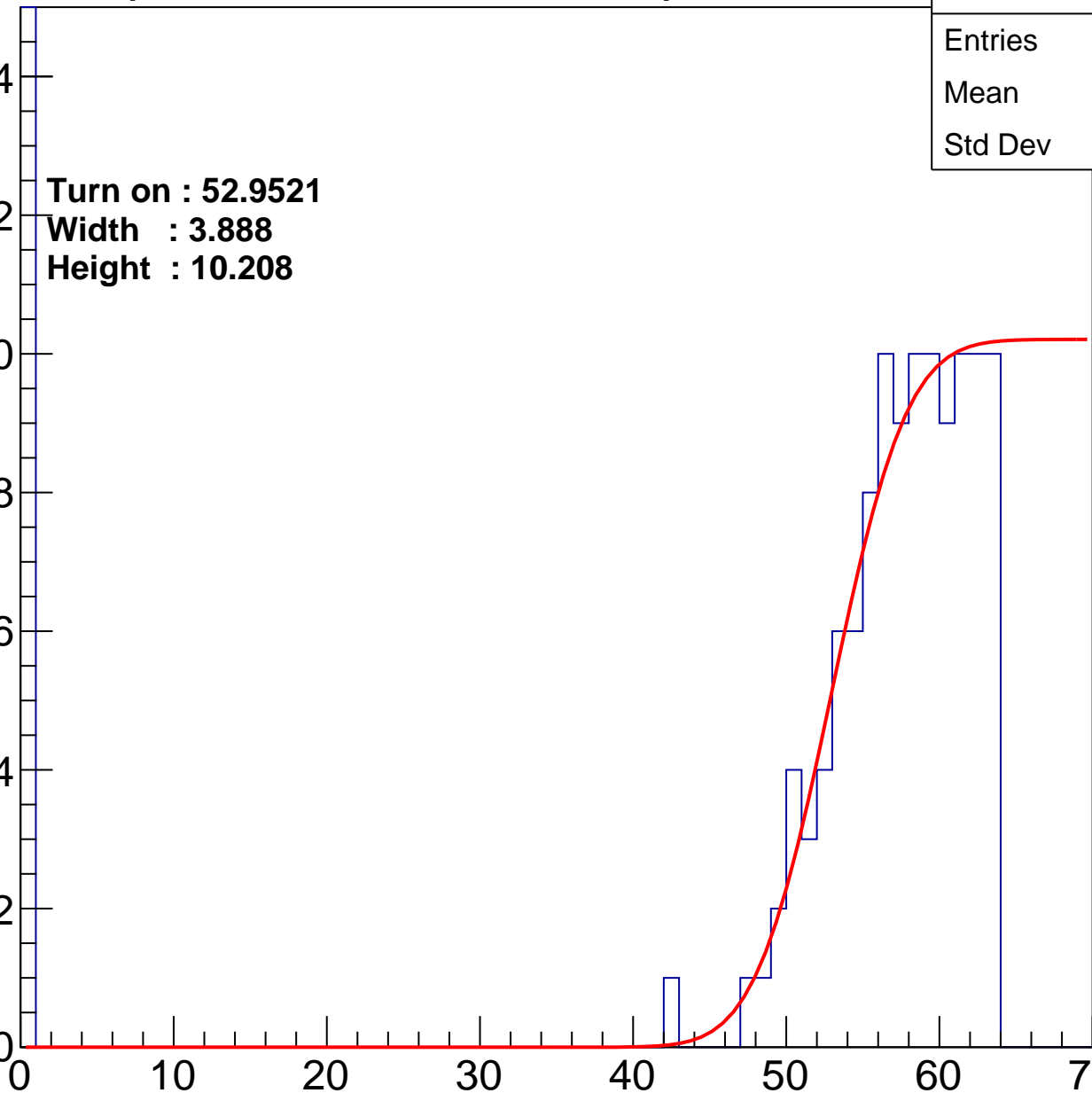
Width : 3.888

Height : 10.208

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	34.8
Std Dev	28.49

Turn on : 54.6109

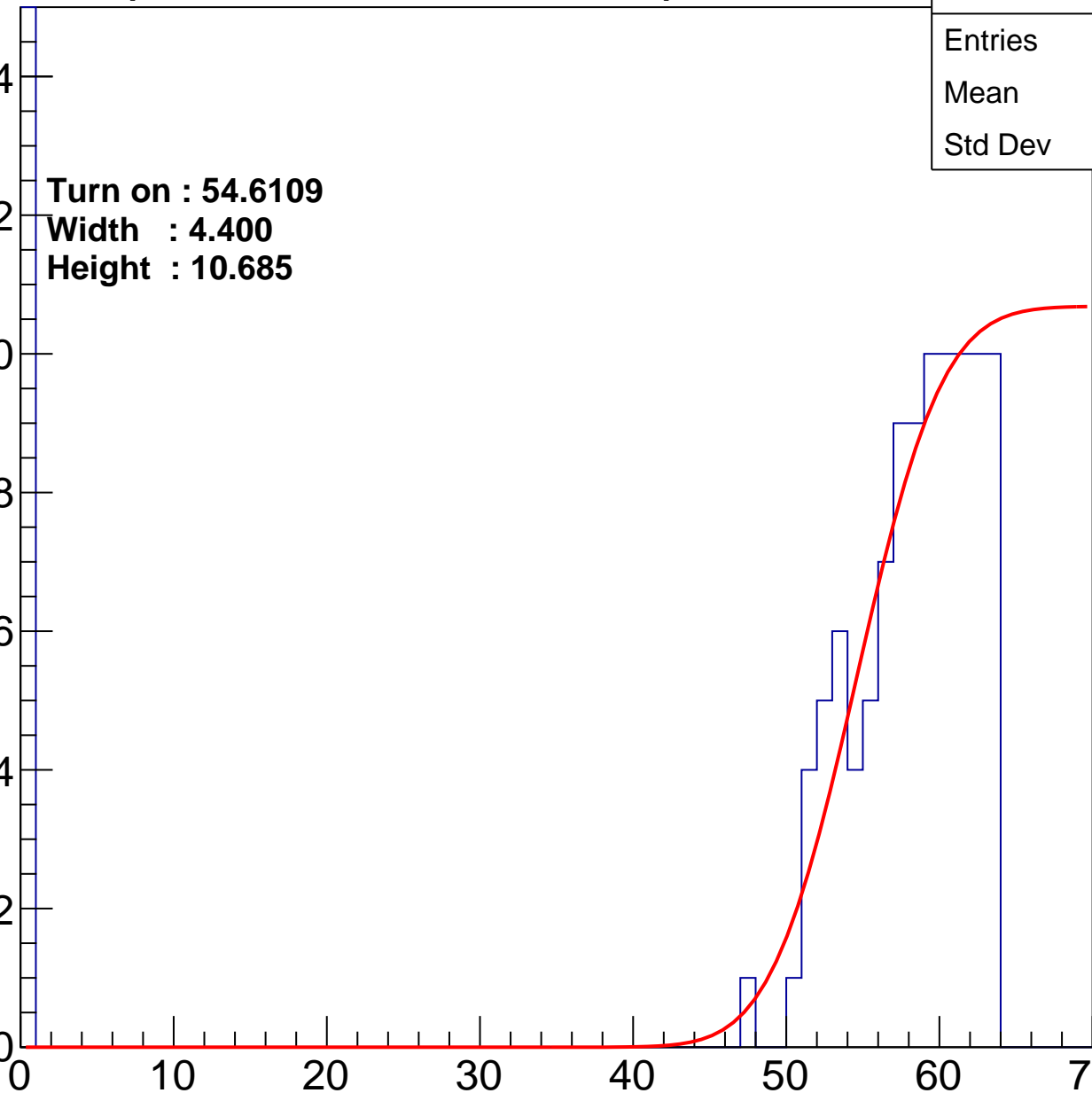
Width : 4.400

Height : 10.685

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	31.55
Std Dev	28.93

Turn on : 54.1127

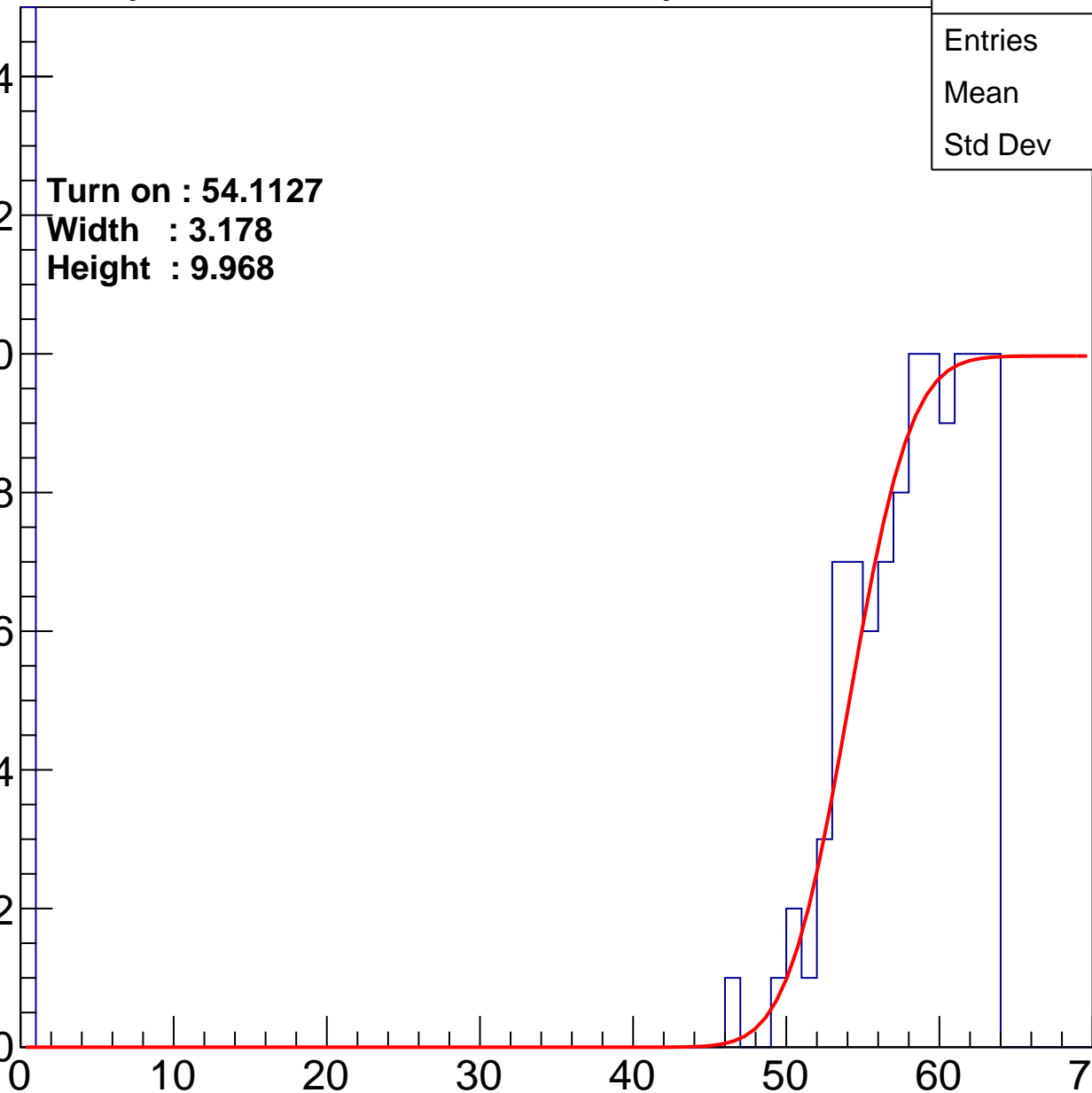
Width : 3.178

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	38.82
Std Dev	27.34

Turn on : 53.4135

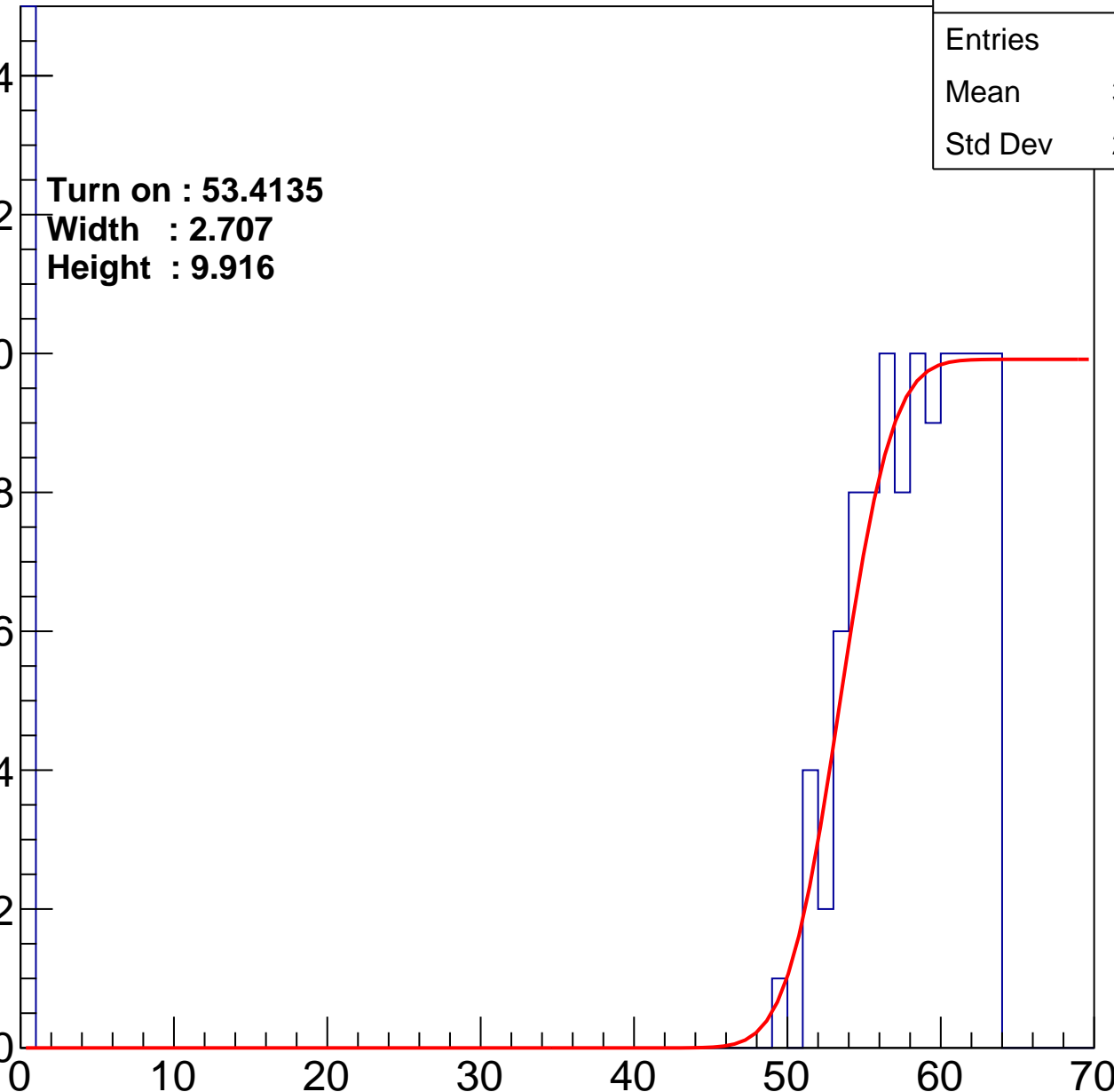
Width : 2.707

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	26.32
Std Dev	29.14

Turn on : 54.7909

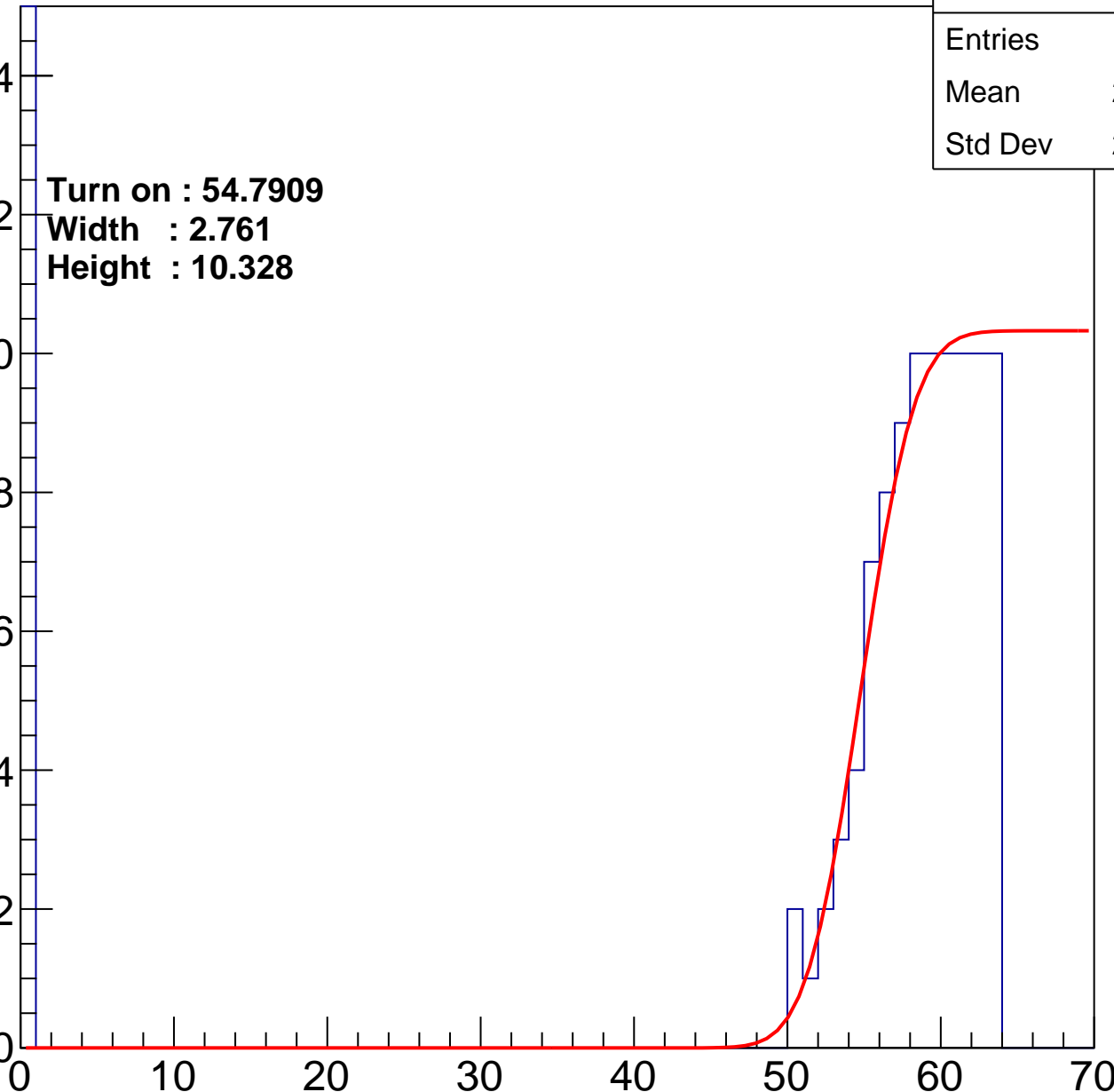
Width : 2.761

Height : 10.328

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	37.46
Std Dev	27.23

Turn on : 52.1545

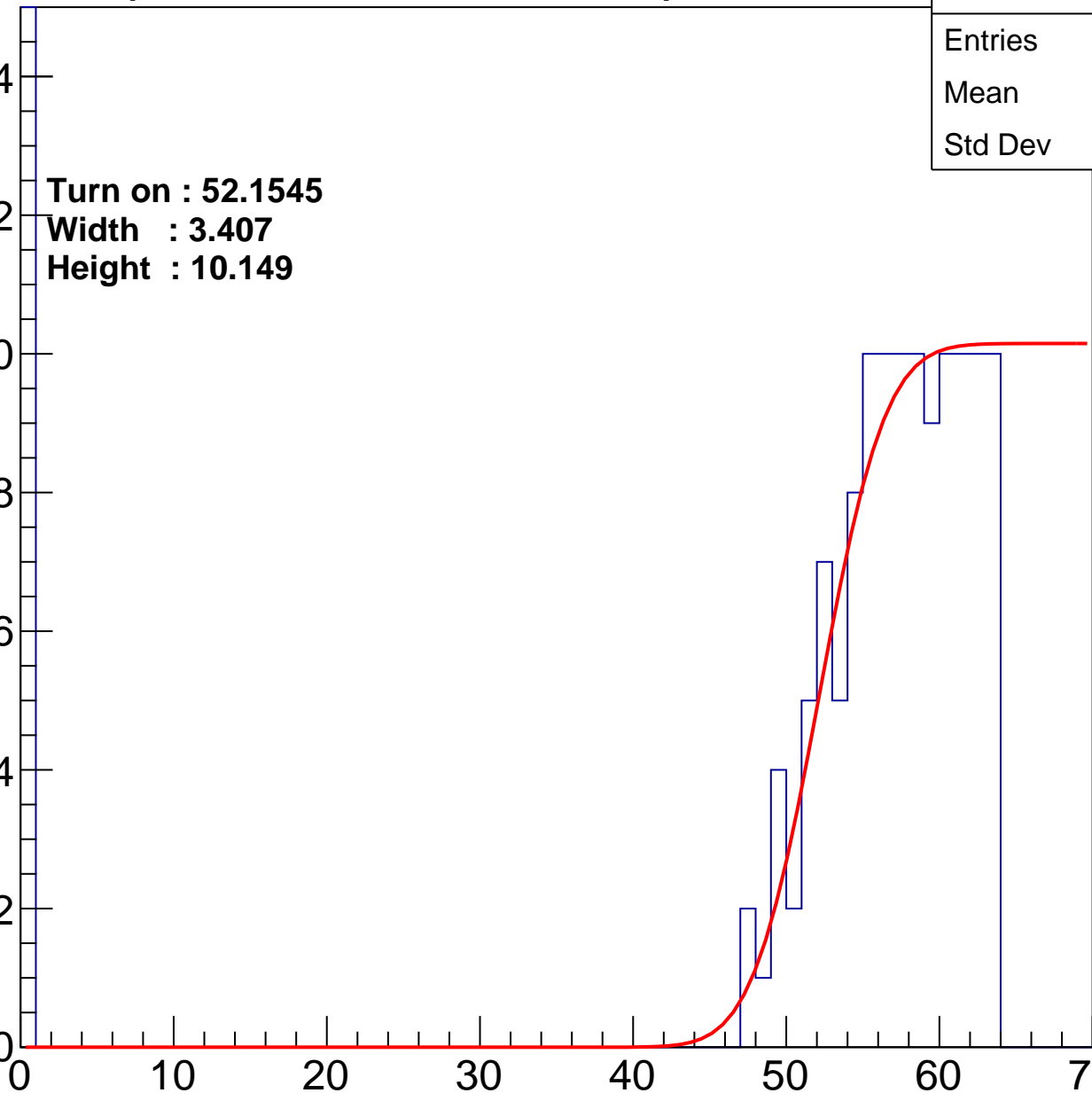
Width : 3.407

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	34.03
Std Dev	28.59

Turn on : 53.8711

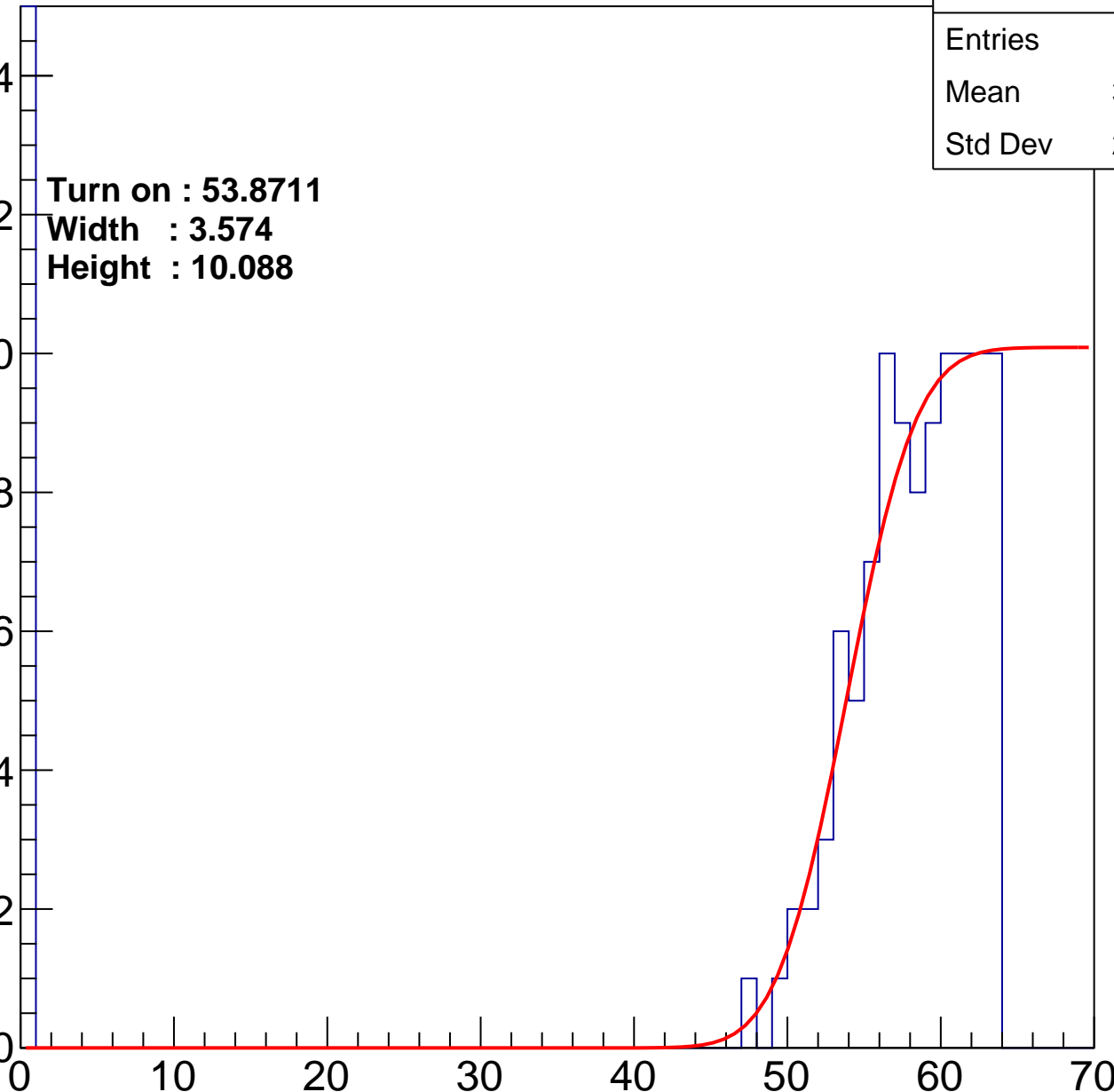
Width : 3.574

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	37.48
Std Dev	27.56

Turn on : 52.2926

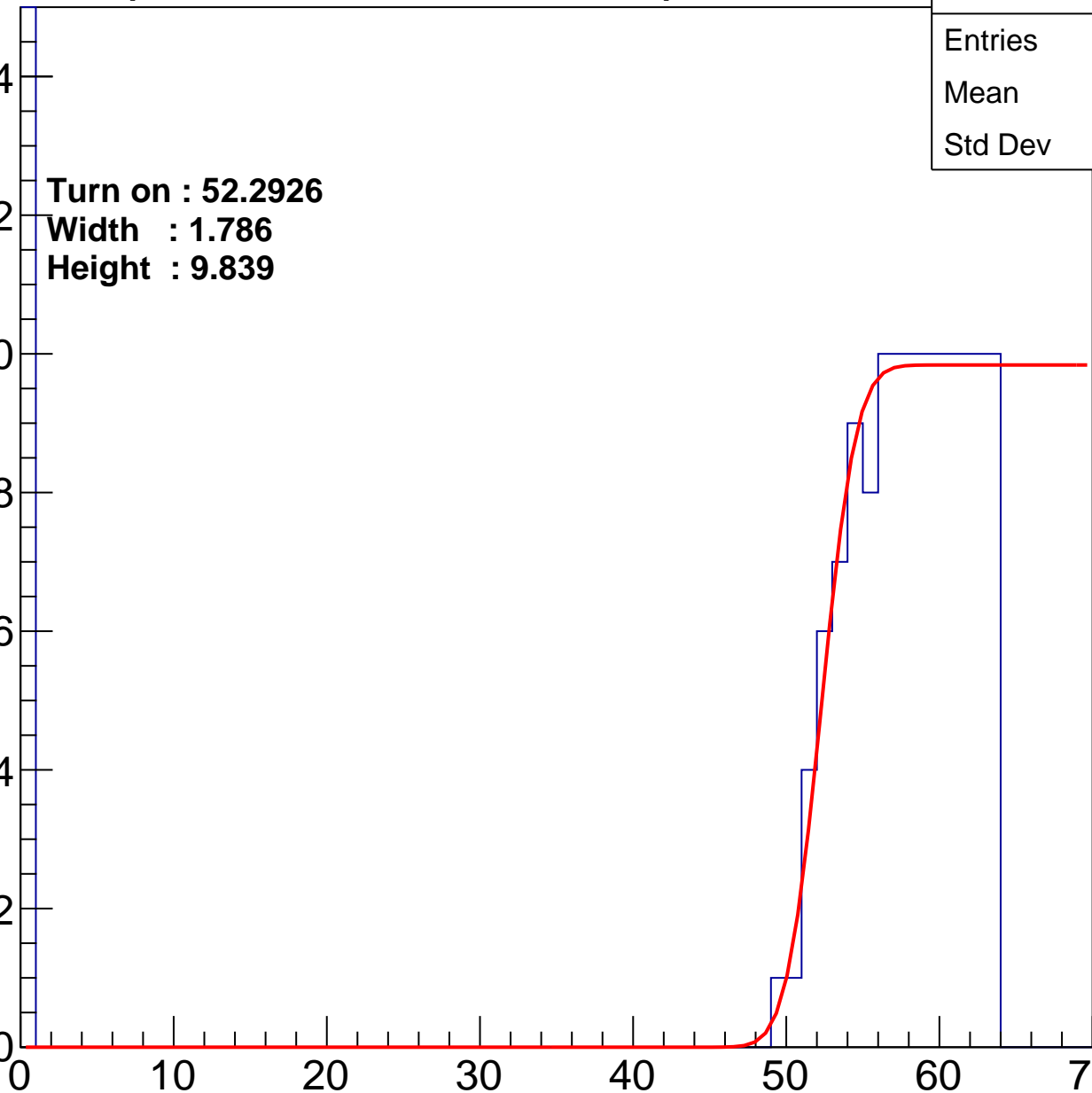
Width : 1.786

Height : 9.839

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch36

calib_packv5_033123_0516.root, FC#4, port A1

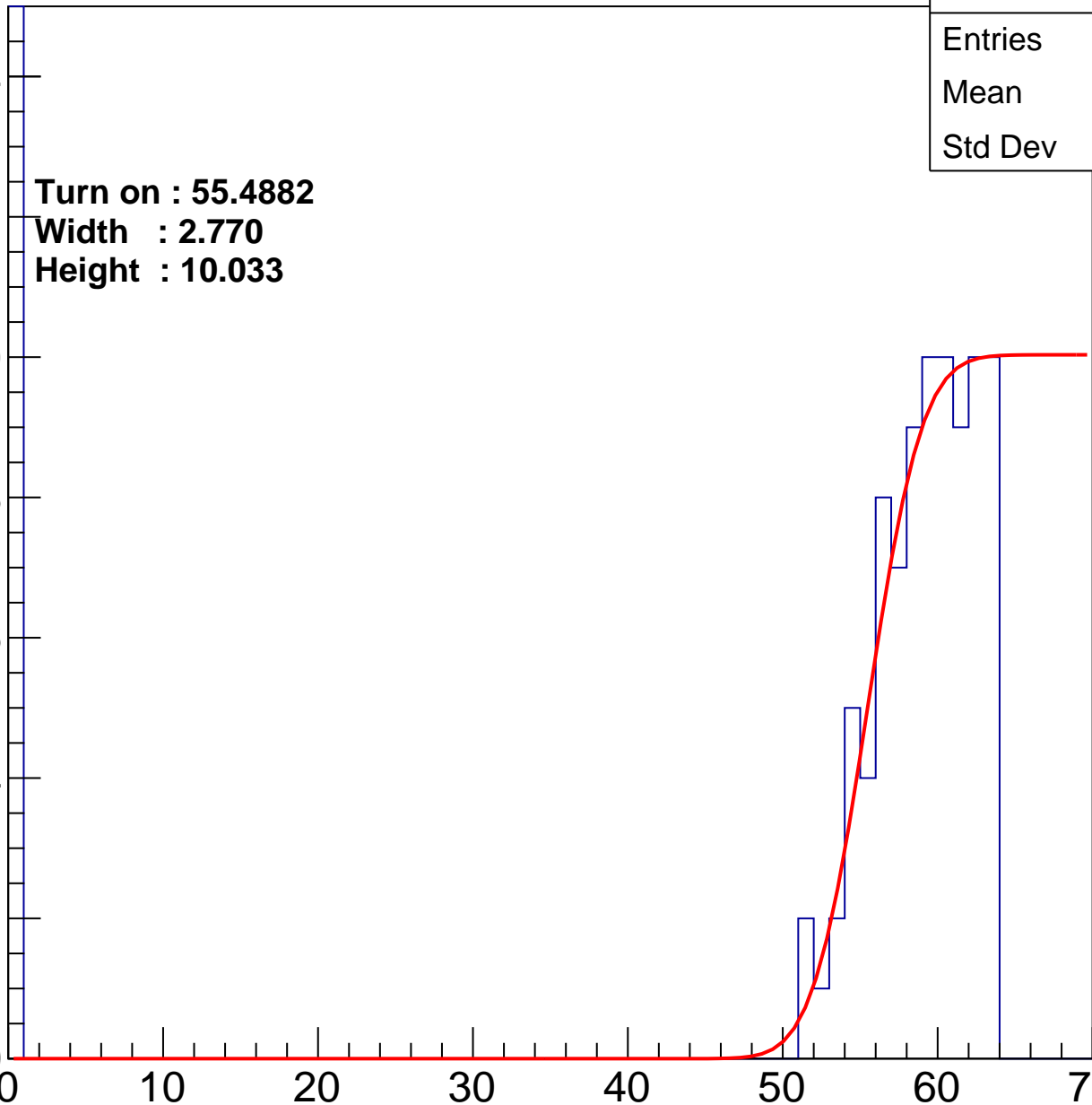
Entry

14
12
10
8
6
4
2
0

Turn on : 55.4882
Width : 2.770
Height : 10.033

Entries	161
Mean	31.73
Std Dev	29.35

ampl



B1L104S, U20-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	36.61
Std Dev	27.41

Turn on : 51.4920

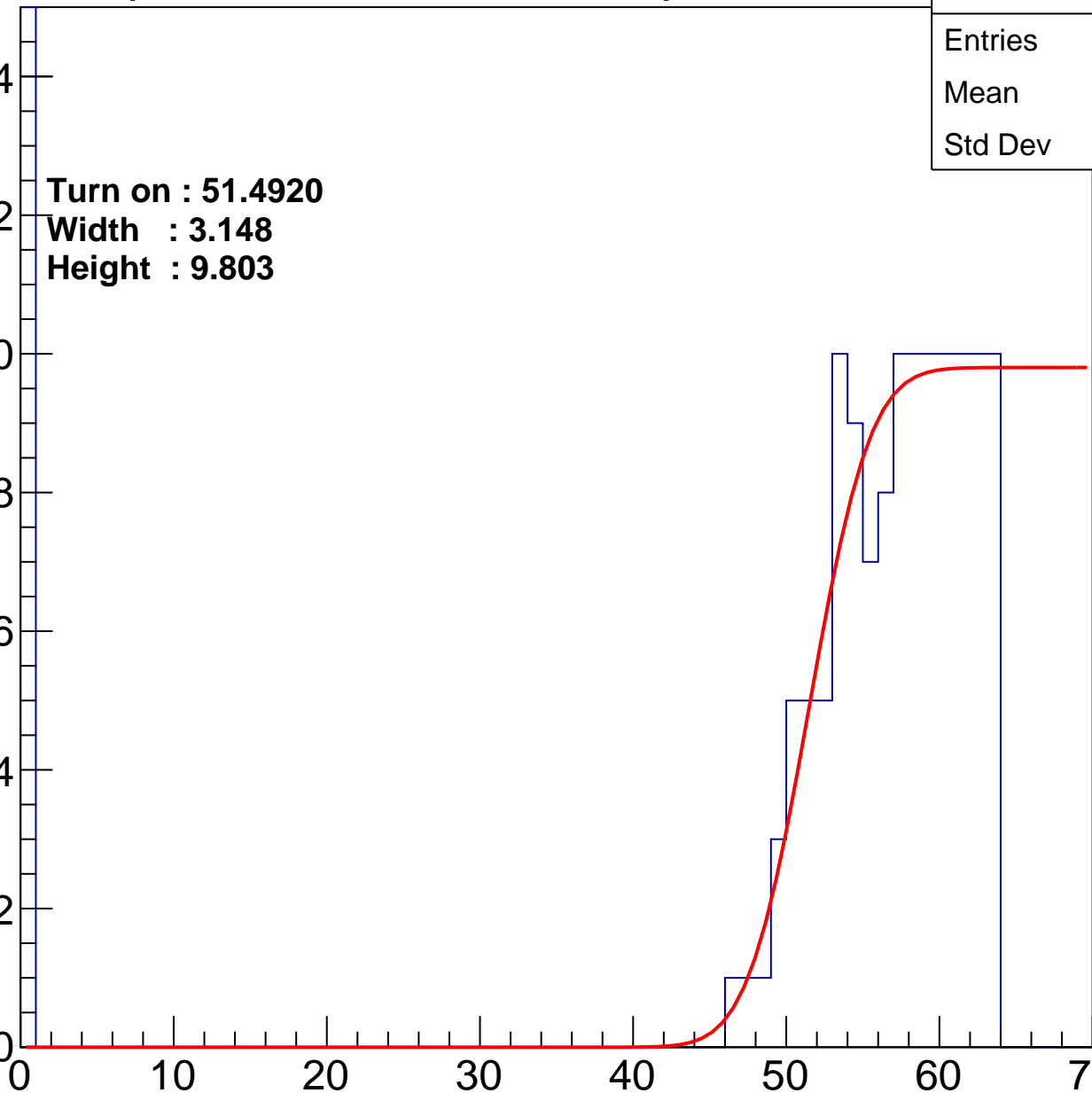
Width : 3.148

Height : 9.803

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.91
Std Dev	28.33

Turn on : 52.5835

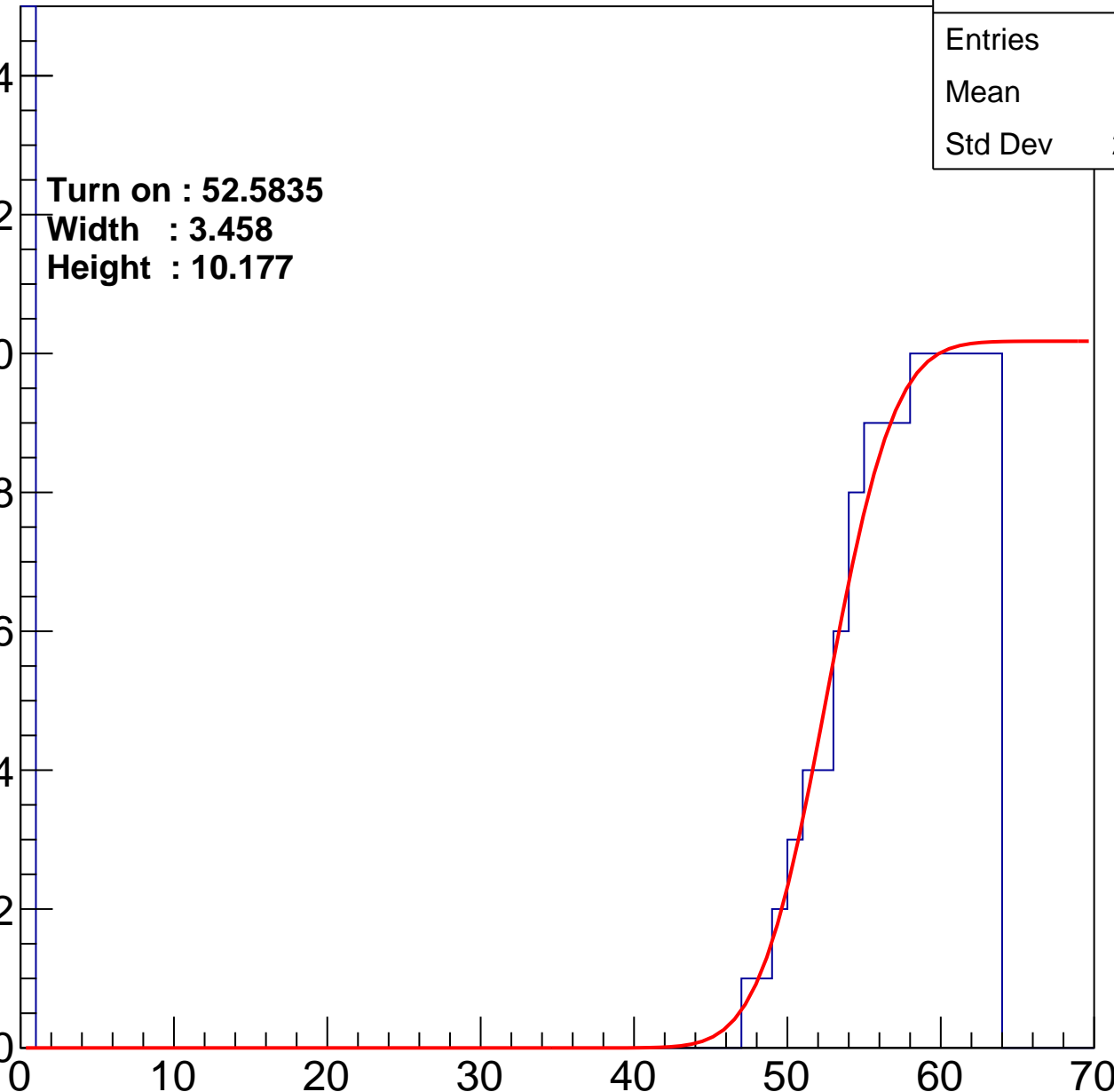
Width : 3.458

Height : 10.177

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.79
Std Dev	28.69

Turn on : 53.9324

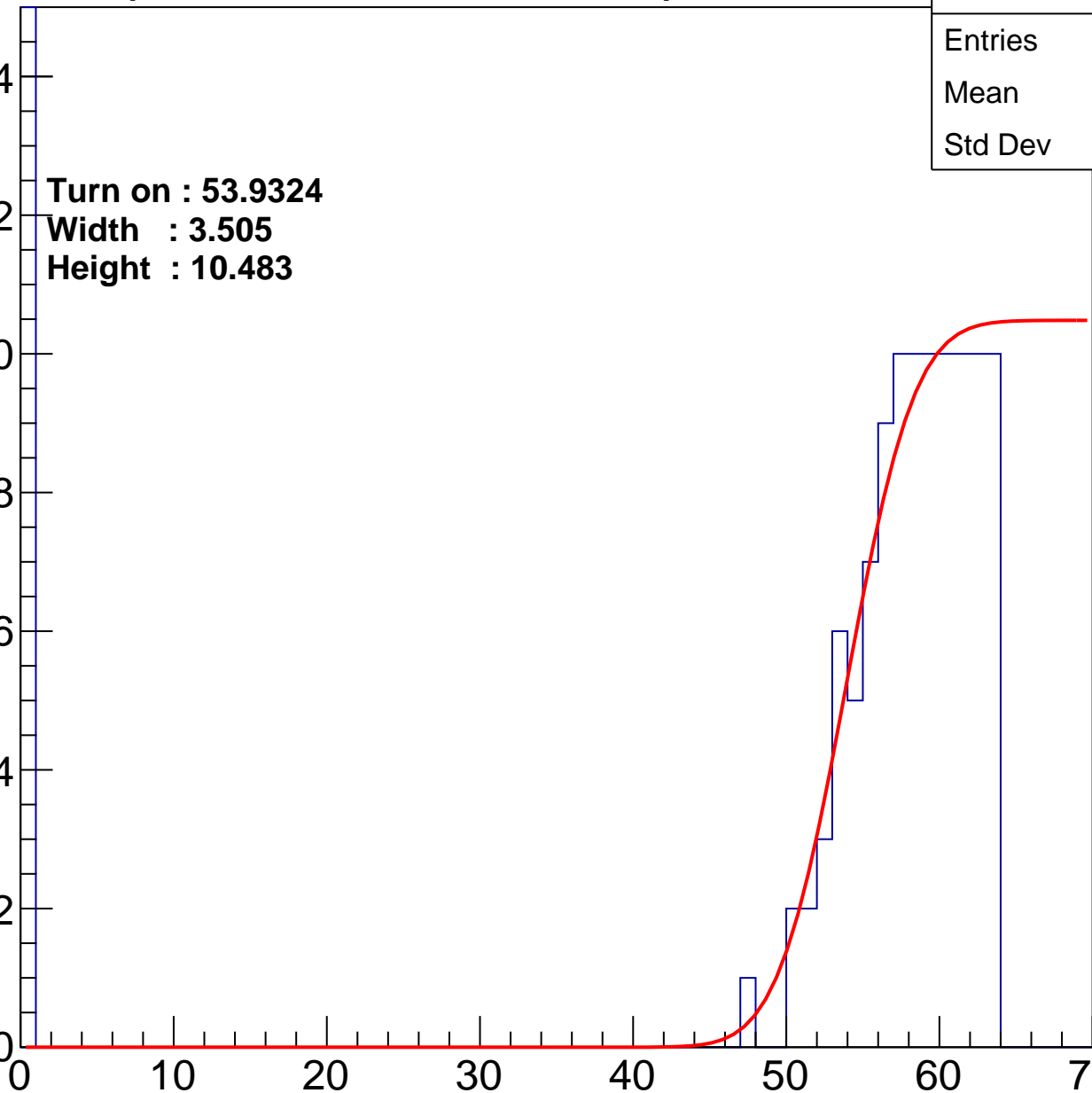
Width : 3.505

Height : 10.483

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	35.16
Std Dev	27.98

Turn on : 52.7009

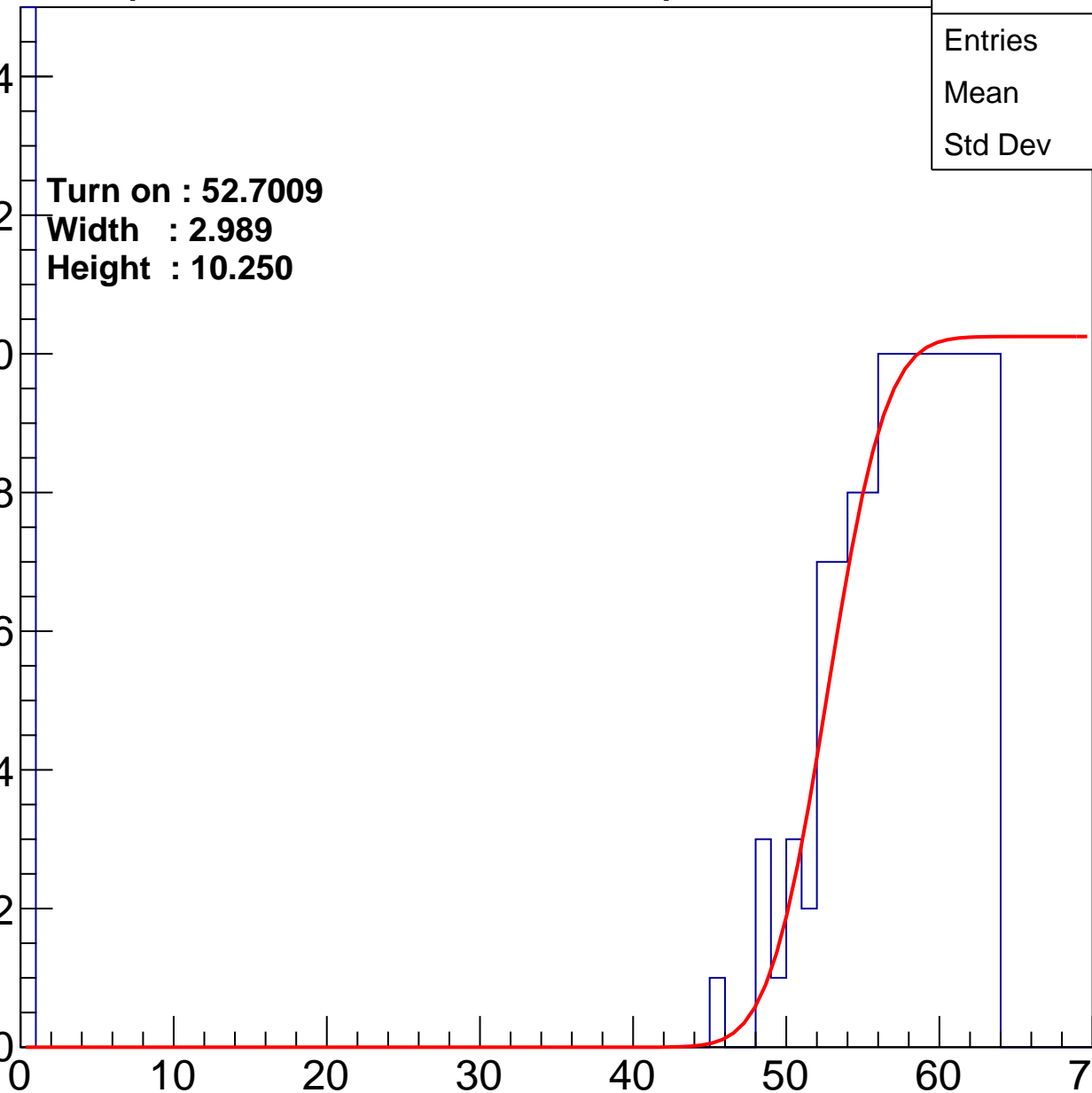
Width : 2.989

Height : 10.250

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	32.18
Std Dev	28.84

Turn on : 53.4201

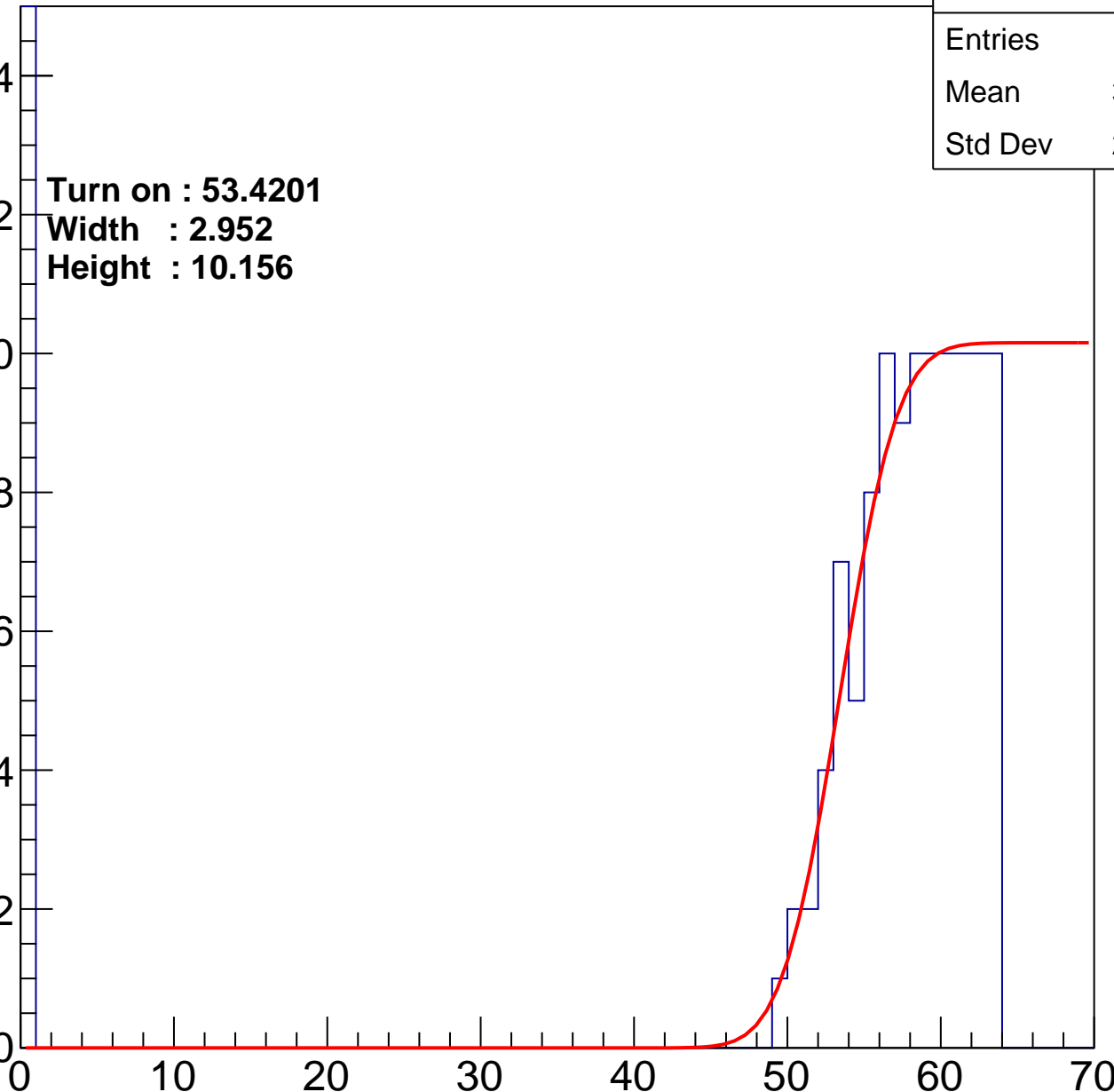
Width : 2.952

Height : 10.156

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	29.85
Std Dev	28.97

Turn on : 54.9508

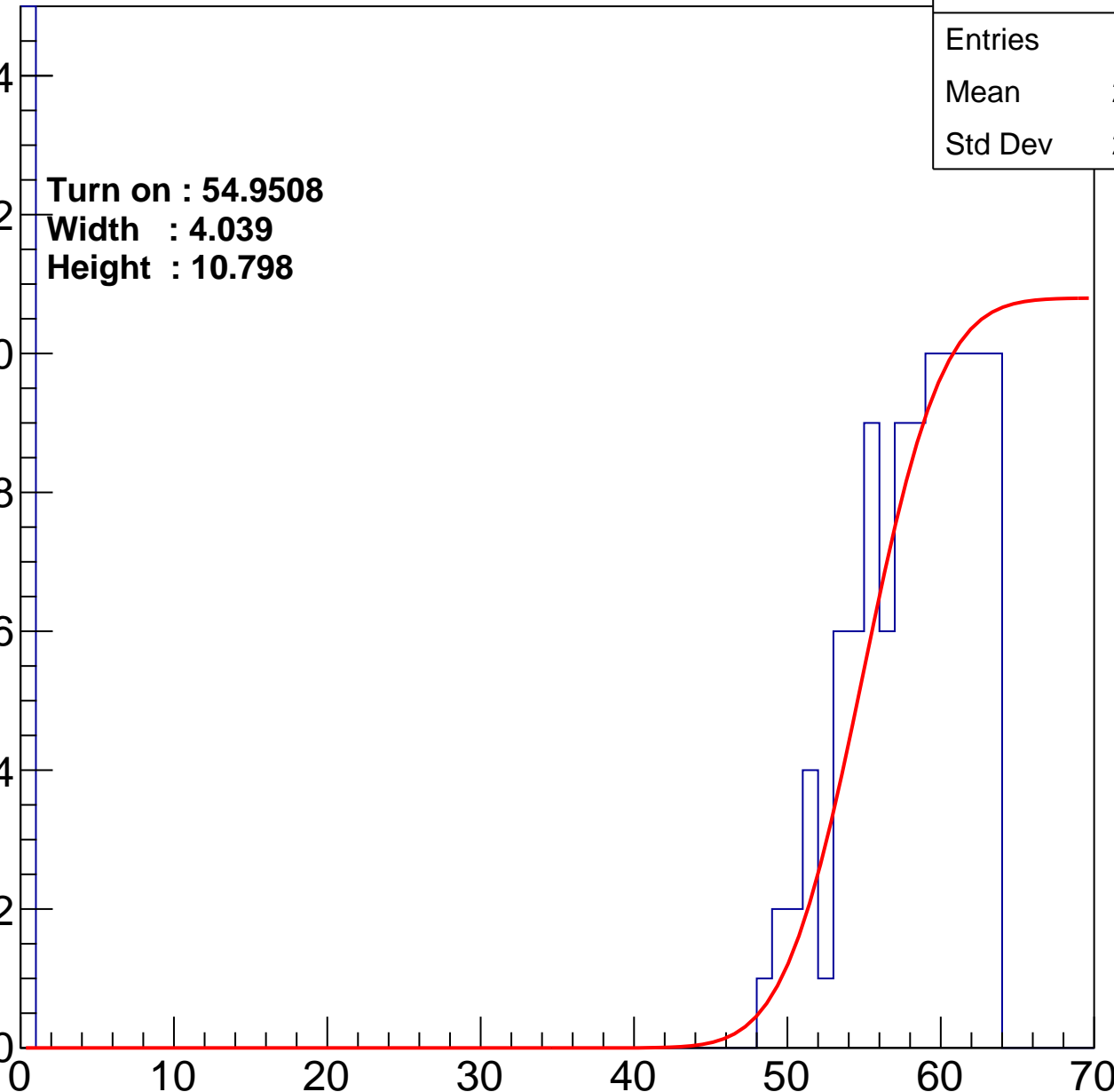
Width : 4.039

Height : 10.798

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	37.5
Std Dev	27.45

Turn on : 52.6313

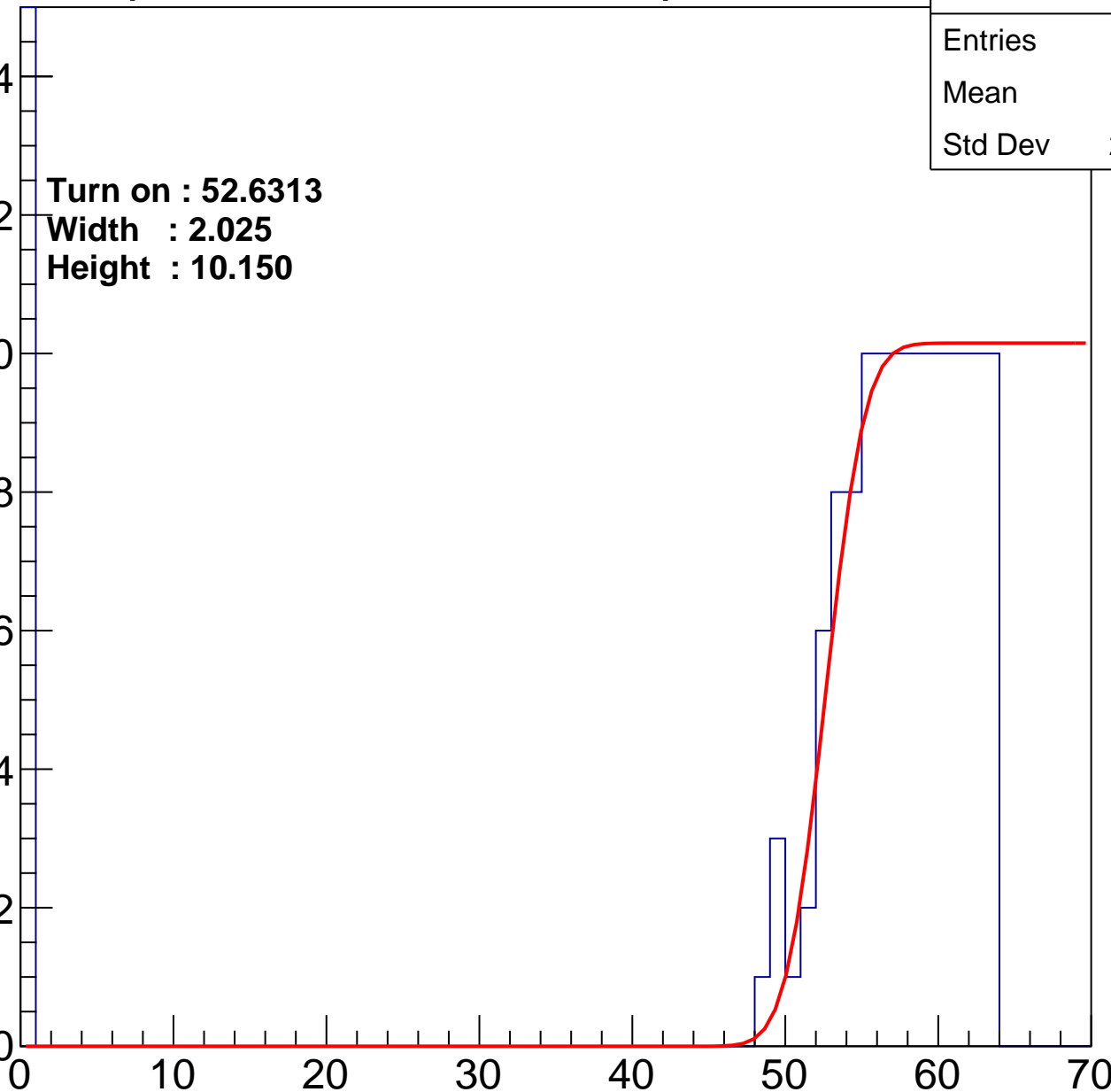
Width : 2.025

Height : 10.150

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	34.3
Std Dev	28.23

Turn on : 52.6058

Width : 4.940

Height : 10.755

Entry

14

12

10

8

6

4

2

0

0

10

20

30

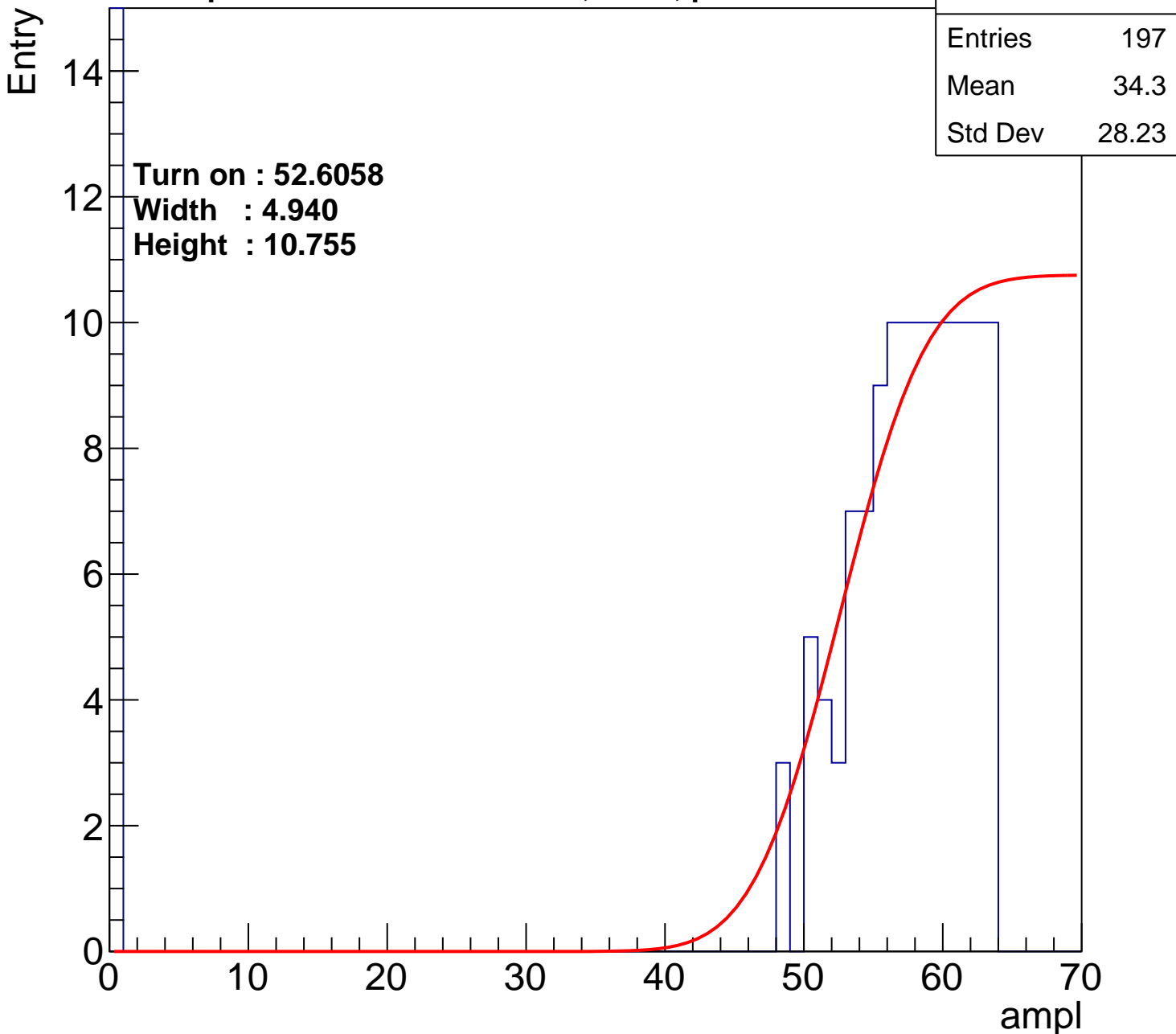
40

50

60

70

ampl



B1L104S, U20-ch45

calib_packv5_033123_0516.root, FC#4, port A1

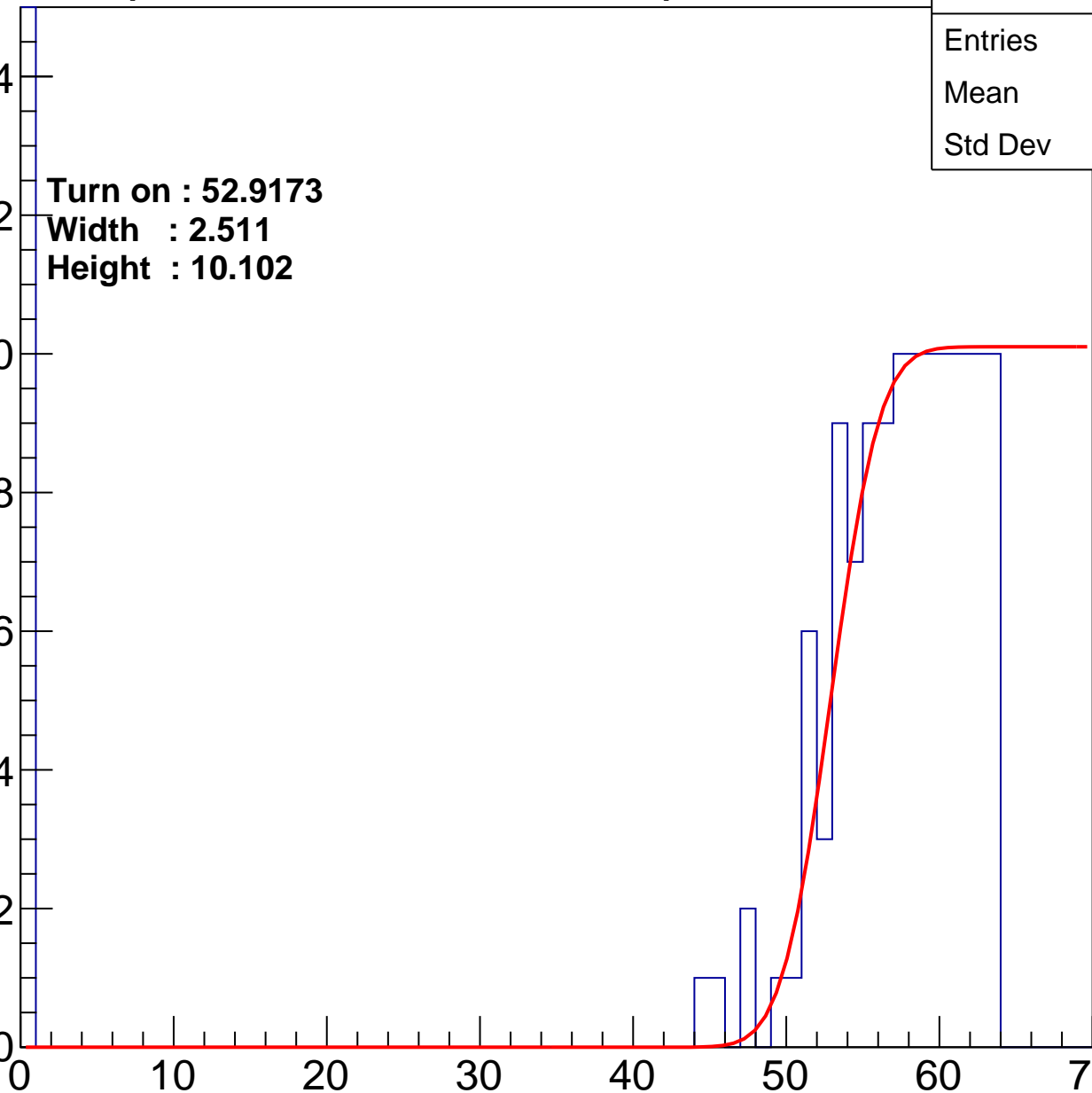
Entry

14
12
10
8
6
4
2
0

Turn on : 52.9173
Width : 2.511
Height : 10.102

Entries	189
Mean	35.97
Std Dev	27.78

ampl



B1L104S, U20-ch46

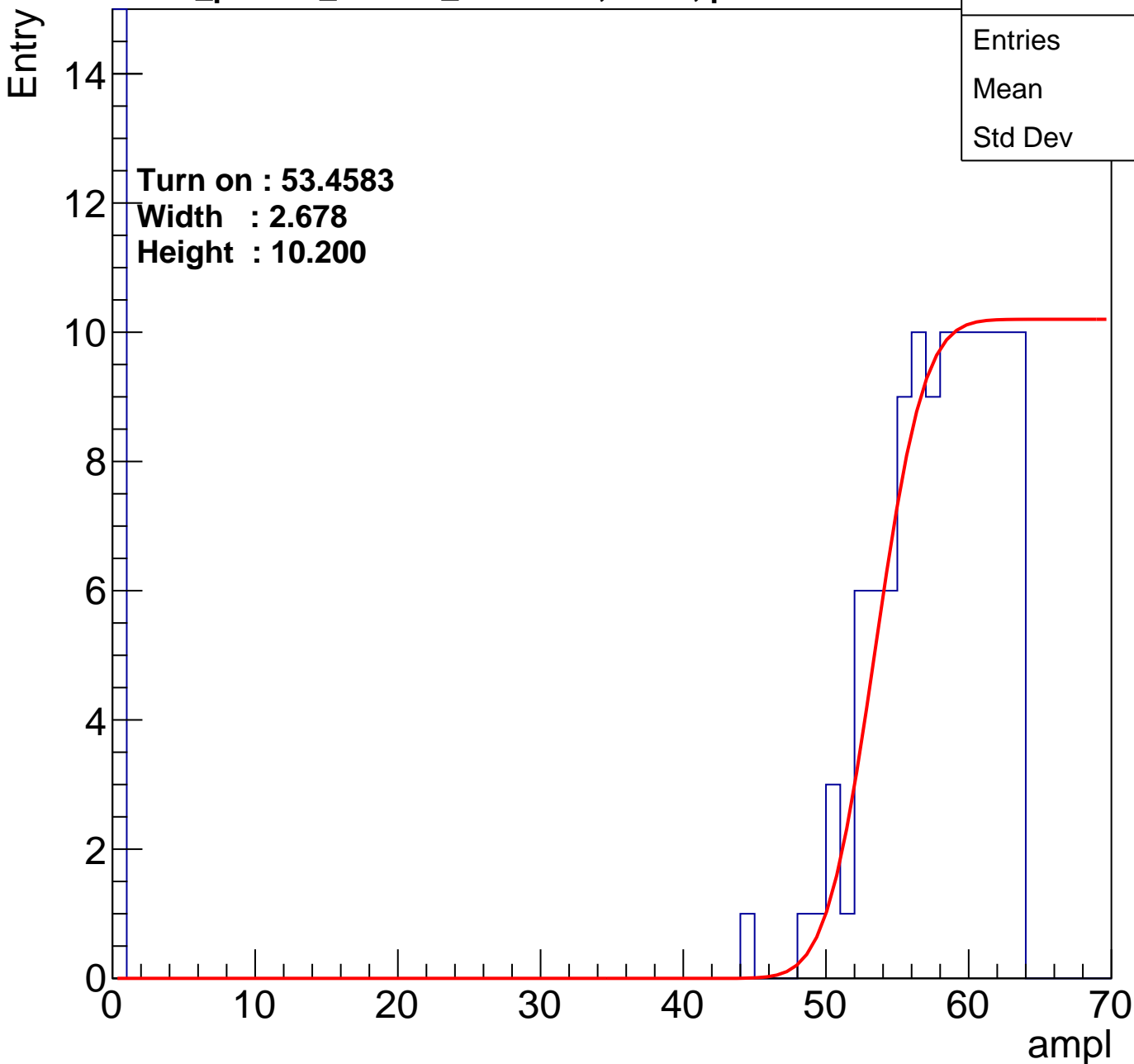
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	32.8
Std Dev	28.6

Turn on : 53.4583

Width : 2.678

Height : 10.200



B1L104S, U20-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	37
Std Dev	27.83

Turn on : 53.3982

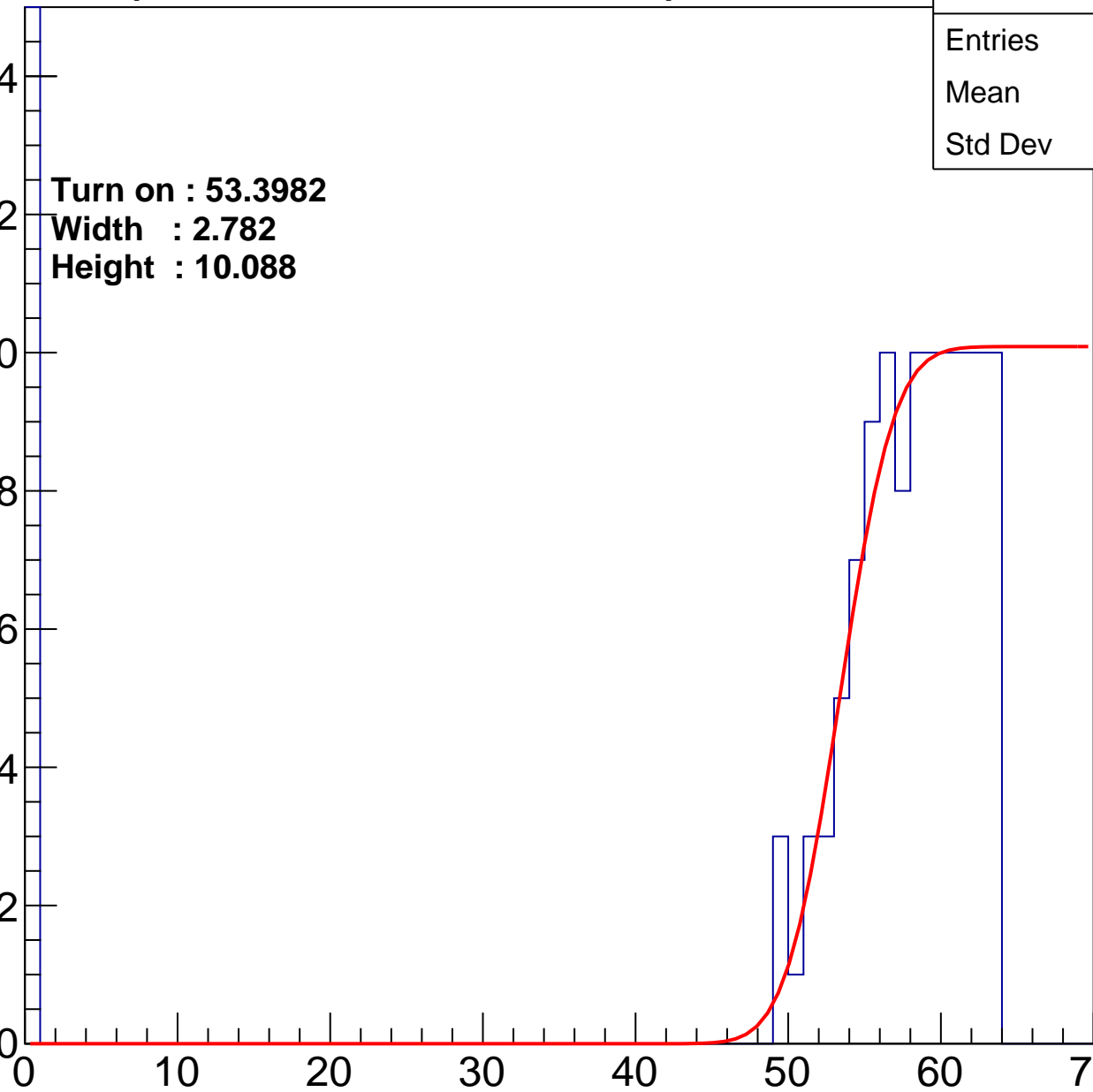
Width : 2.782

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch48

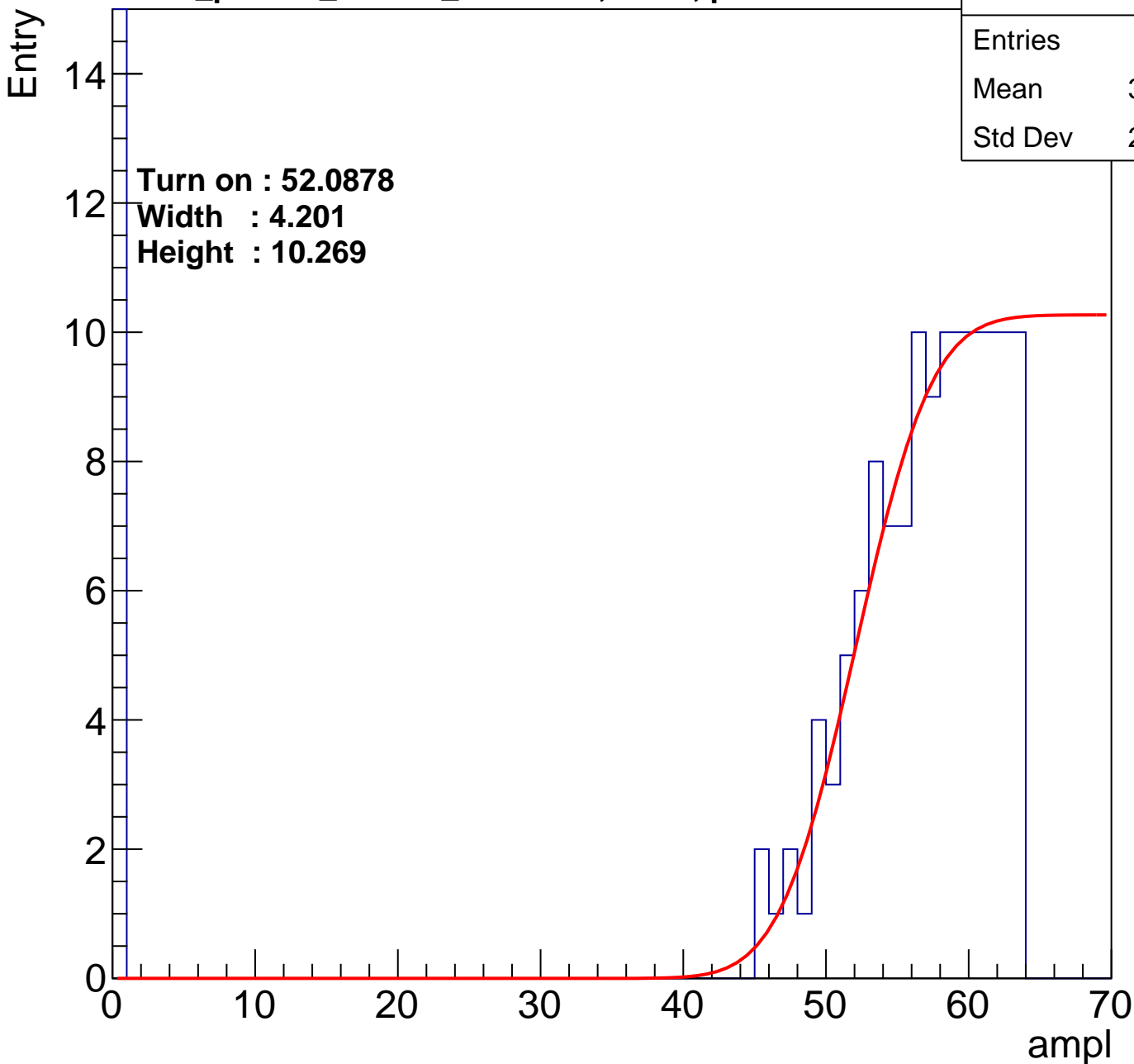
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	35.76
Std Dev	27.56

Turn on : 52.0878

Width : 4.201

Height : 10.269



B1L104S, U20-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	36.55
Std Dev	27.44

Turn on : 51.7951

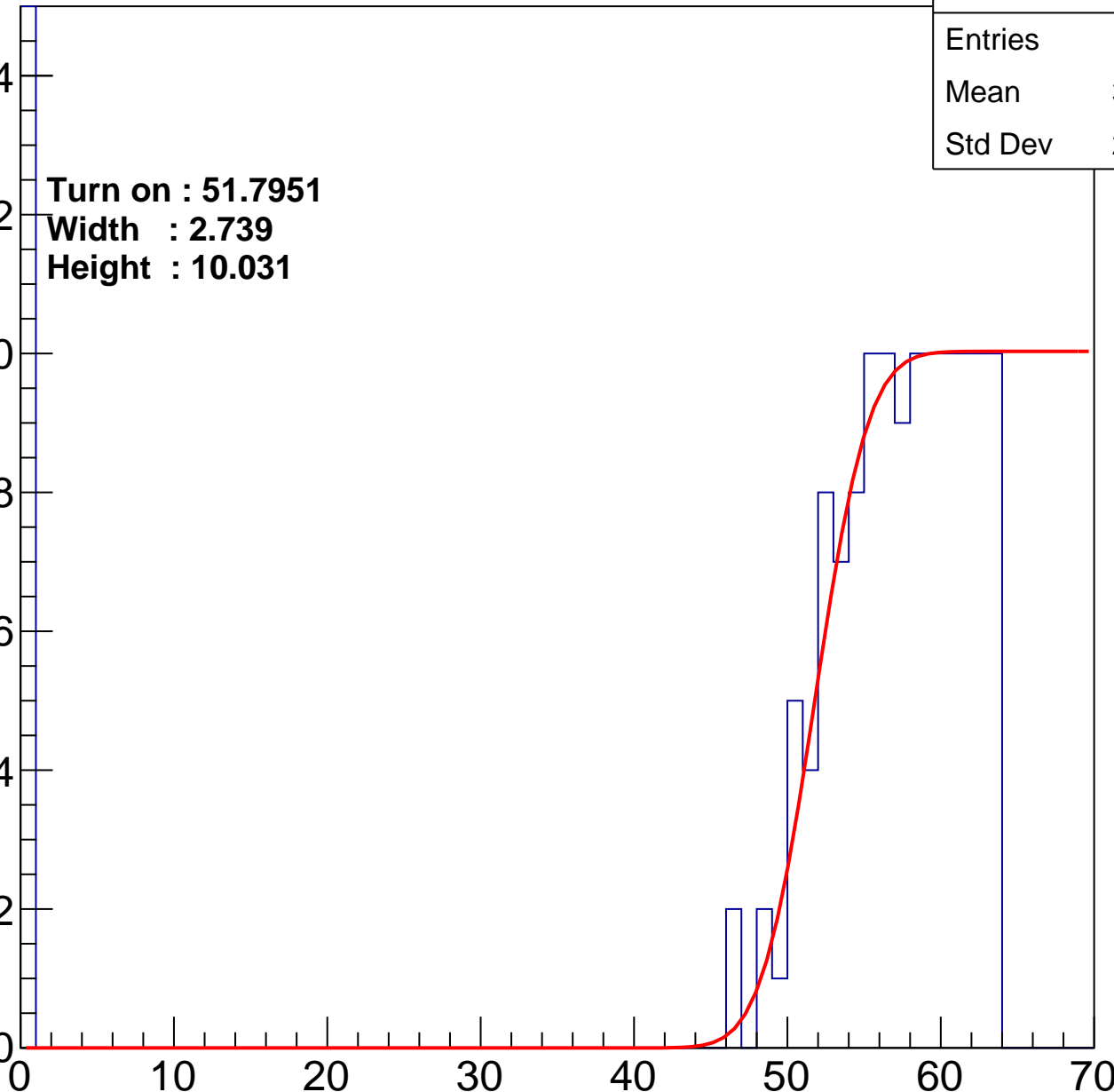
Width : 2.739

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	32.61
Std Dev	28.75

Turn on : 53.6756

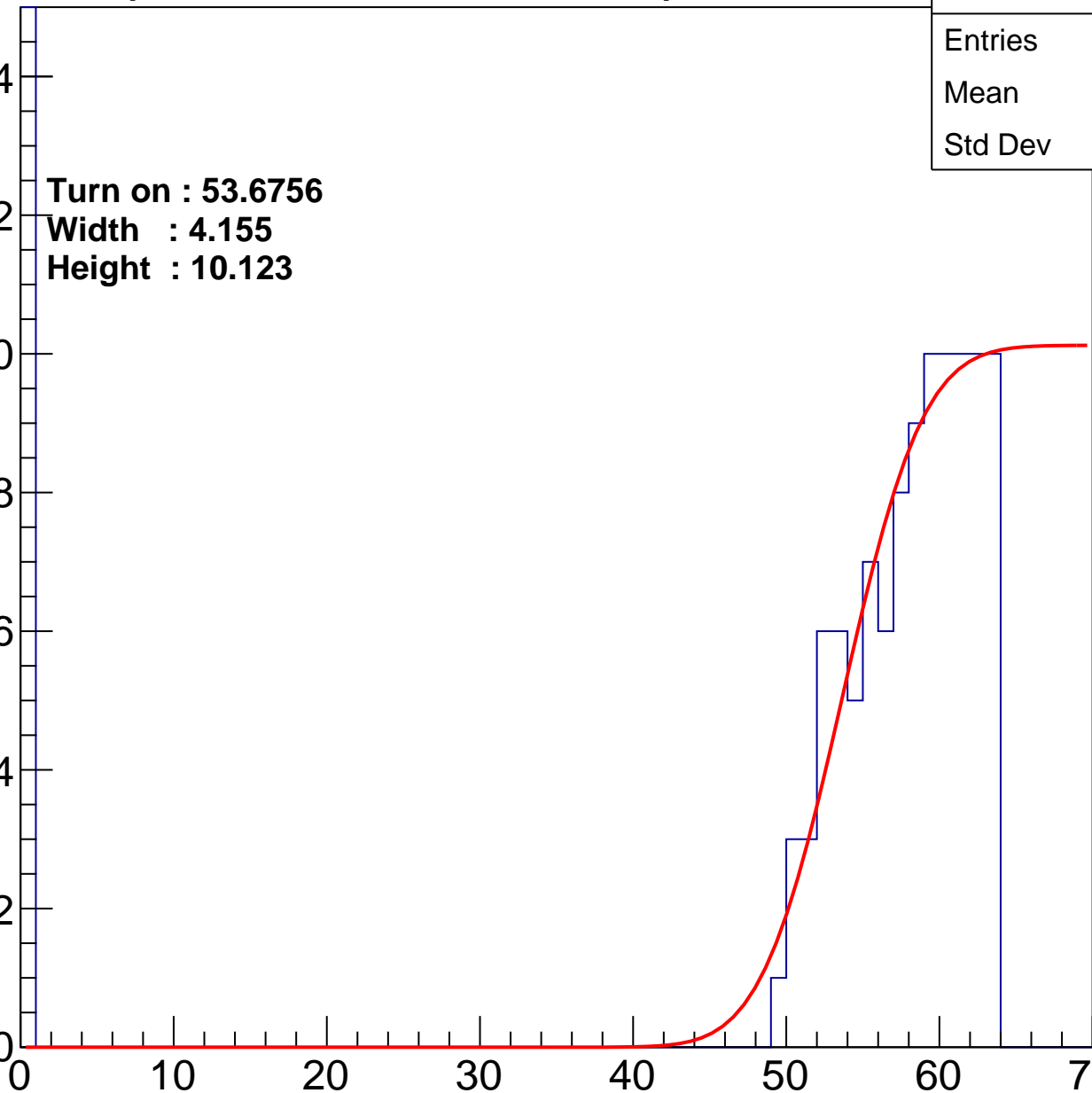
Width : 4.155

Height : 10.123

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	32.57
Std Dev	28.41

Turn on : 52.7443

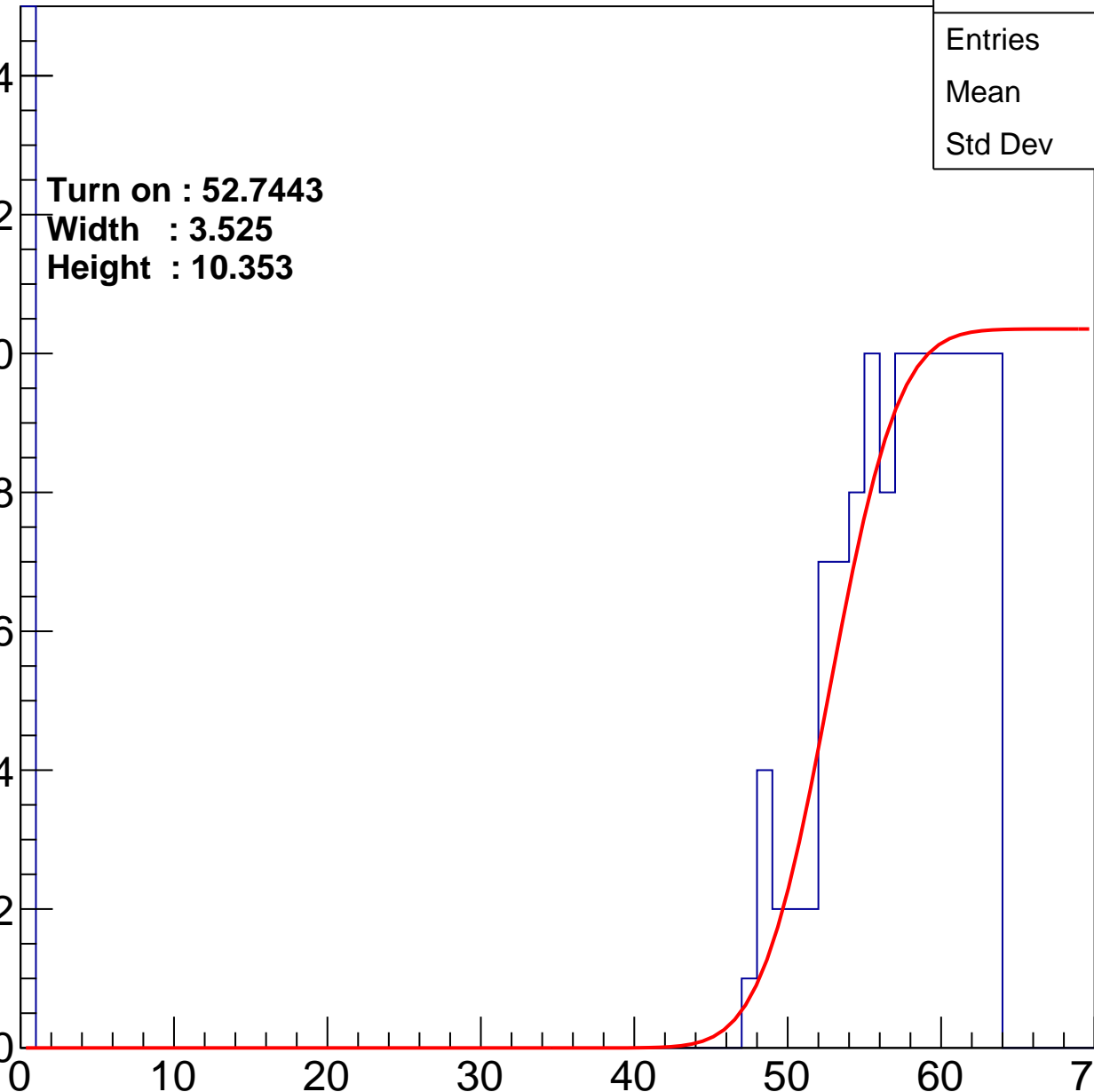
Width : 3.525

Height : 10.353

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	31.6
Std Dev	28.78

Turn on : 54.3622

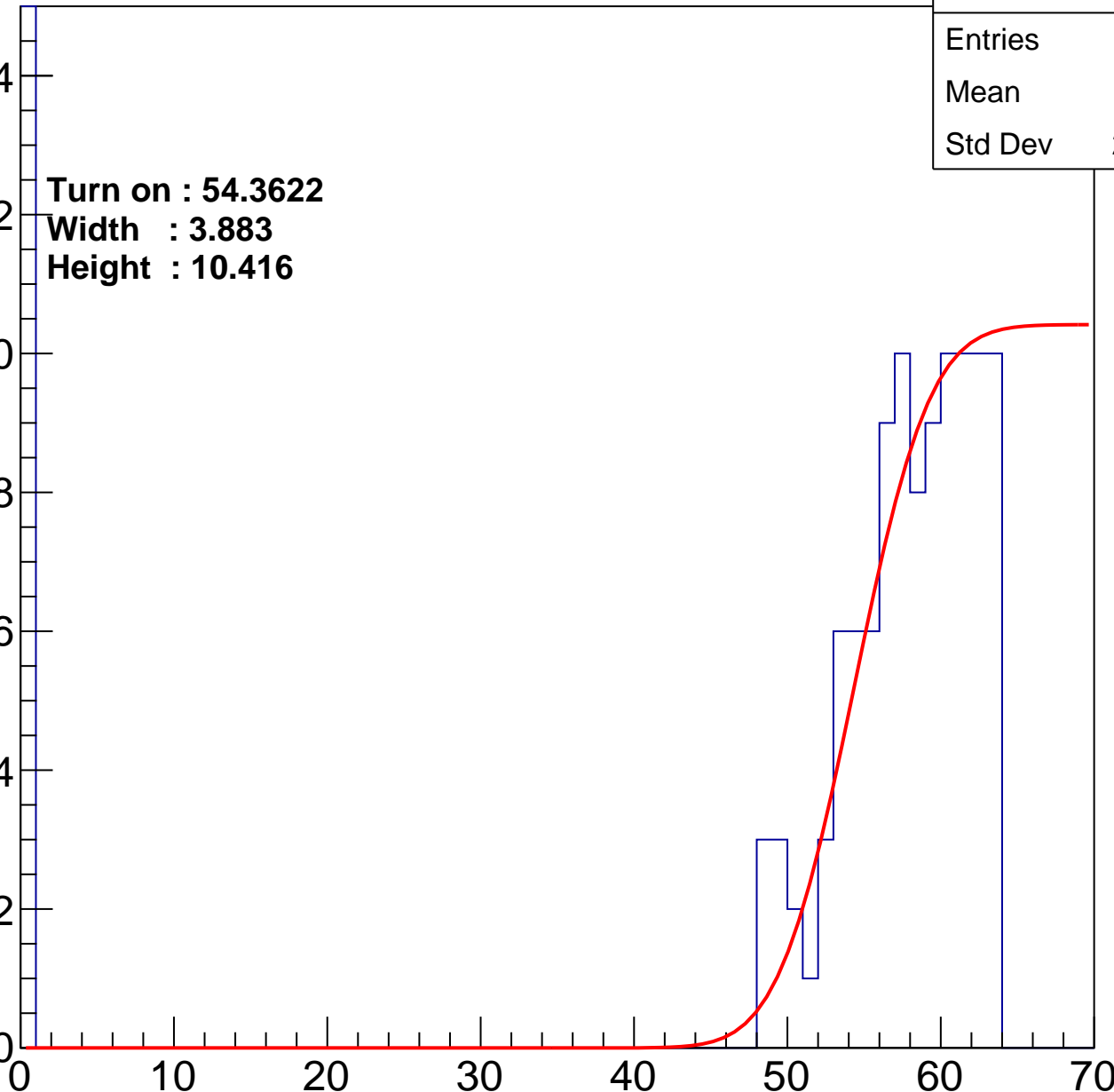
Width : 3.883

Height : 10.416

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch53

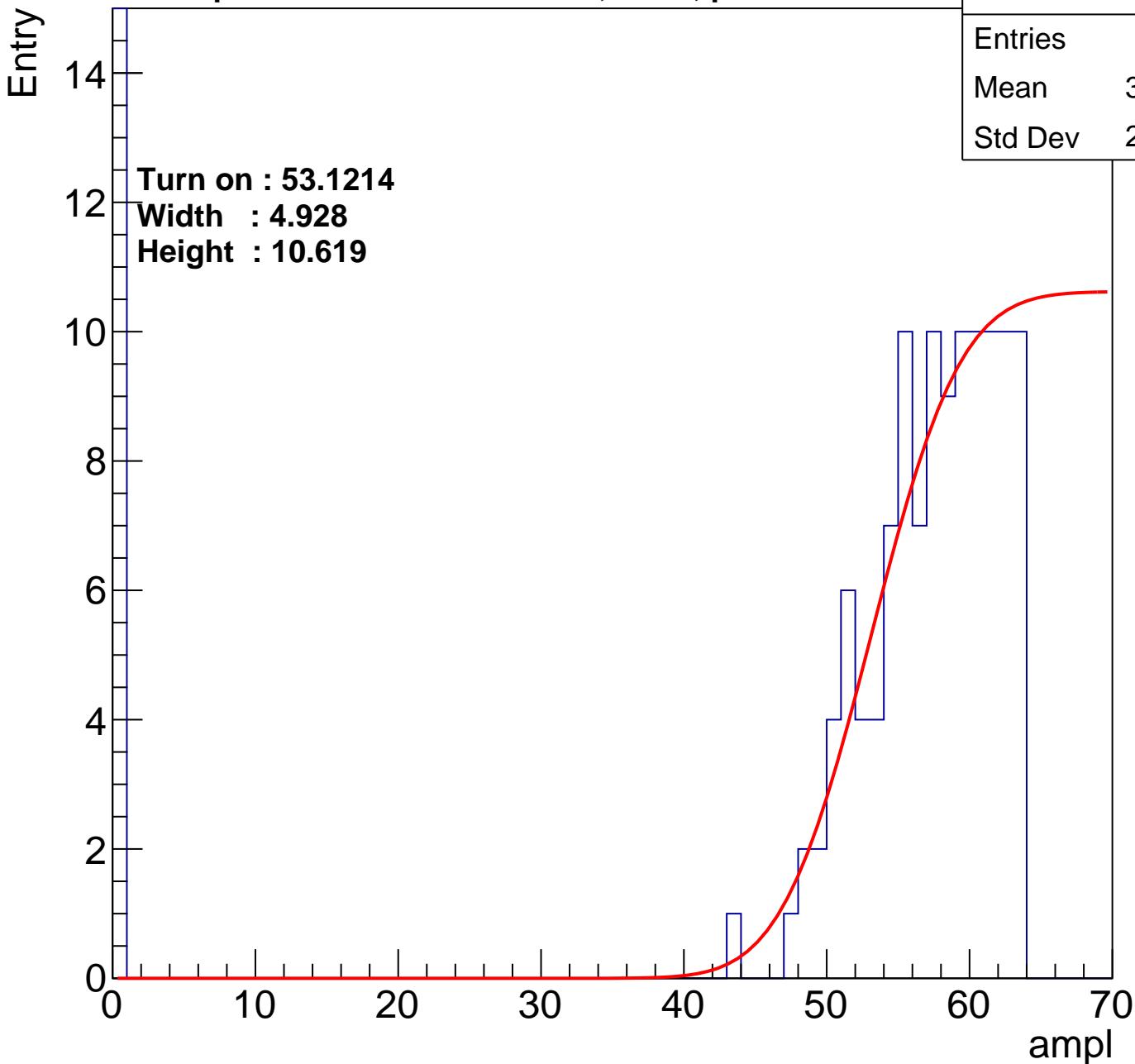
calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	33.19
Std Dev	28.32

Turn on : 53.1214

Width : 4.928

Height : 10.619



B1L104S, U20-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	37.38
Std Dev	27.27

Turn on : 52.1387

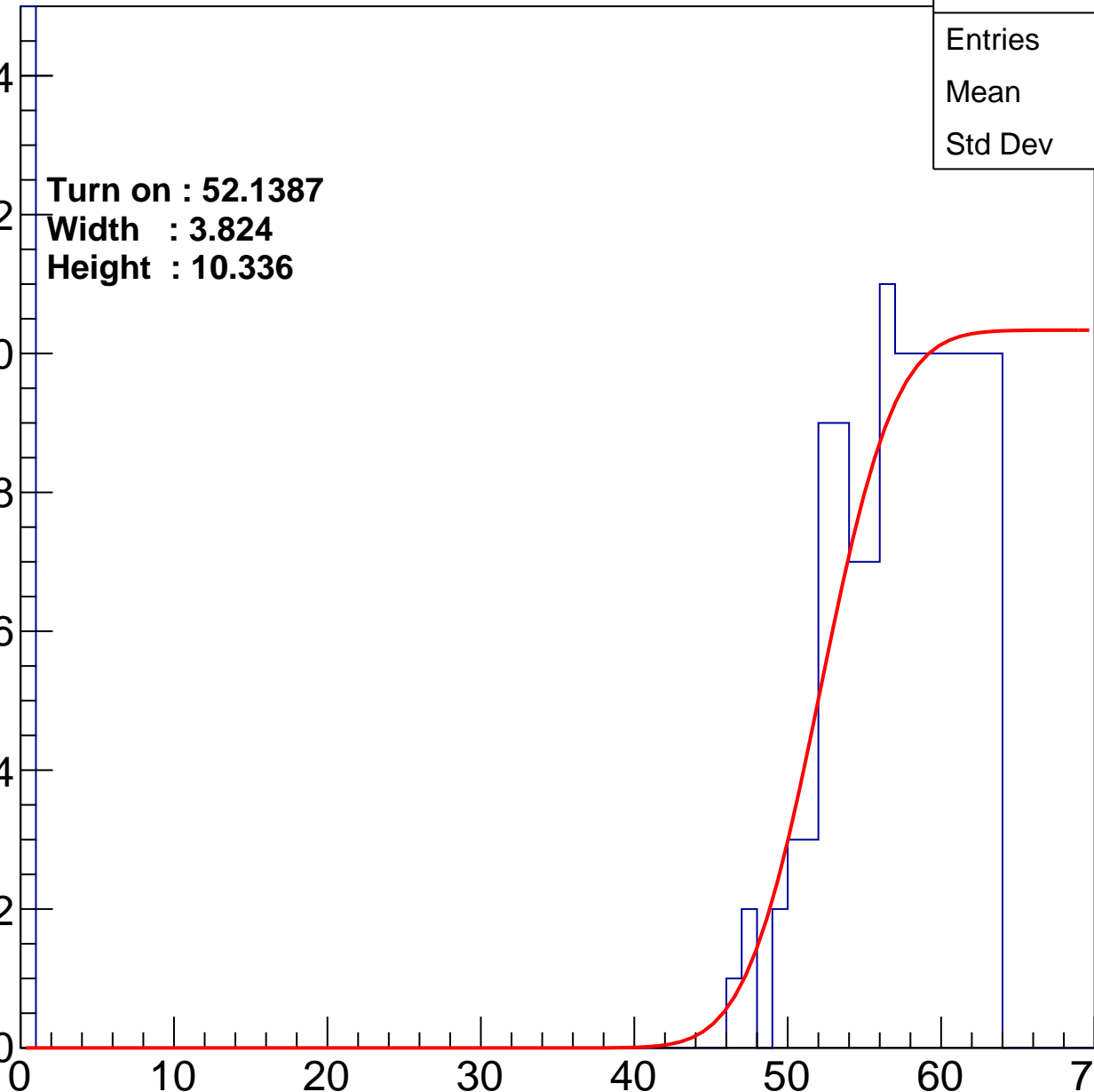
Width : 3.824

Height : 10.336

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	32.07
Std Dev	28.64

Turn on : 52.7421

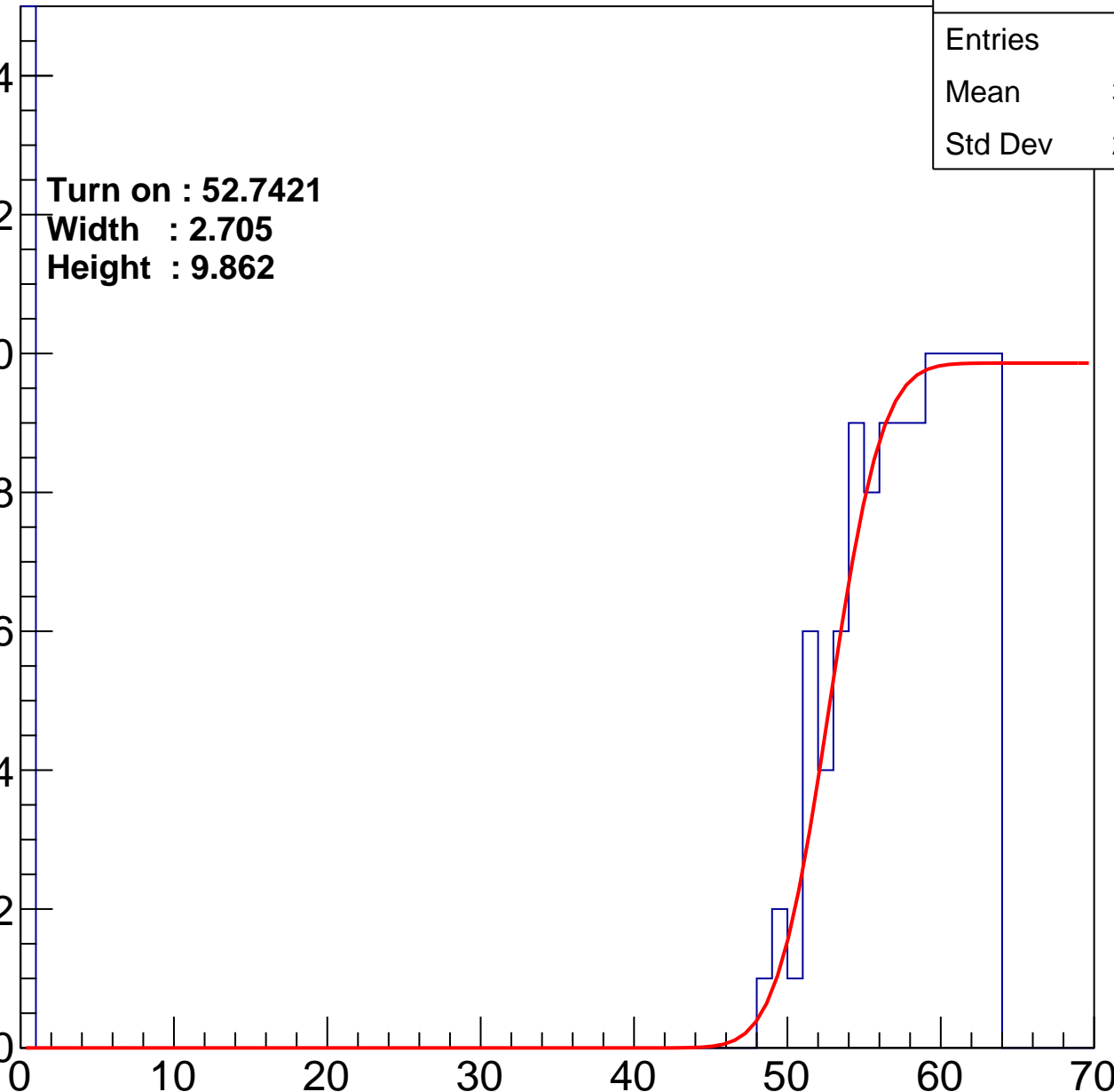
Width : 2.705

Height : 9.862

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.77
Std Dev	28.28

Turn on : 53.0274

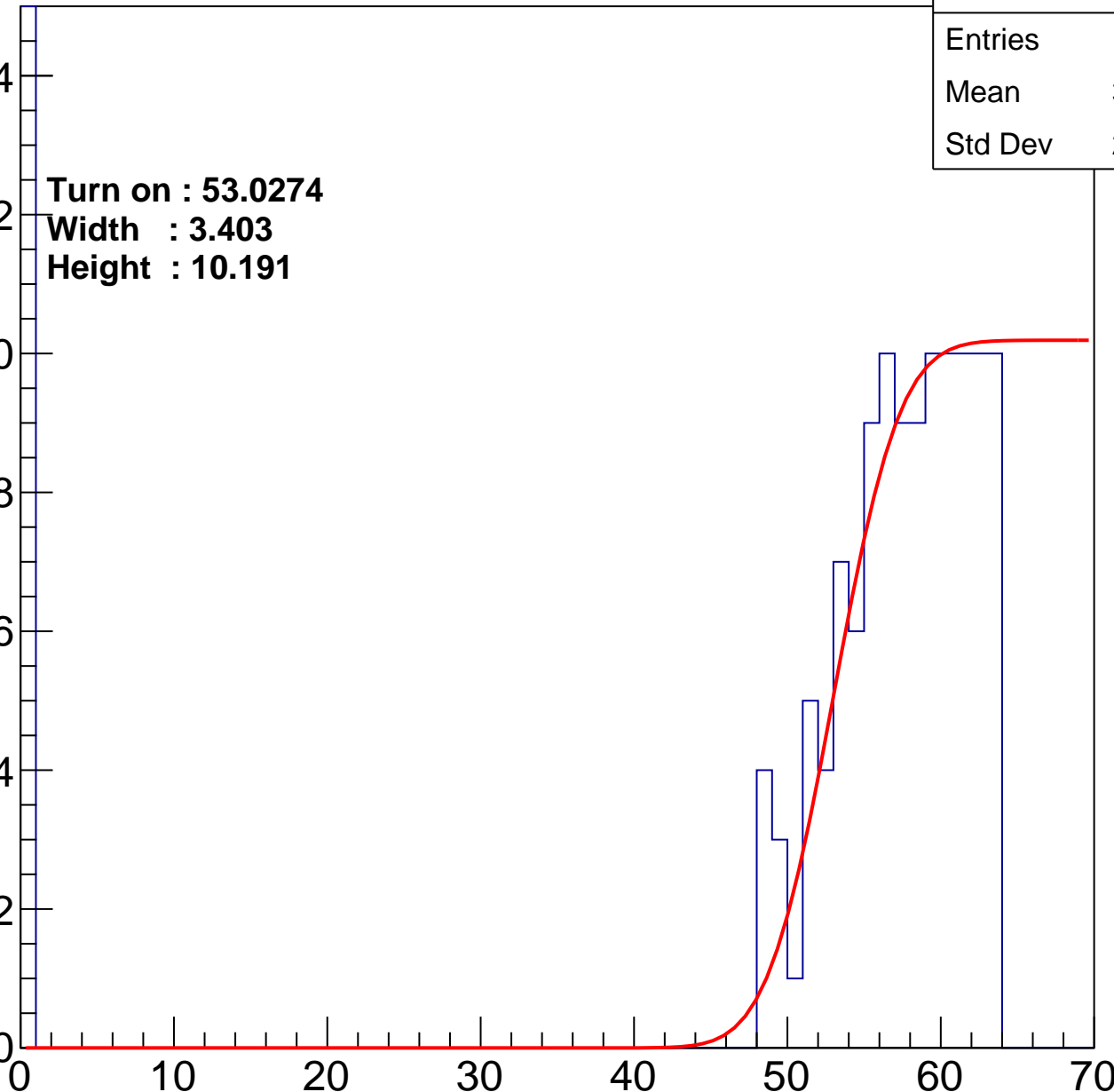
Width : 3.403

Height : 10.191

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch57

calib_packv5_033123_0516.root, FC#4, port A1

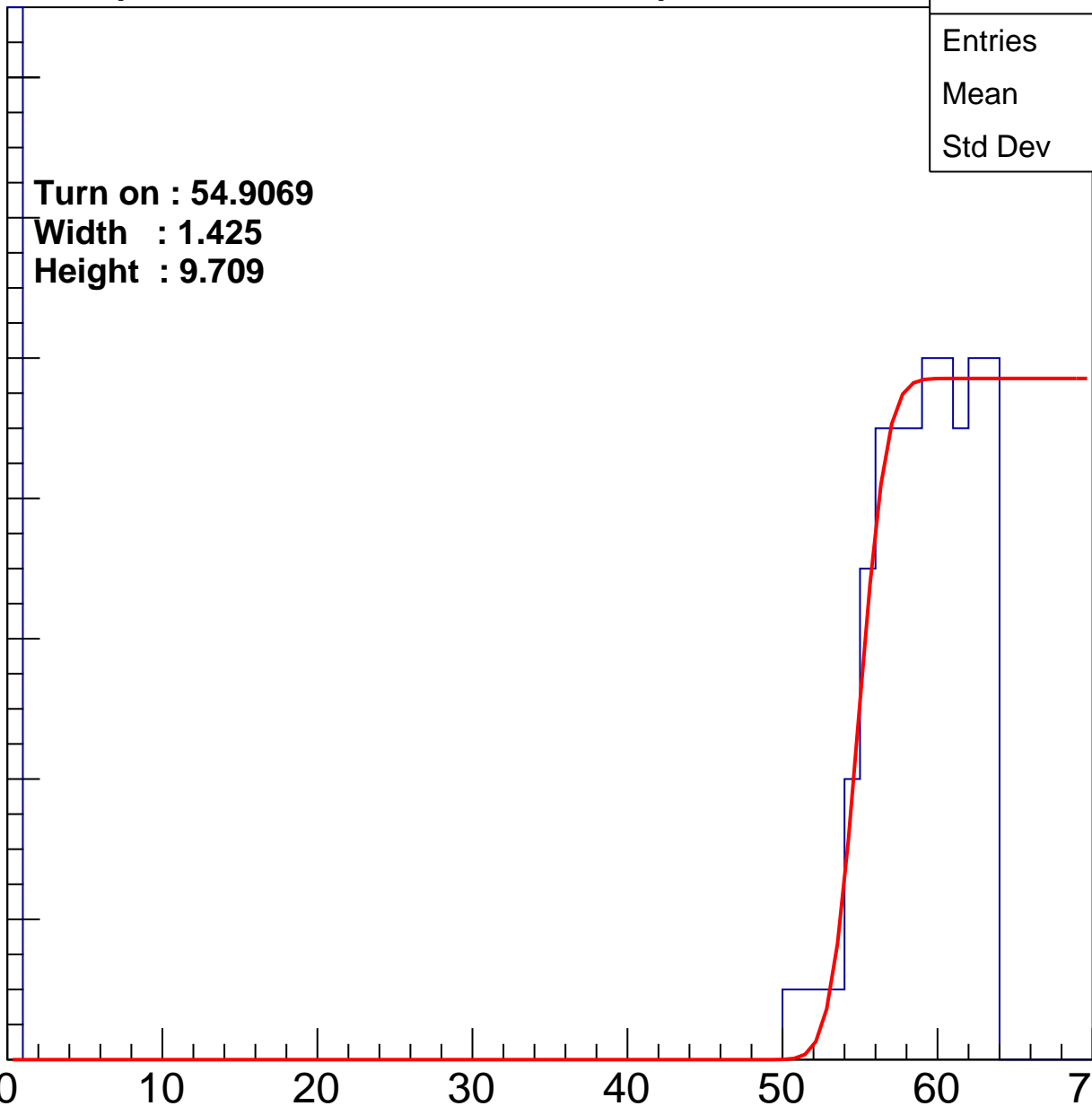
Entry

14
12
10
8
6
4
2
0

Turn on : 54.9069
Width : 1.425
Height : 9.709

Entries	153
Mean	34.87
Std Dev	28.88

ampl



B1L104S, U20-ch58

calib_packv5_033123_0516.root, FC#4, port A1

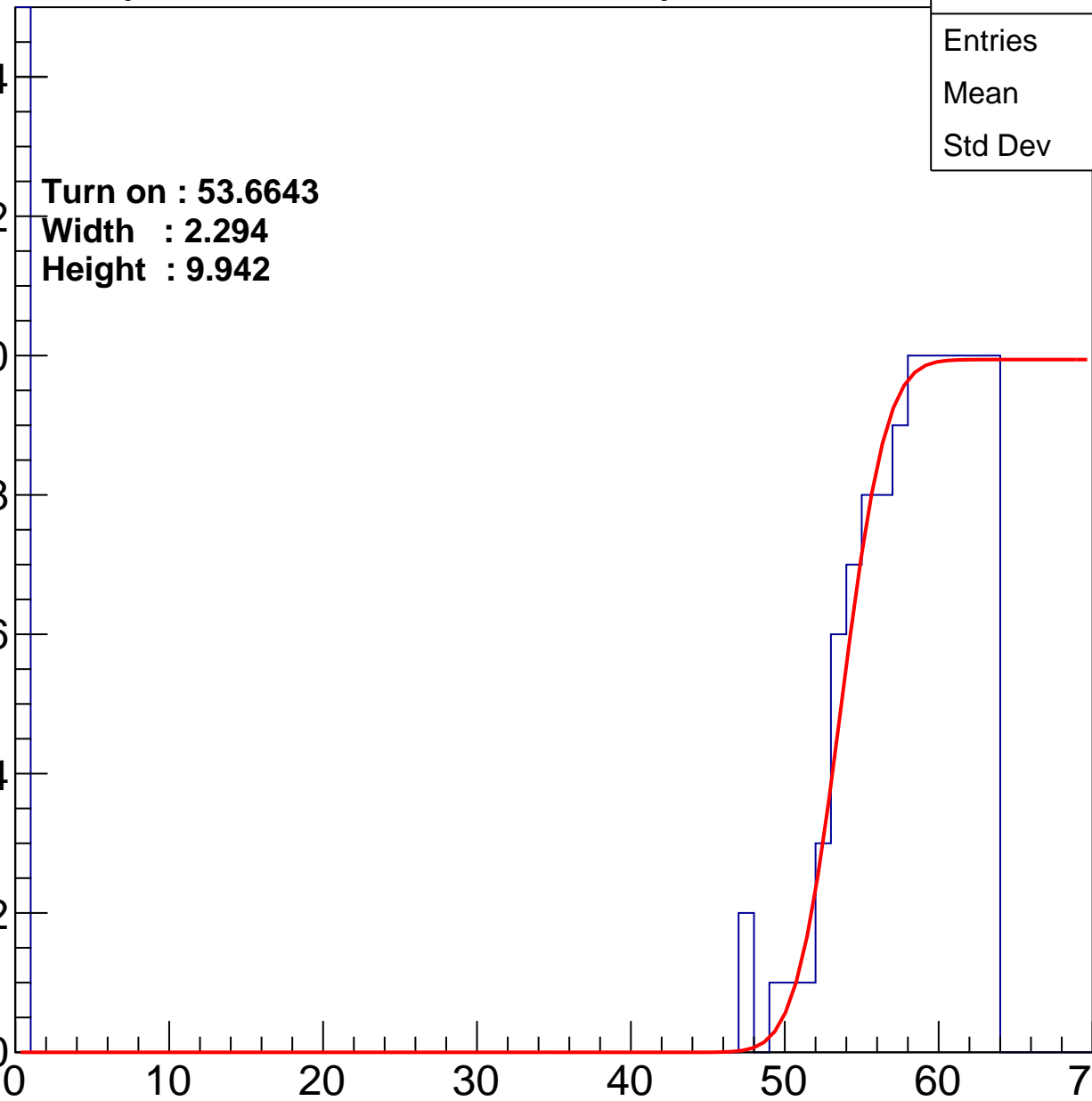
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6643
Width : 2.294
Height : 9.942

Entries	196
Mean	31.26
Std Dev	28.93

ampl



B1L104S, U20-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	37.9
Std Dev	27.12

Turn on : 51.3872

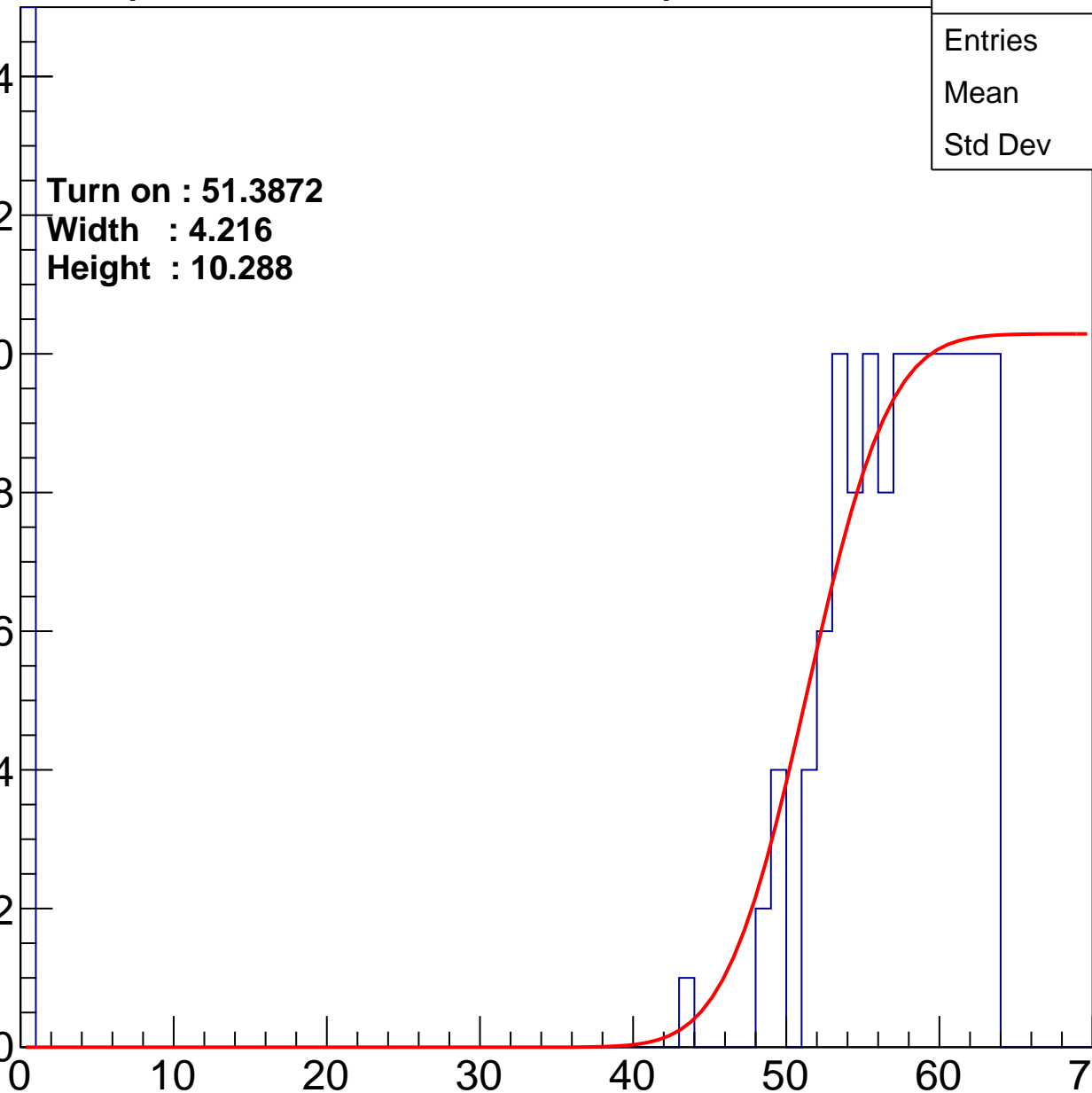
Width : 4.216

Height : 10.288

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	28.62
Std Dev	29

Turn on : 53.8905

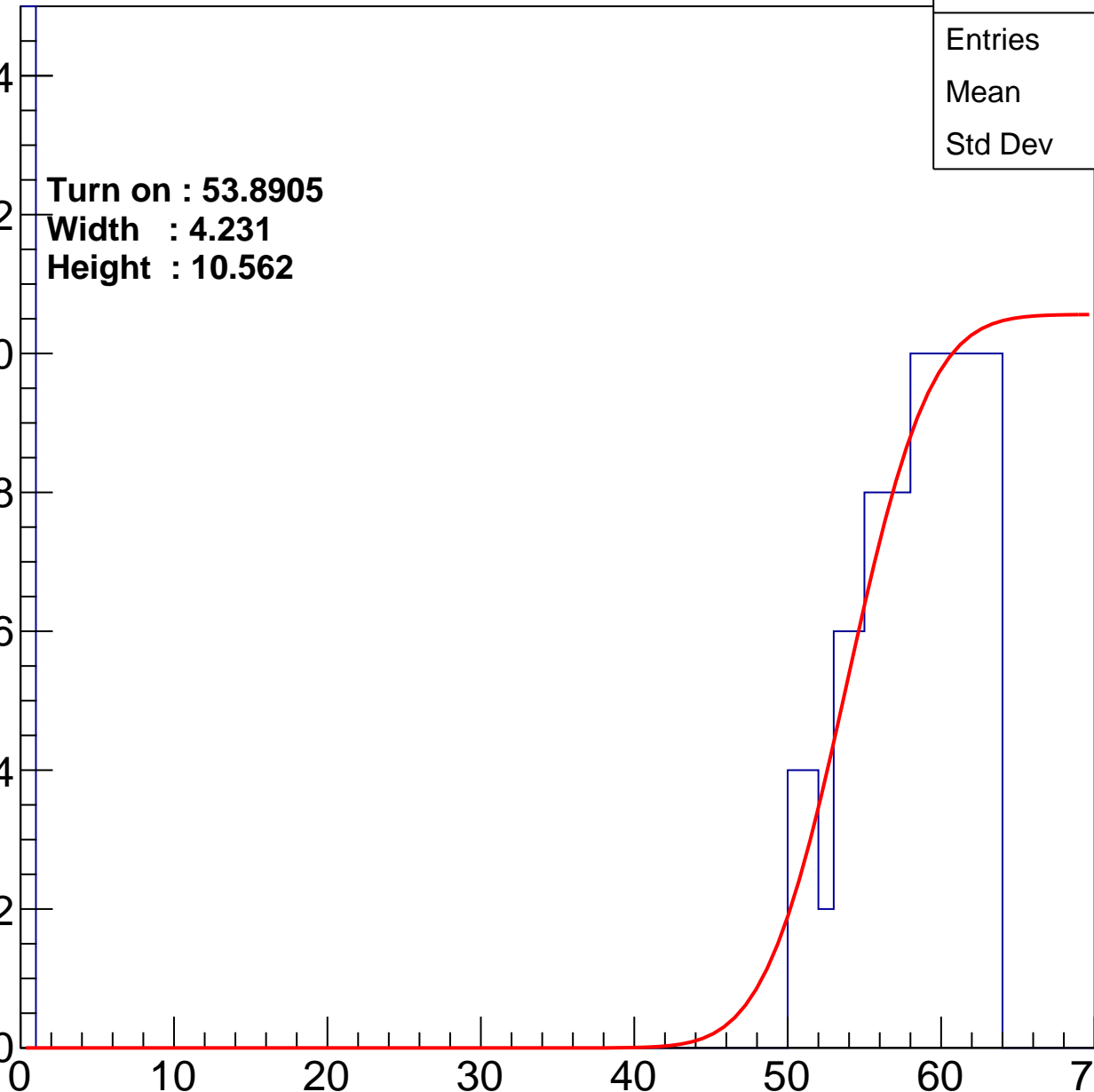
Width : 4.231

Height : 10.562

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	34.01
Std Dev	28.6

Turn on : 53.3483

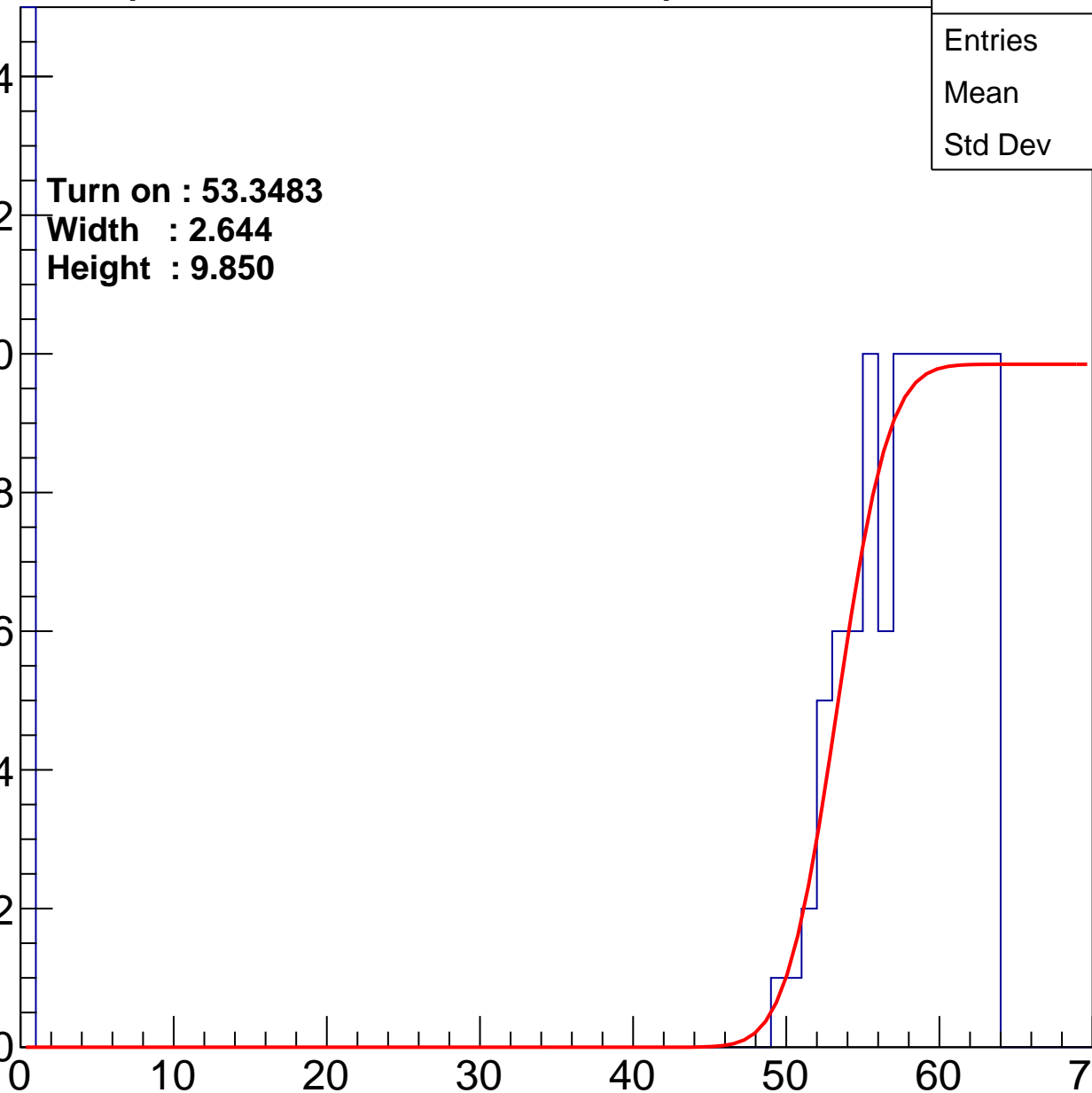
Width : 2.644

Height : 9.850

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.4
Std Dev	28.15

Turn on : 53.2574

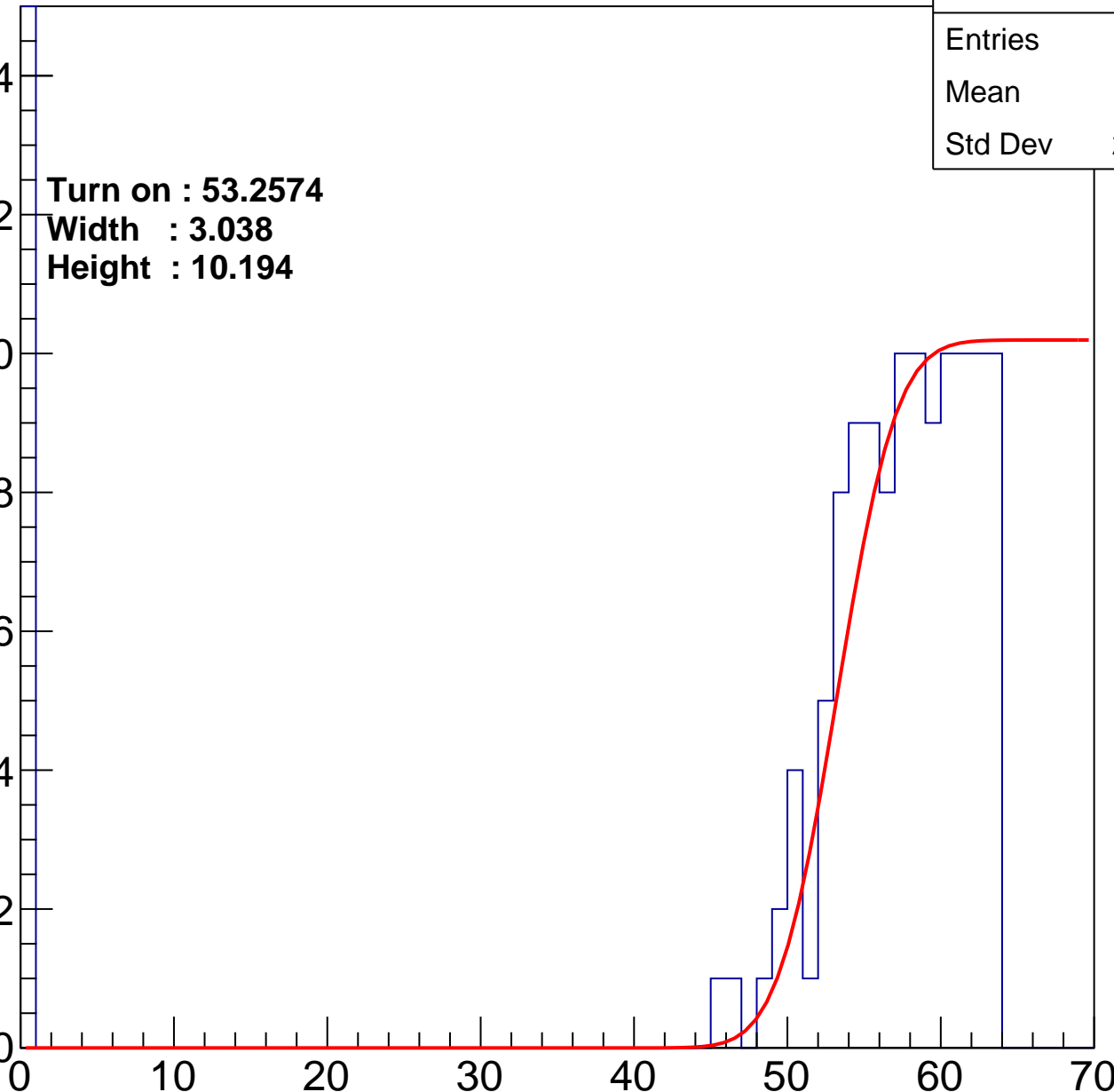
Width : 3.038

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	38.31
Std Dev	27.15

Turn on : 52.0753

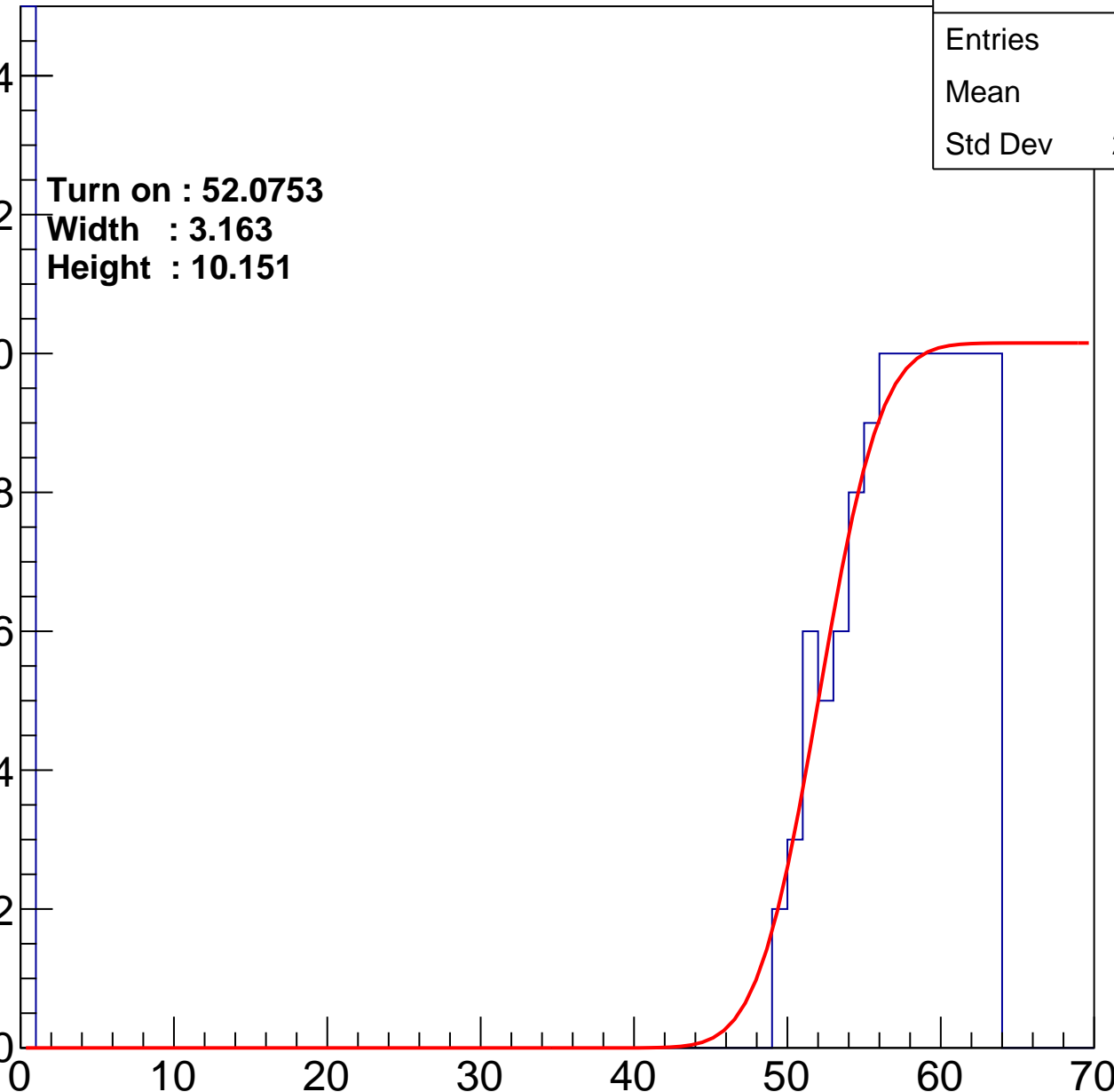
Width : 3.163

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	34.81
Std Dev	28.05

Turn on : 51.6480

Width : 2.614

Height : 9.698

Entry

14

12

10

8

6

4

2

0

0

10

20

30

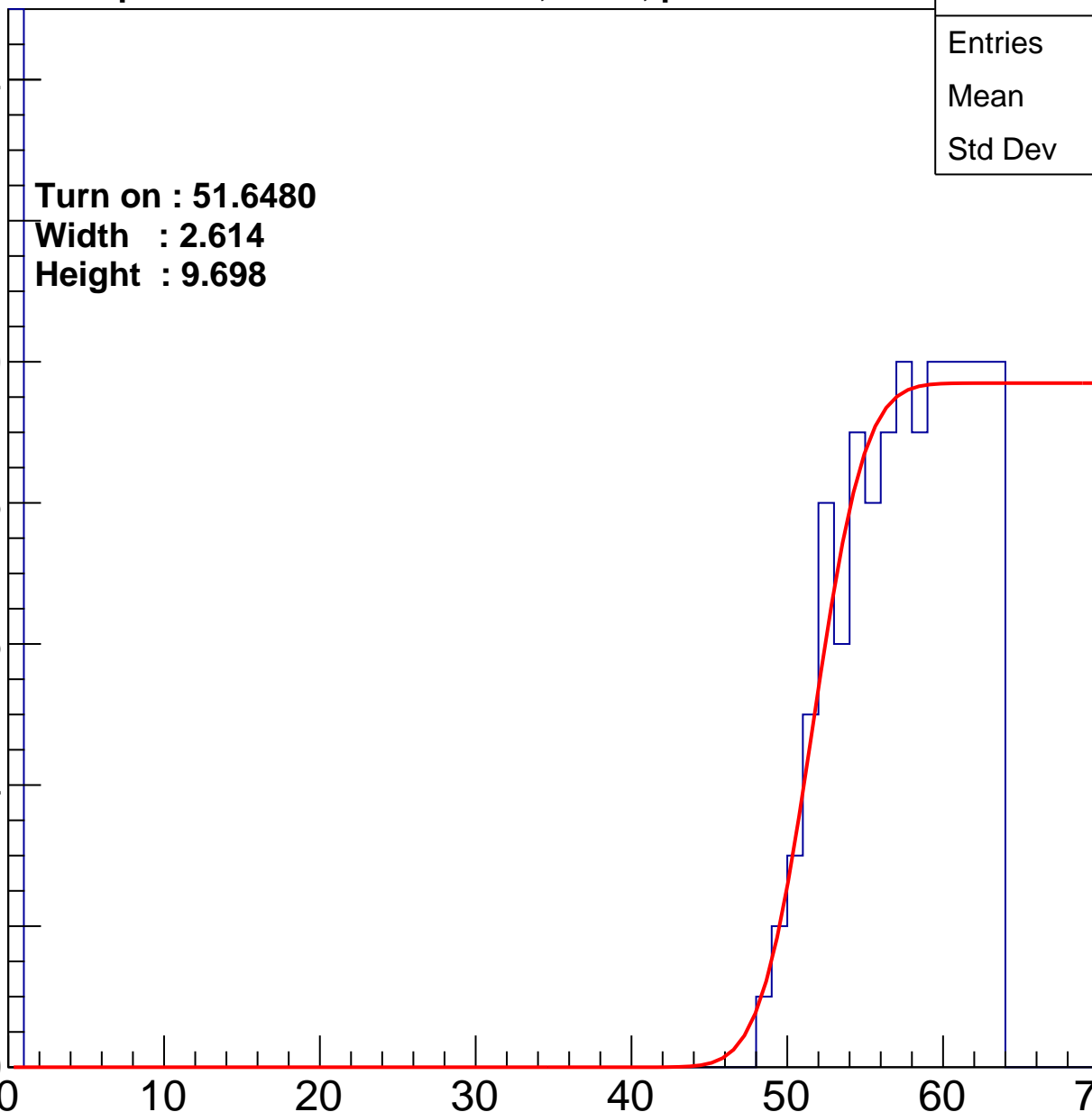
40

50

60

70

ampl



B1L104S, U20-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	30.18
Std Dev	28.98

Turn on : 53.4445

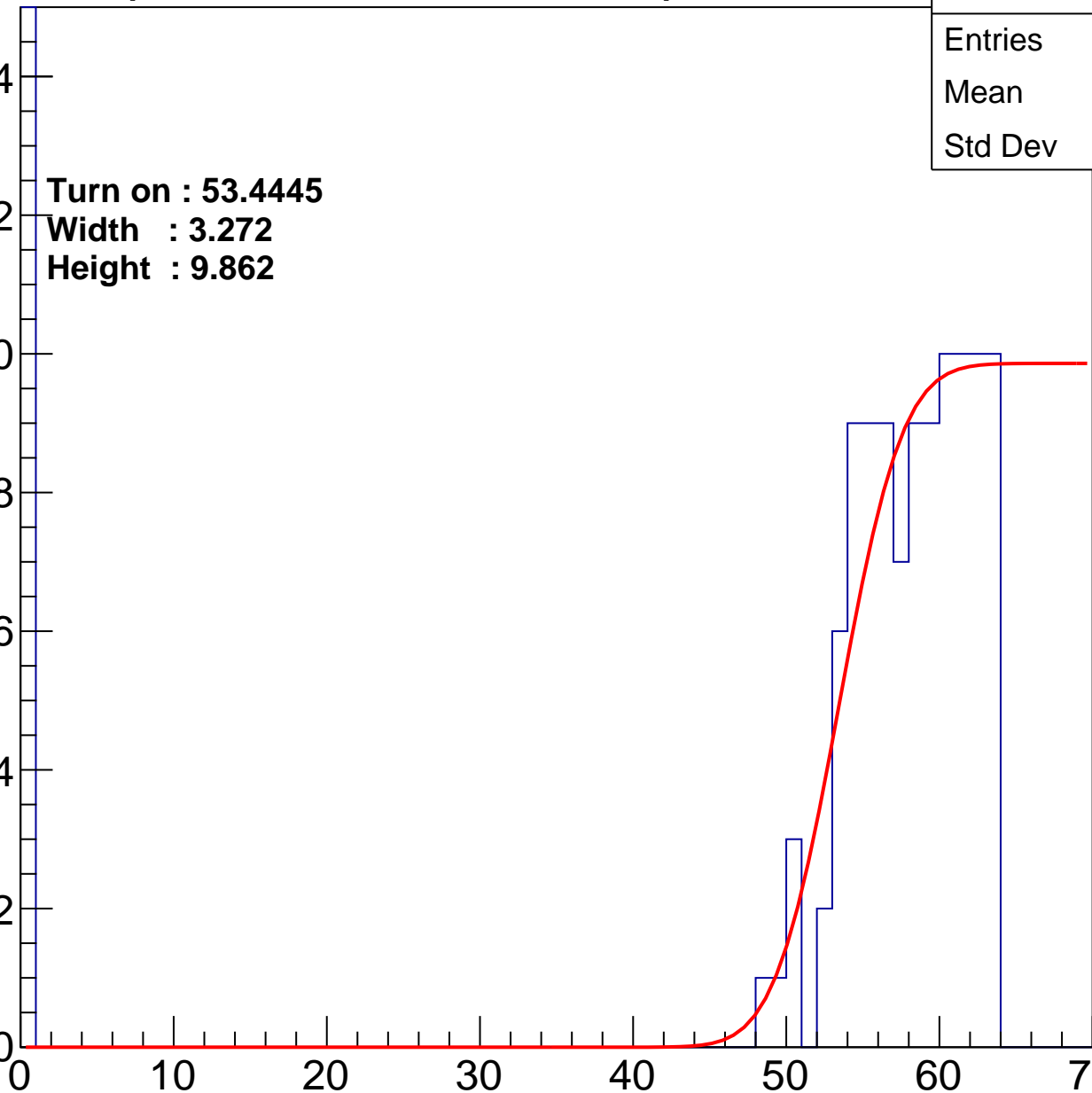
Width : 3.272

Height : 9.862

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch66

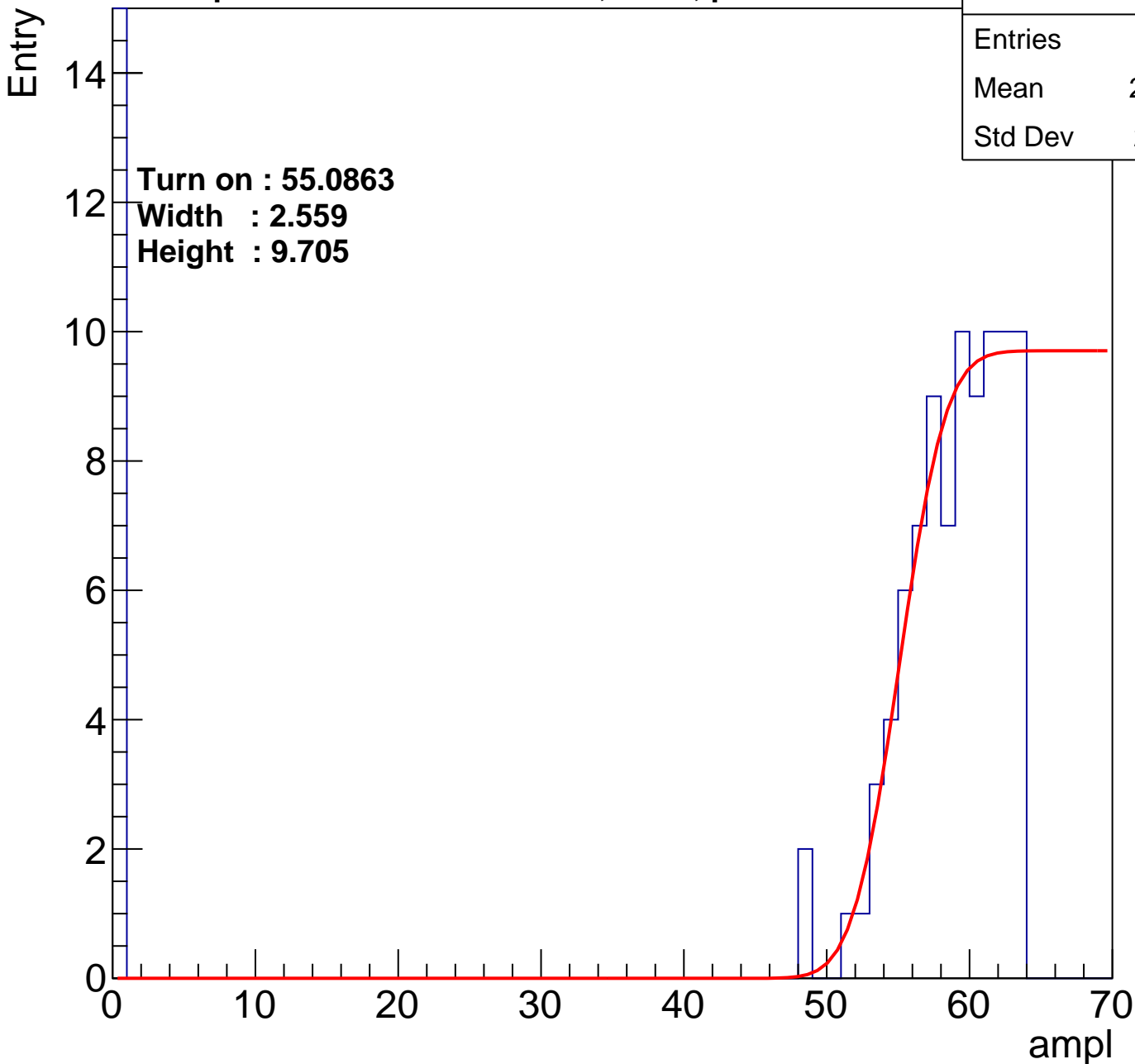
calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	26.56
Std Dev	29.21

Turn on : 55.0863

Width : 2.559

Height : 9.705



B1L104S, U20-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.49
Std Dev	28.42

Turn on : 52.9266

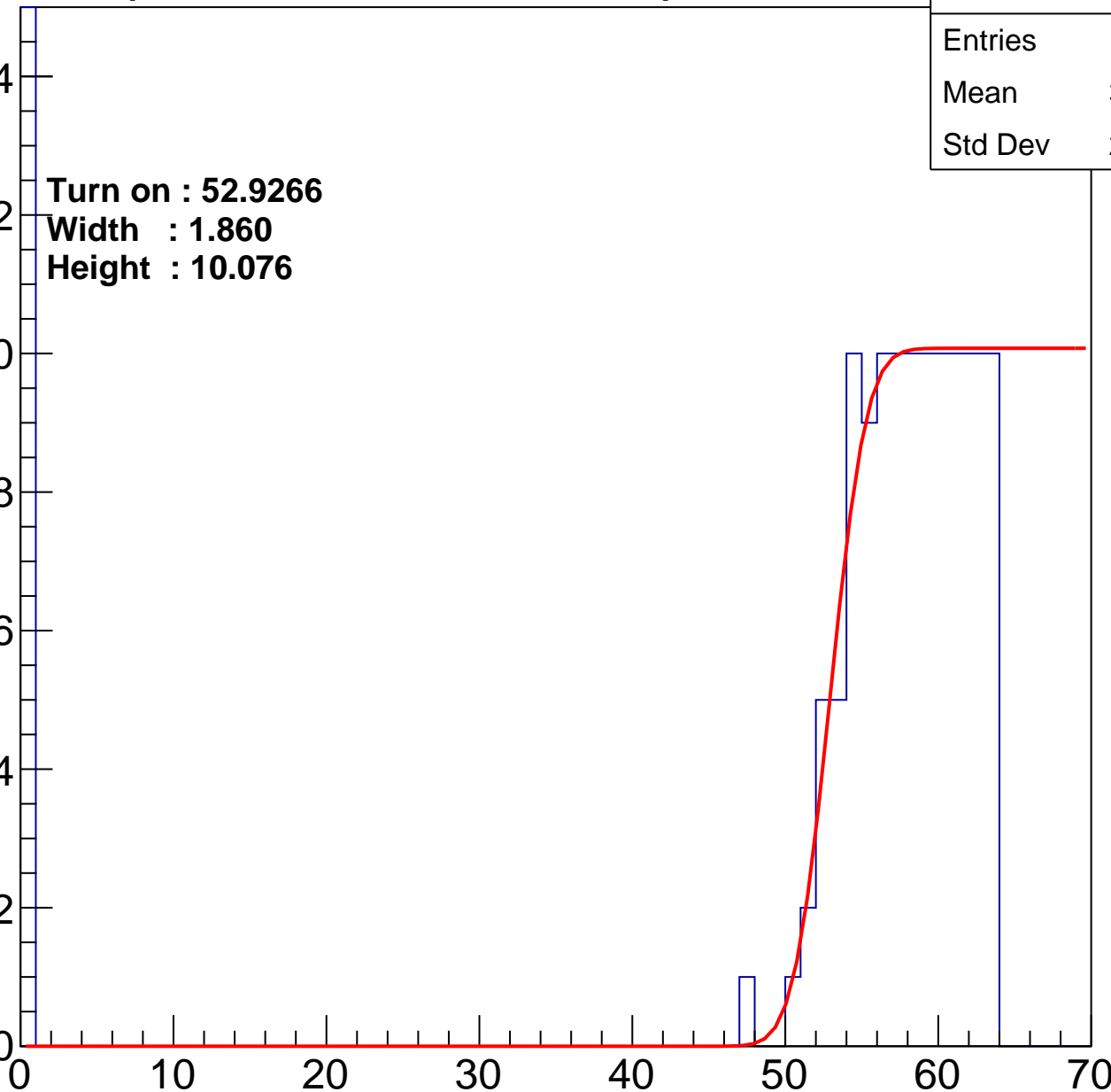
Width : 1.860

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	33.61
Std Dev	28.75

Turn on : 54.0482

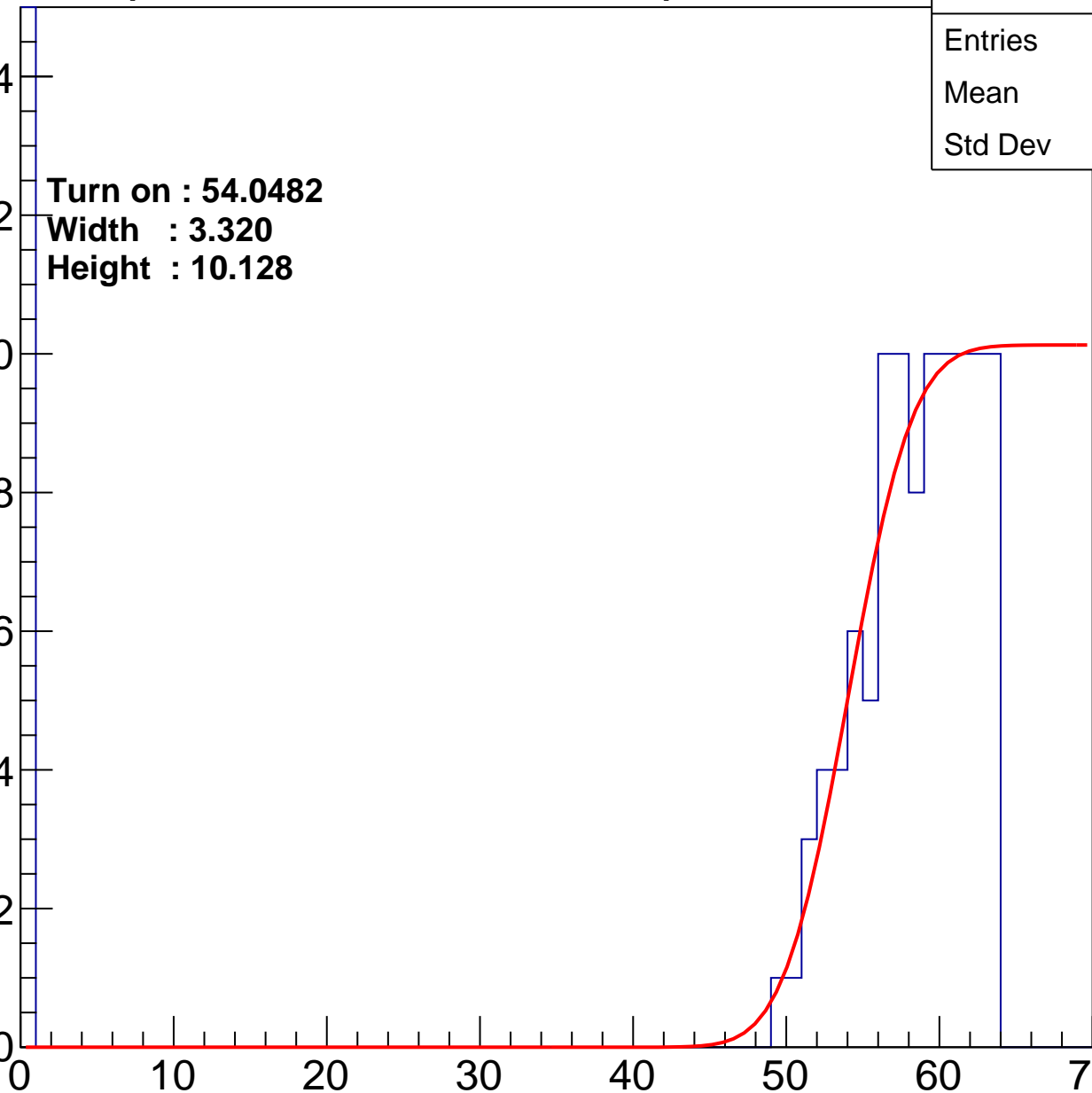
Width : 3.320

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch69

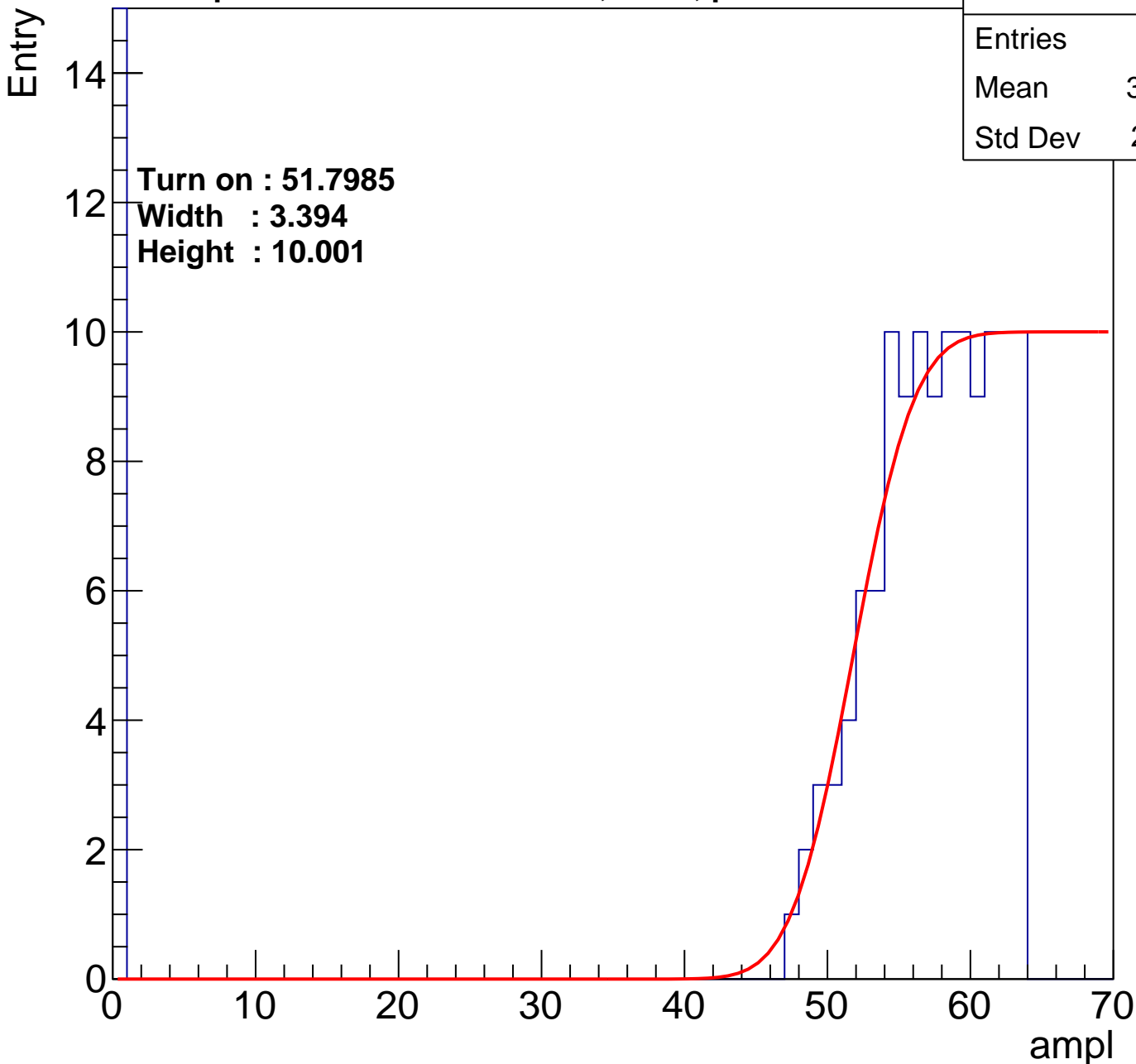
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	32.95
Std Dev	28.31

Turn on : 51.7985

Width : 3.394

Height : 10.001



B1L104S, U20-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.92
Std Dev	28.51

Turn on : 53.1087

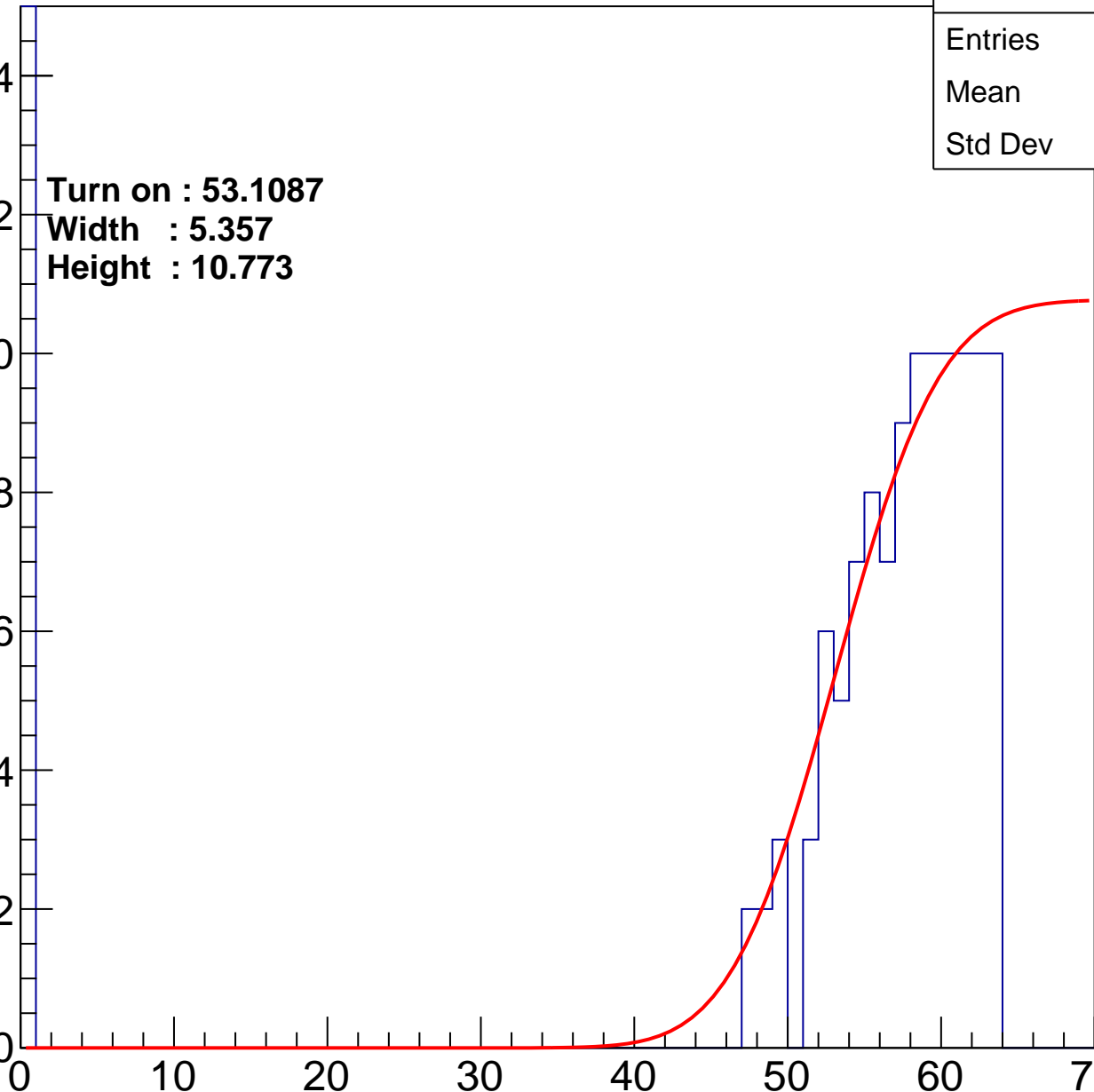
Width : 5.357

Height : 10.773

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	33.56
Std Dev	28.88

Turn on : 54.7234

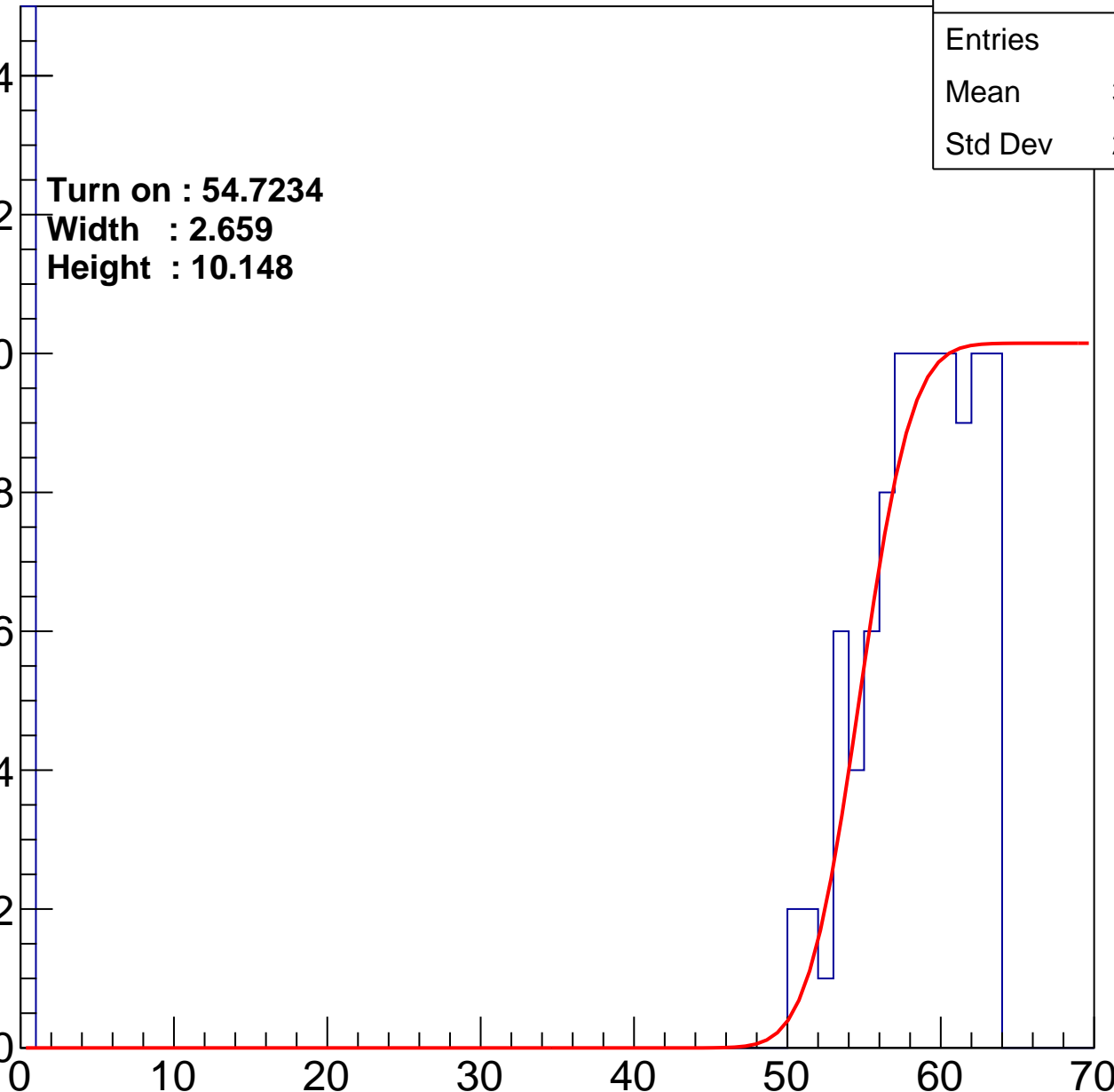
Width : 2.659

Height : 10.148

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.8
Std Dev	28.67

Turn on : 53.2135

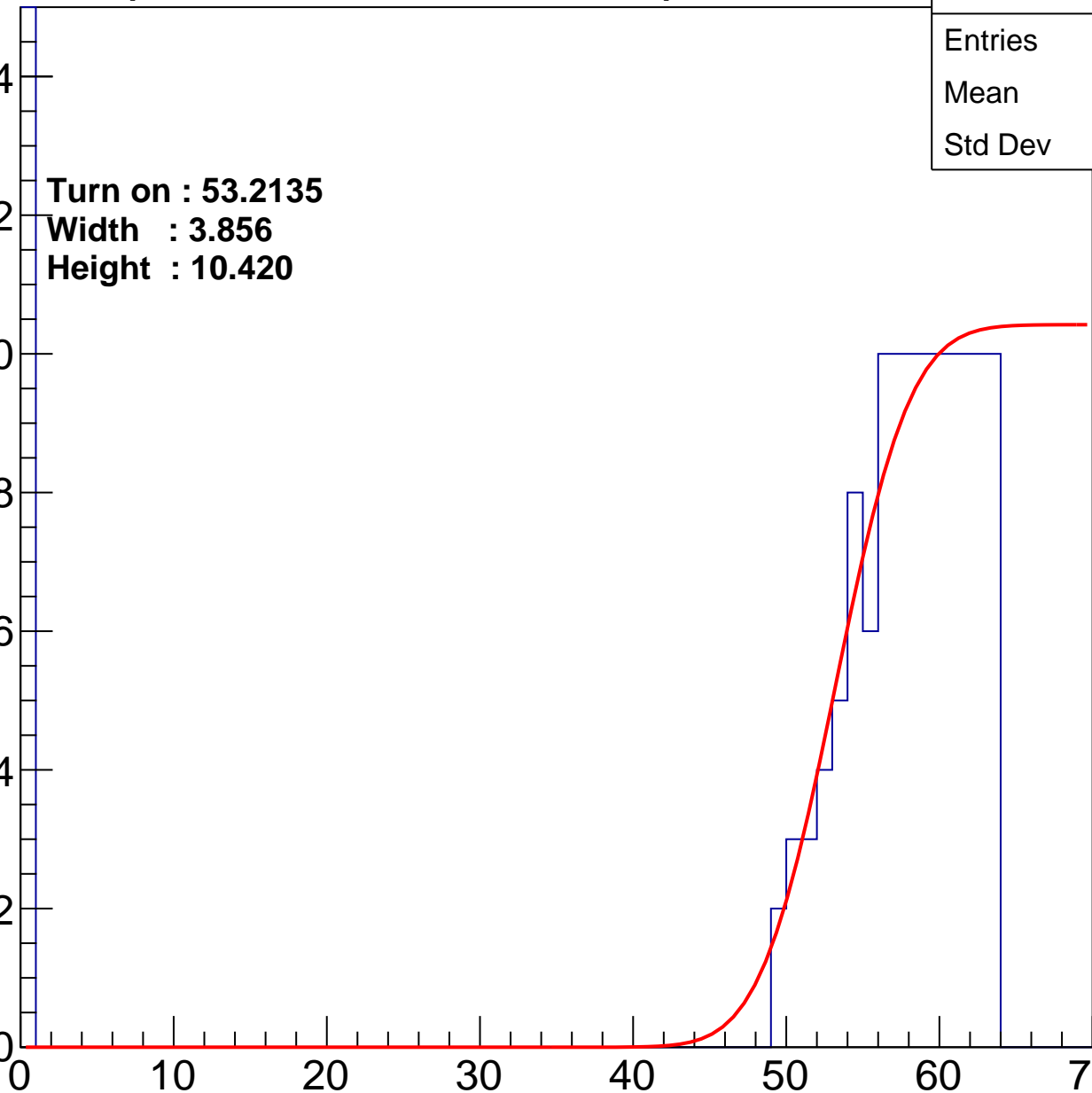
Width : 3.856

Height : 10.420

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	33.28
Std Dev	29.19

Turn on : 55.7739

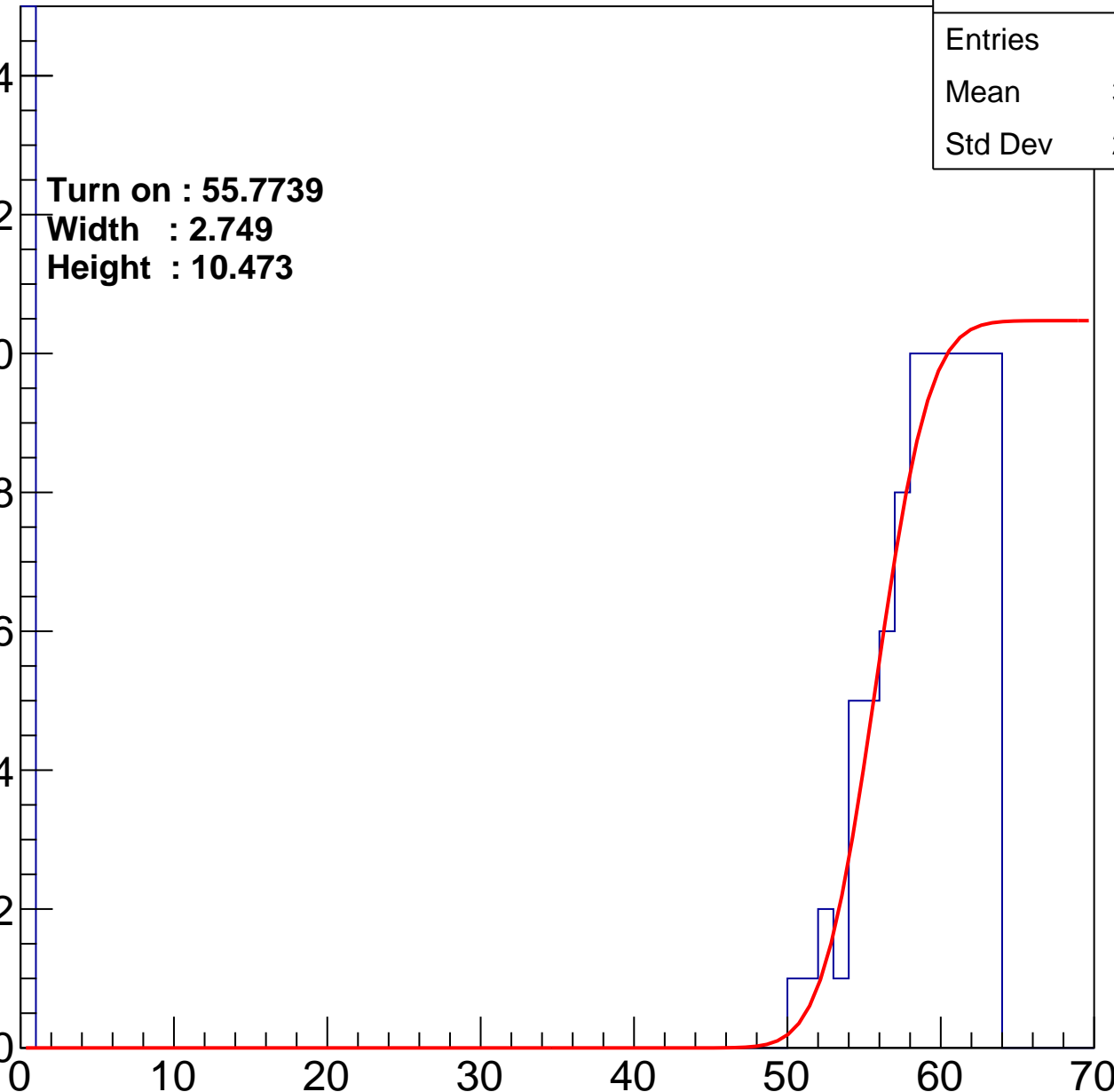
Width : 2.749

Height : 10.473

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	31
Std Dev	29.29

Turn on : 55.9883

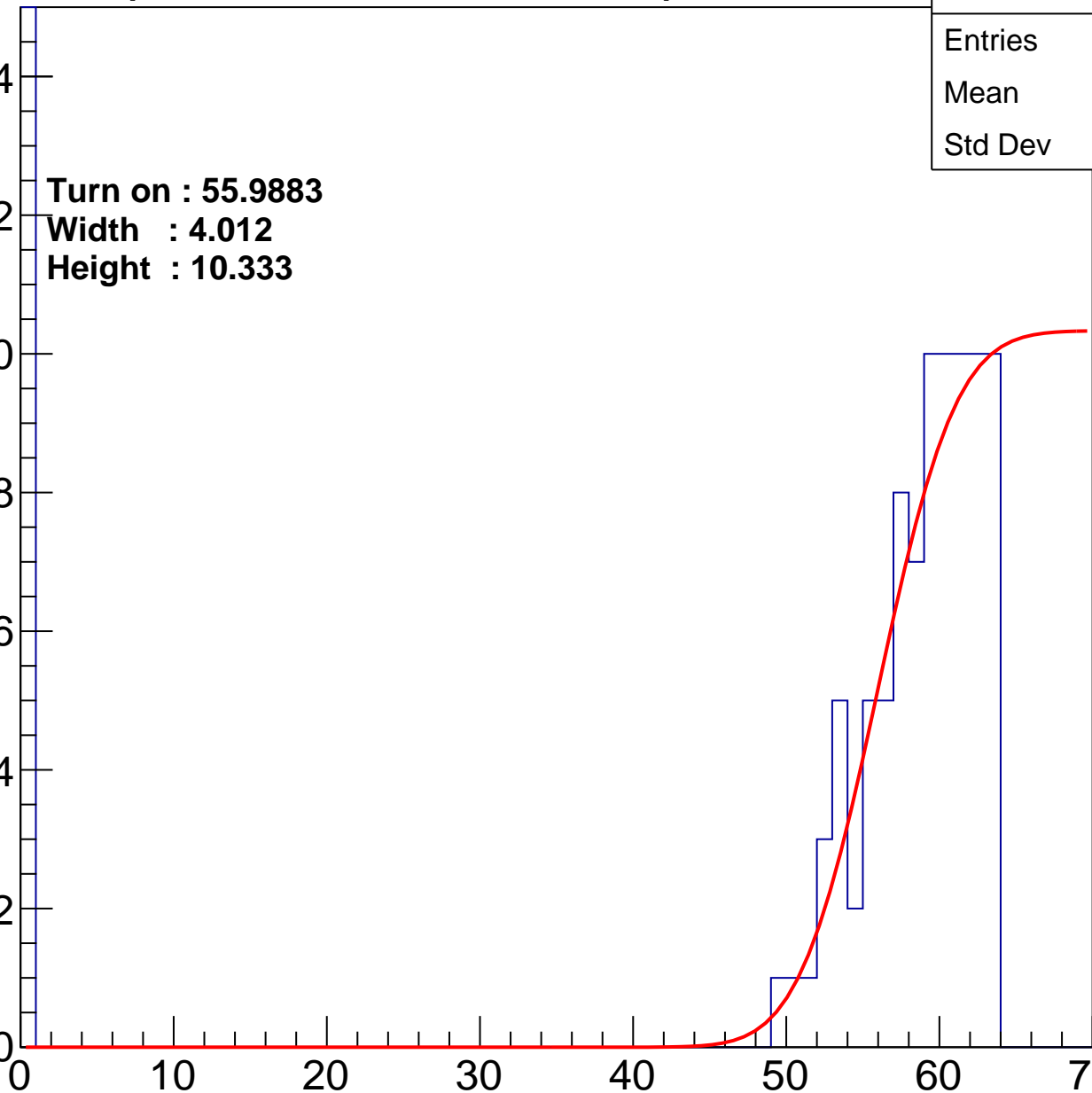
Width : 4.012

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch75

calib_packv5_033123_0516.root, FC#4, port A1

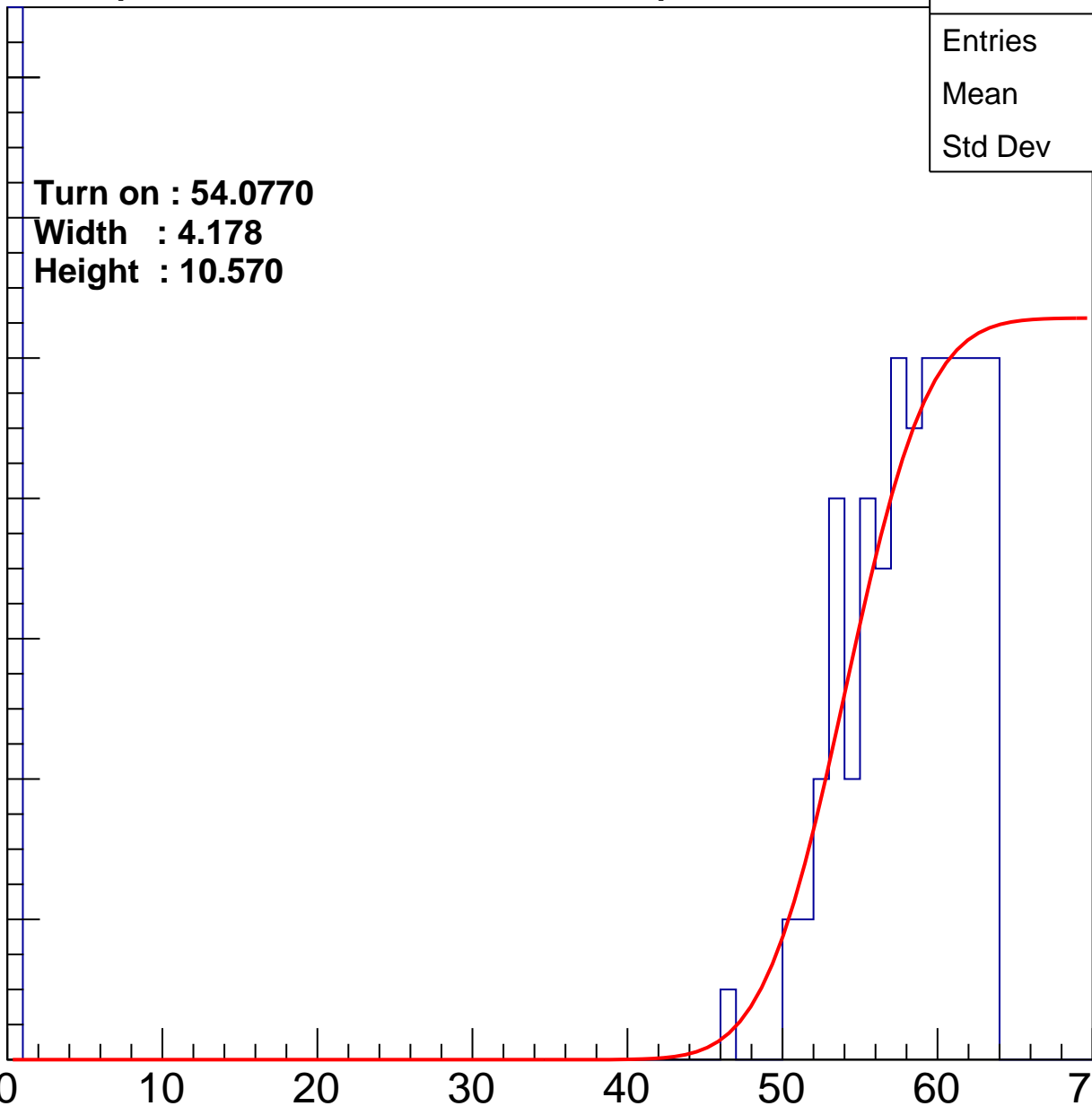
Entry

14
12
10
8
6
4
2
0

Turn on : 54.0770
Width : 4.178
Height : 10.570

Entries	179
Mean	33.91
Std Dev	28.61

ampl



B1L104S, U20-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	36.73
Std Dev	27.73

Turn on : 53.4667

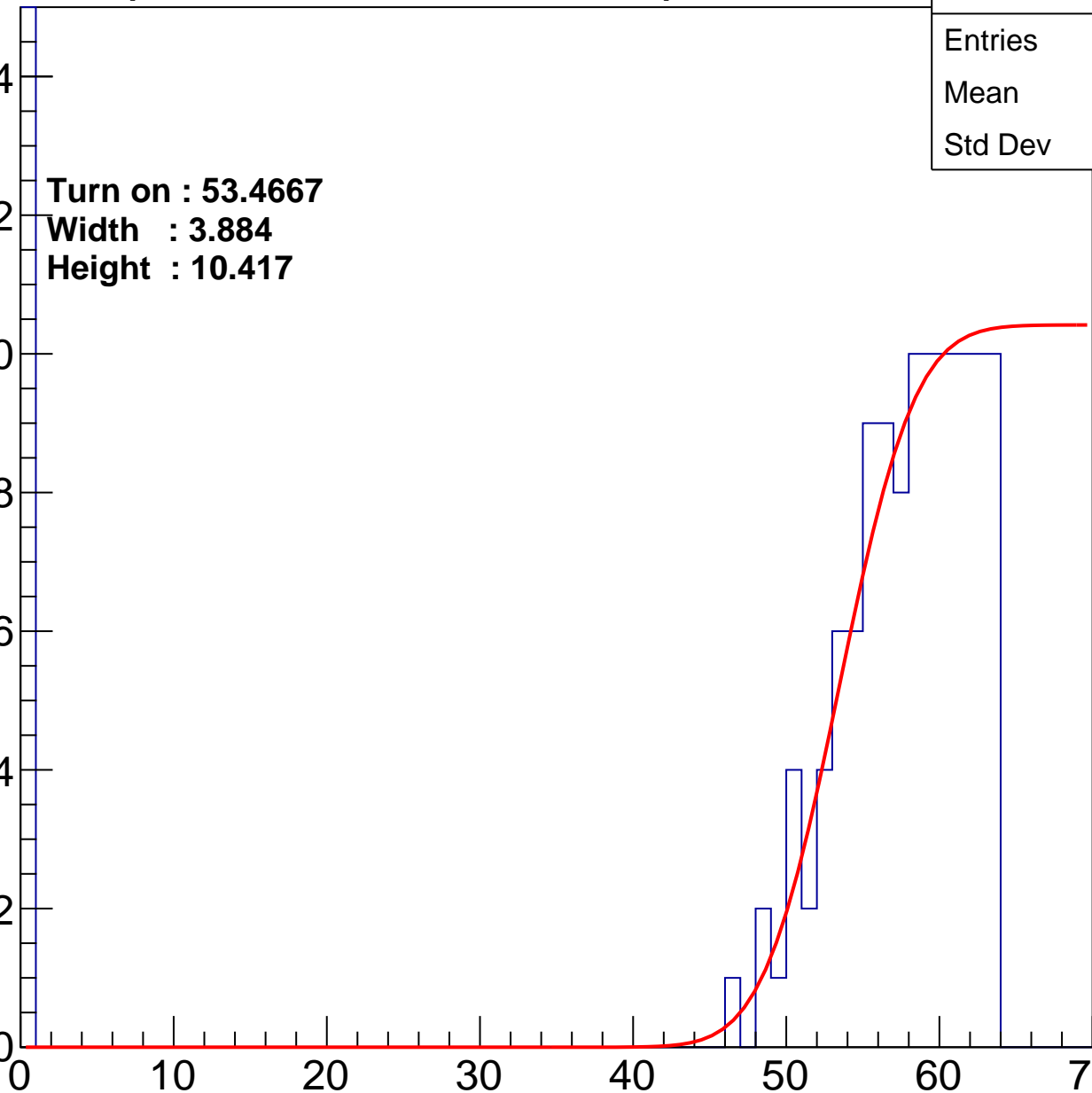
Width : 3.884

Height : 10.417

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	30.98
Std Dev	28.87

Turn on : 54.5340

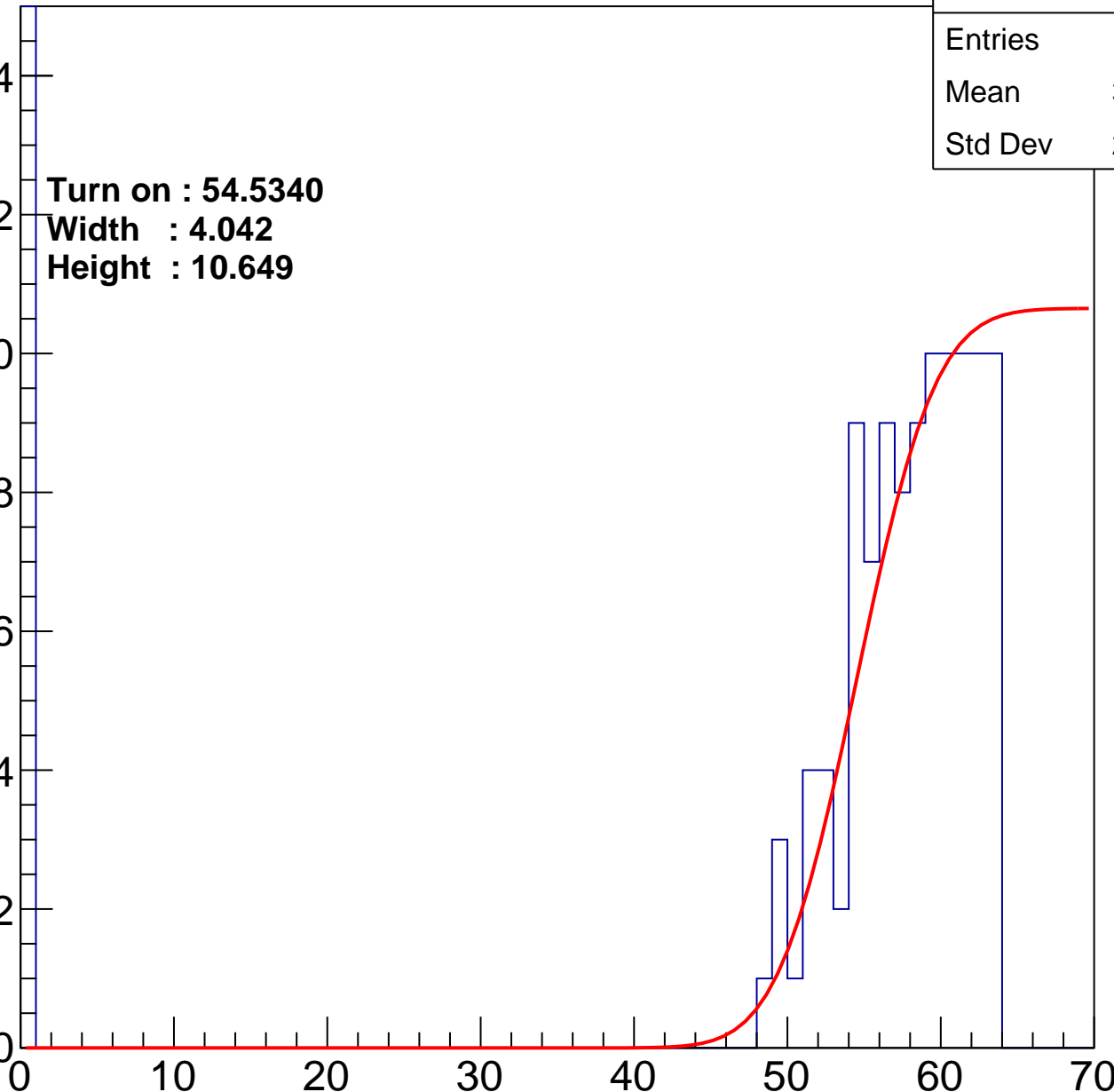
Width : 4.042

Height : 10.649

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch78

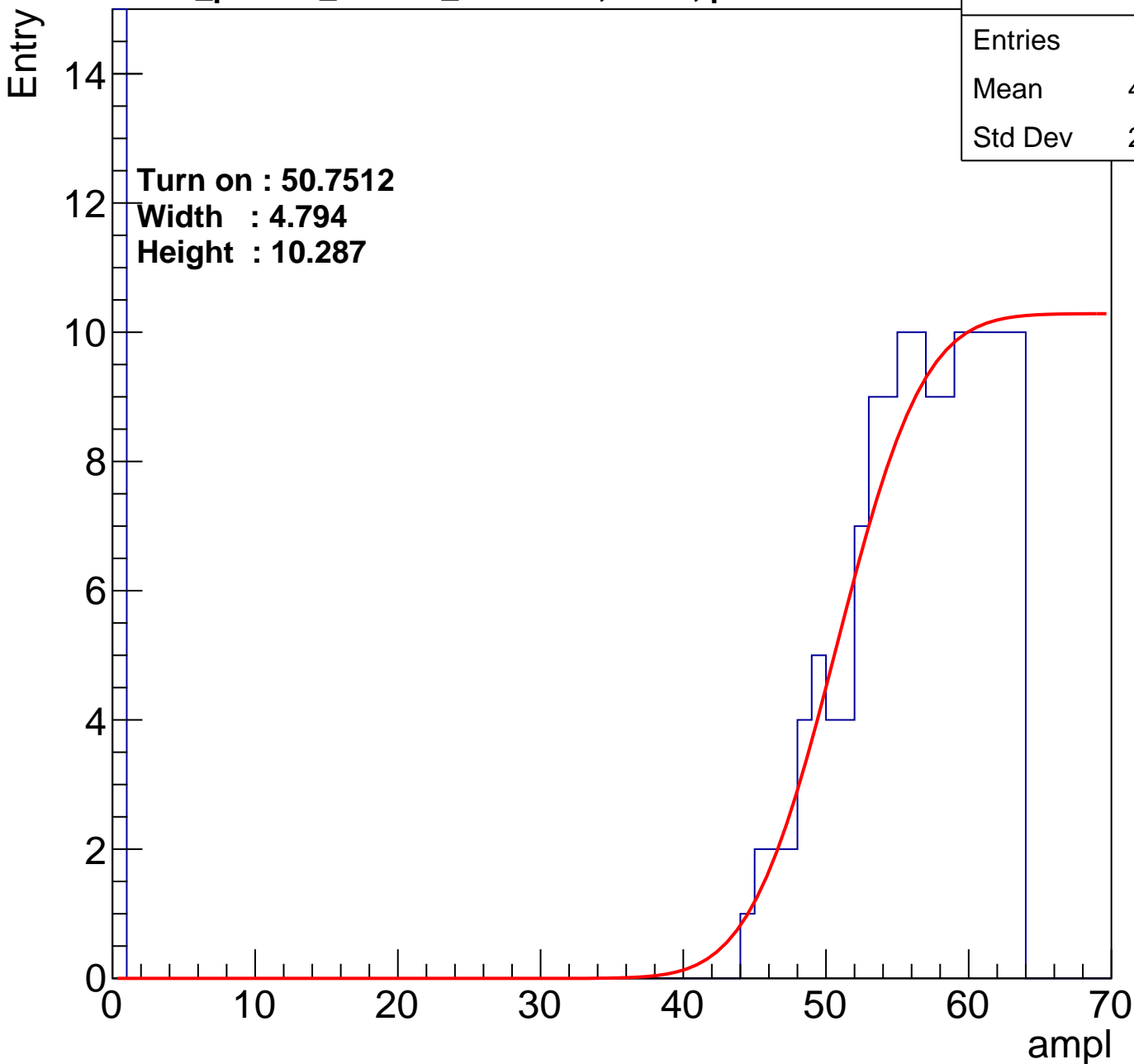
calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	40.44
Std Dev	25.48

Turn on : 50.7512

Width : 4.794

Height : 10.287



B1L104S, U20-ch79

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	29.75
Std Dev	29.12

Turn on : 54.7737

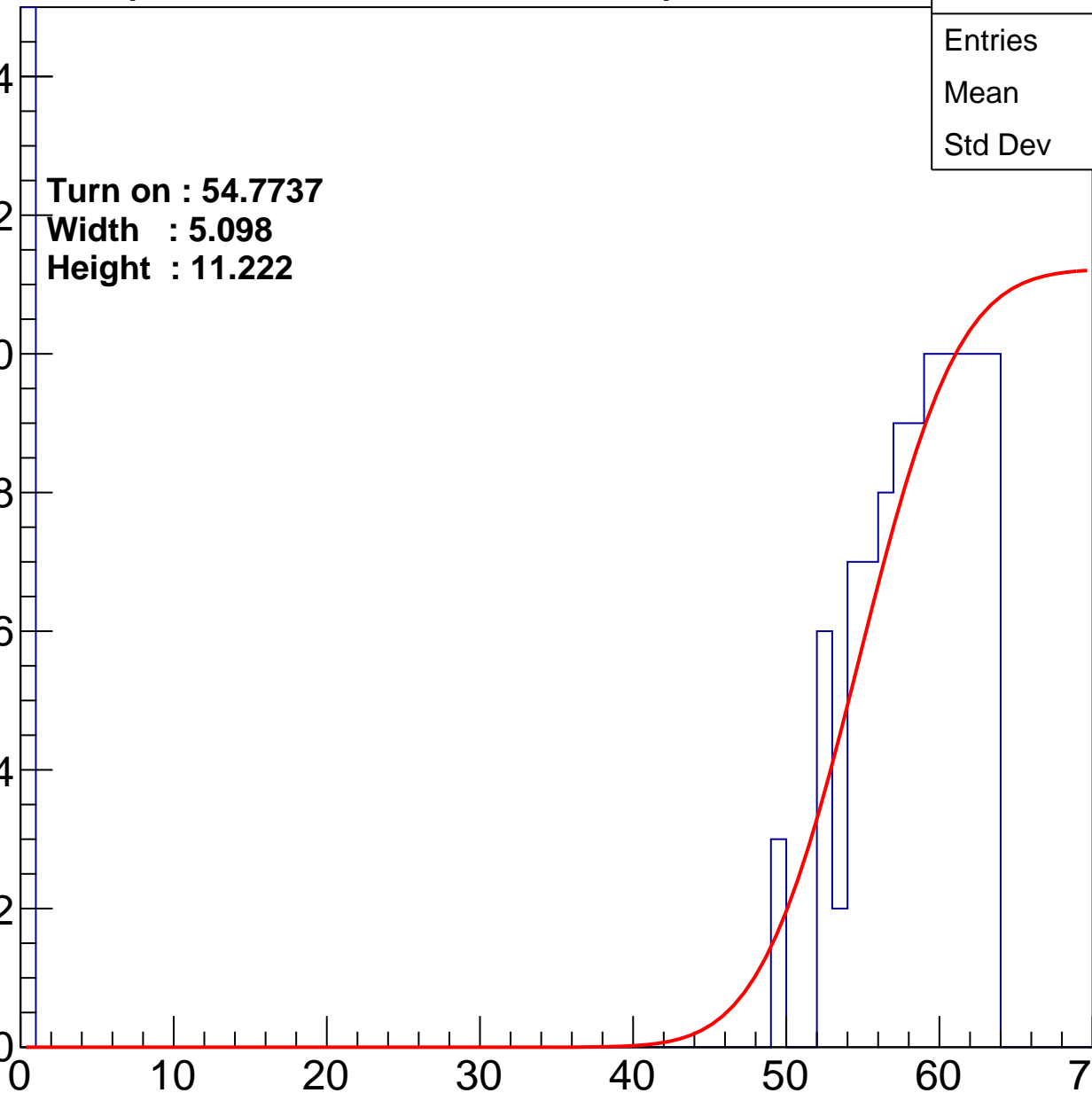
Width : 5.098

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	35.16
Std Dev	28.16

Turn on : 52.5696

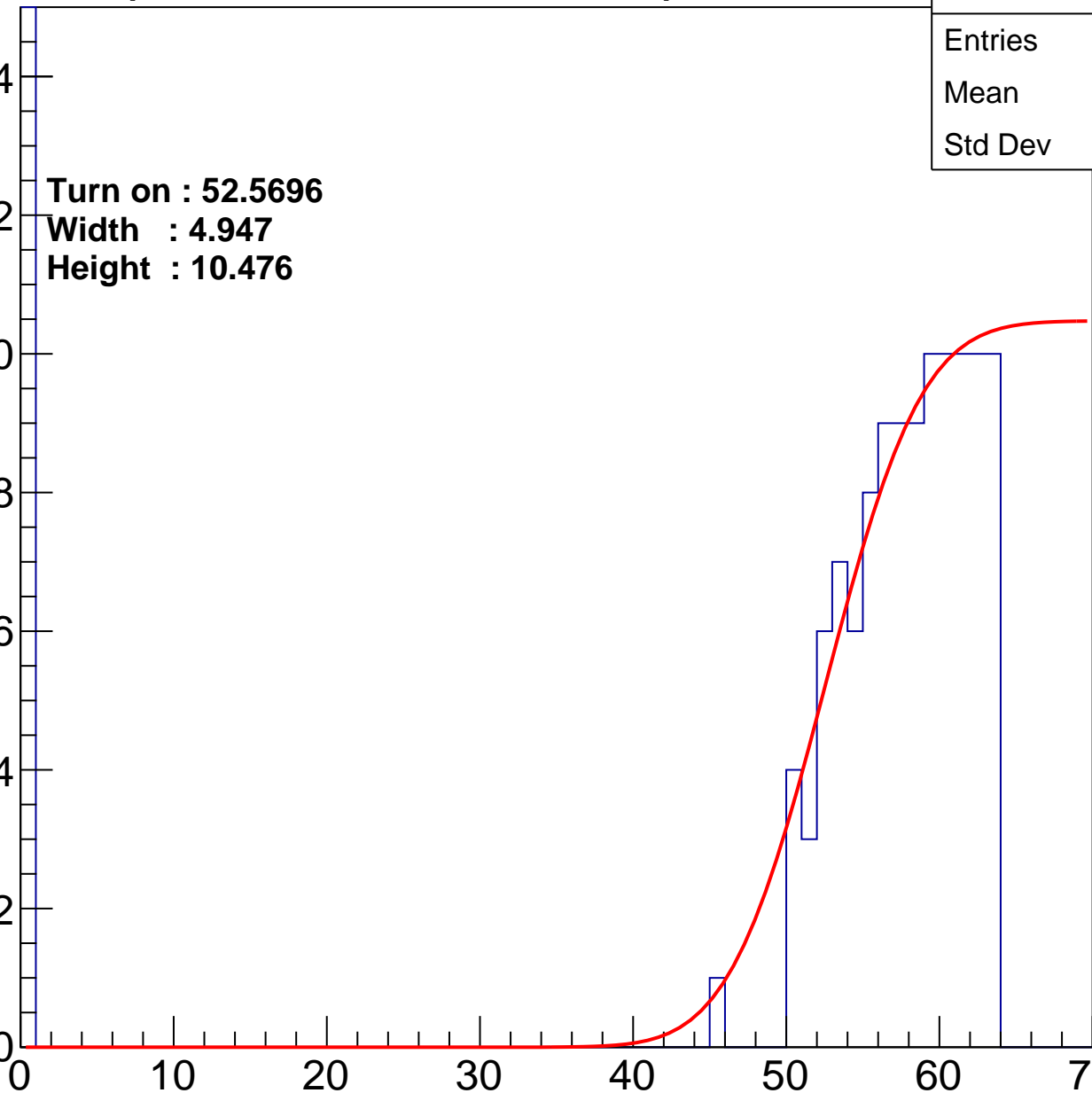
Width : 4.947

Height : 10.476

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	31.18
Std Dev	29.1

Turn on : 54.6302

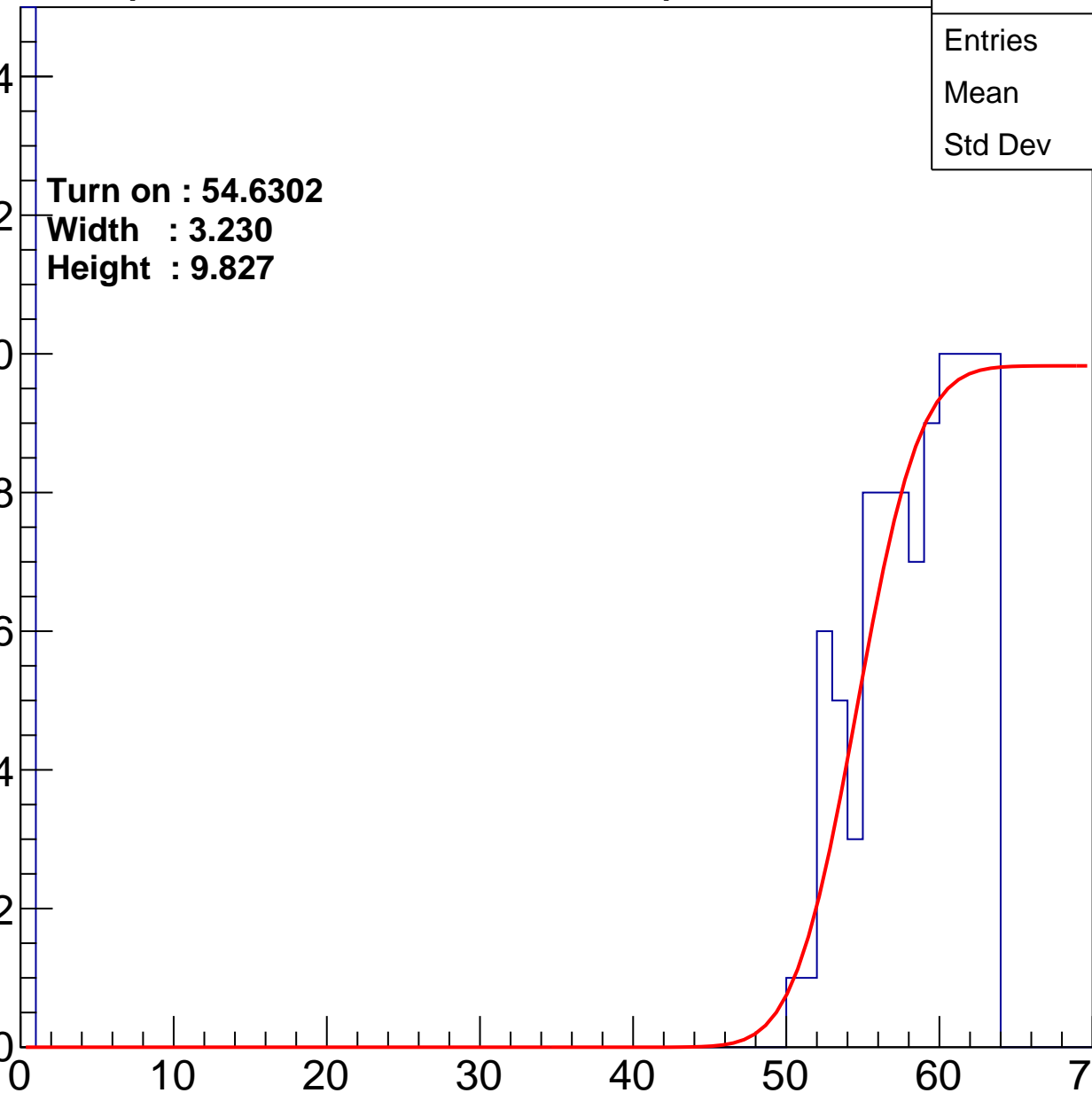
Width : 3.230

Height : 9.827

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	31.45
Std Dev	28.69

Turn on : 53.4269

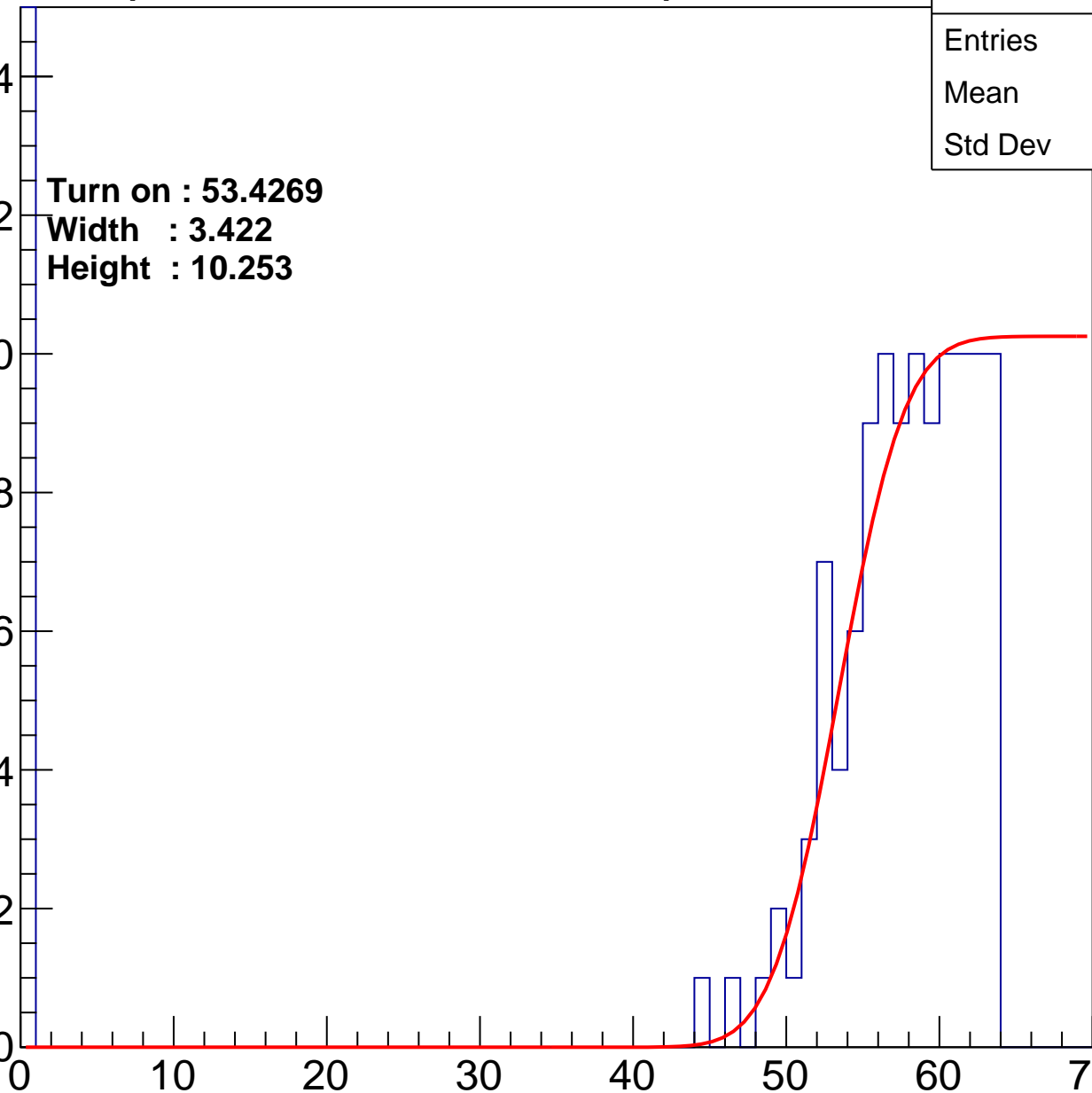
Width : 3.422

Height : 10.253

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch83

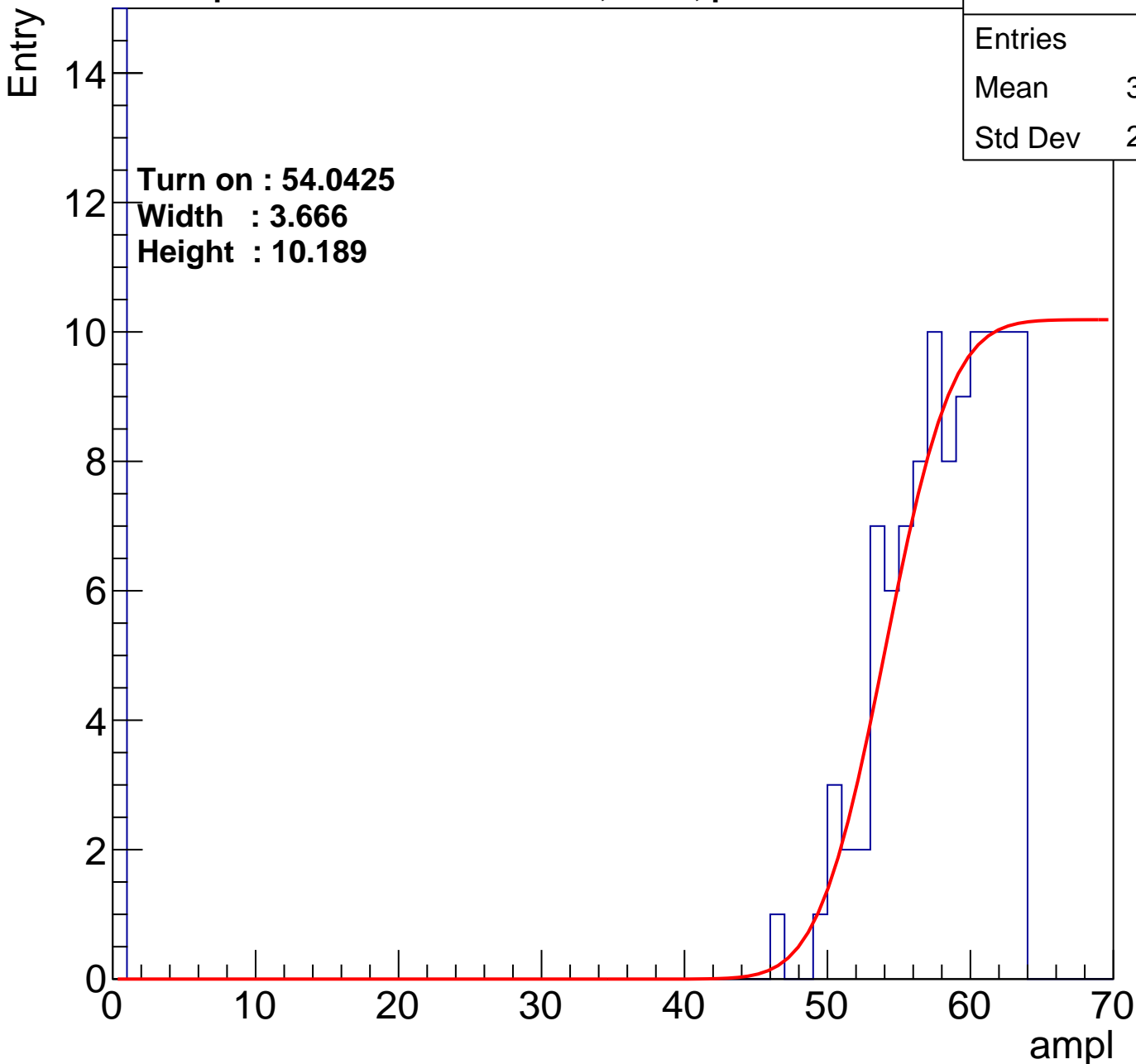
calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	31.43
Std Dev	28.89

Turn on : 54.0425

Width : 3.666

Height : 10.189



B1L104S, U20-ch84

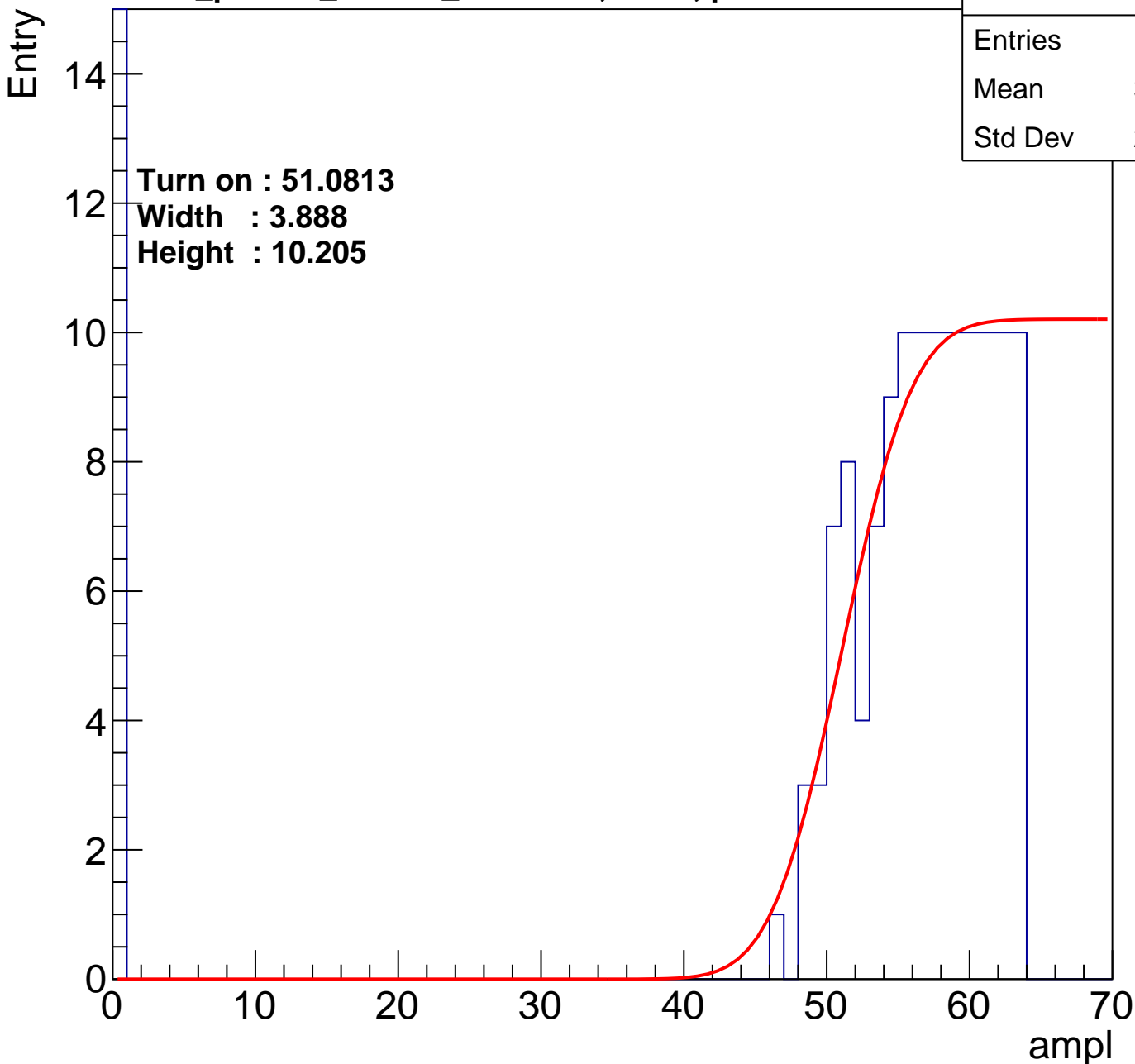
calib_packv5_033123_0516.root, FC#4, port A1

Entries	227
Mean	32.91
Std Dev	28.11

Turn on : 51.0813

Width : 3.888

Height : 10.205



B1L104S, U20-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	35.43
Std Dev	27.64

Turn on : 52.5701

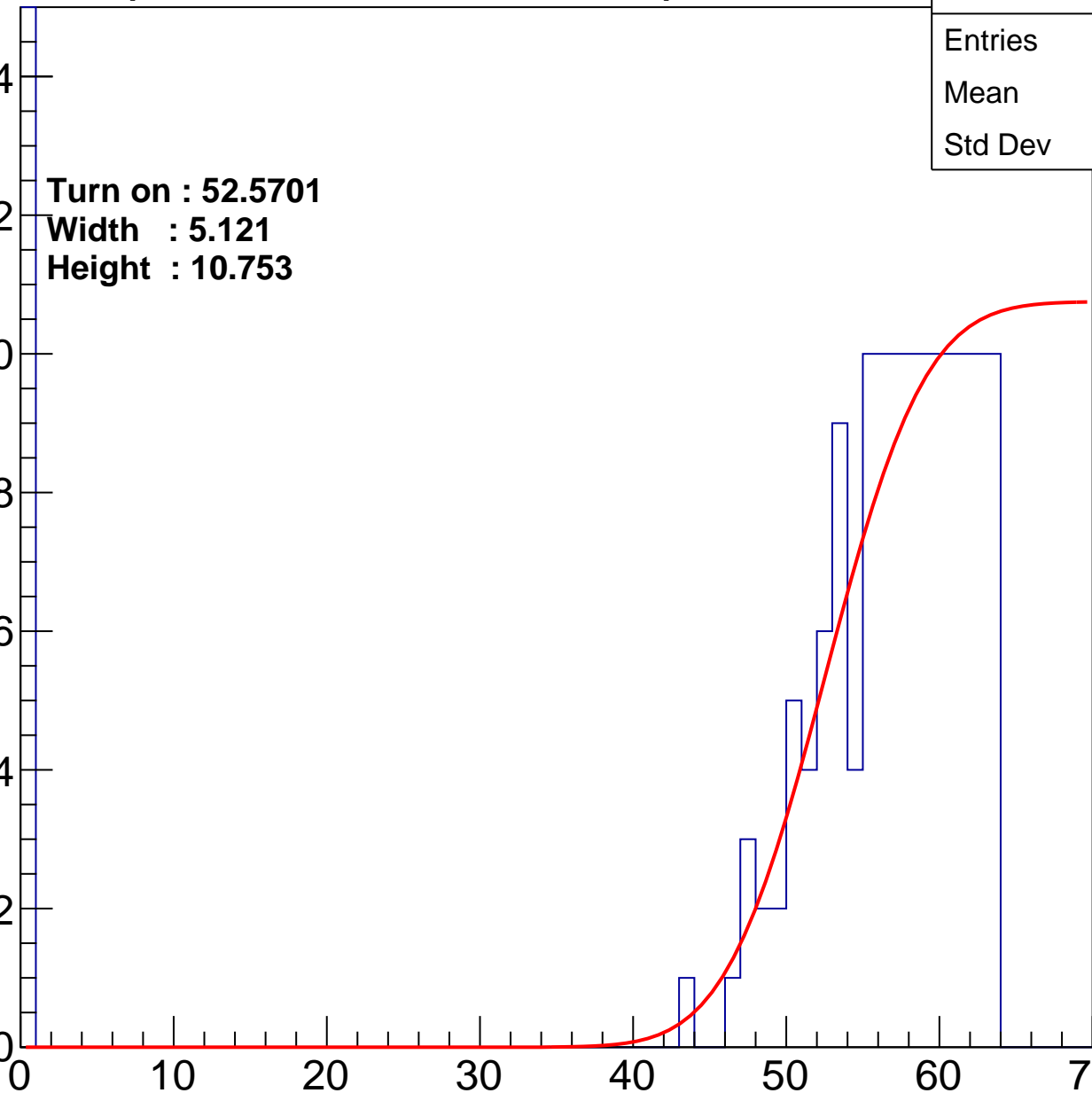
Width : 5.121

Height : 10.753

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	32.81
Std Dev	28.94

Turn on : 54.4352

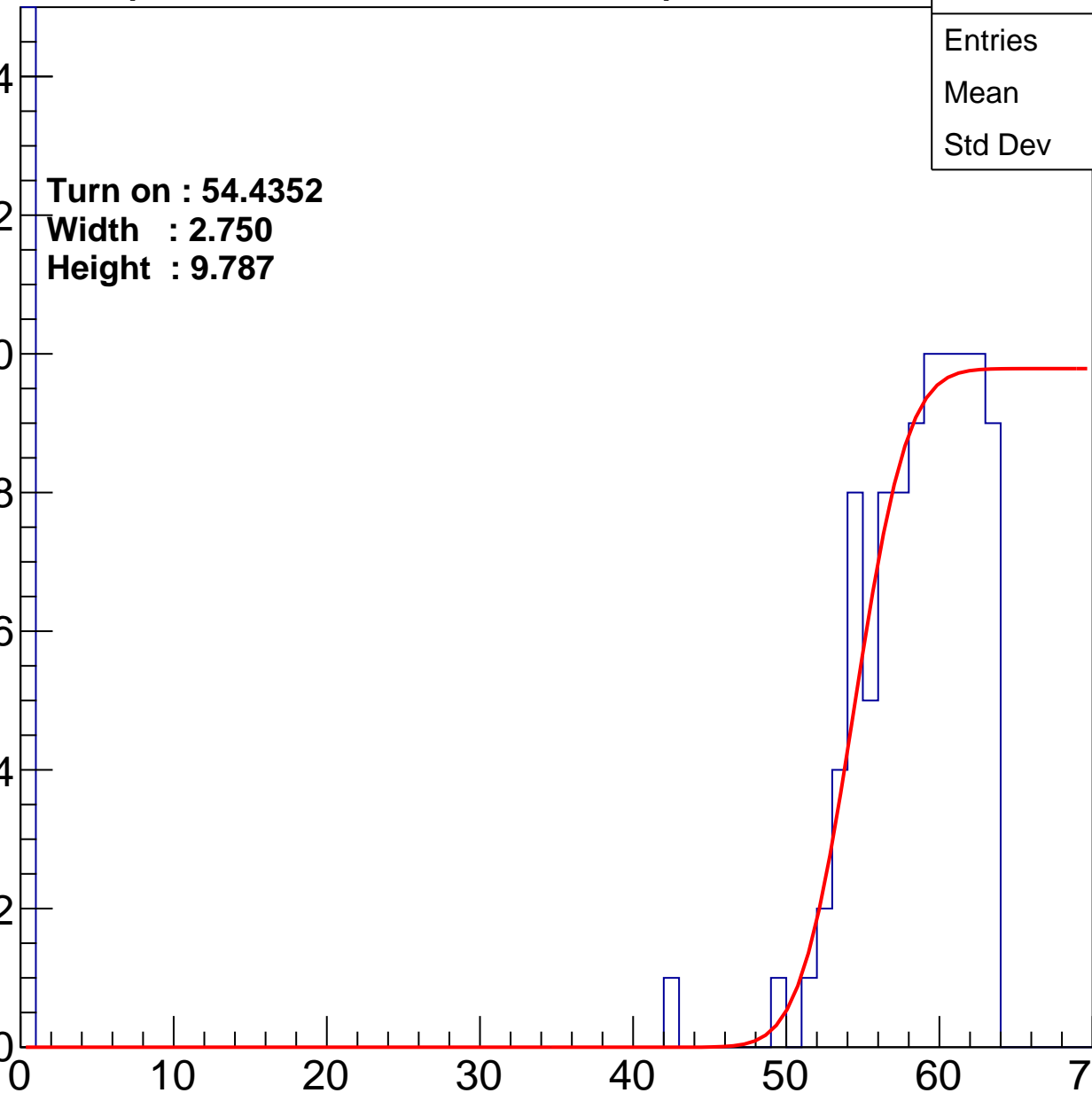
Width : 2.750

Height : 9.787

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	35.33
Std Dev	28.11

Turn on : 52.9160

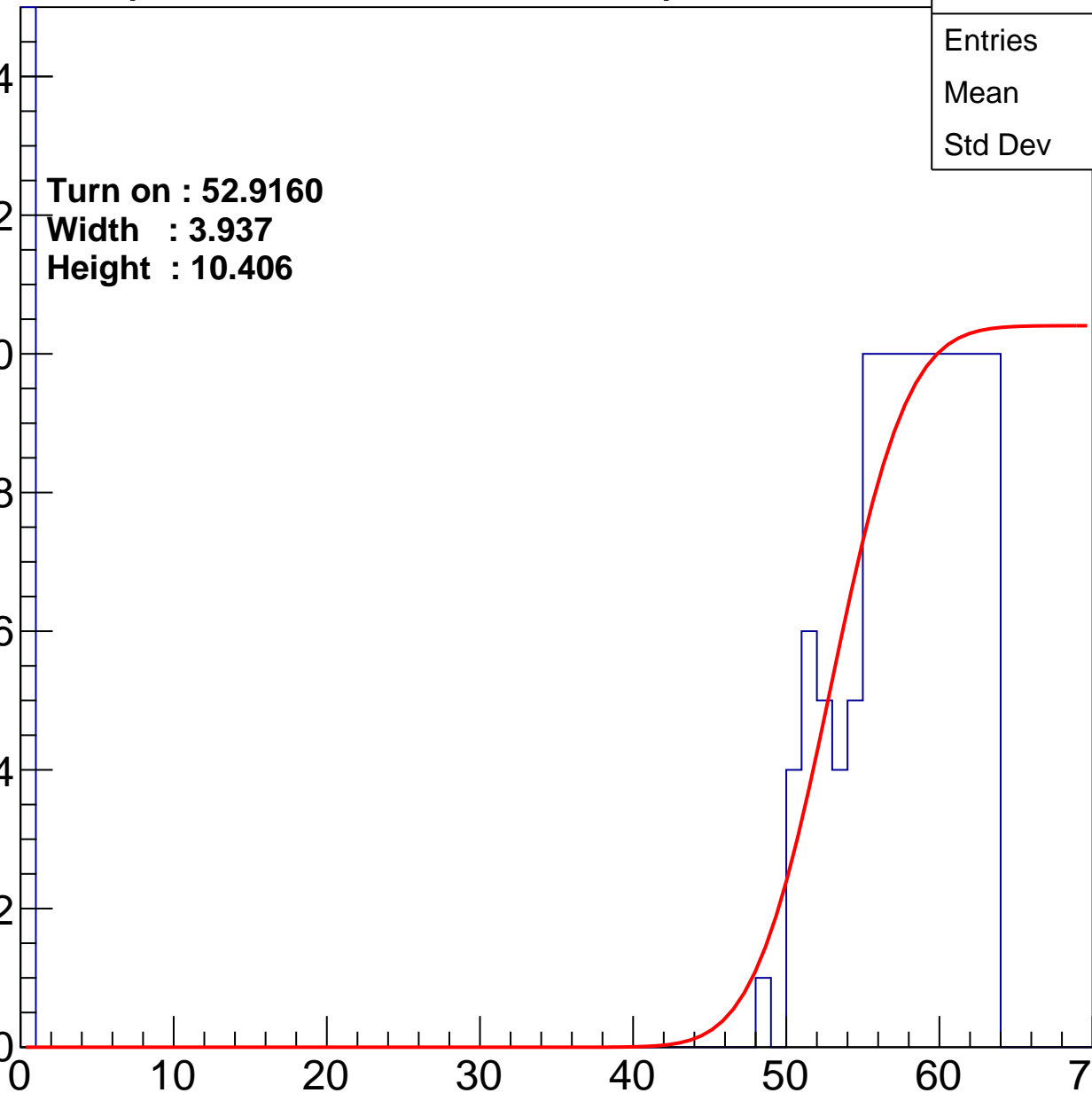
Width : 3.937

Height : 10.406

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	33.37
Std Dev	28.63

Turn on : 53.1471

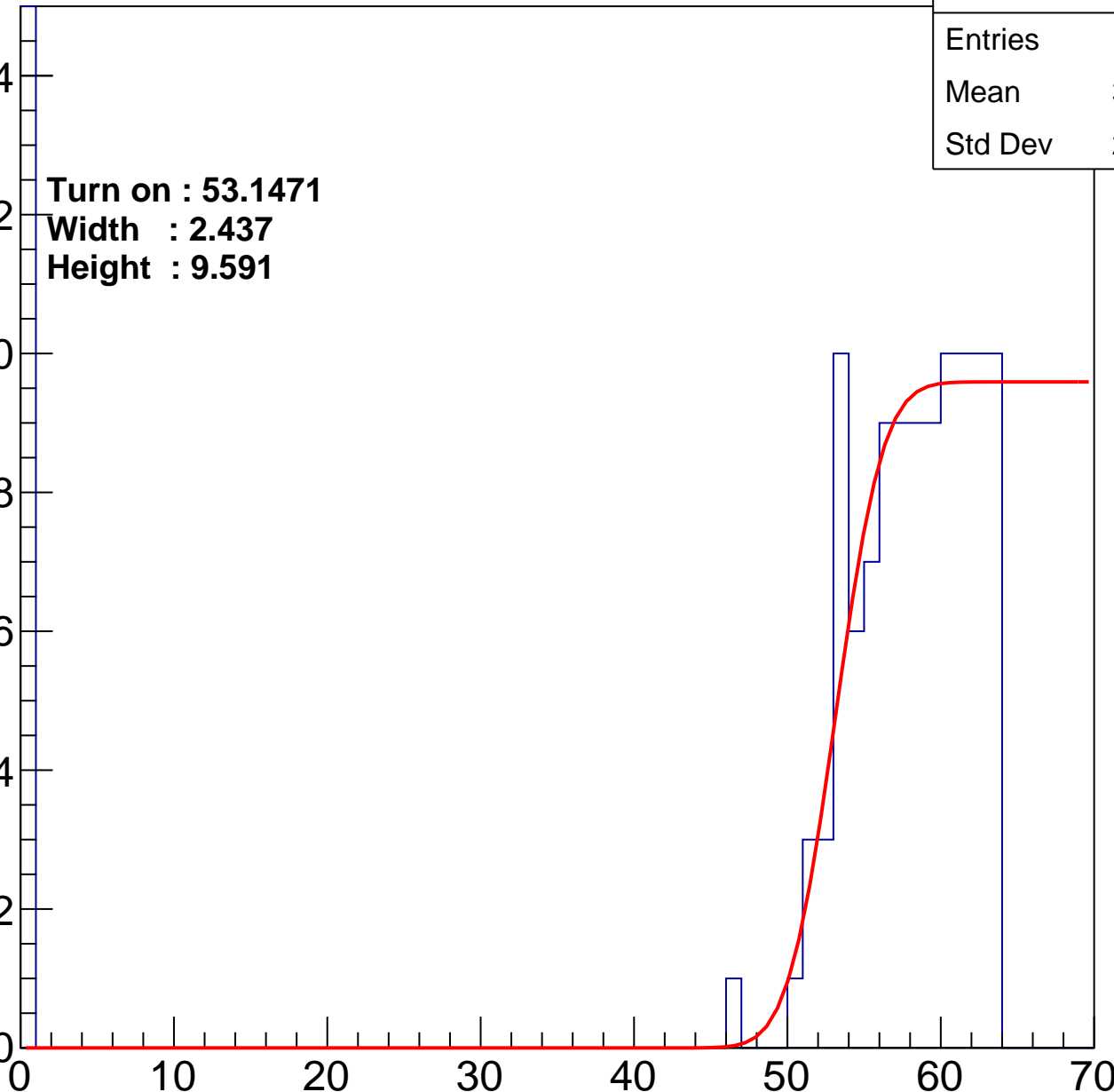
Width : 2.437

Height : 9.591

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	40.93
Std Dev	26.29

Turn on : 54.7610

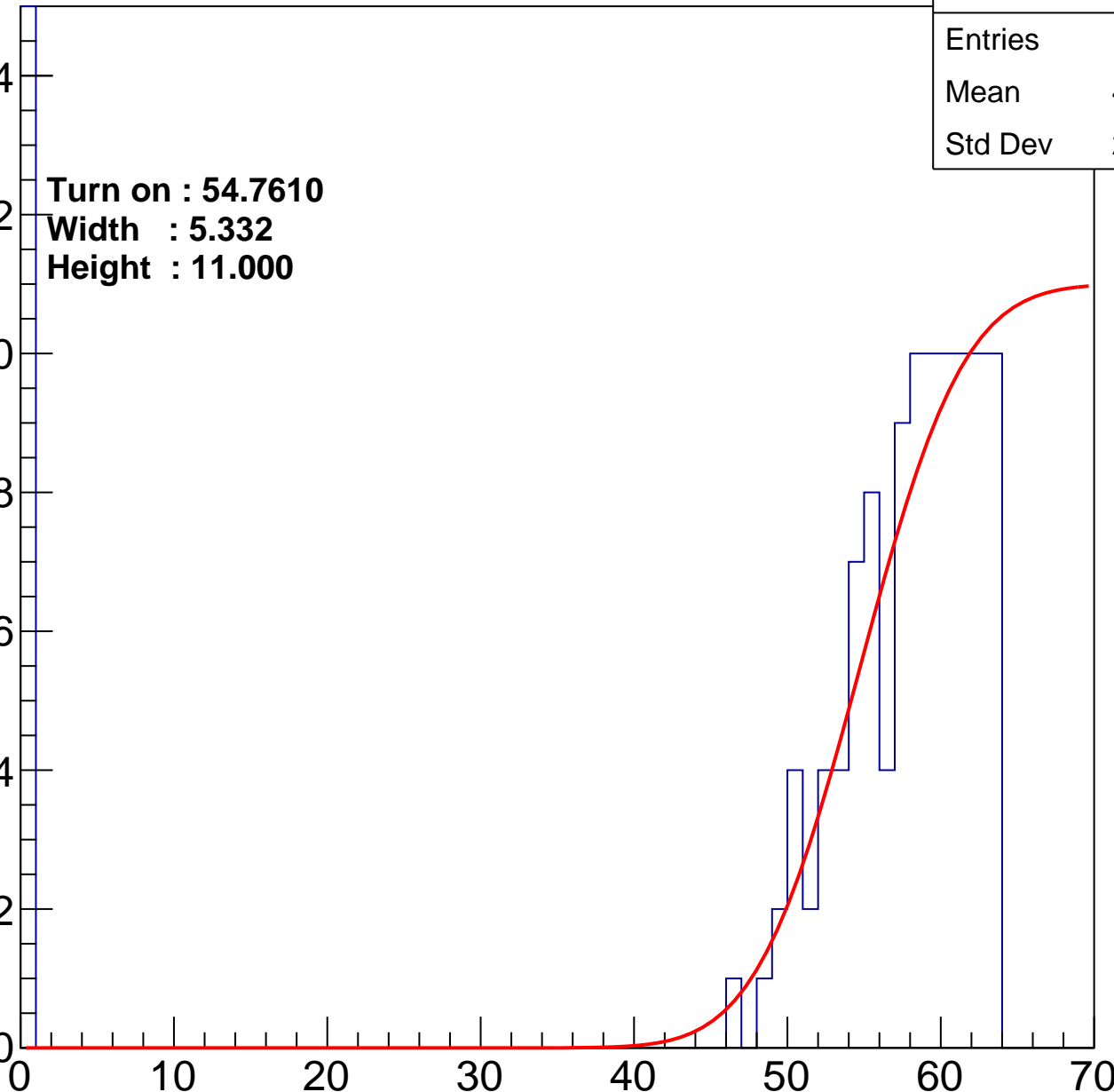
Width : 5.332

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	35.63
Std Dev	27.76

Turn on : 52.7391

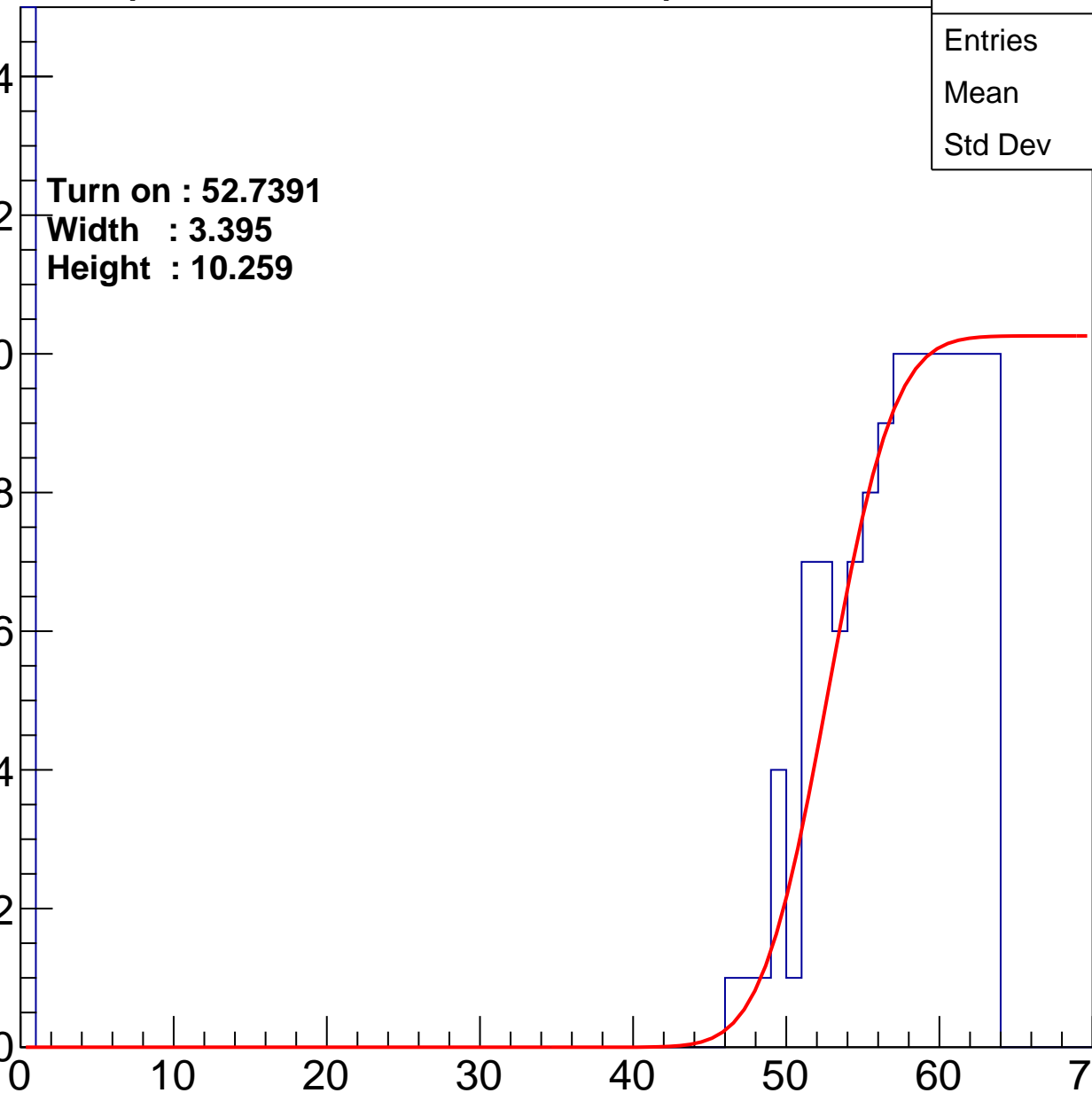
Width : 3.395

Height : 10.259

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	34.9
Std Dev	28.38

Turn on : 54.4200

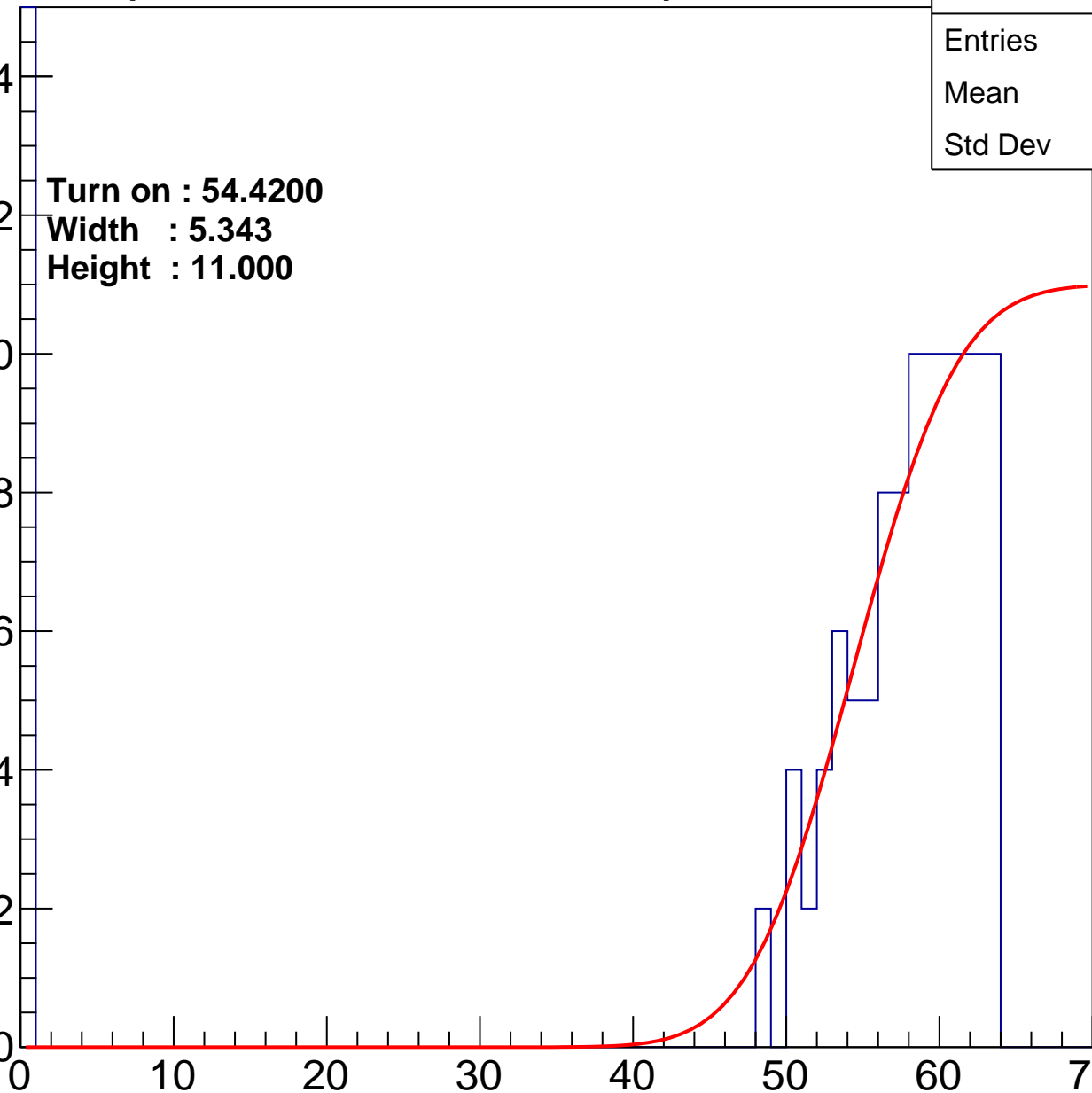
Width : 5.343

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch92

calib_packv5_033123_0516.root, FC#4, port A1

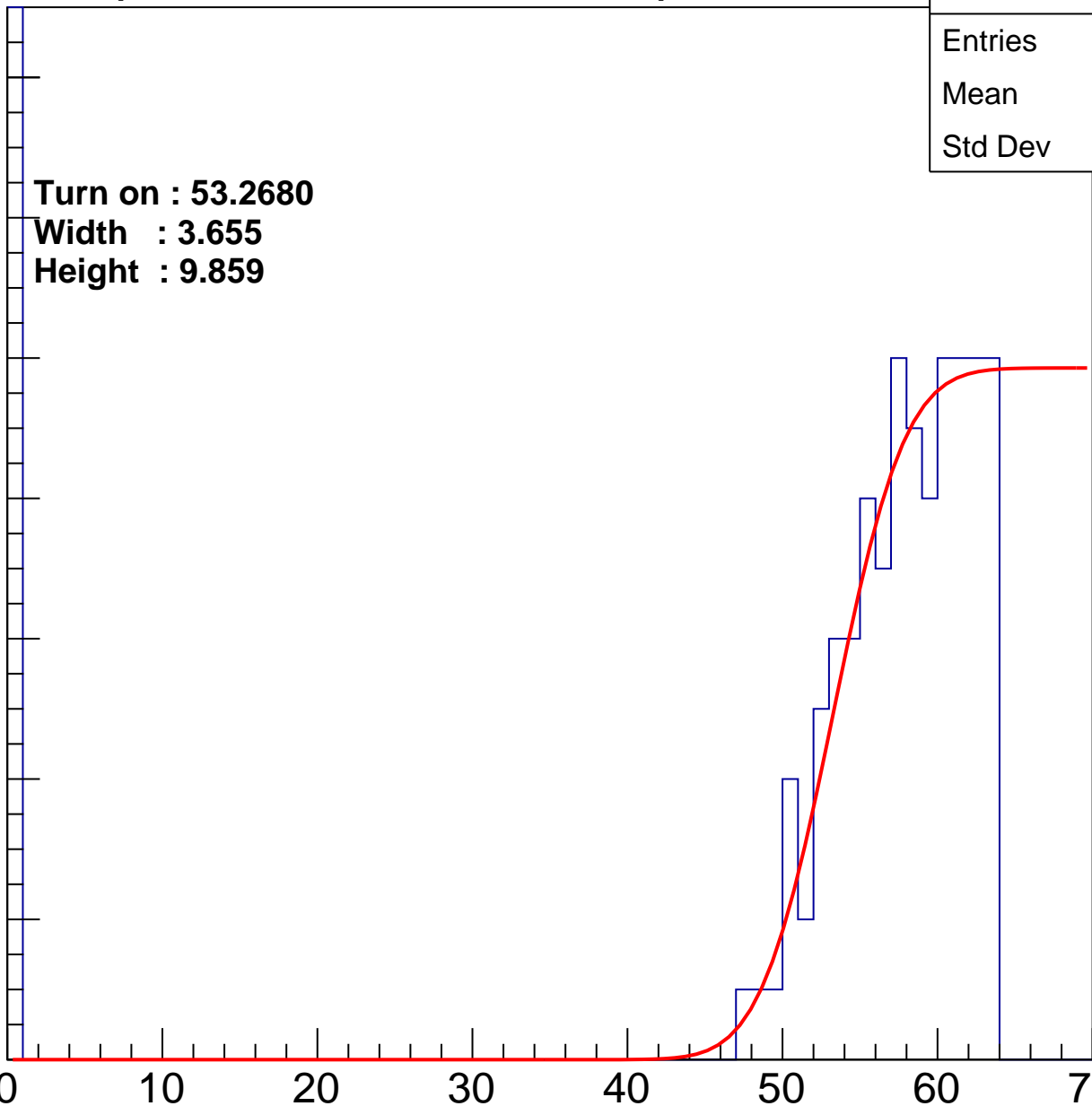
Entry

14
12
10
8
6
4
2
0

Turn on : 53.2680
Width : 3.655
Height : 9.859

Entries	207
Mean	29.97
Std Dev	28.84

ampl



B1L104S, U20-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	32.09
Std Dev	28.8

Turn on : 53.1099

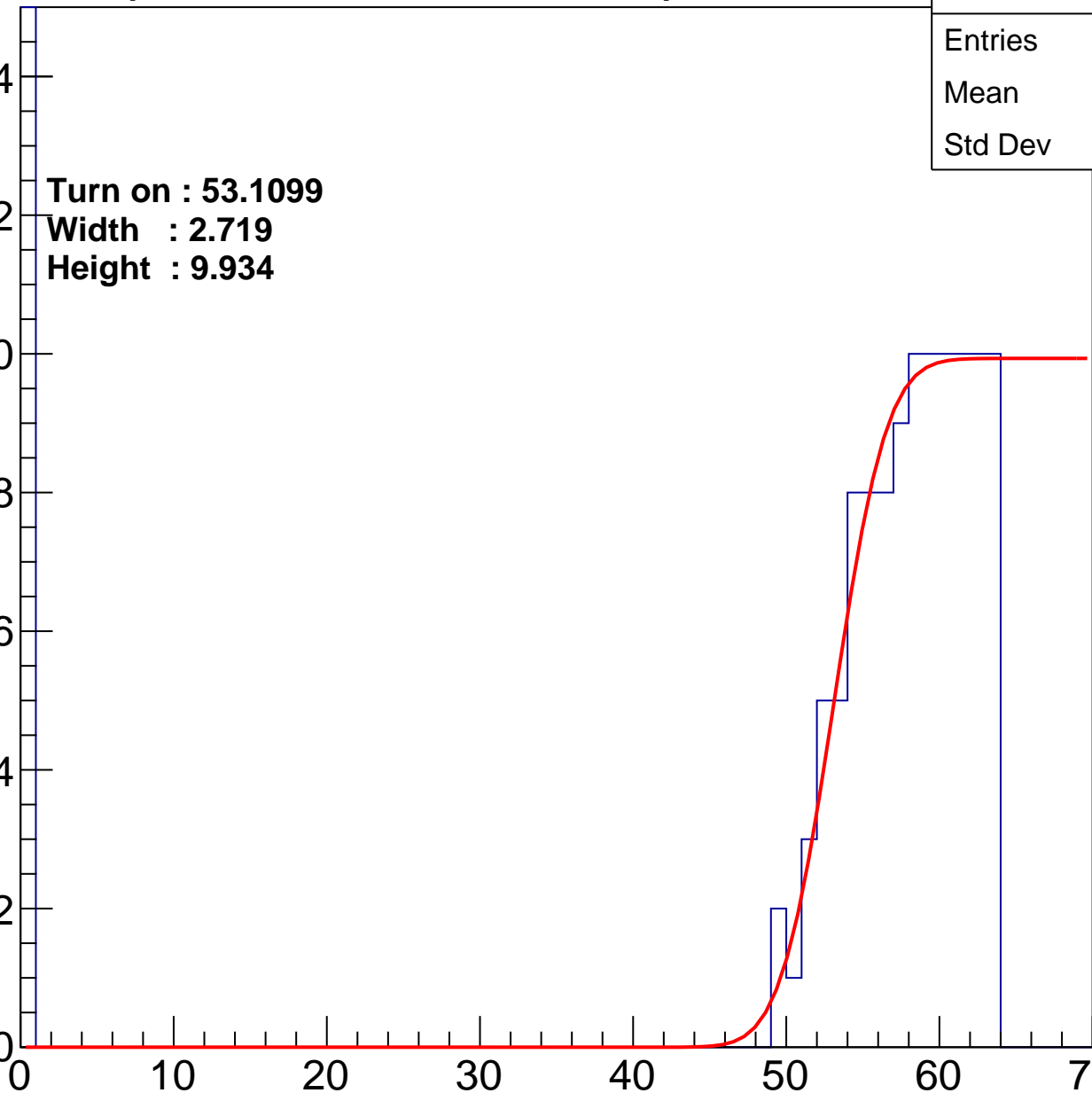
Width : 2.719

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	34.52
Std Dev	27.93

Turn on : 51.9593

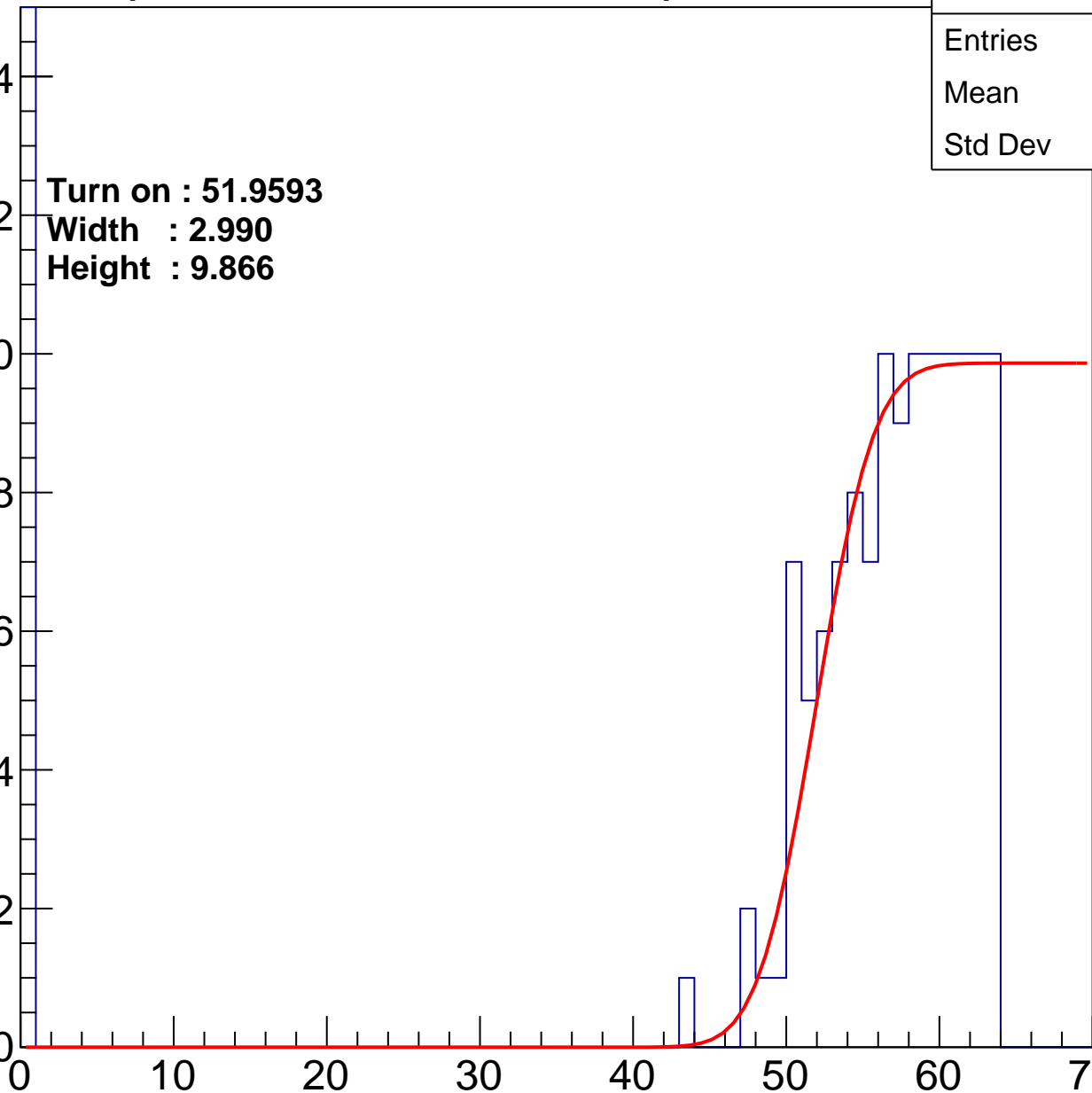
Width : 2.990

Height : 9.866

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	33.84
Std Dev	28.75

Turn on : 54.2938

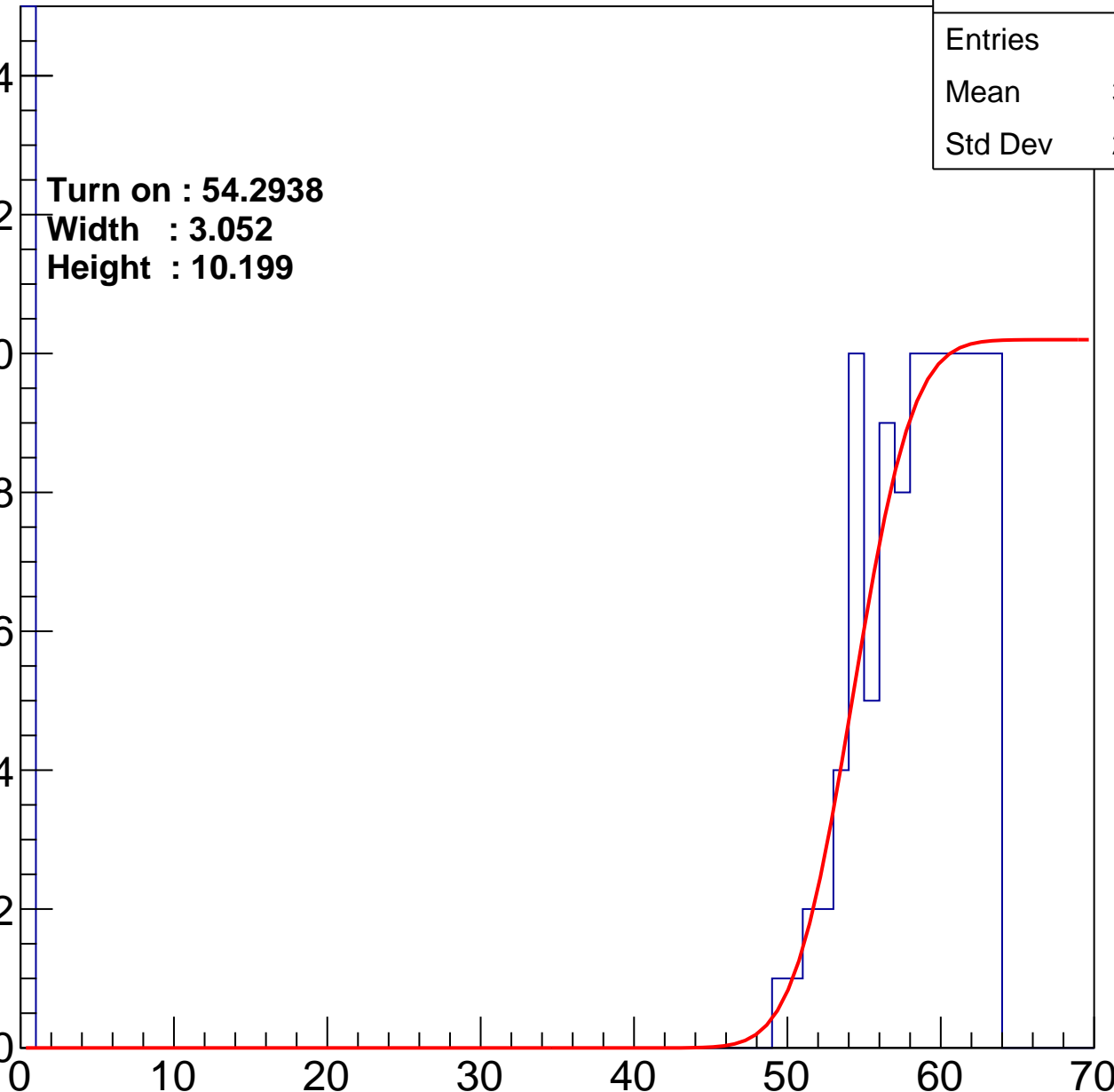
Width : 3.052

Height : 10.199

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	34.41
Std Dev	28.1

Turn on : 52.3062

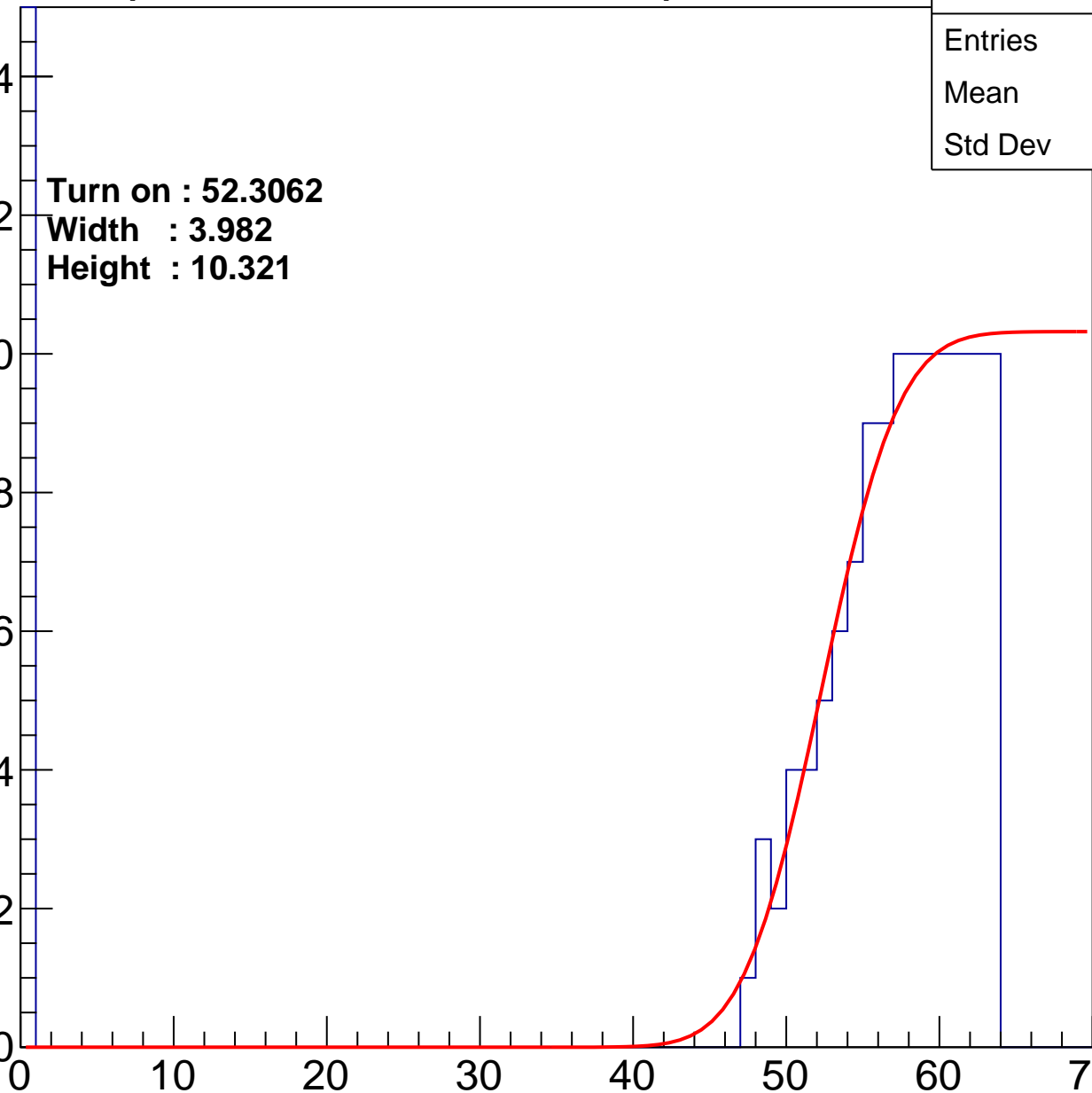
Width : 3.982

Height : 10.321

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	31.58
Std Dev	29.27

Turn on : 54.8399

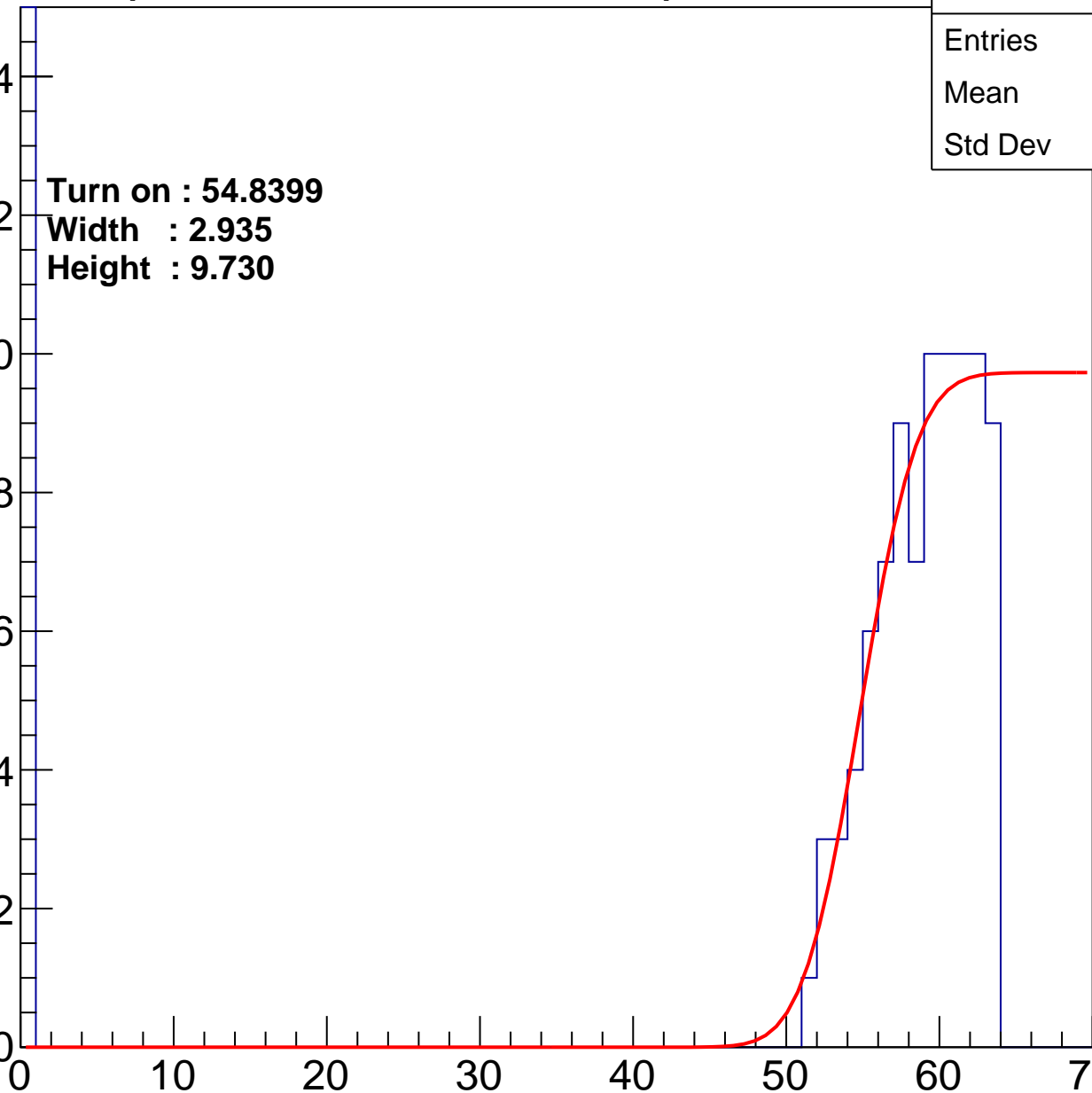
Width : 2.935

Height : 9.730

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch98

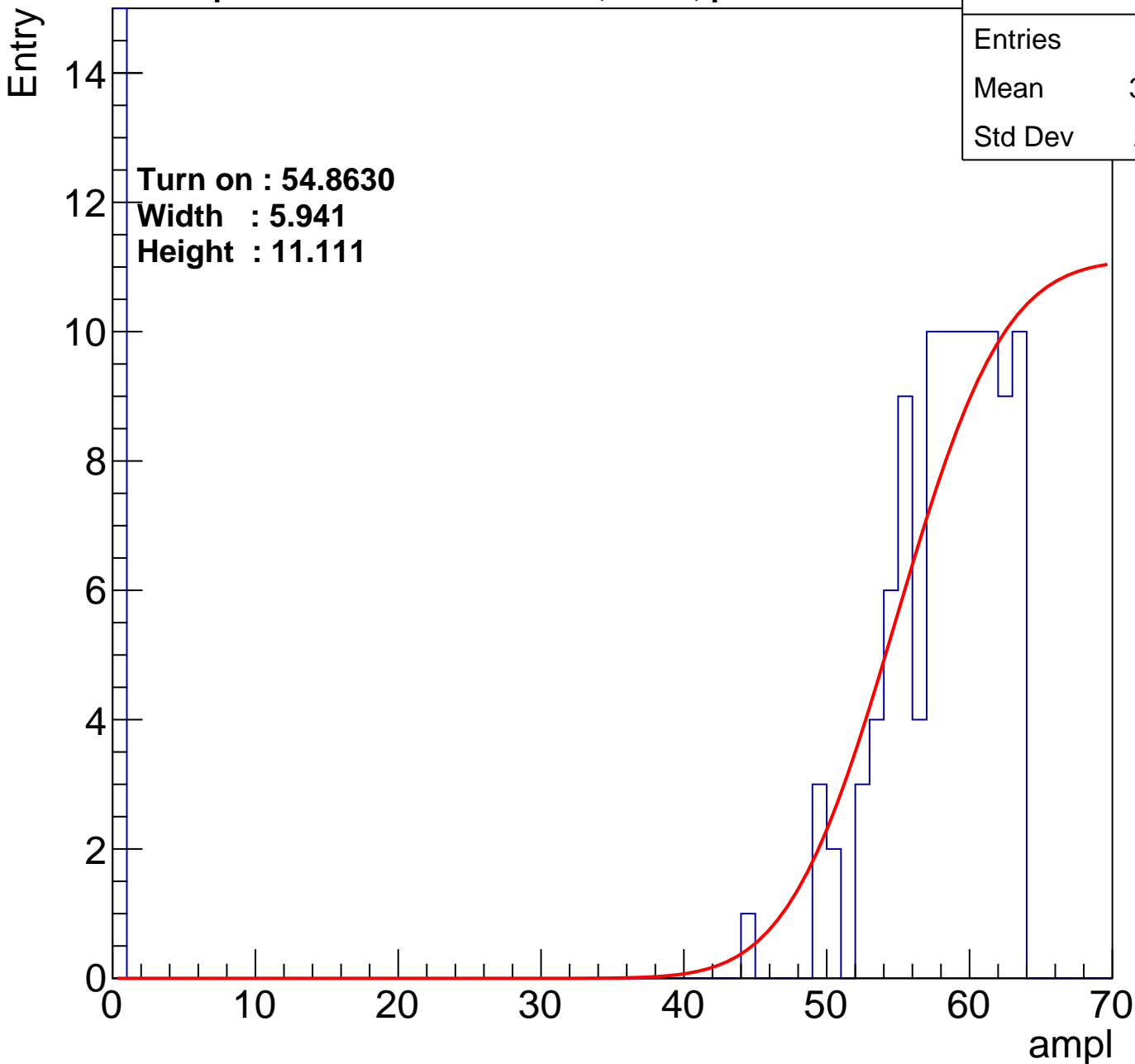
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	30.26
Std Dev	29.01

Turn on : 54.8630

Width : 5.941

Height : 11.111



B1L104S, U20-ch99

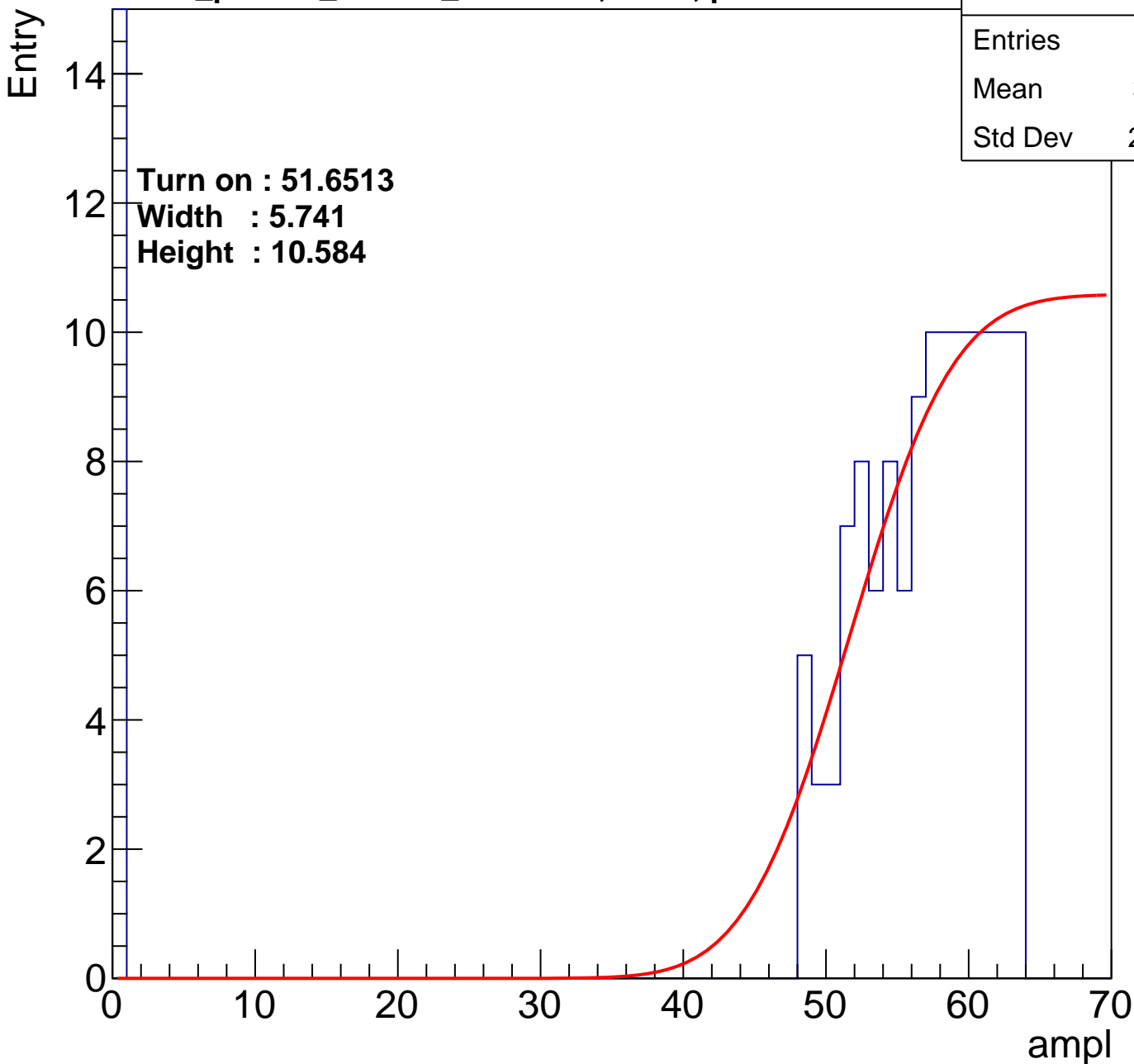
calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	36.01
Std Dev	27.54

Turn on : 51.6513

Width : 5.741

Height : 10.584



B1L104S, U20-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	235
Mean	30.22
Std Dev	28.52

Turn on : 51.7945

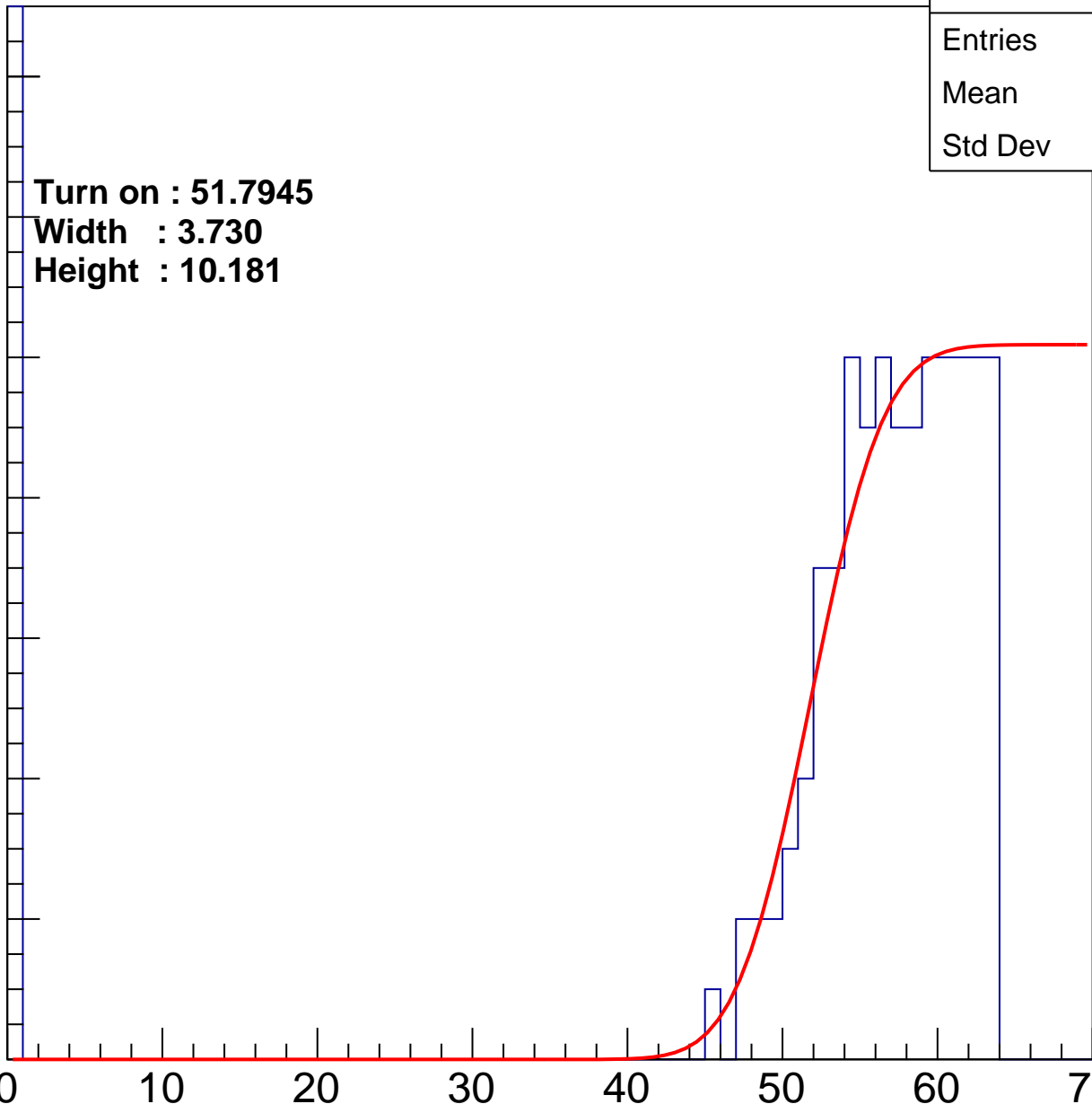
Width : 3.730

Height : 10.181

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	30.77
Std Dev	28.91

Turn on : 54.2013

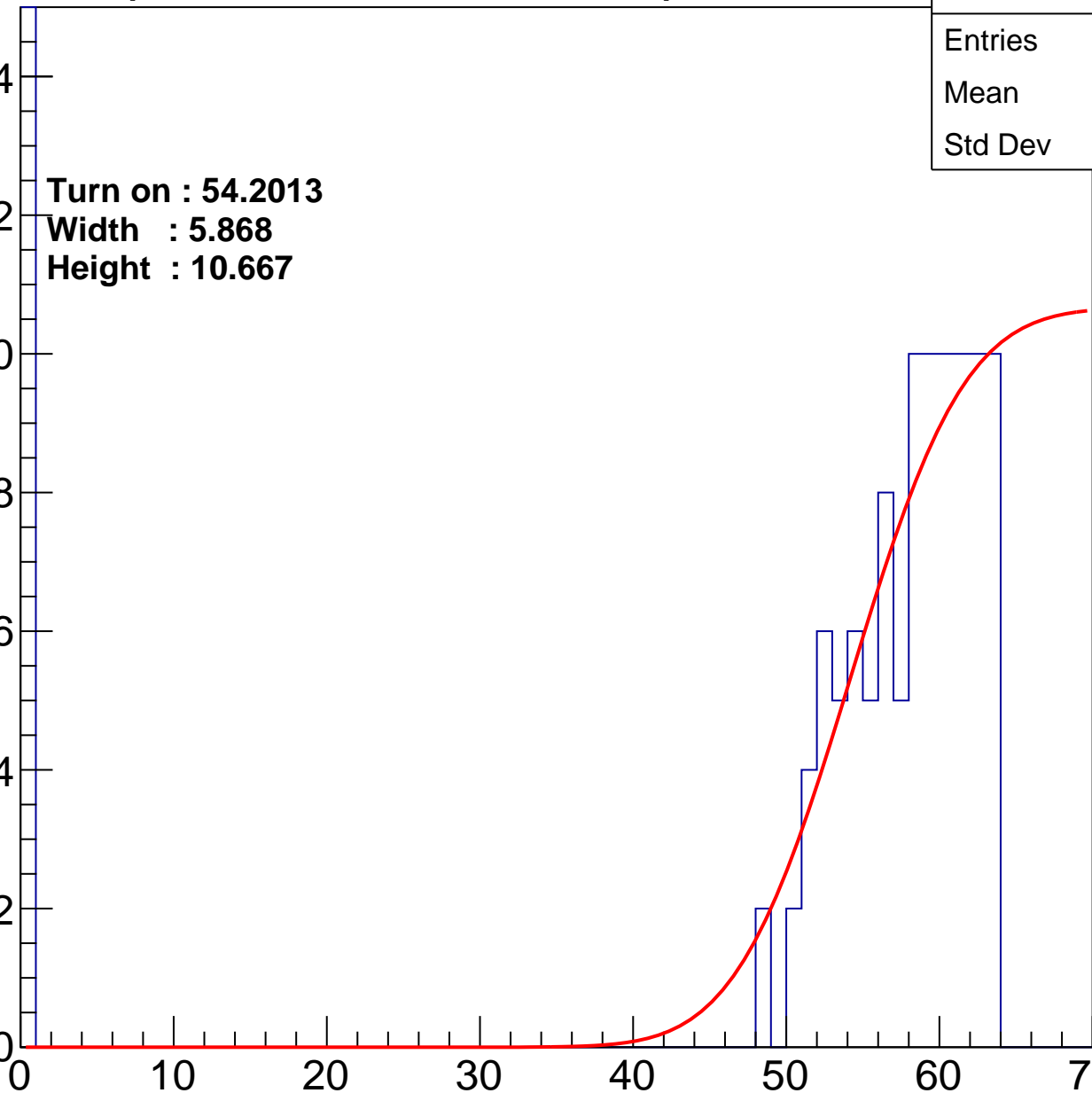
Width : 5.868

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	34.91
Std Dev	27.93

Turn on : 51.7904

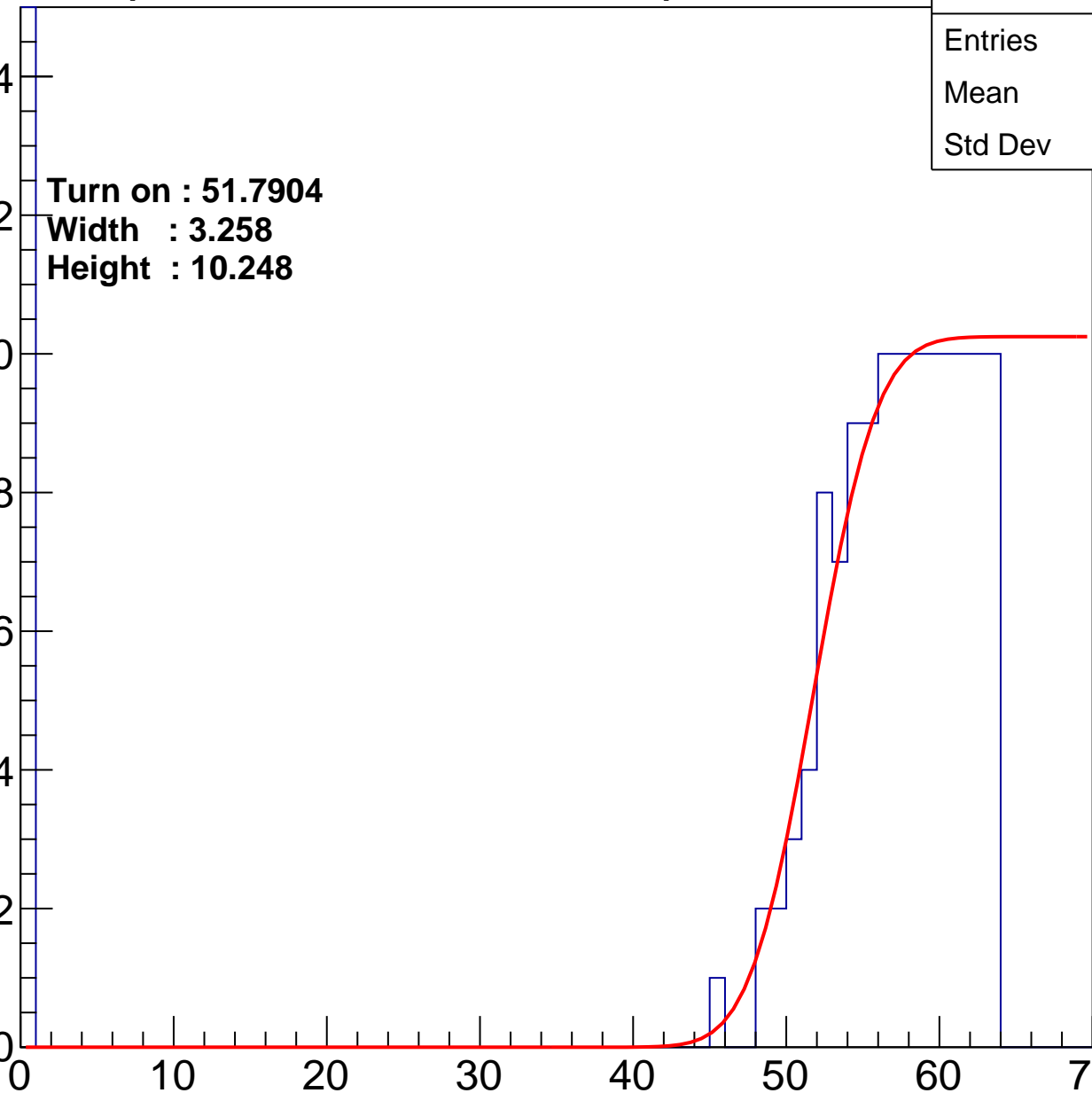
Width : 3.258

Height : 10.248

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	31.63
Std Dev	29.25

Turn on : 54.8074

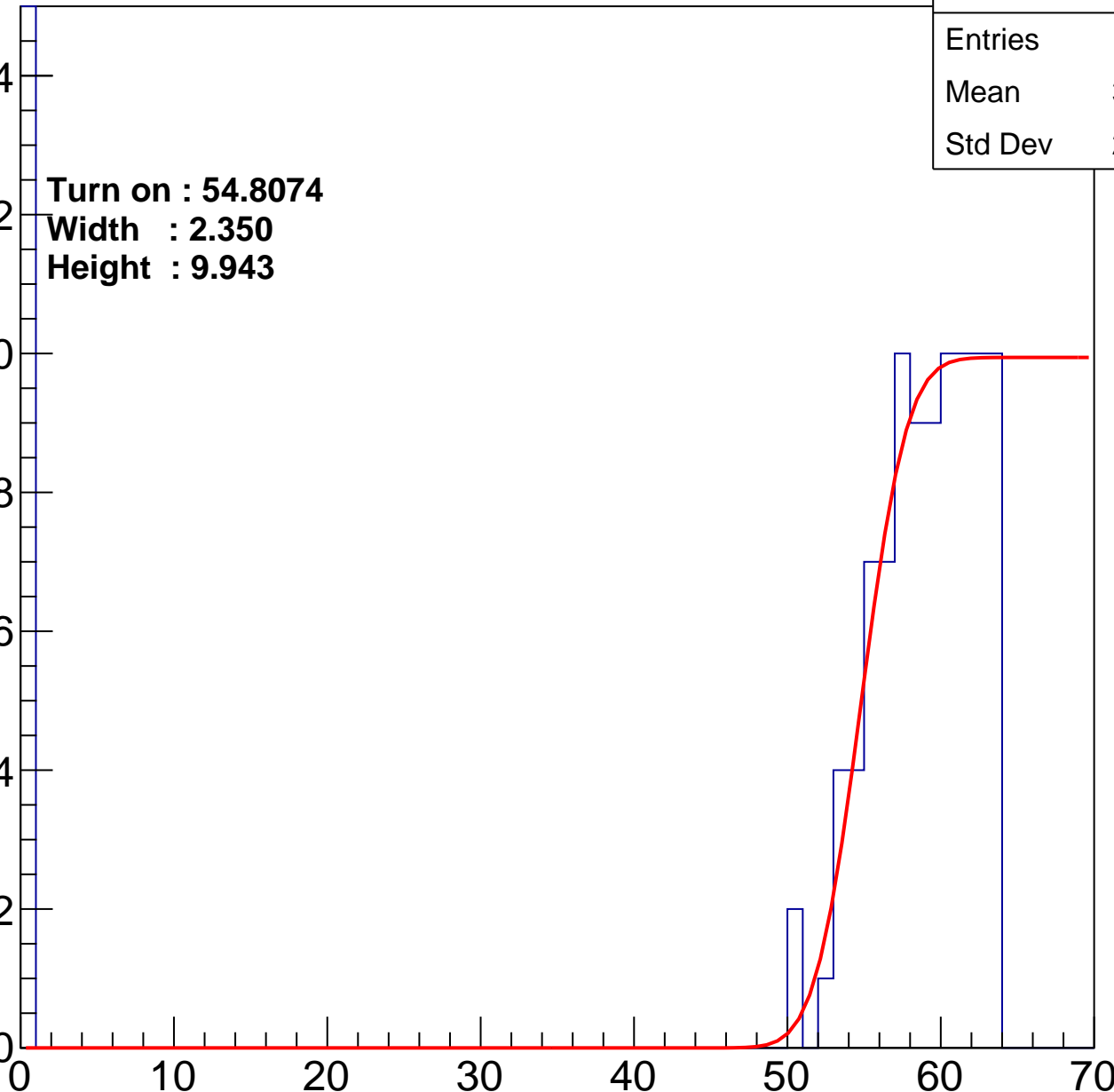
Width : 2.350

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch104

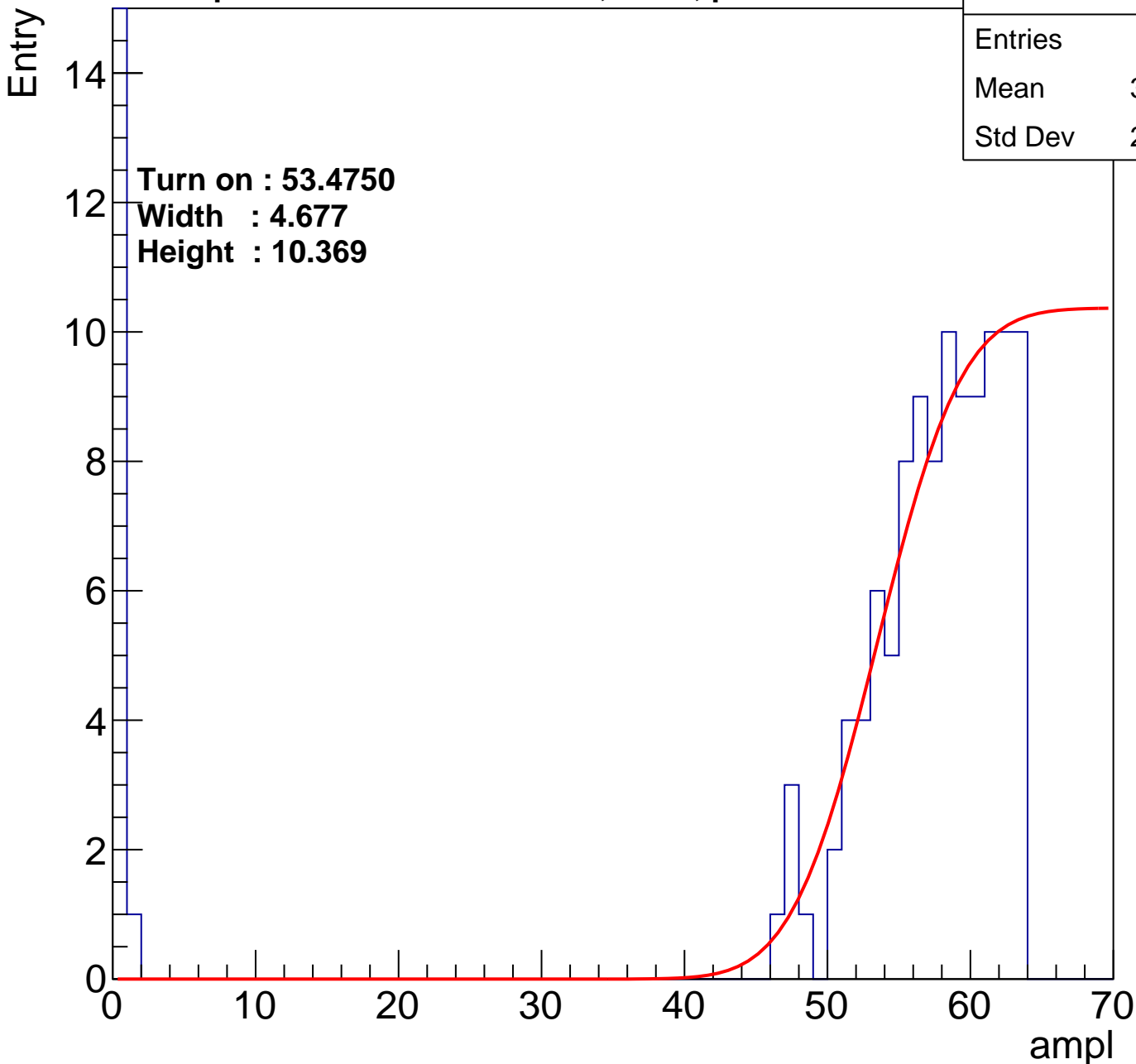
calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	31.87
Std Dev	28.64

Turn on : 53.4750

Width : 4.677

Height : 10.369



B1L104S, U20-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	31.5
Std Dev	29.14

Turn on : 53.7945

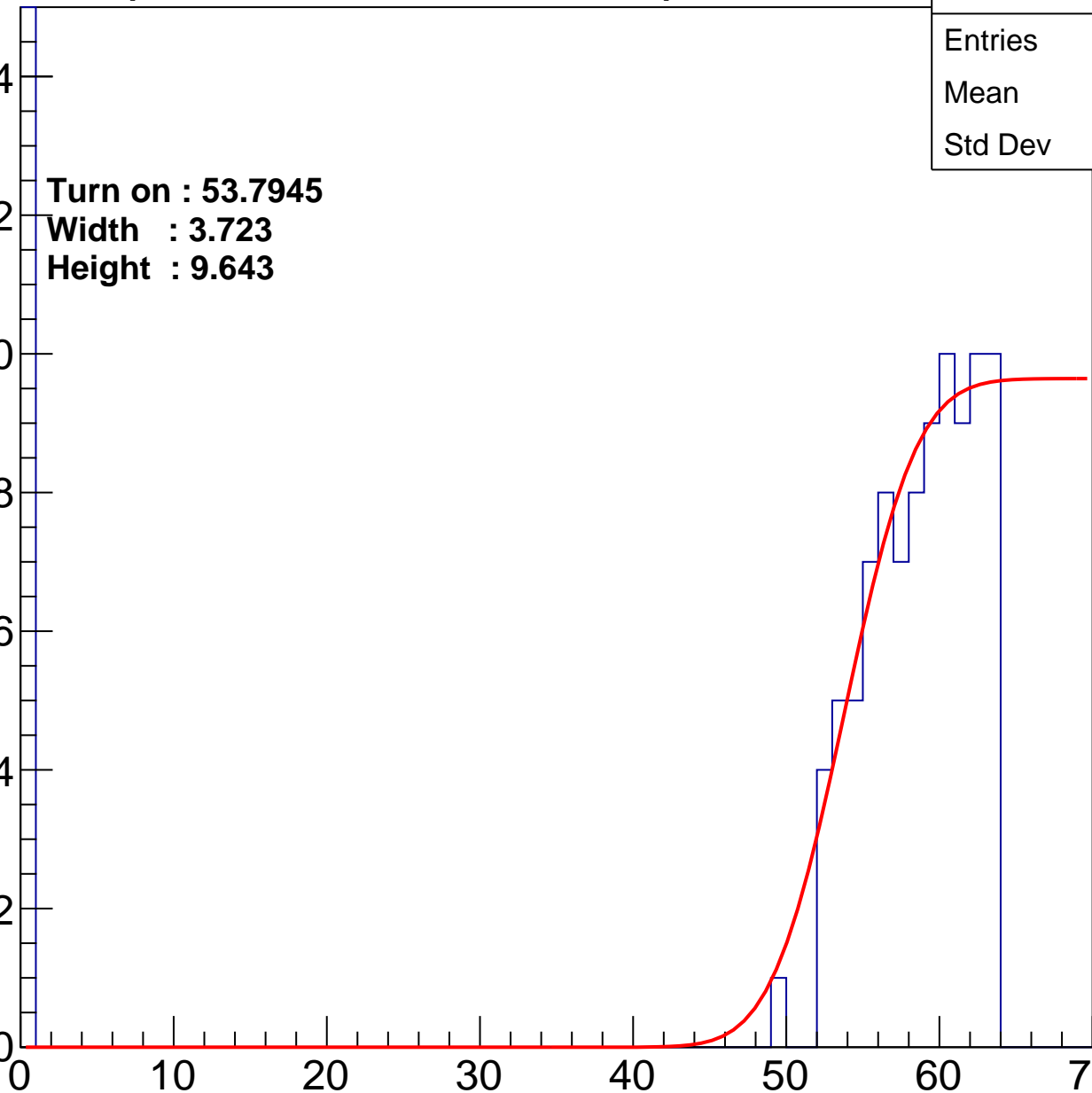
Width : 3.723

Height : 9.643

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch106

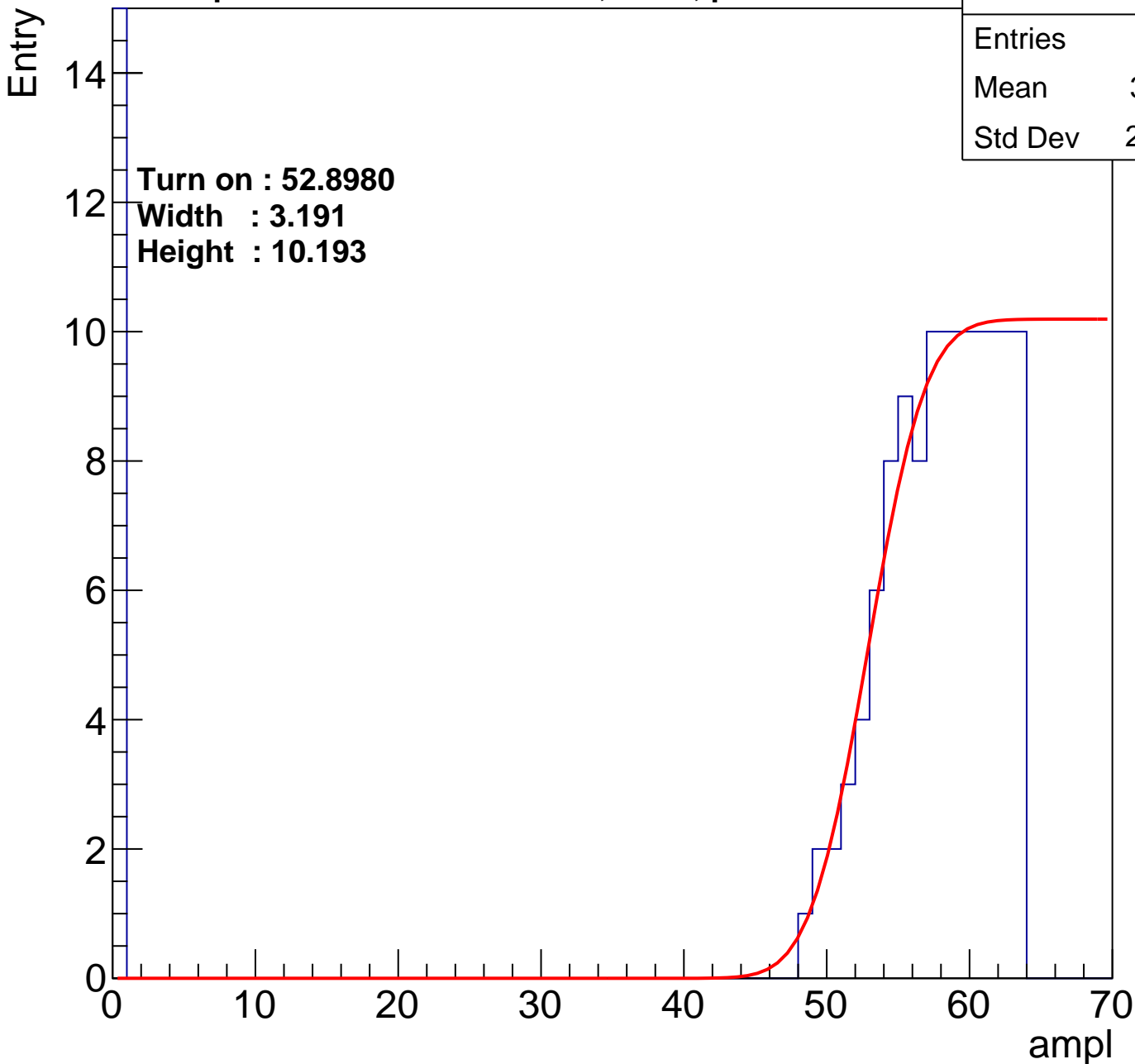
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	30.81
Std Dev	28.82

Turn on : 52.8980

Width : 3.191

Height : 10.193



B1L104S, U20-ch107

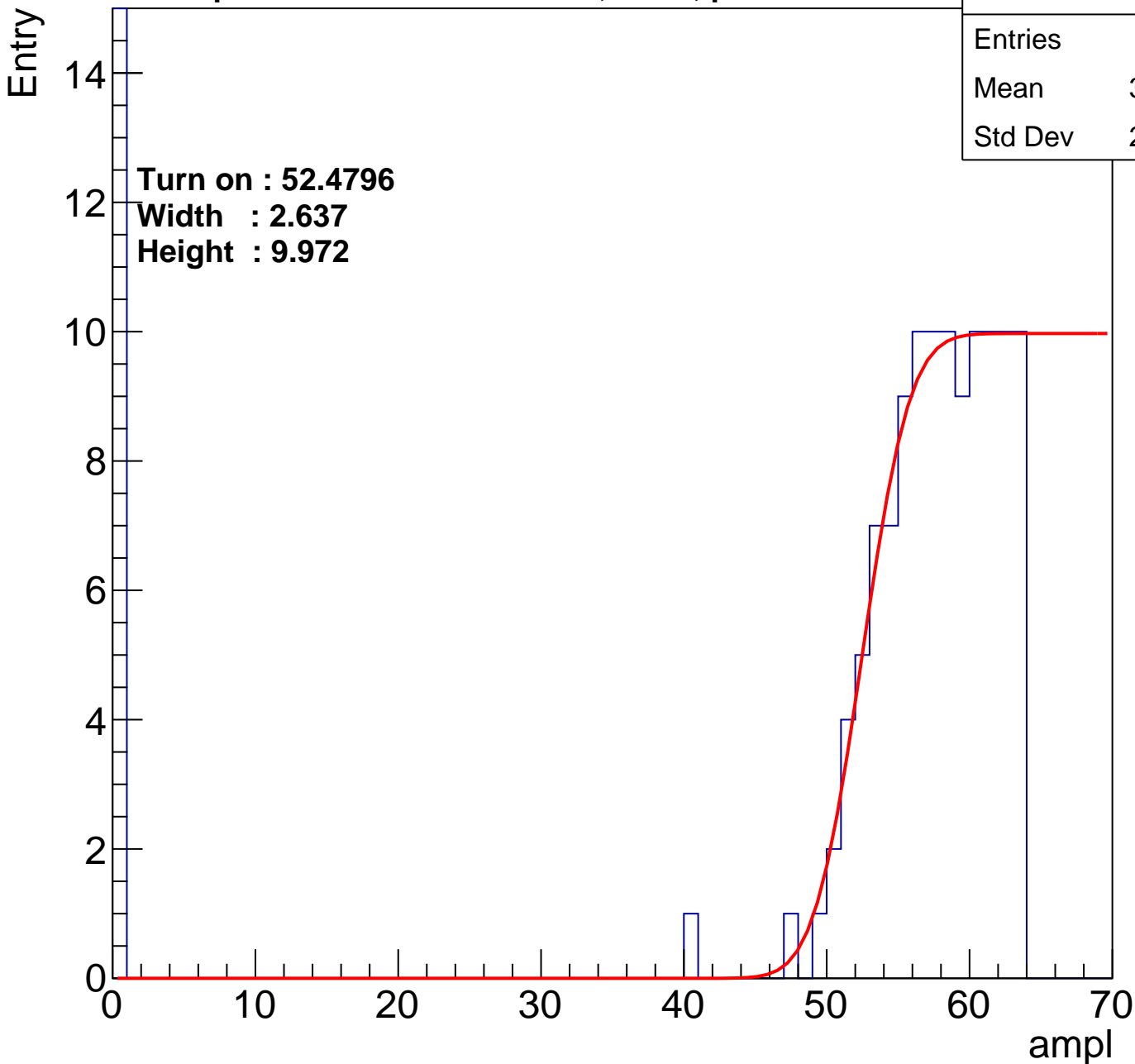
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.56
Std Dev	28.39

Turn on : 52.4796

Width : 2.637

Height : 9.972



B1L104S, U20-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	34.37
Std Dev	28.03

Turn on : 52.7655

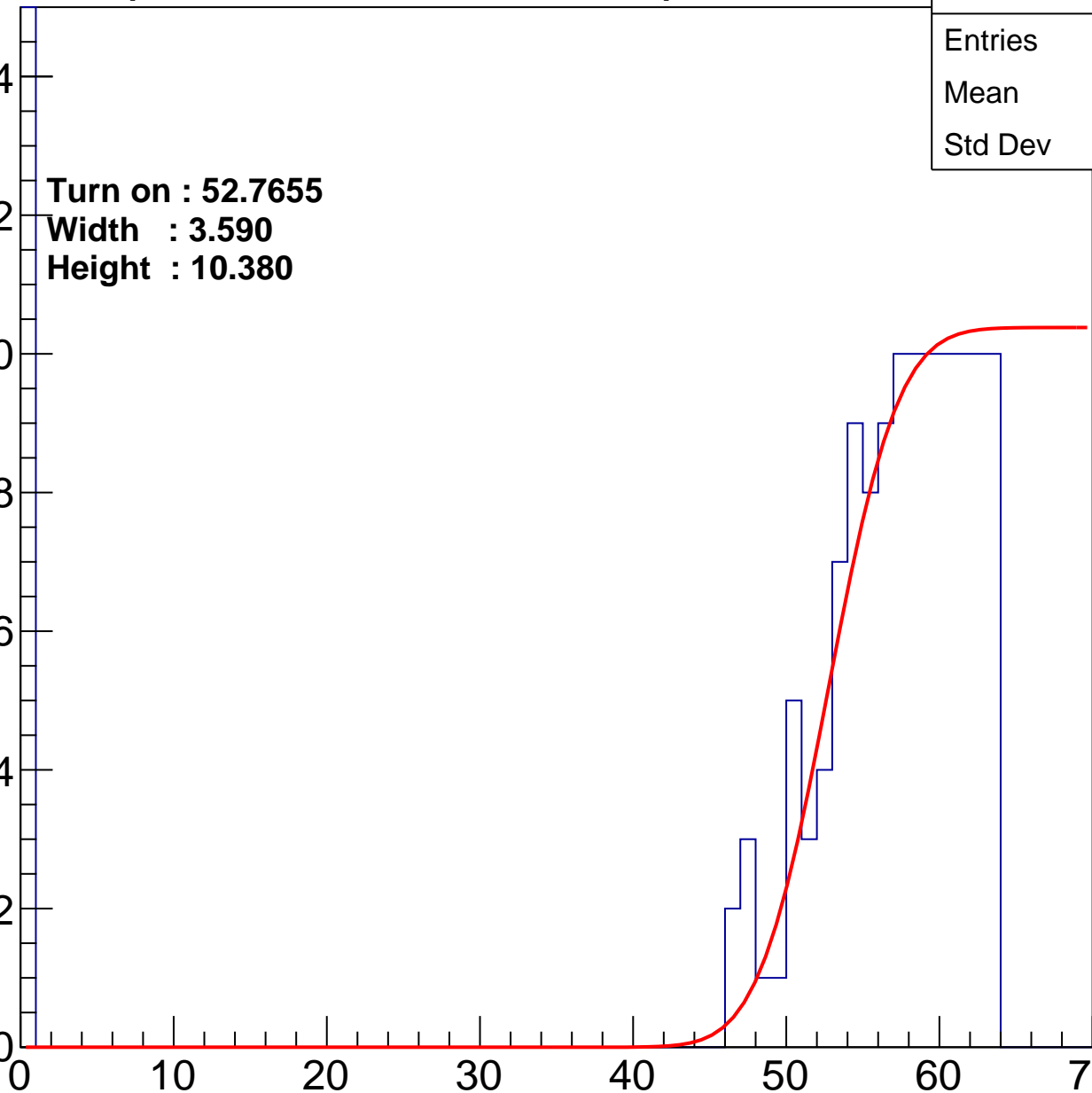
Width : 3.590

Height : 10.380

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch109

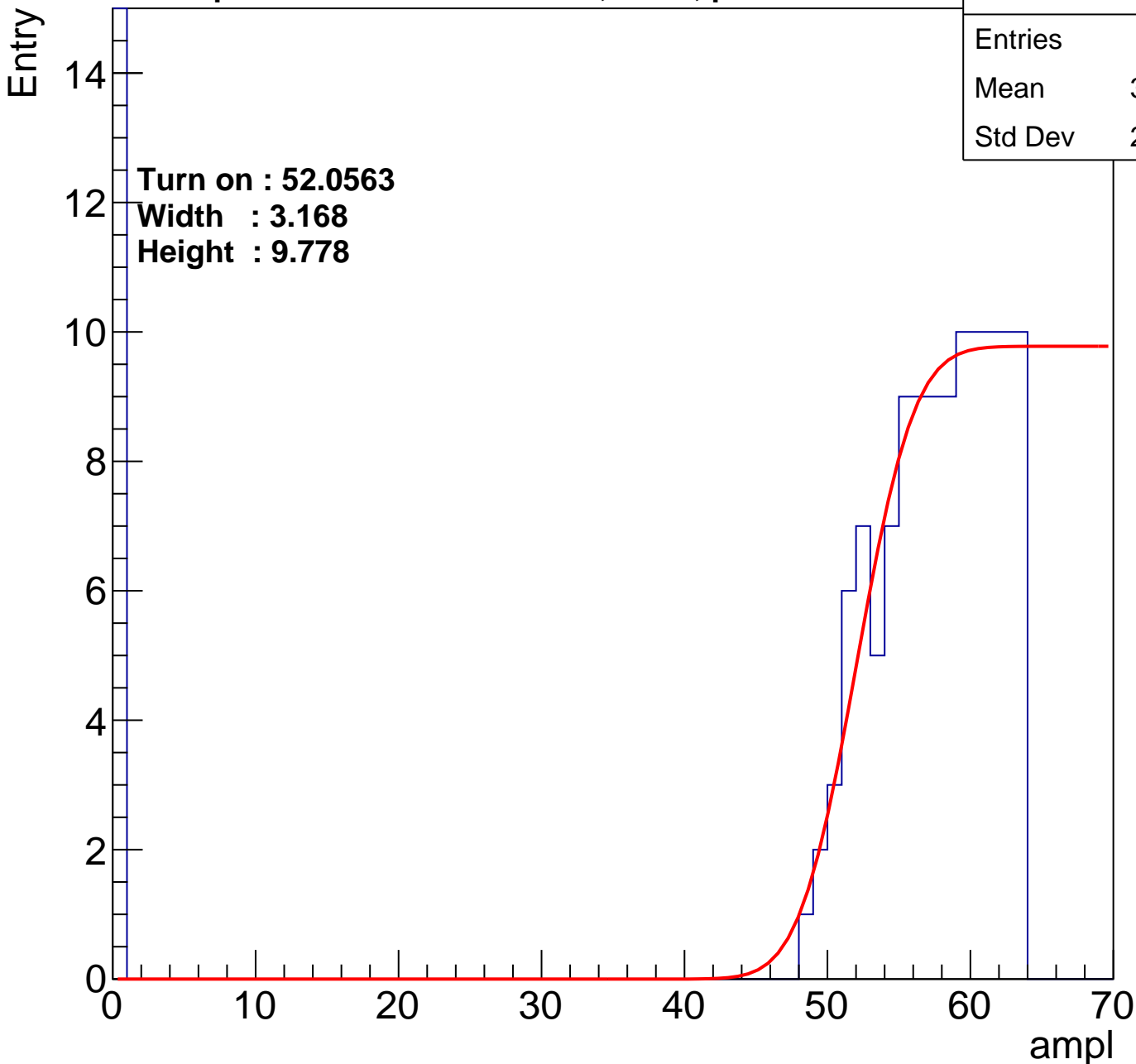
calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	36.18
Std Dev	27.76

Turn on : 52.0563

Width : 3.168

Height : 9.778



B1L104S, U20-ch110

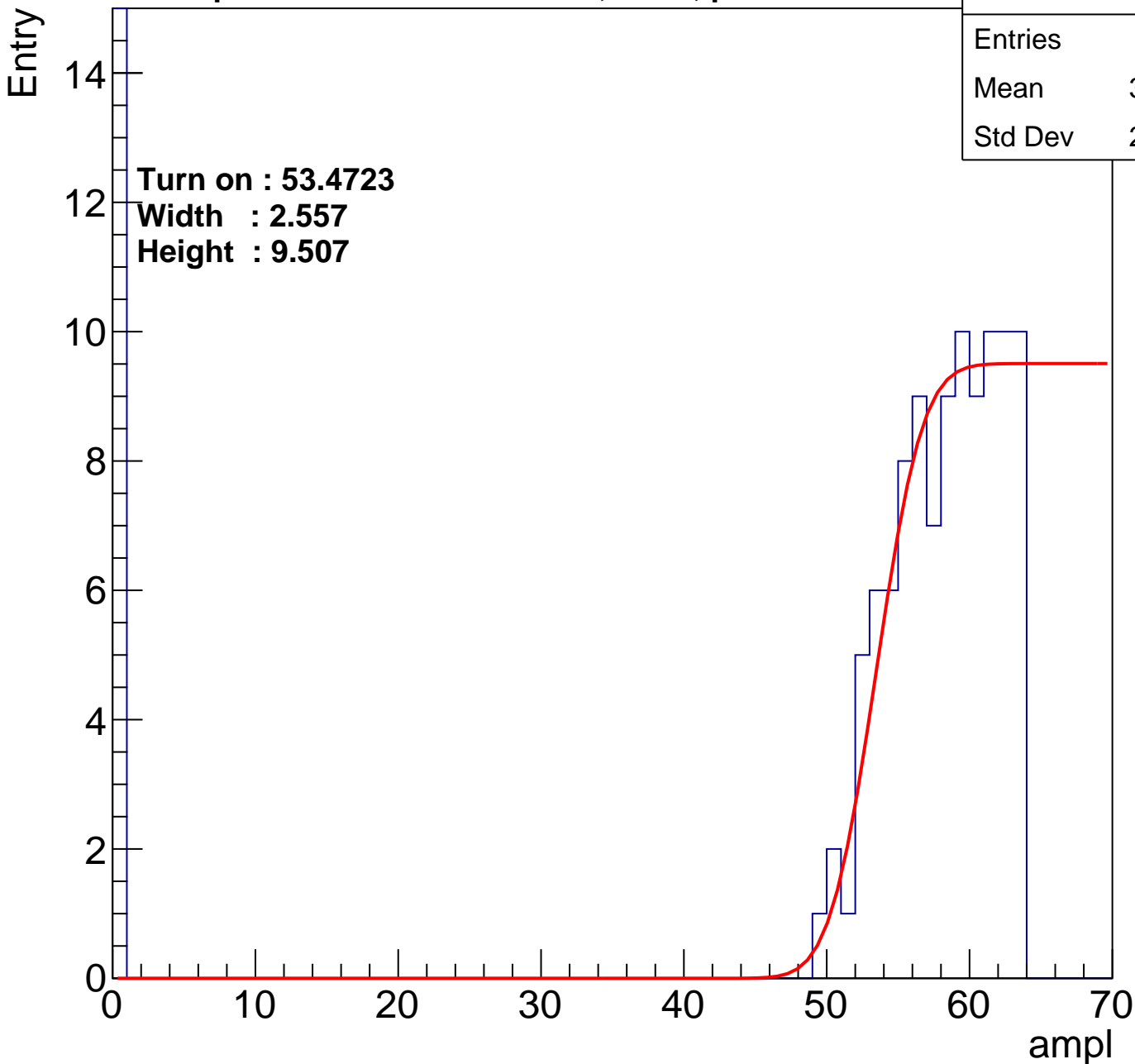
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	30.87
Std Dev	28.97

Turn on : 53.4723

Width : 2.557

Height : 9.507



B1L104S, U20-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	26.17
Std Dev	28.92

Turn on : 54.5744

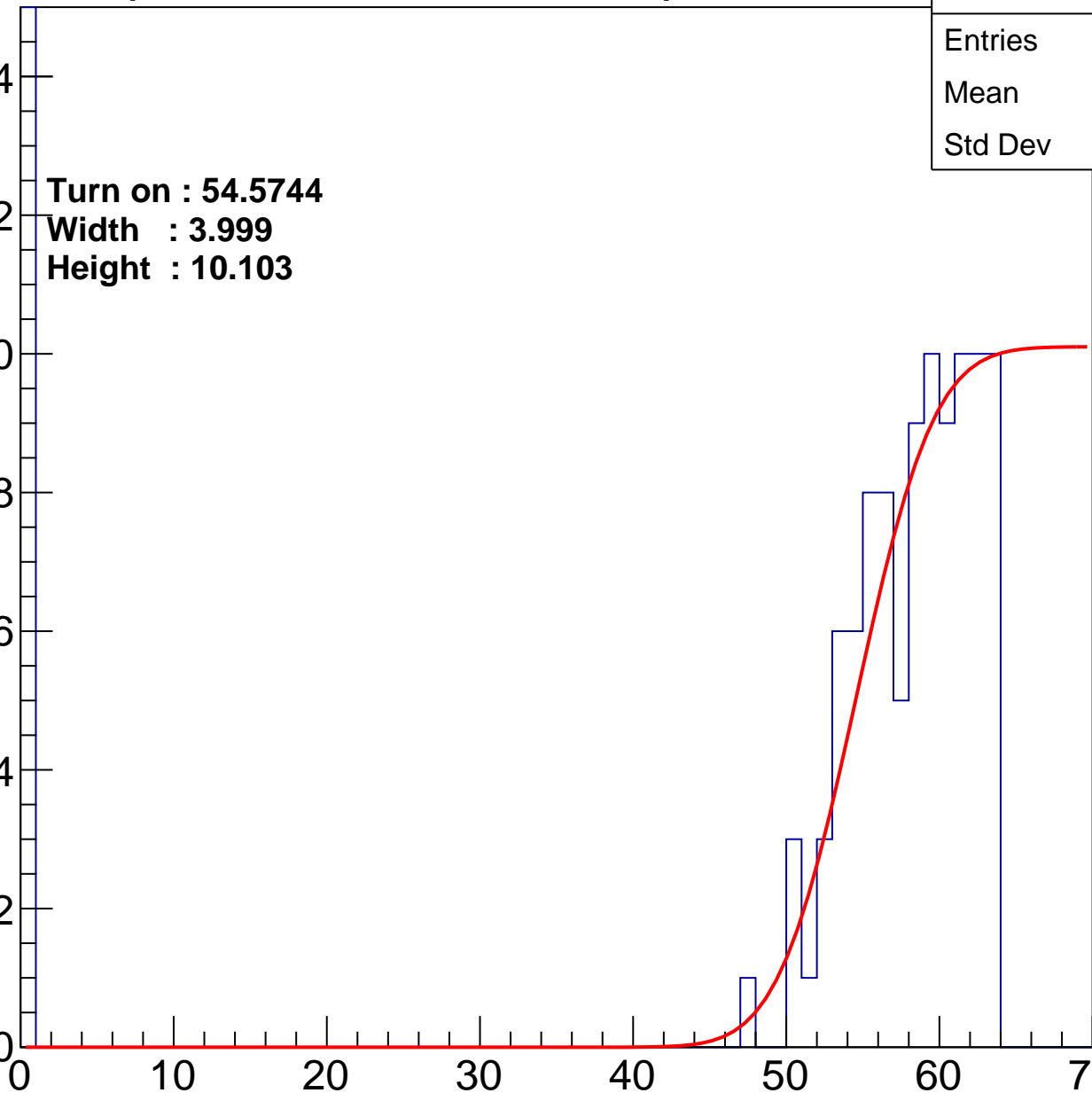
Width : 3.999

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	36.6
Std Dev	27.67

Turn on : 52.8138

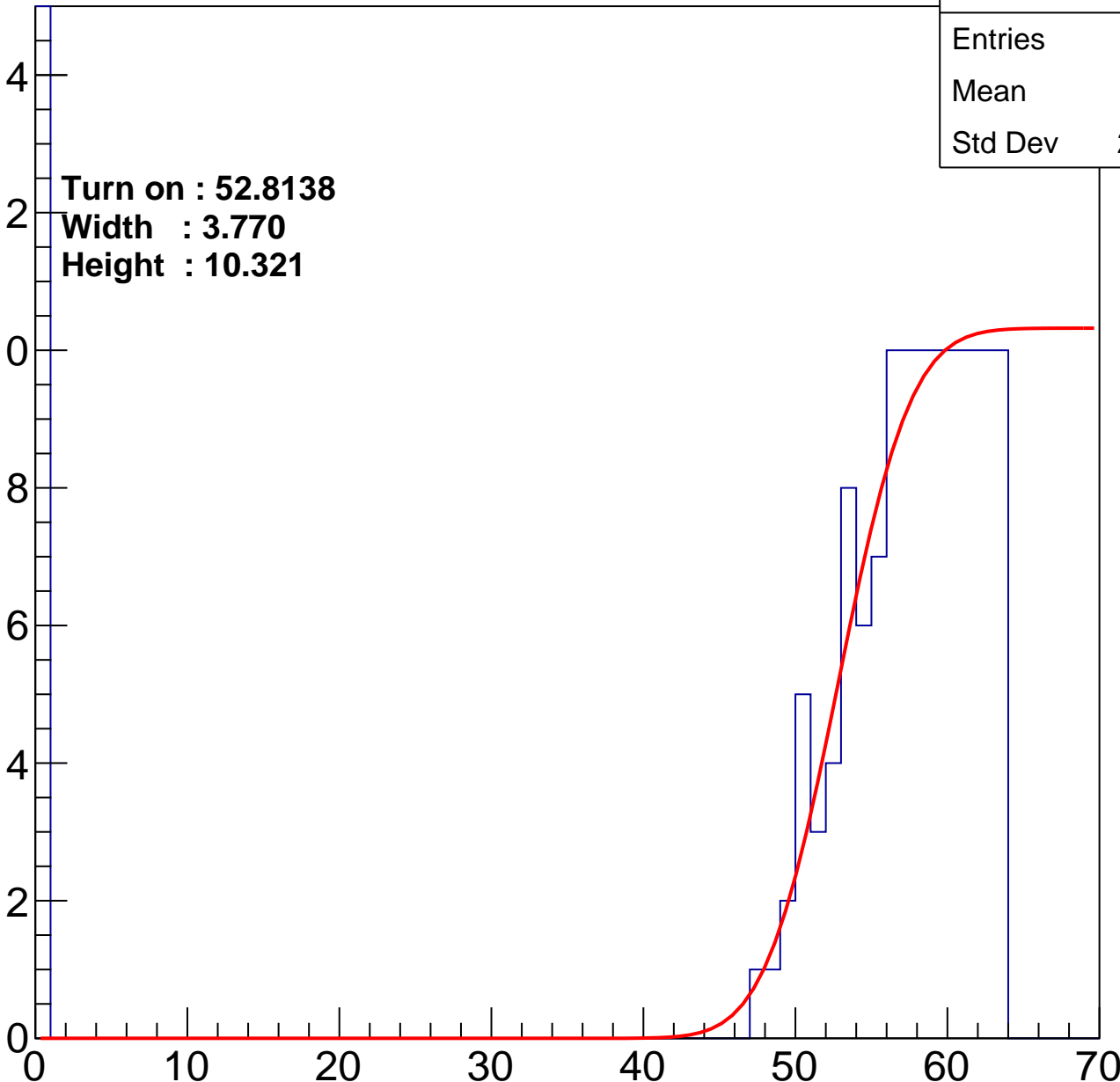
Width : 3.770

Height : 10.321

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	29.64
Std Dev	29.14

Turn on : 55.4437

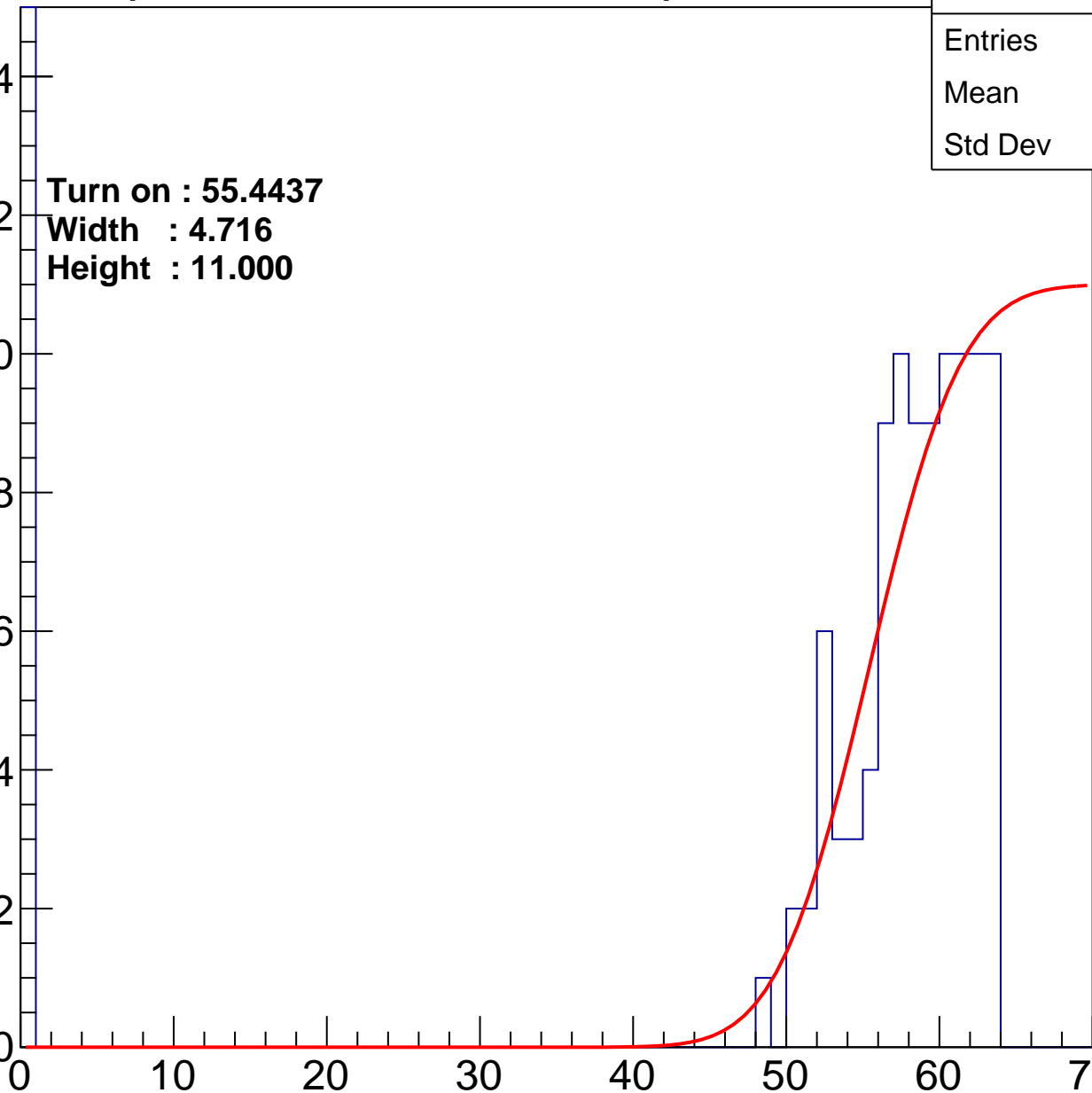
Width : 4.716

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	37.28
Std Dev	26.95

Turn on : 52.8916

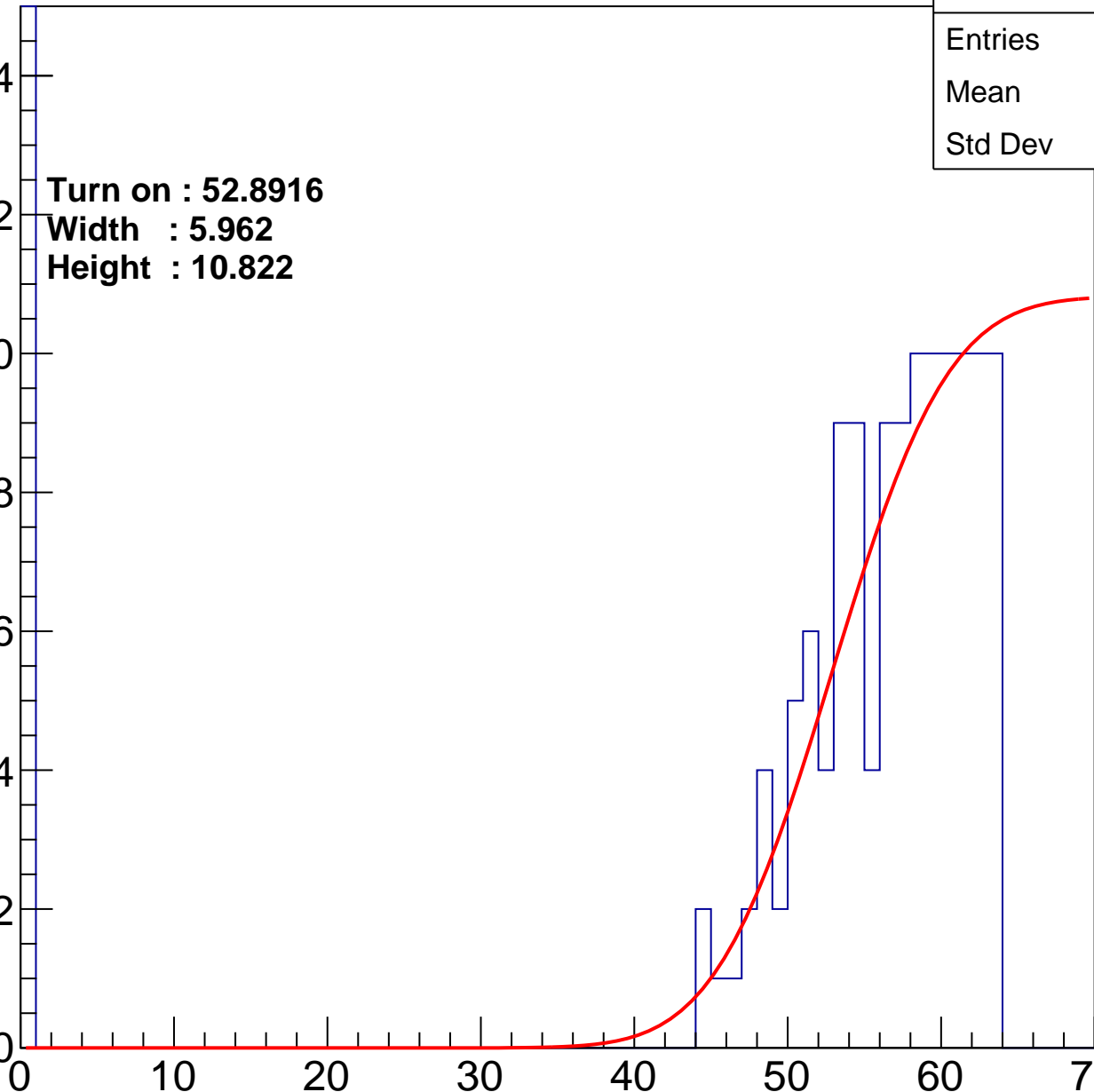
Width : 5.962

Height : 10.822

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	29.97
Std Dev	29.65

Turn on : 56.9175

Width : 2.858

Height : 9.778

Entry

70

60

50

40

30

20

10

0

0

10

20

30

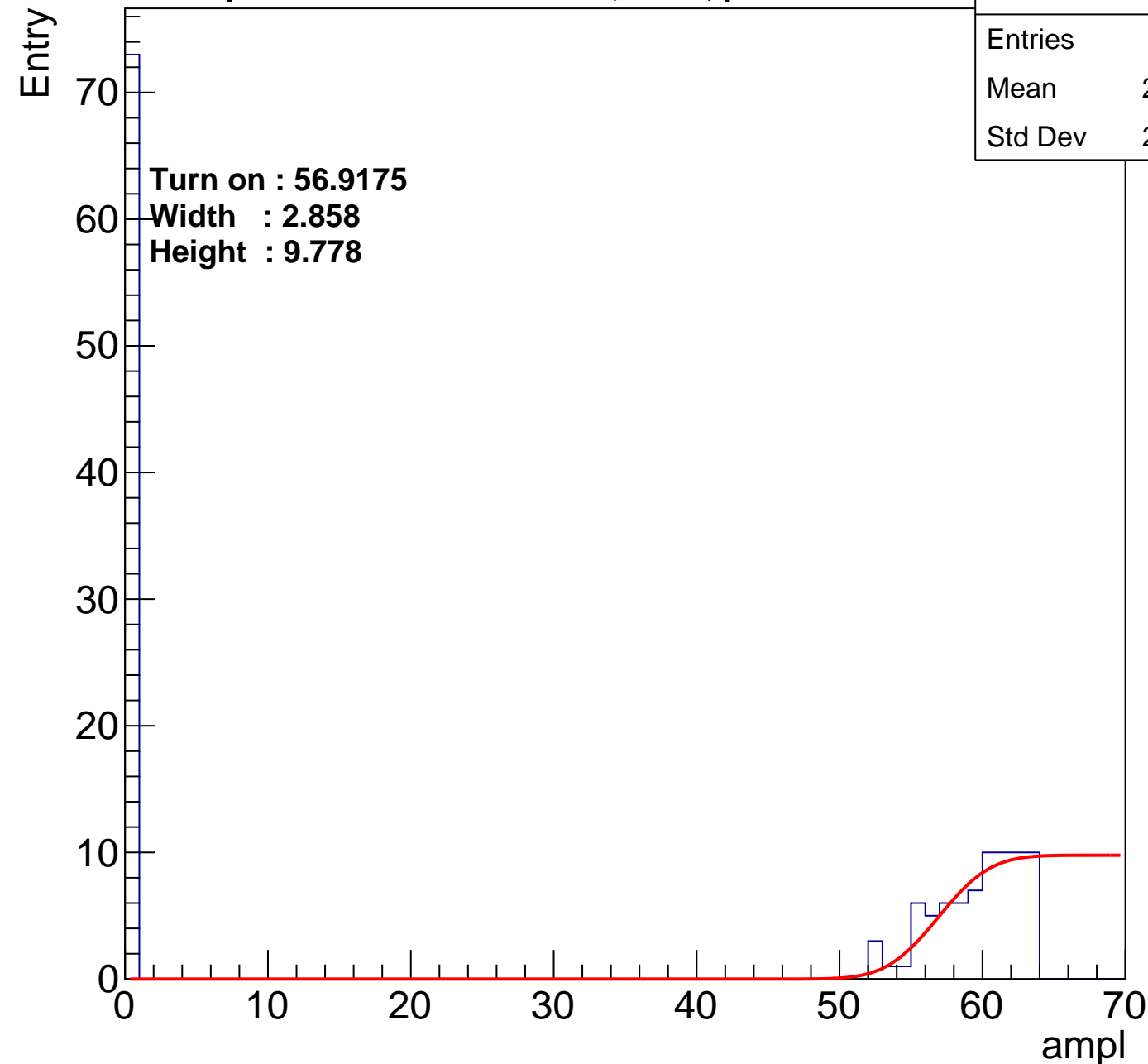
40

50

60

70

ampl



B1L104S, U20-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.34
Std Dev	28.2

Turn on : 54.0495

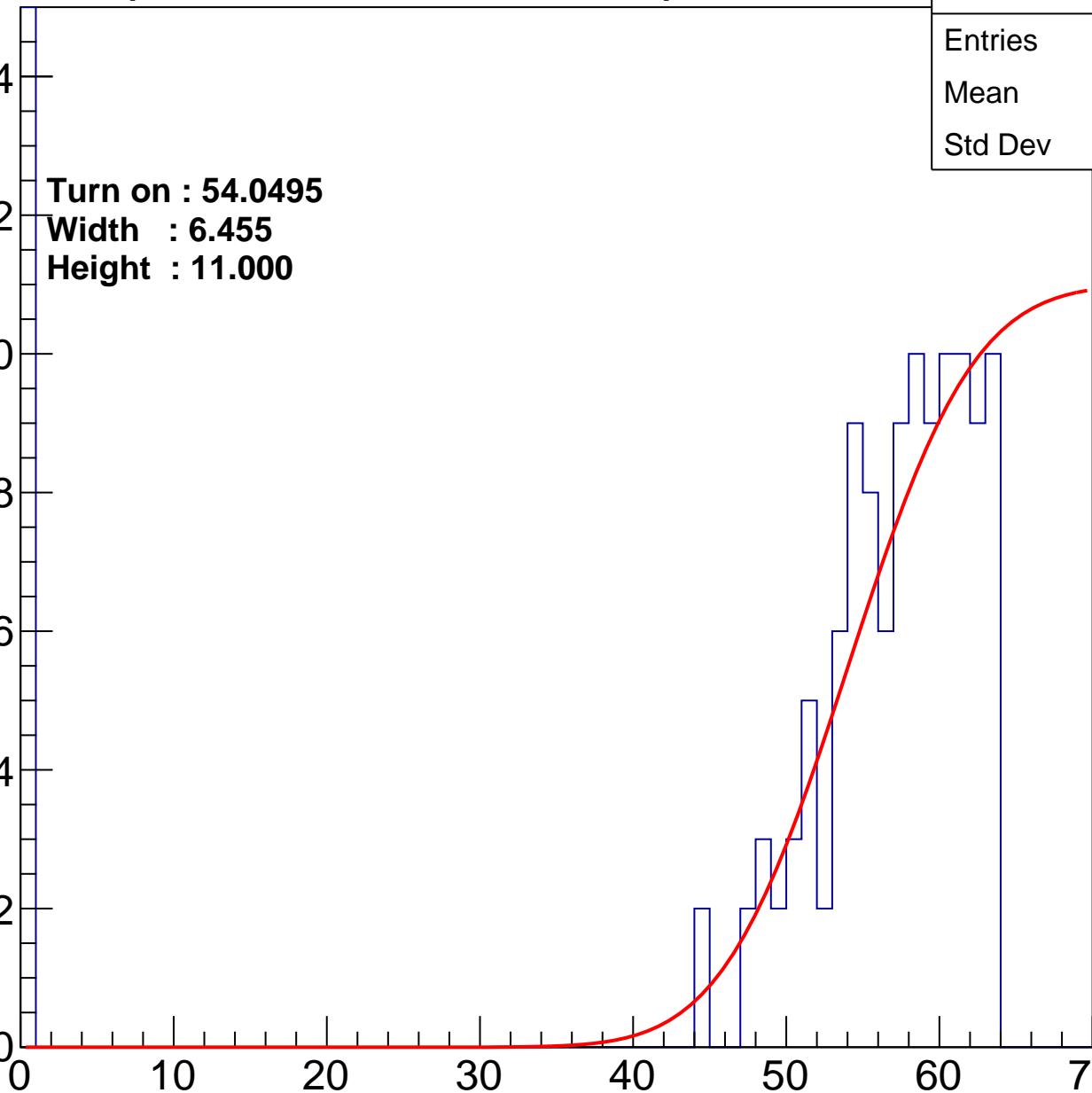
Width : 6.455

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch117

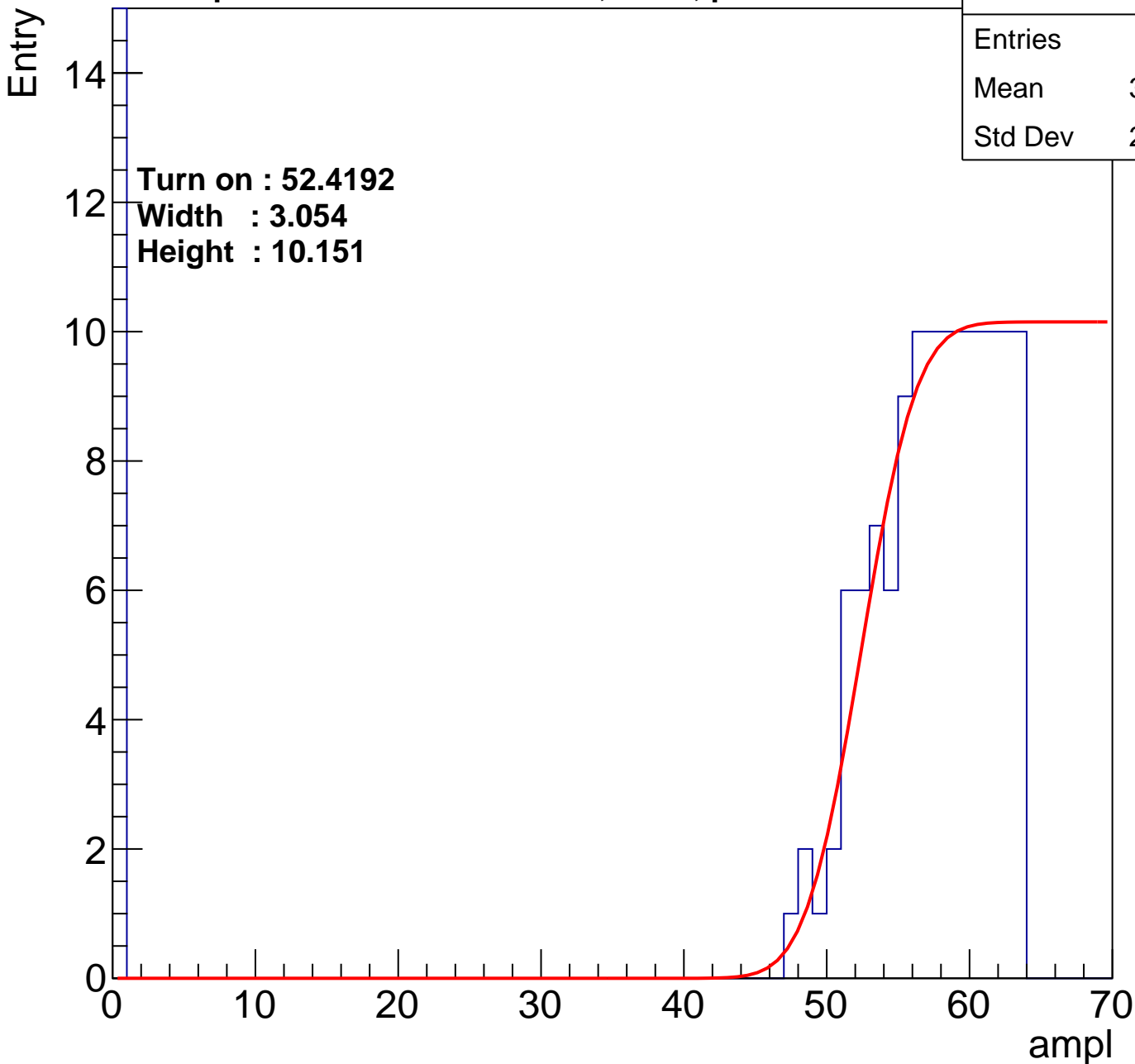
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	35.18
Std Dev	27.99

Turn on : 52.4192

Width : 3.054

Height : 10.151



B1L104S, U20-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	32.7
Std Dev	28.95

Turn on : 54.2103

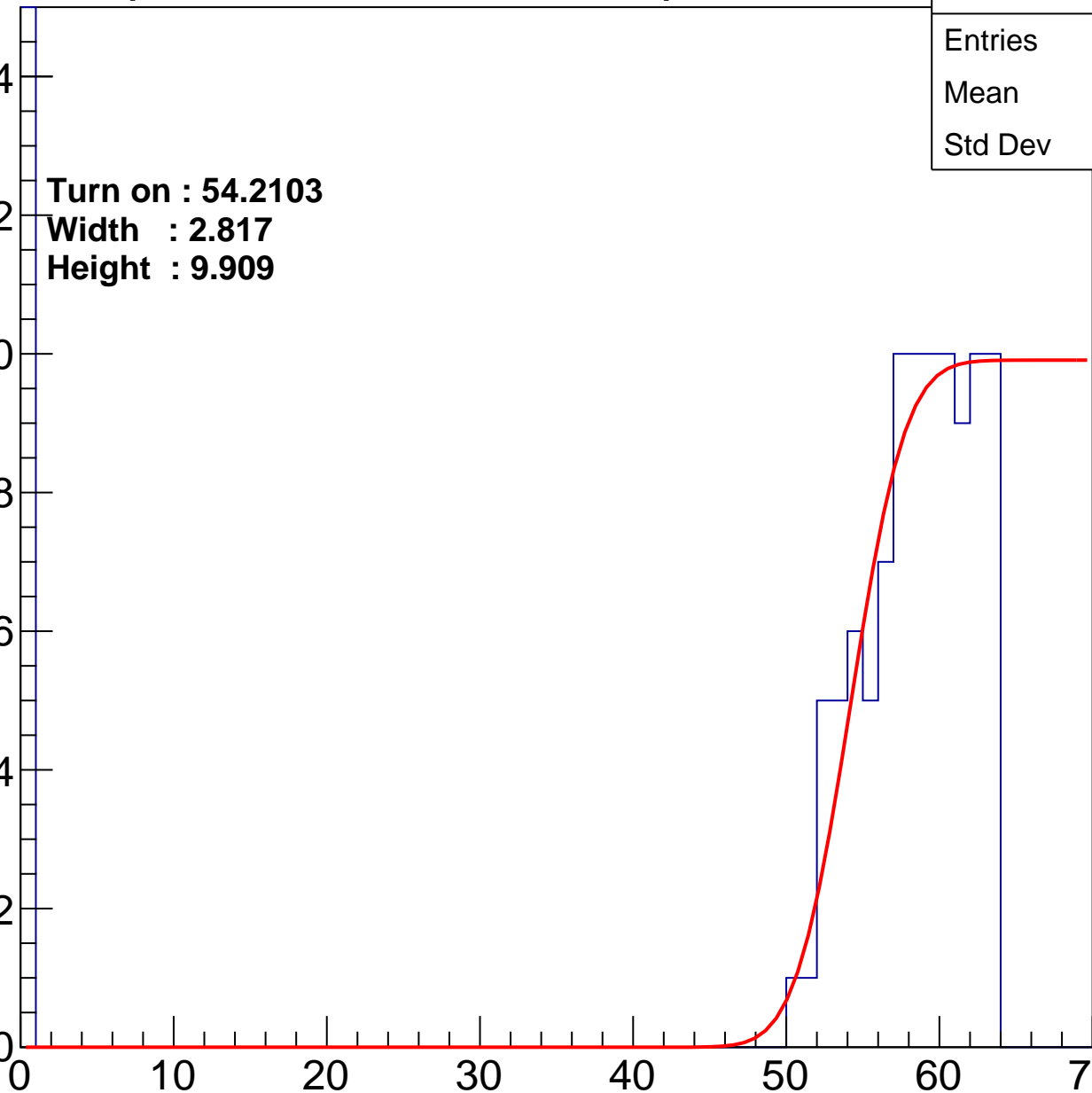
Width : 2.817

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch119

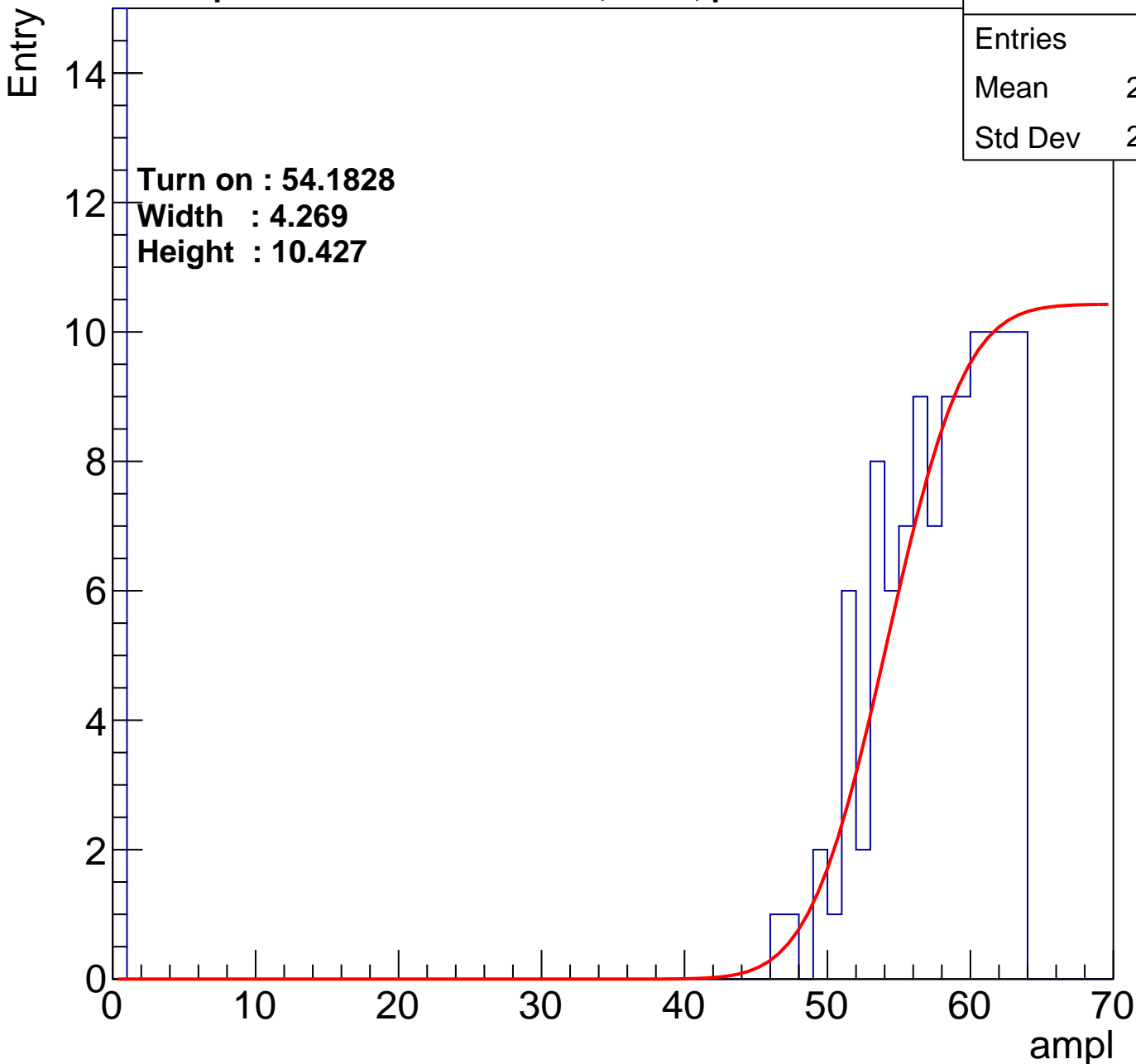
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	29.38
Std Dev	28.84

Turn on : 54.1828

Width : 4.269

Height : 10.427



B1L104S, U20-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	26.35
Std Dev	28.64

Turn on : 54.4917

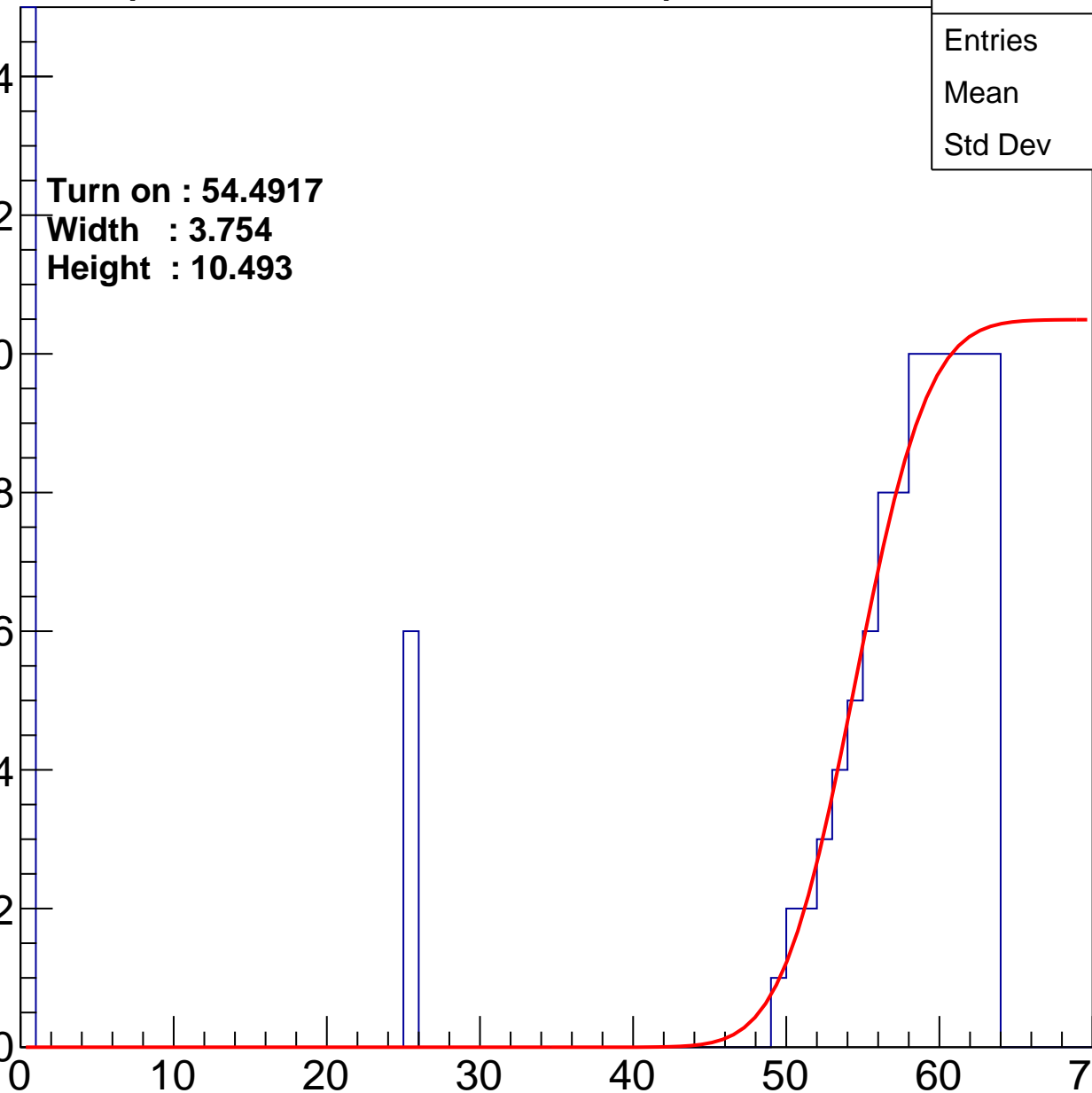
Width : 3.754

Height : 10.493

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	30.89
Std Dev	28.94

Turn on : 54.9504

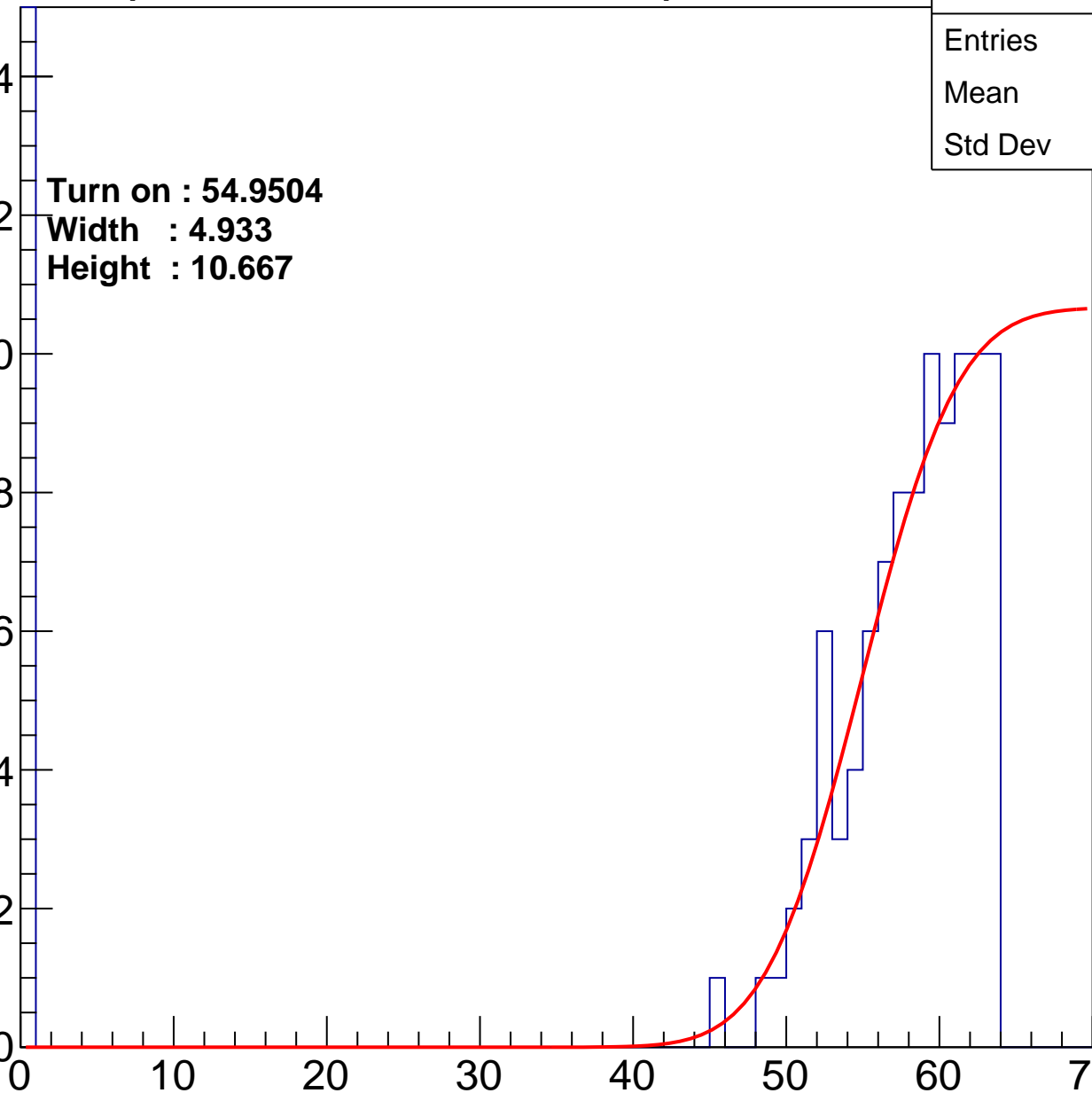
Width : 4.933

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.89
Std Dev	28.64

Turn on : 52.7632

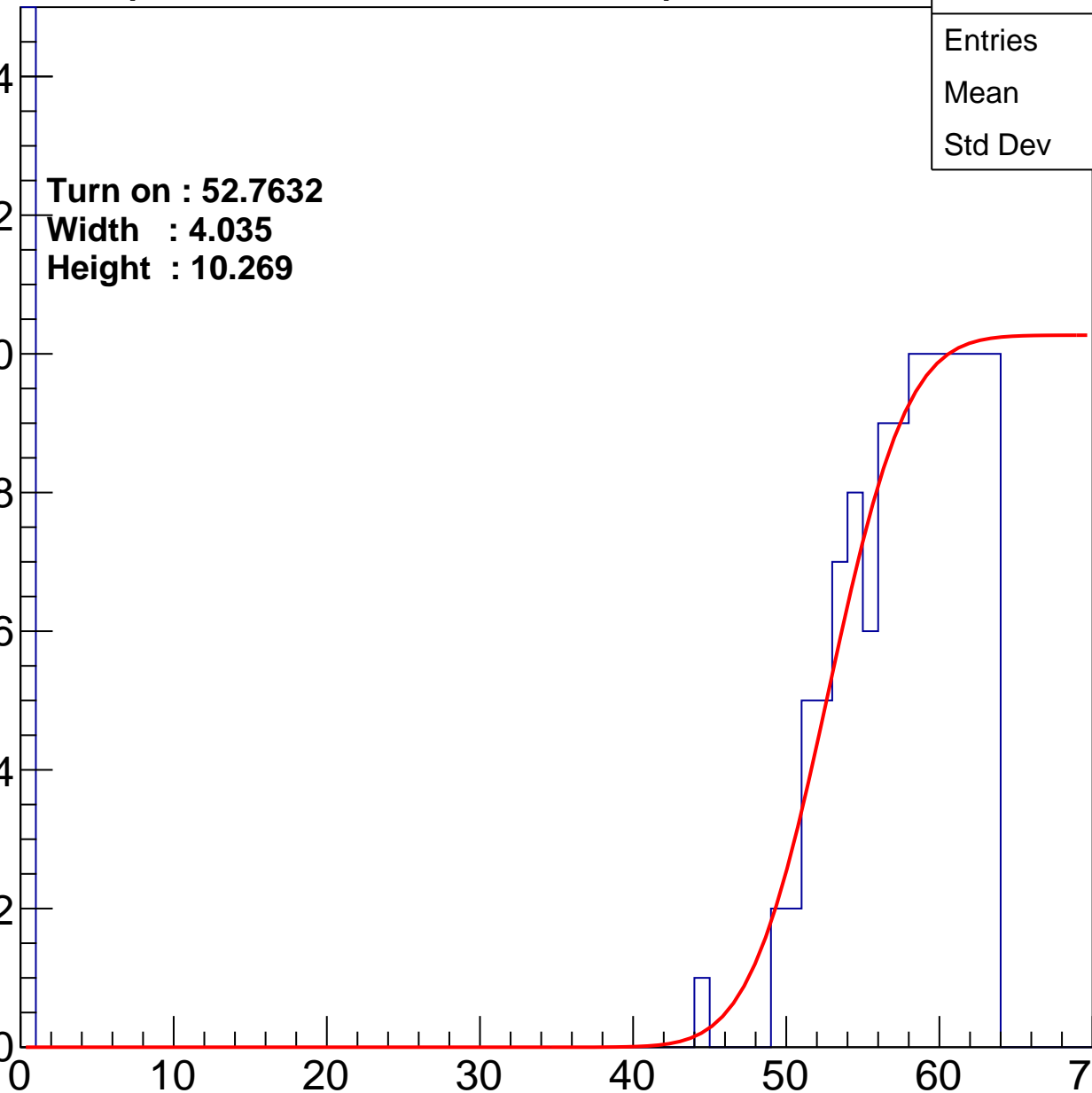
Width : 4.035

Height : 10.269

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	32.07
Std Dev	28.69

Turn on : 52.9400

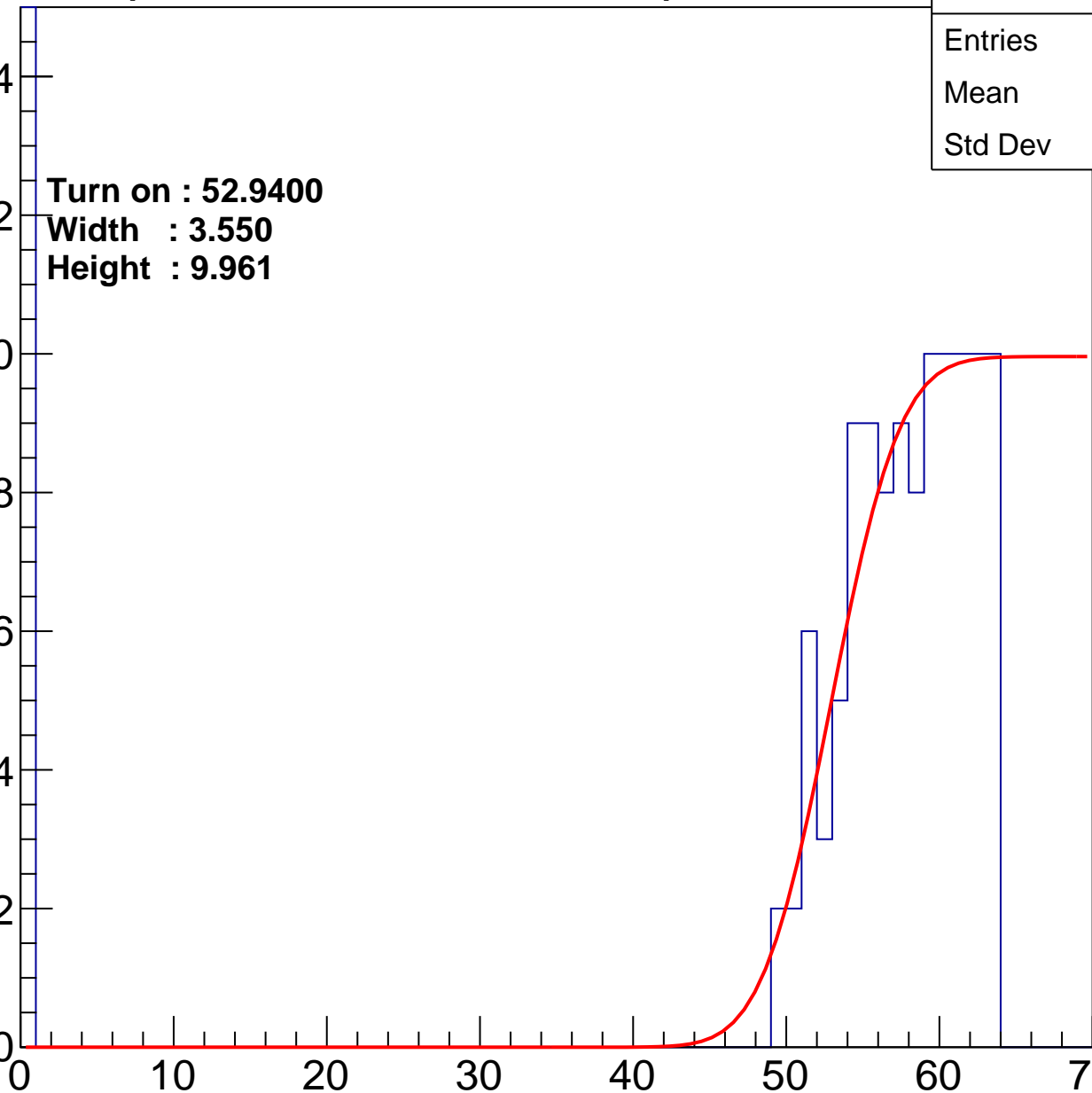
Width : 3.550

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch124

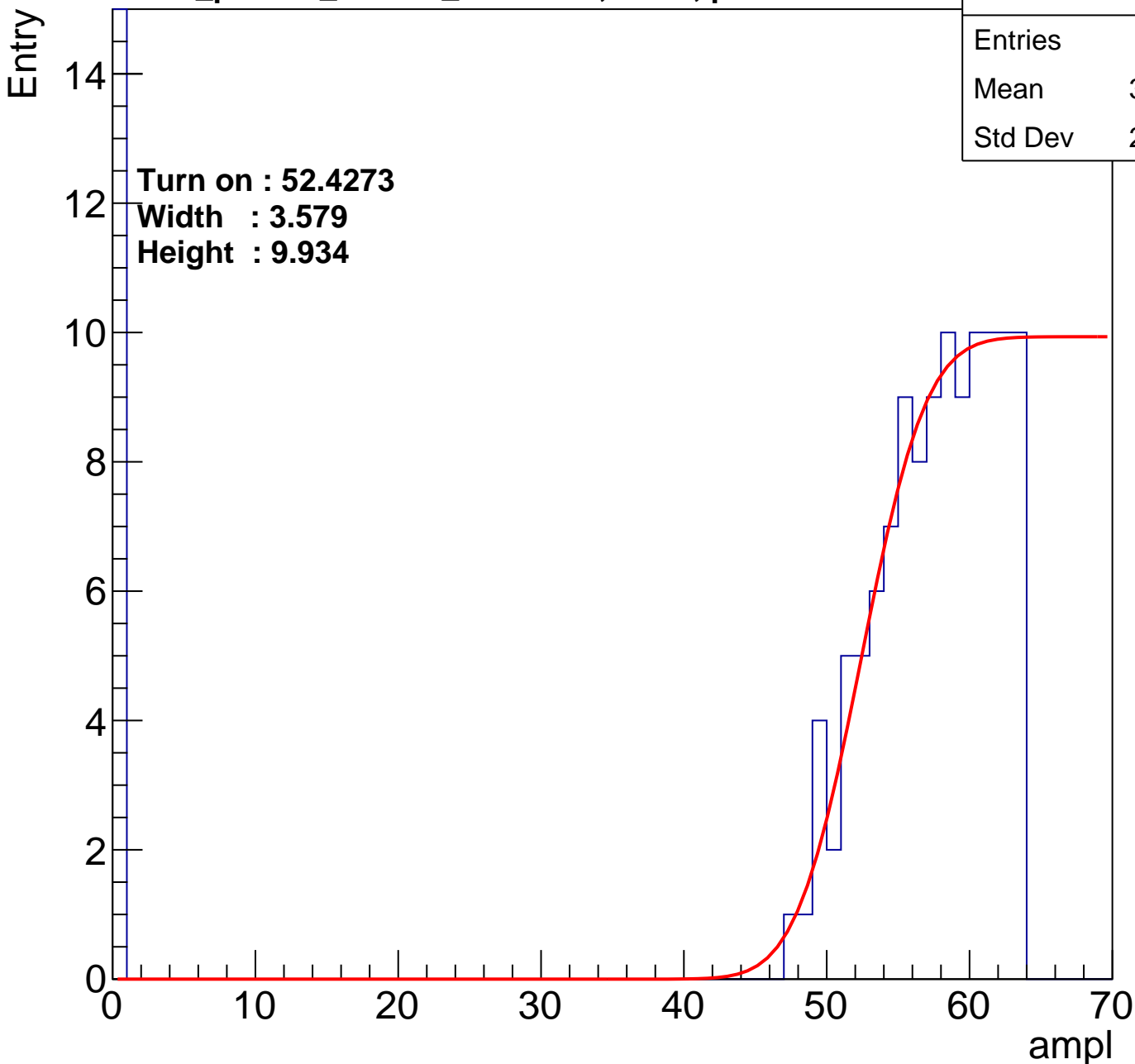
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.48
Std Dev	28.32

Turn on : 52.4273

Width : 3.579

Height : 9.934



B1L104S, U20-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	29.45
Std Dev	29

Turn on : 54.9487

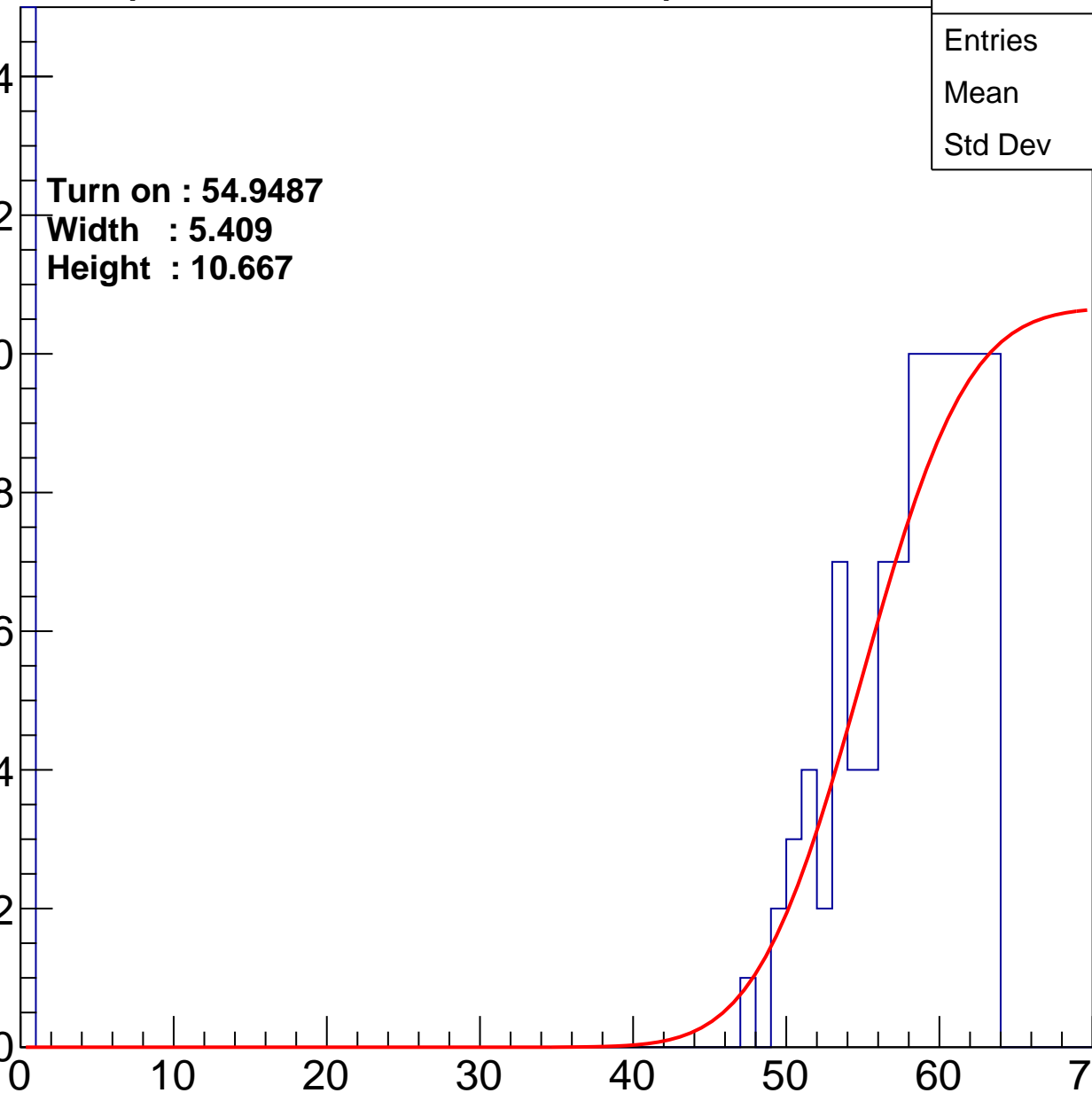
Width : 5.409

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	31.47
Std Dev	28.67

Turn on : 53.4773

Width : 4.866

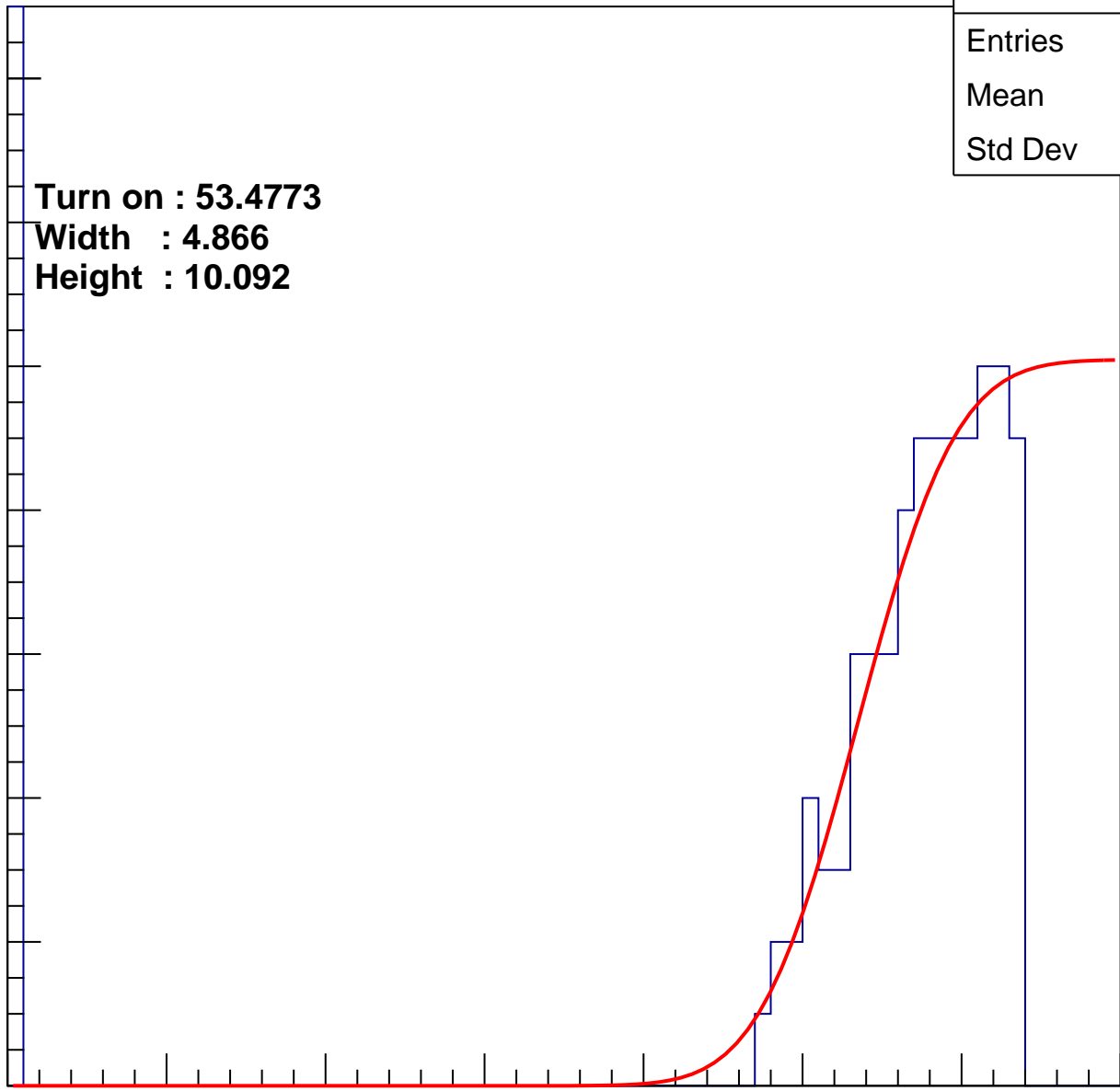
Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U20-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	29.75
Std Dev	28.72

Turn on : 52.6353

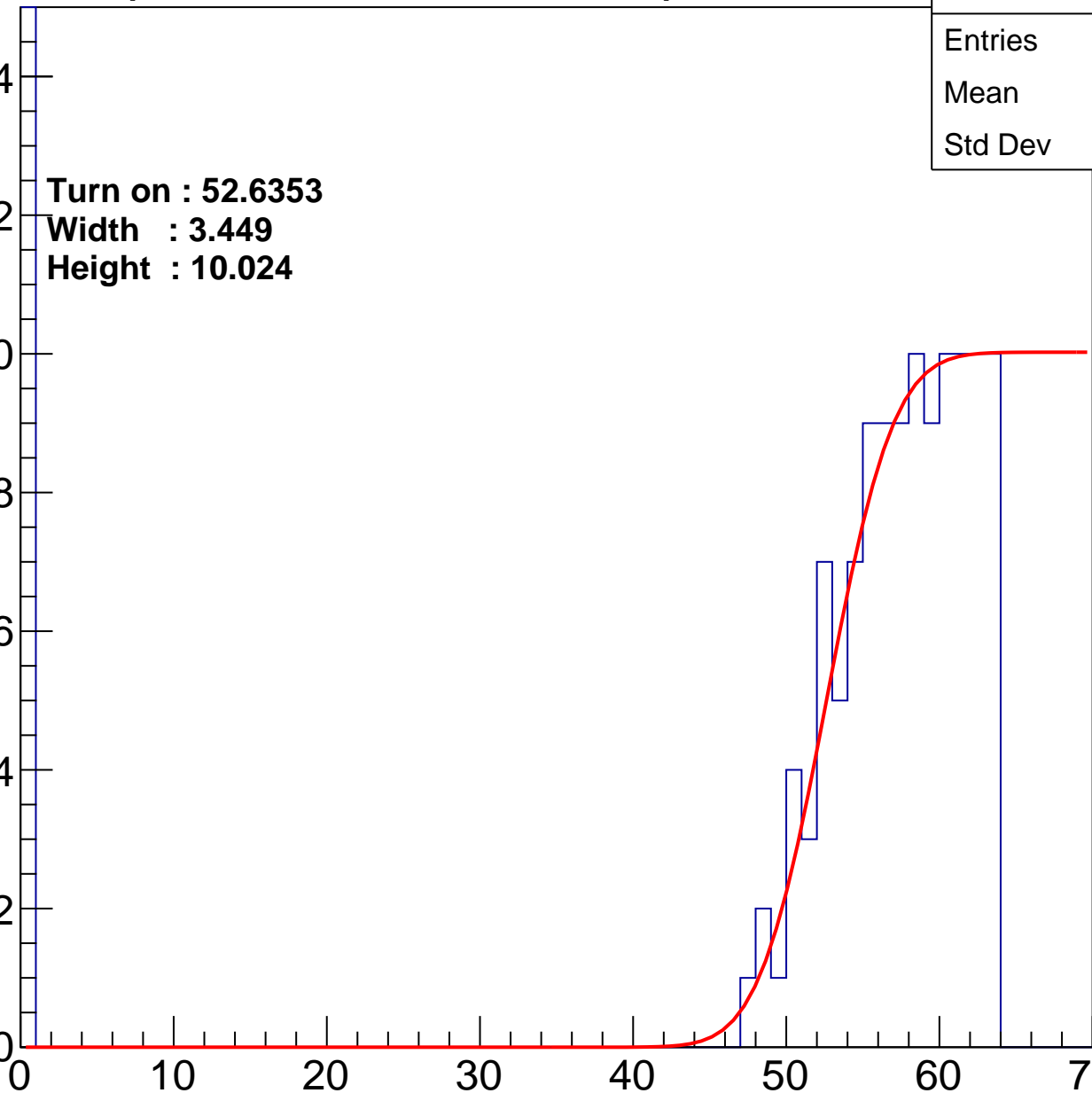
Width : 3.449

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U20-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	29.75
Std Dev	28.72

Turn on : 52.6353

Width : 3.449

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

