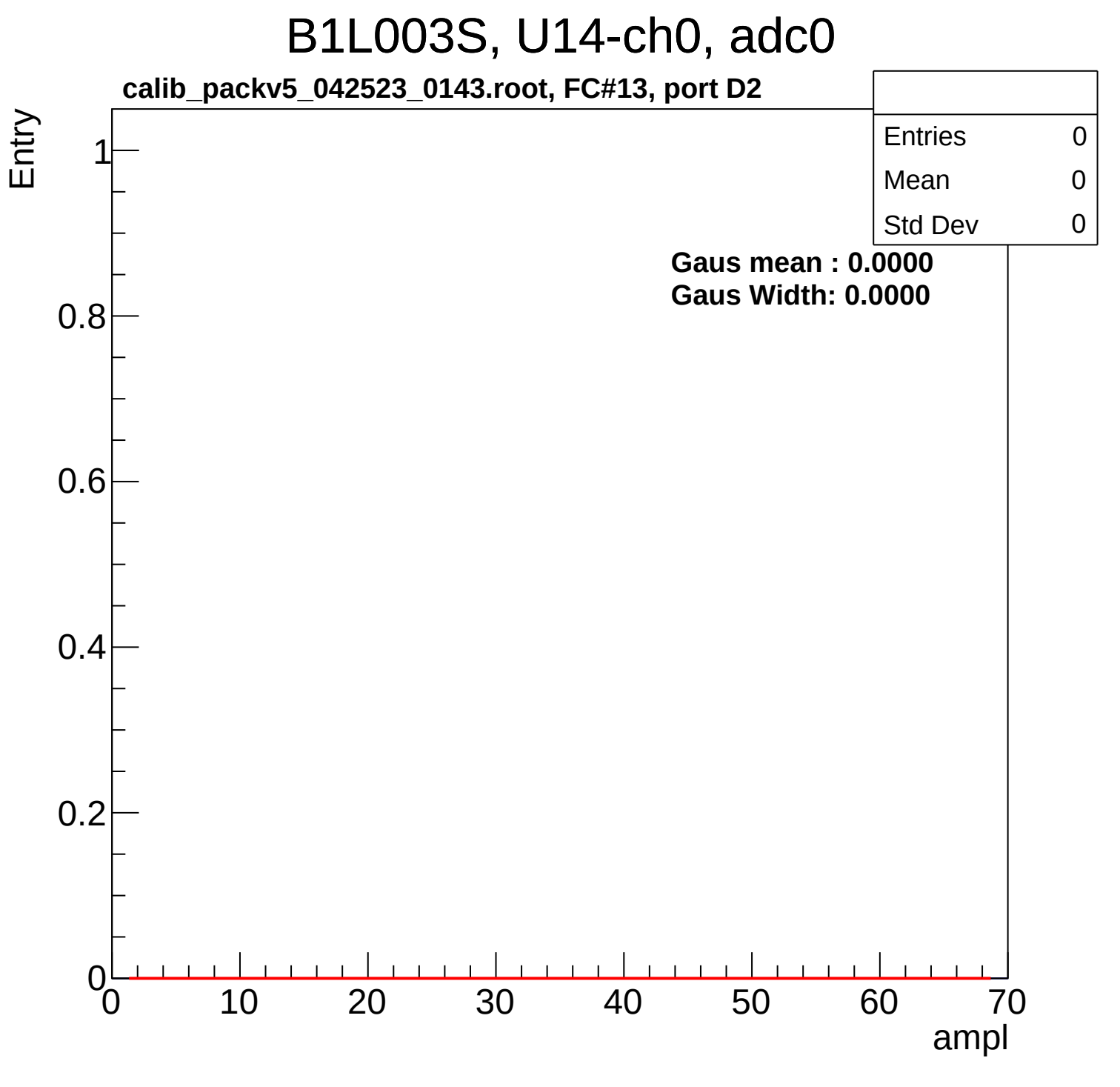
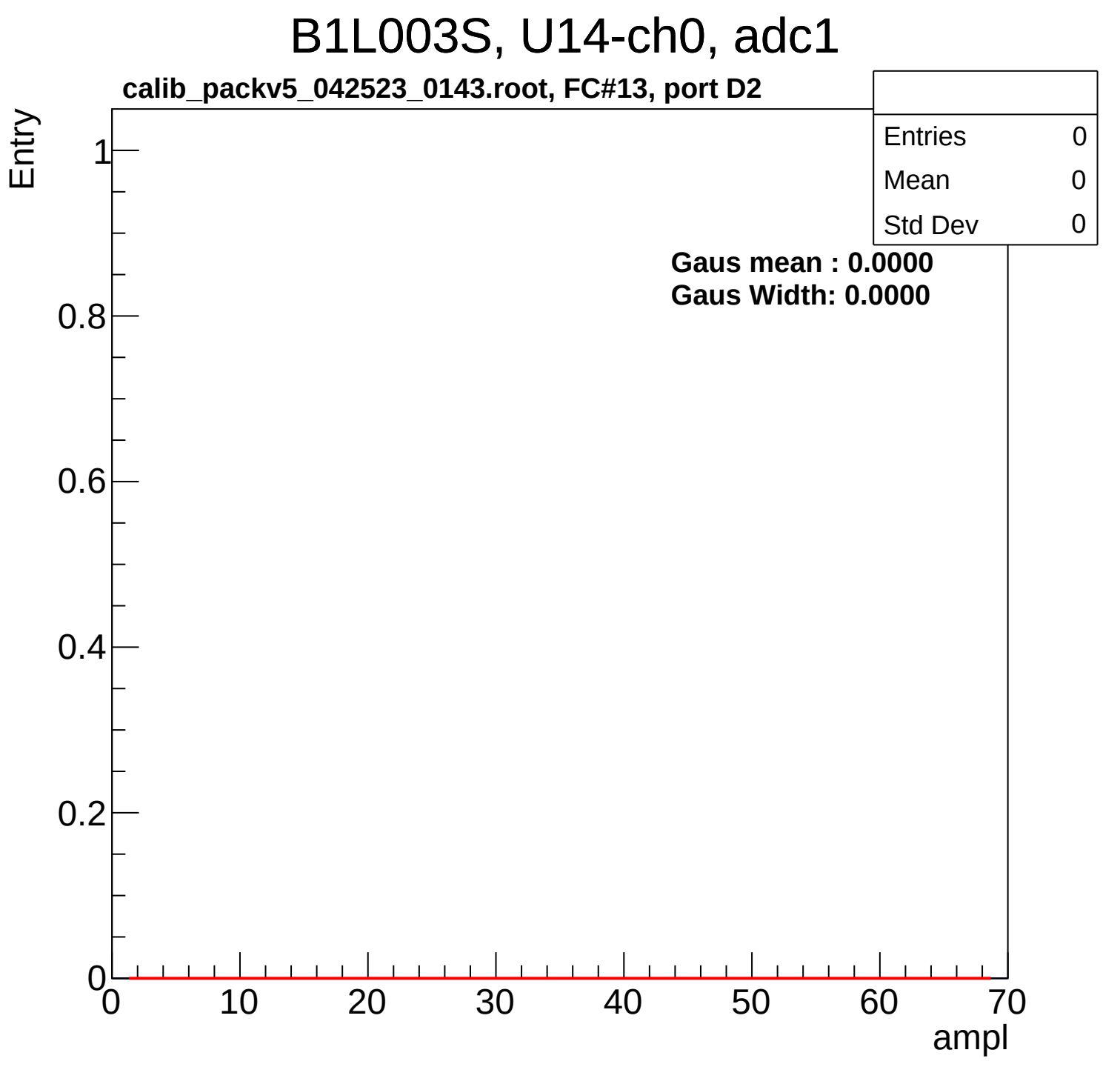
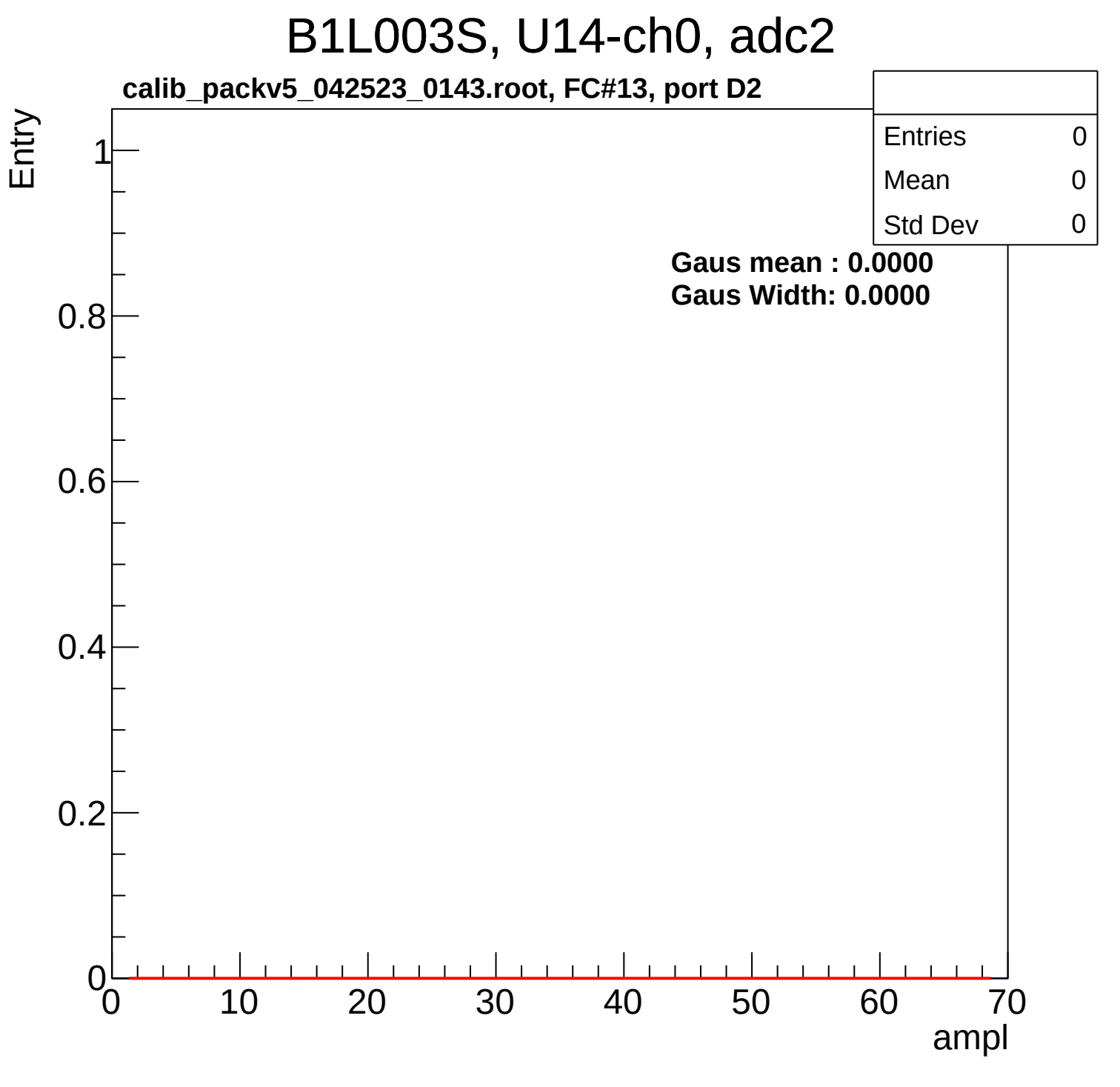
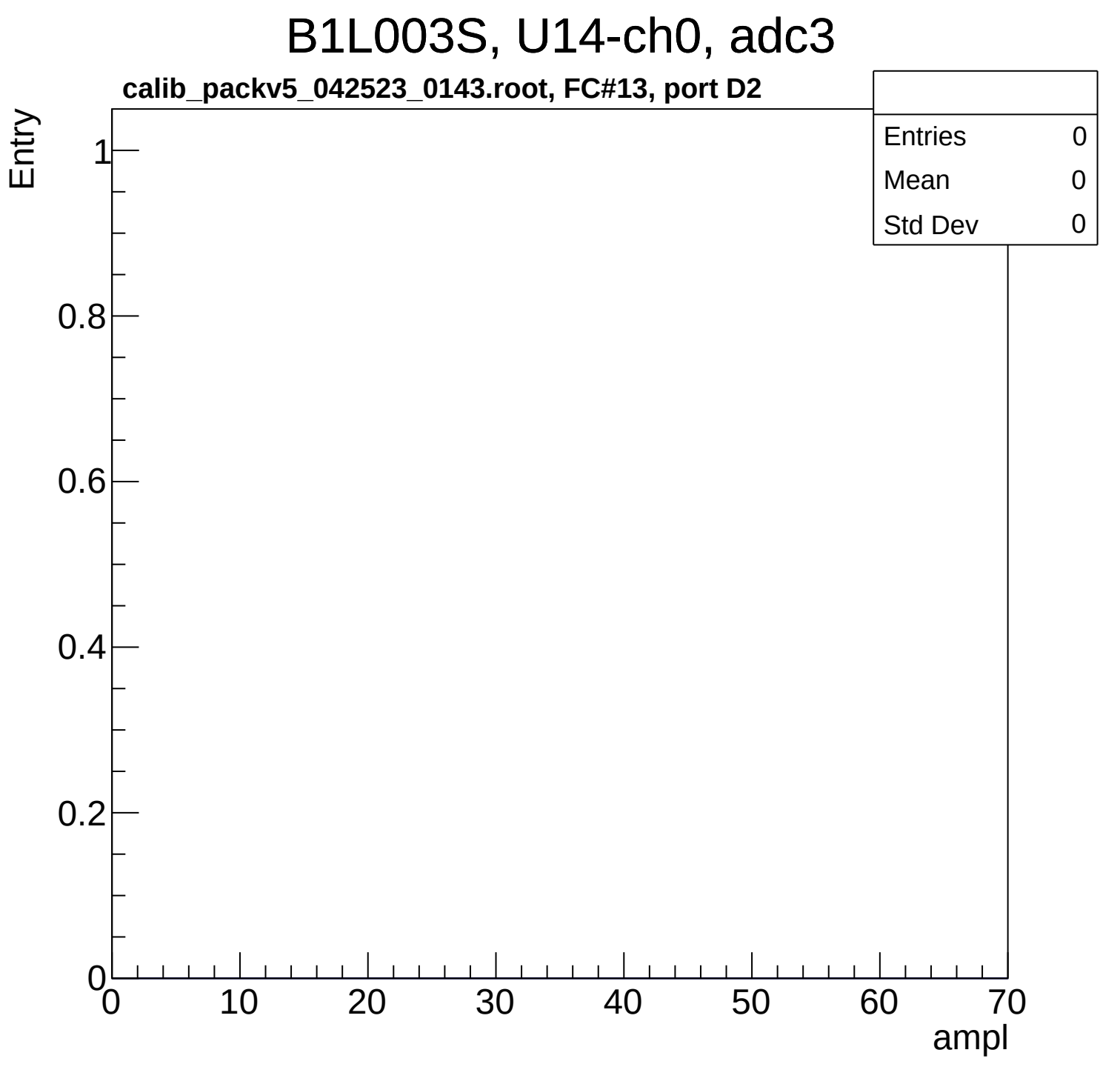
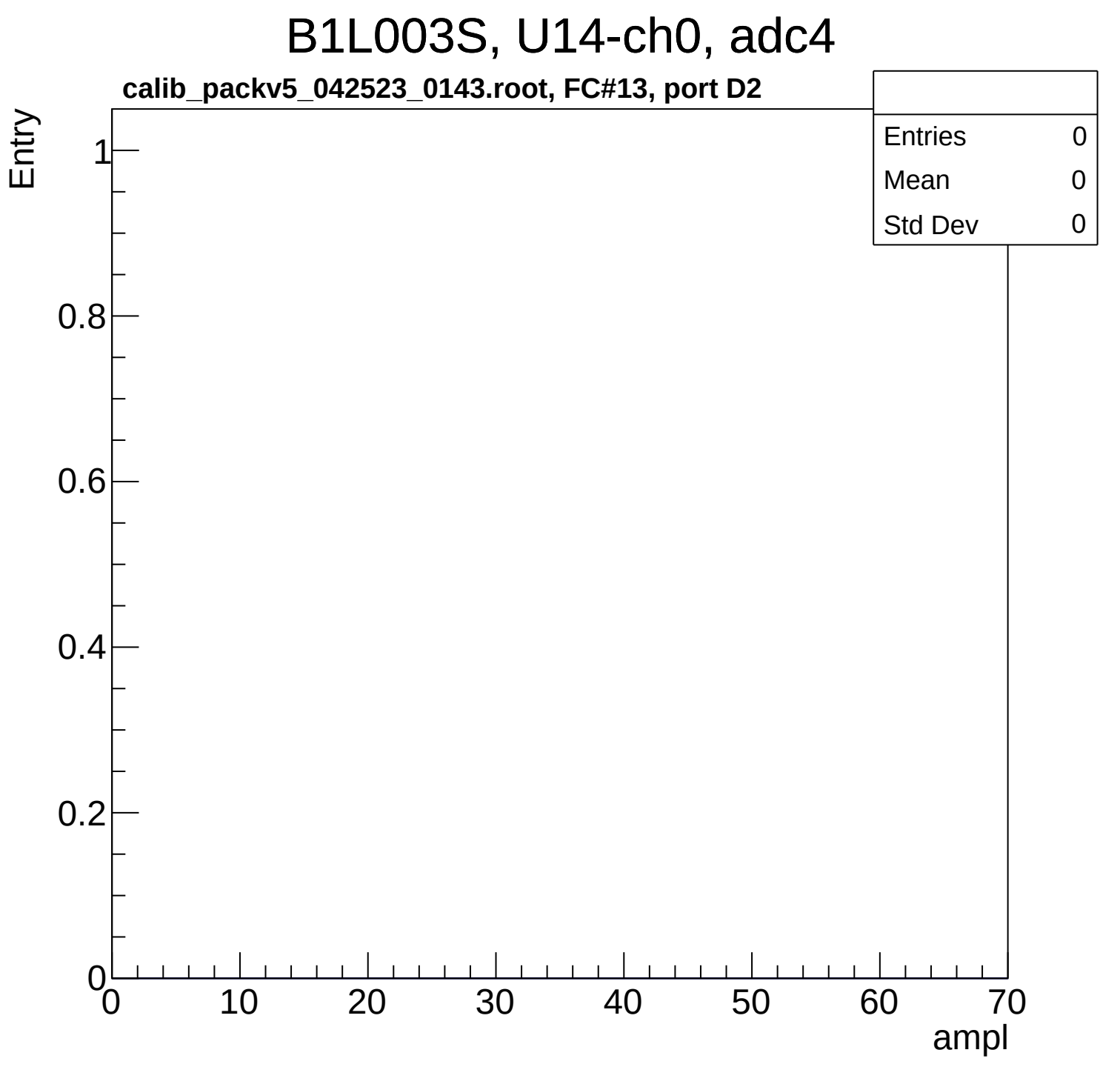
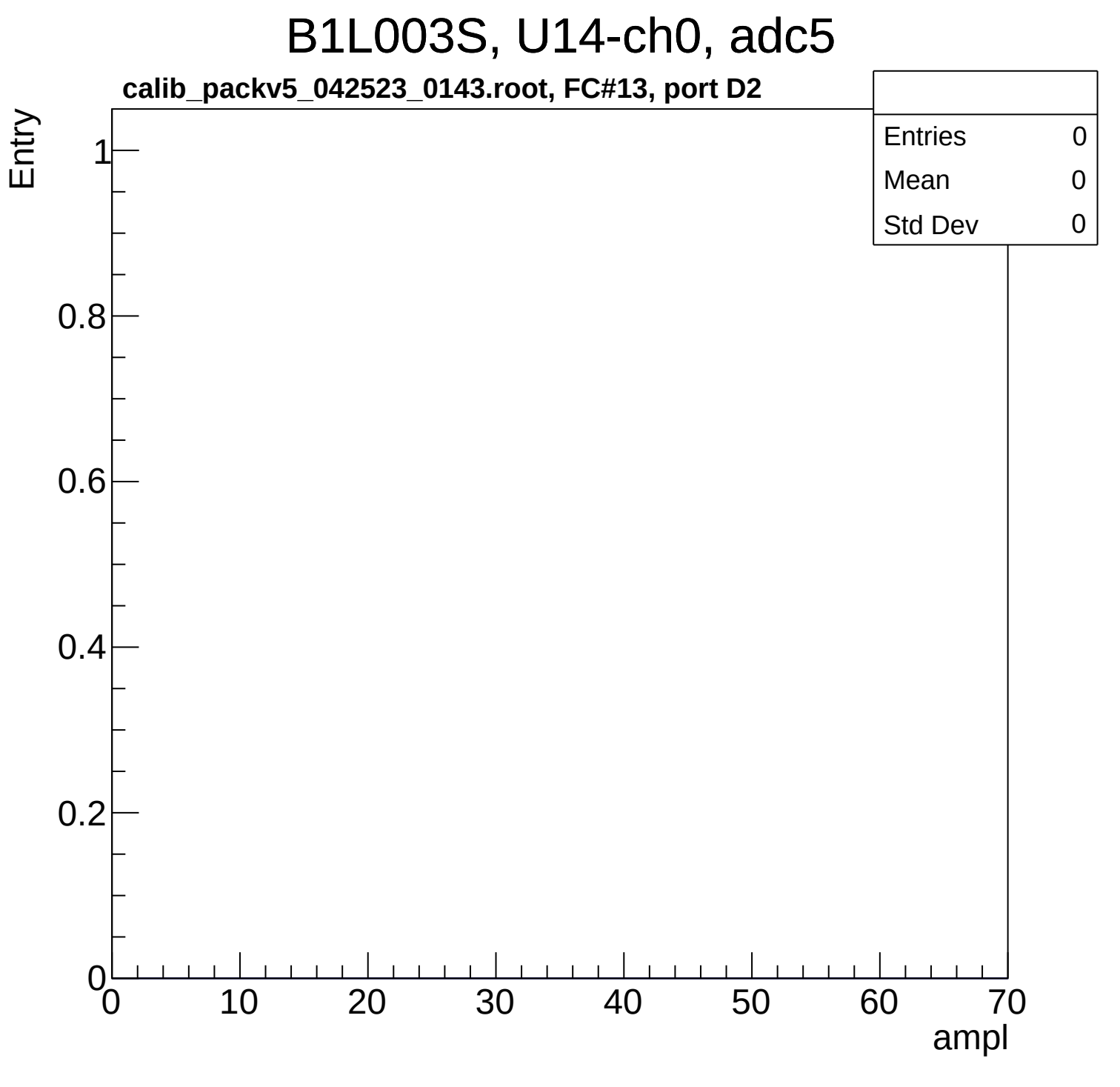

B1L003S, U14-ch0, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

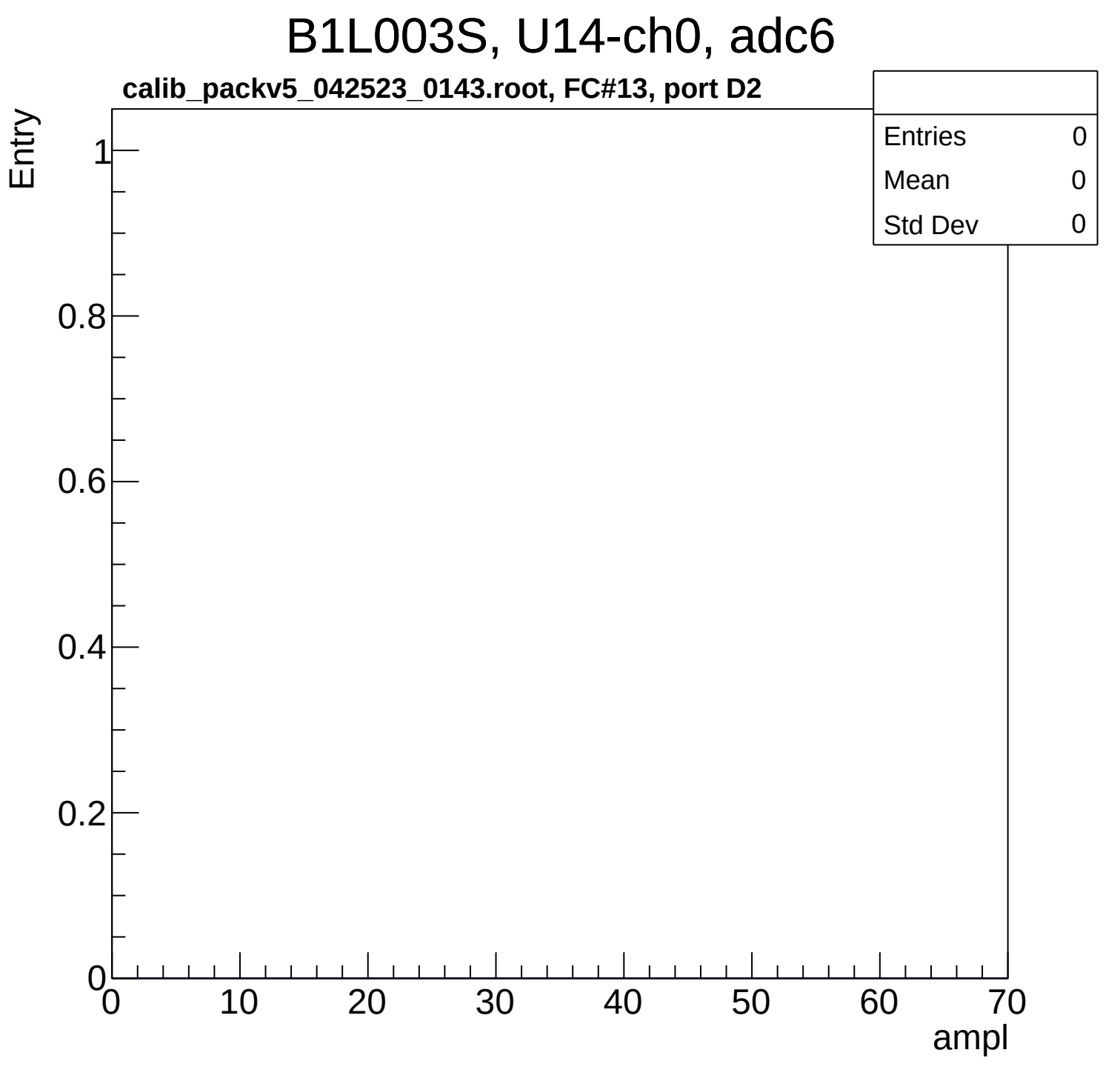

B1L003S, U14-ch0, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

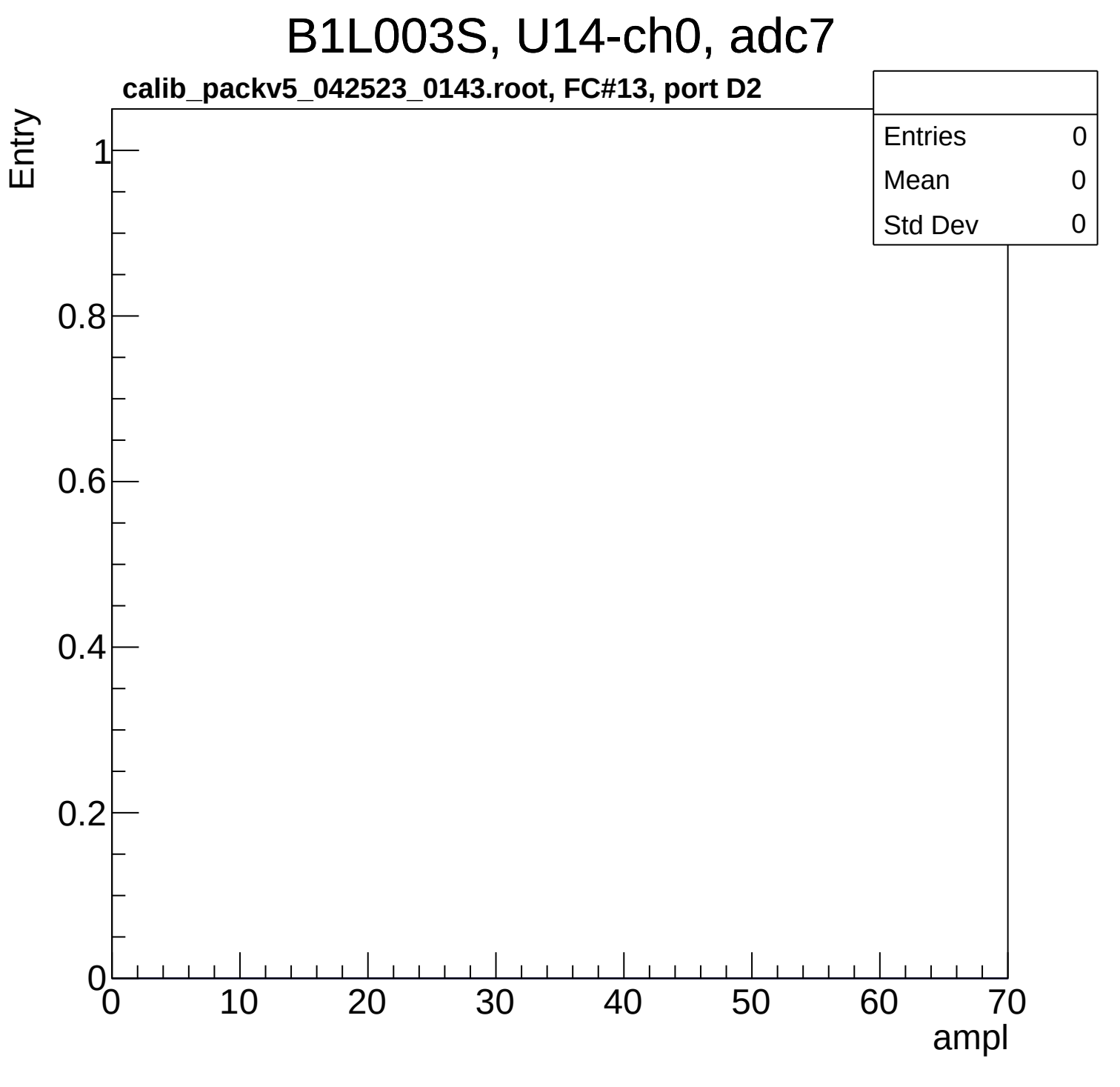

B1L003S, U14-ch0, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

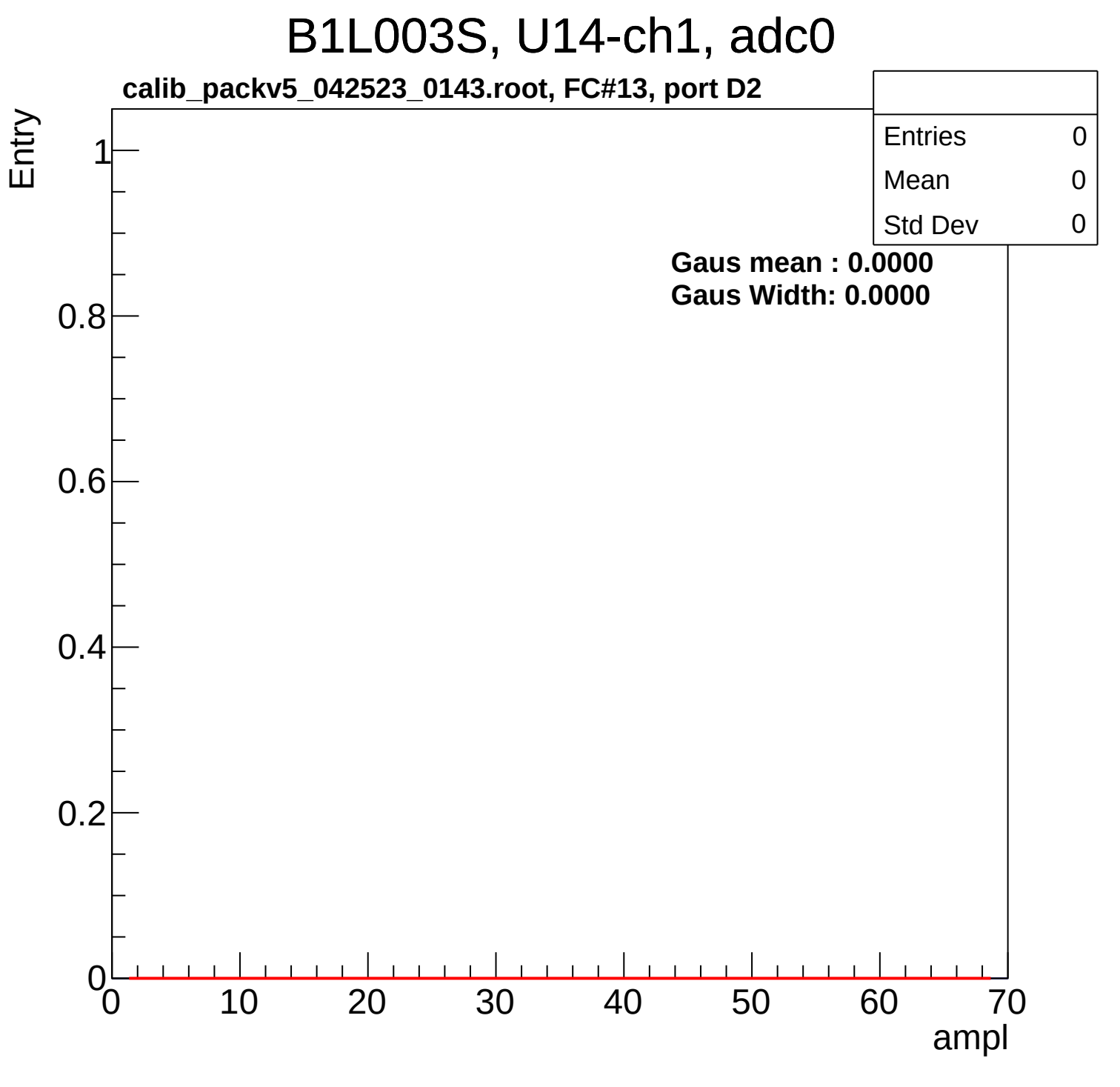

B1L003S, U14-ch0, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

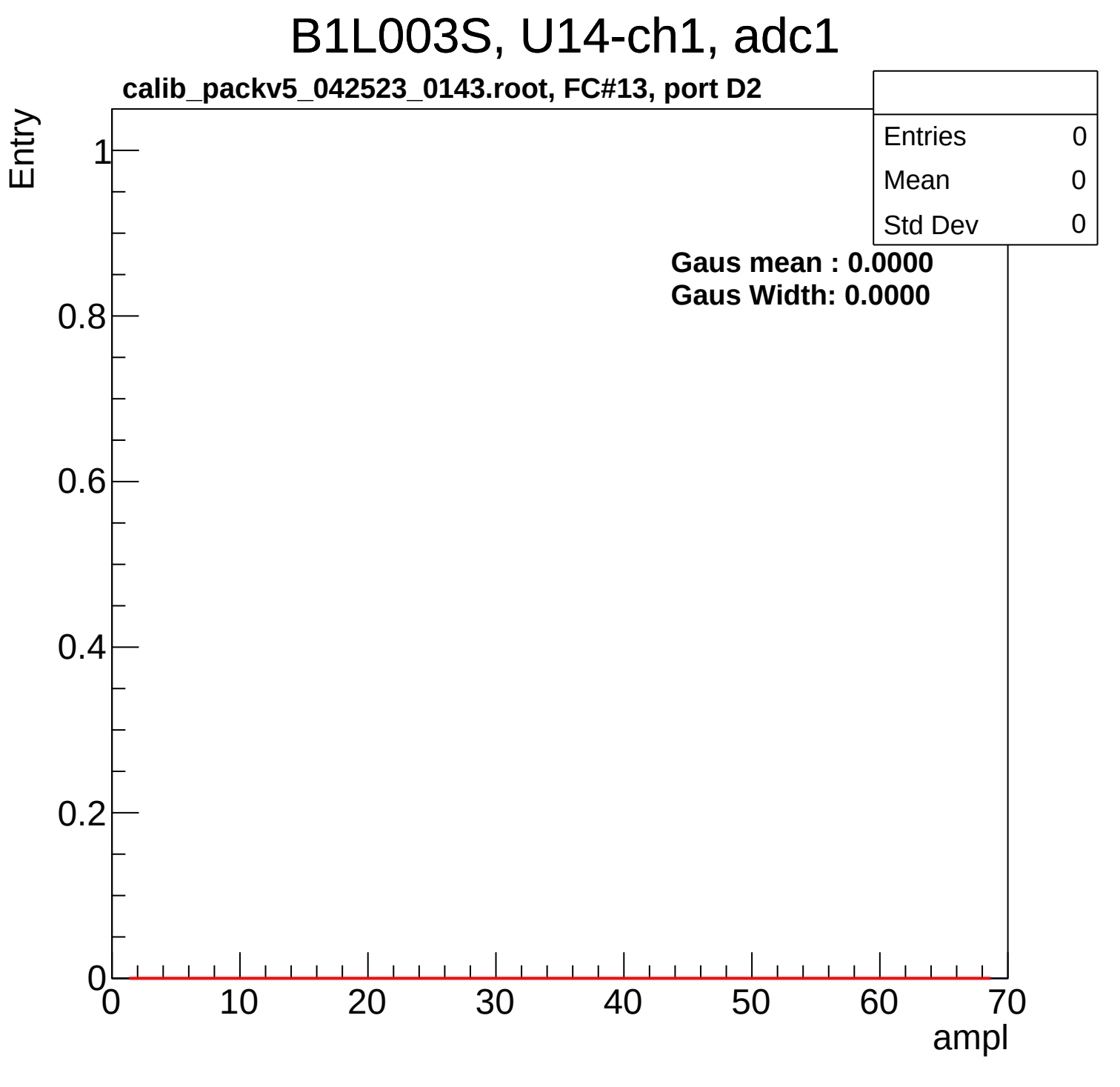

B1L003S, U14-ch0, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

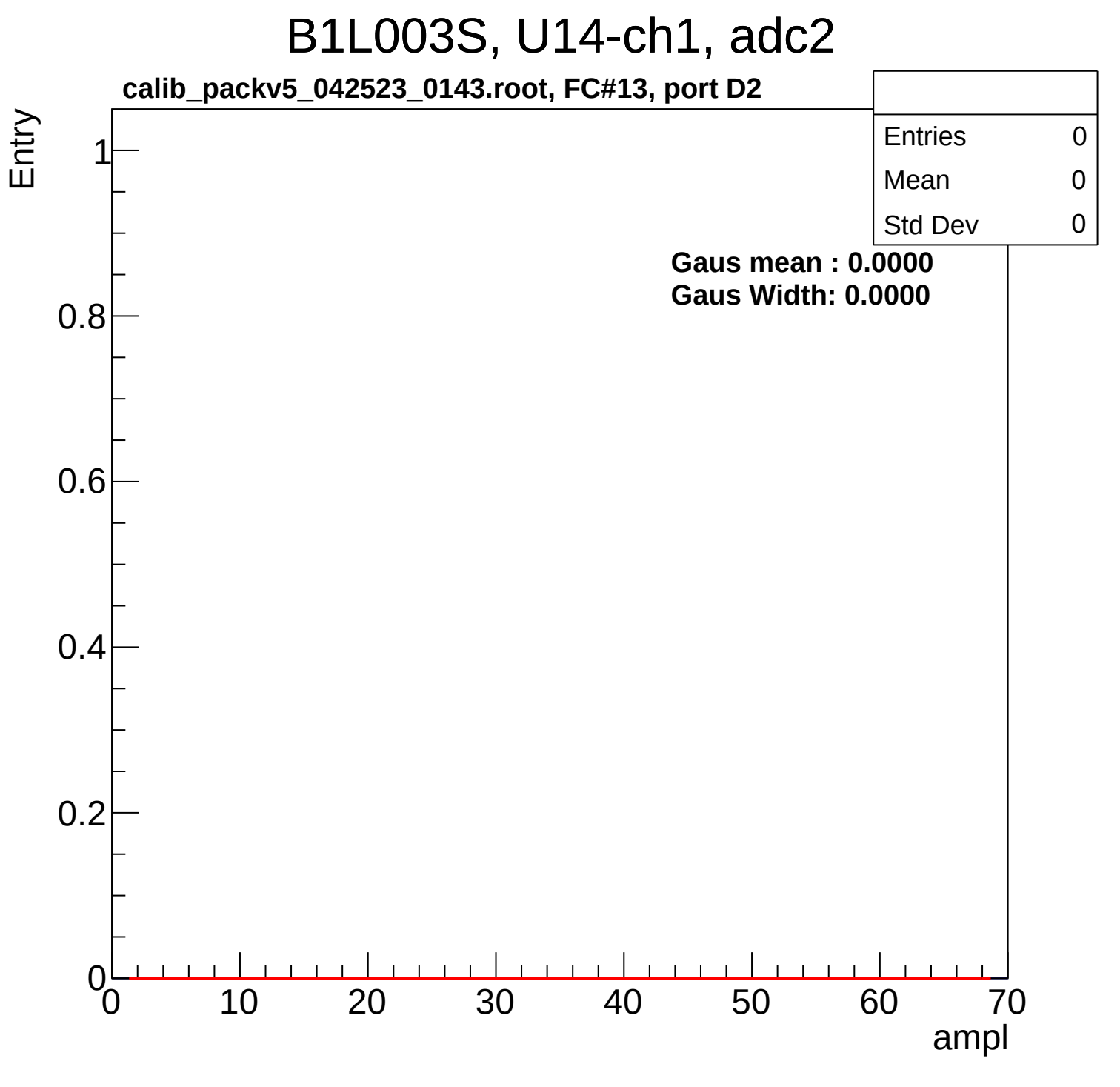

B1L003S, U14-ch0, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

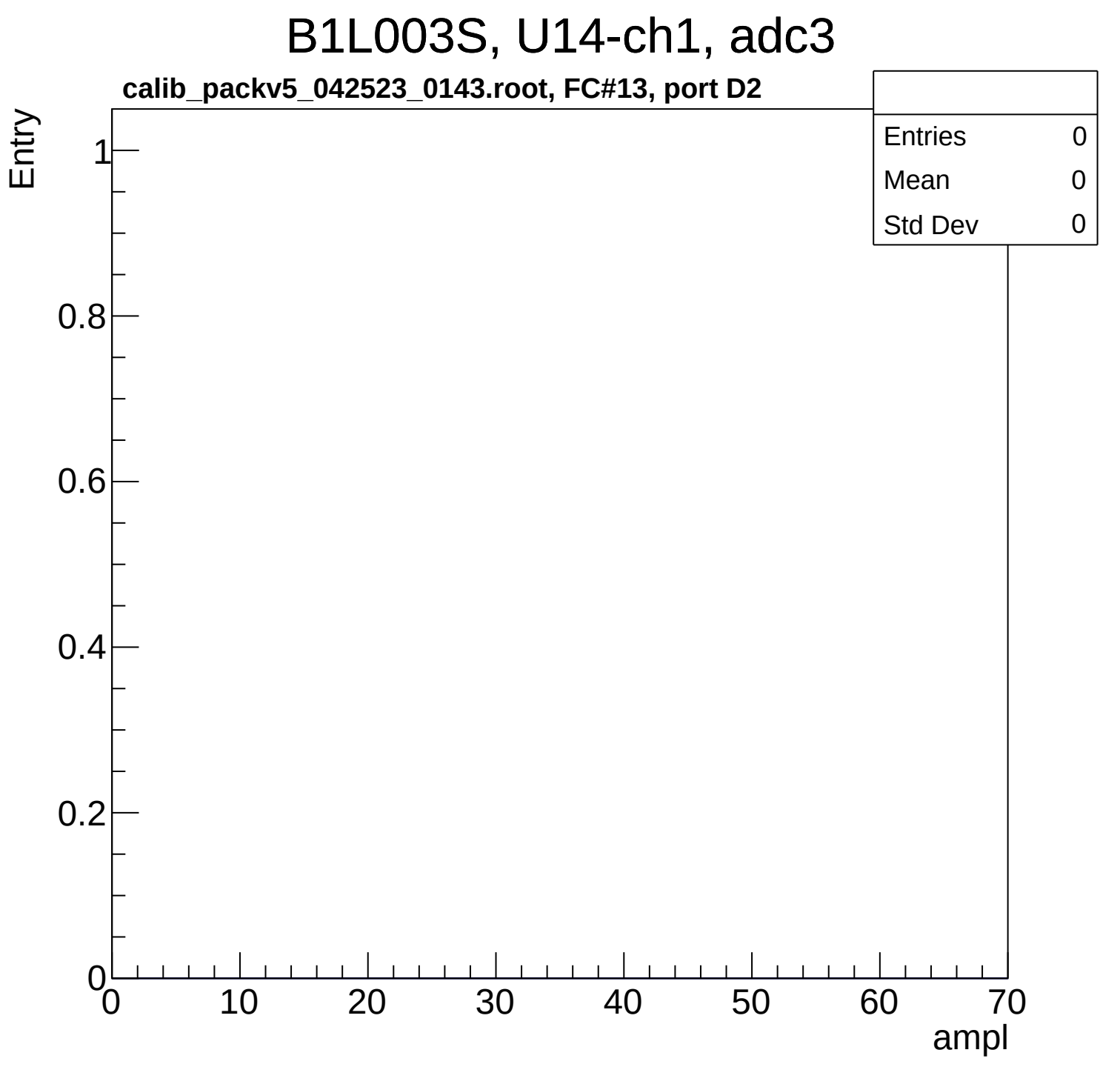

B1L003S, U14-ch0, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

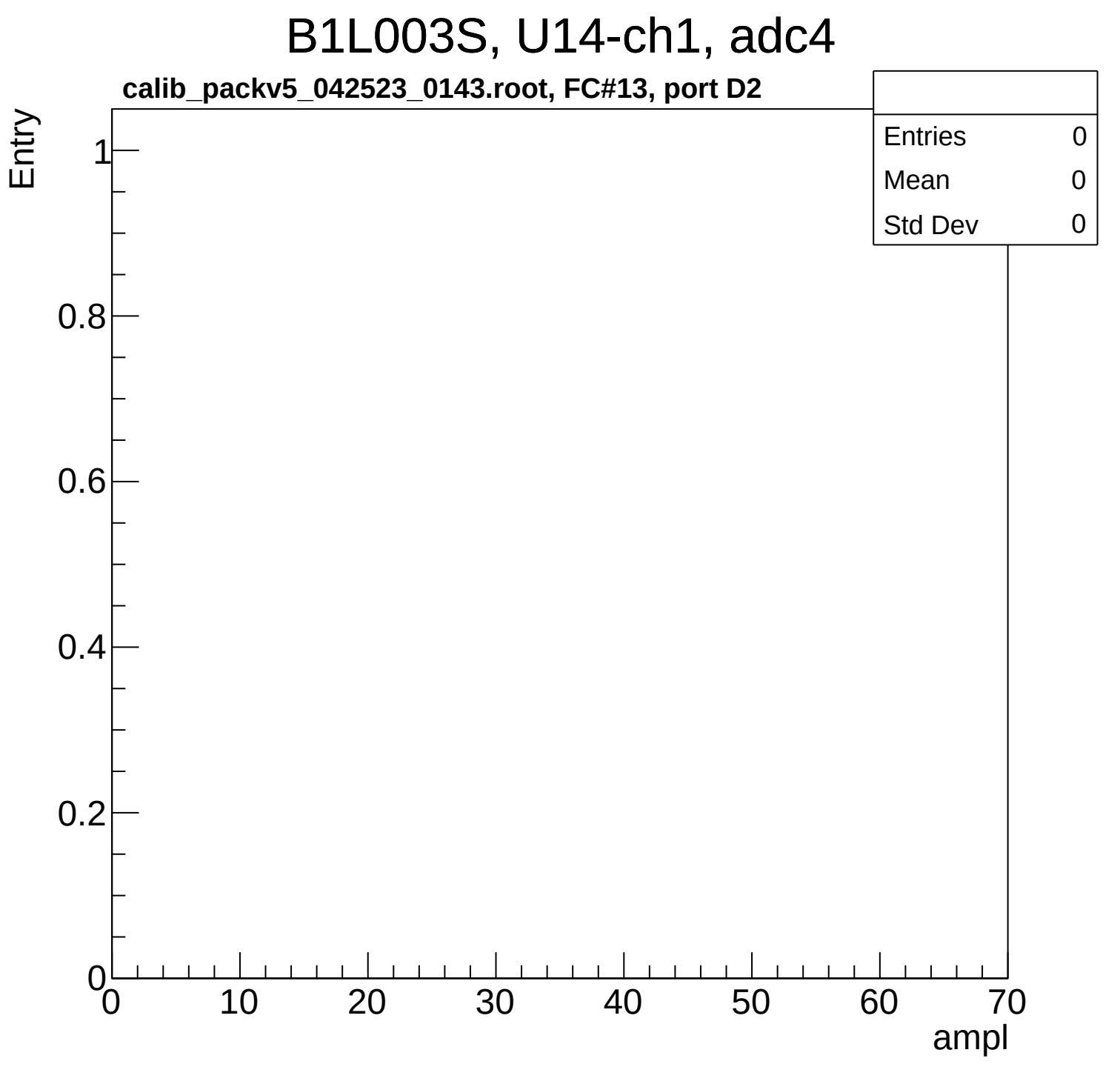

B1L003S, U14-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

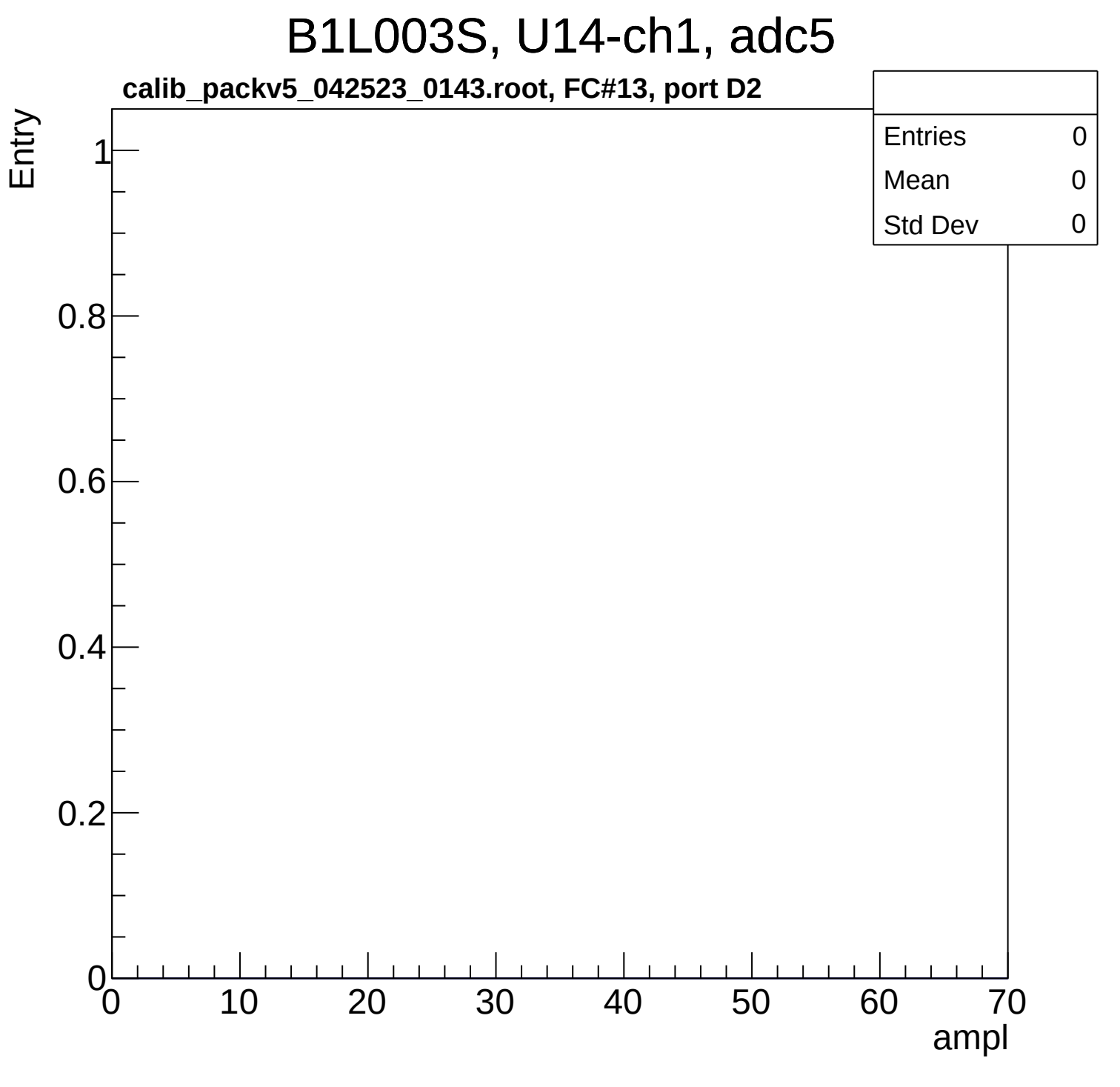

B1L003S, U14-ch1, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

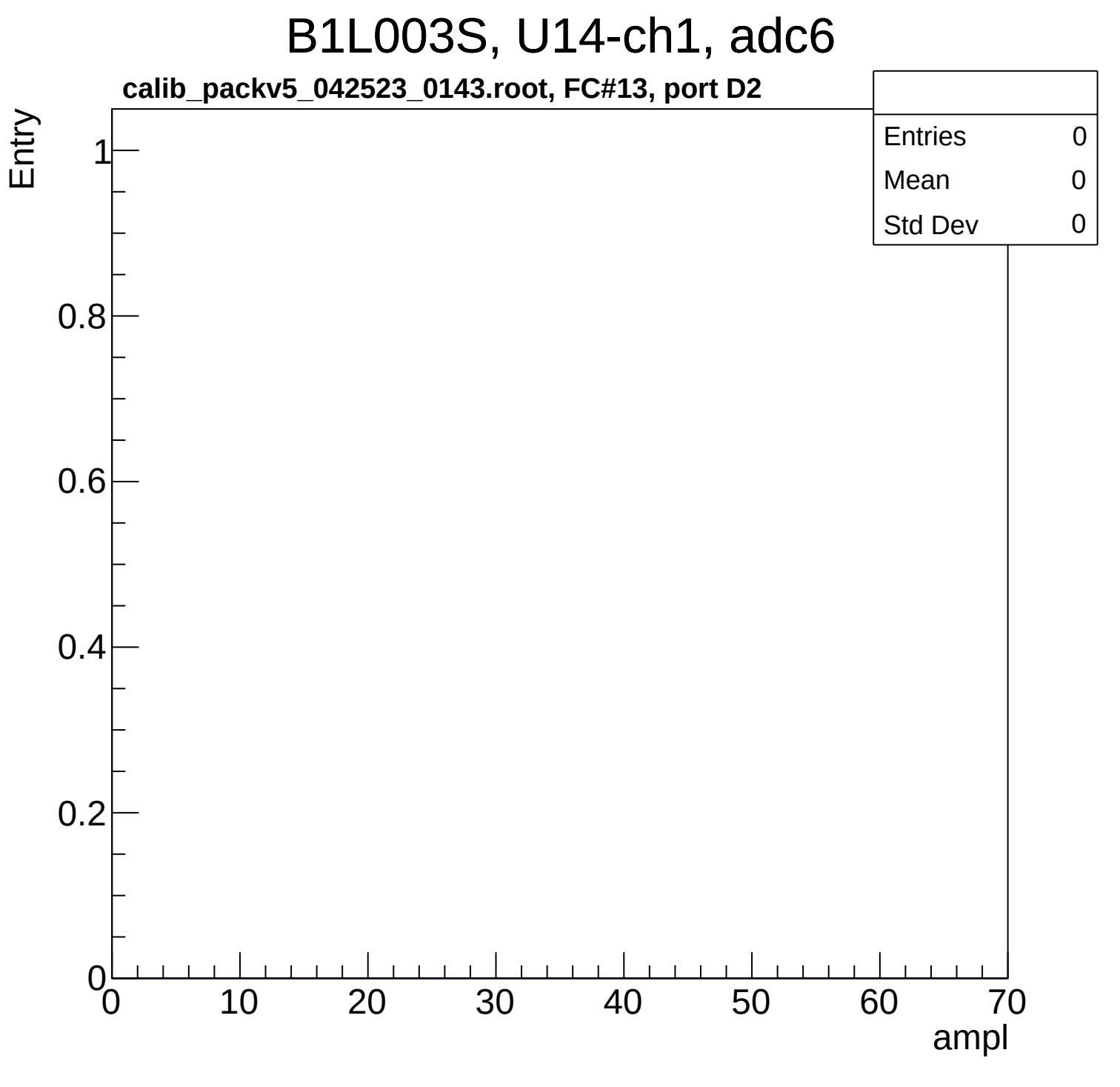

B1L003S, U14-ch1, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

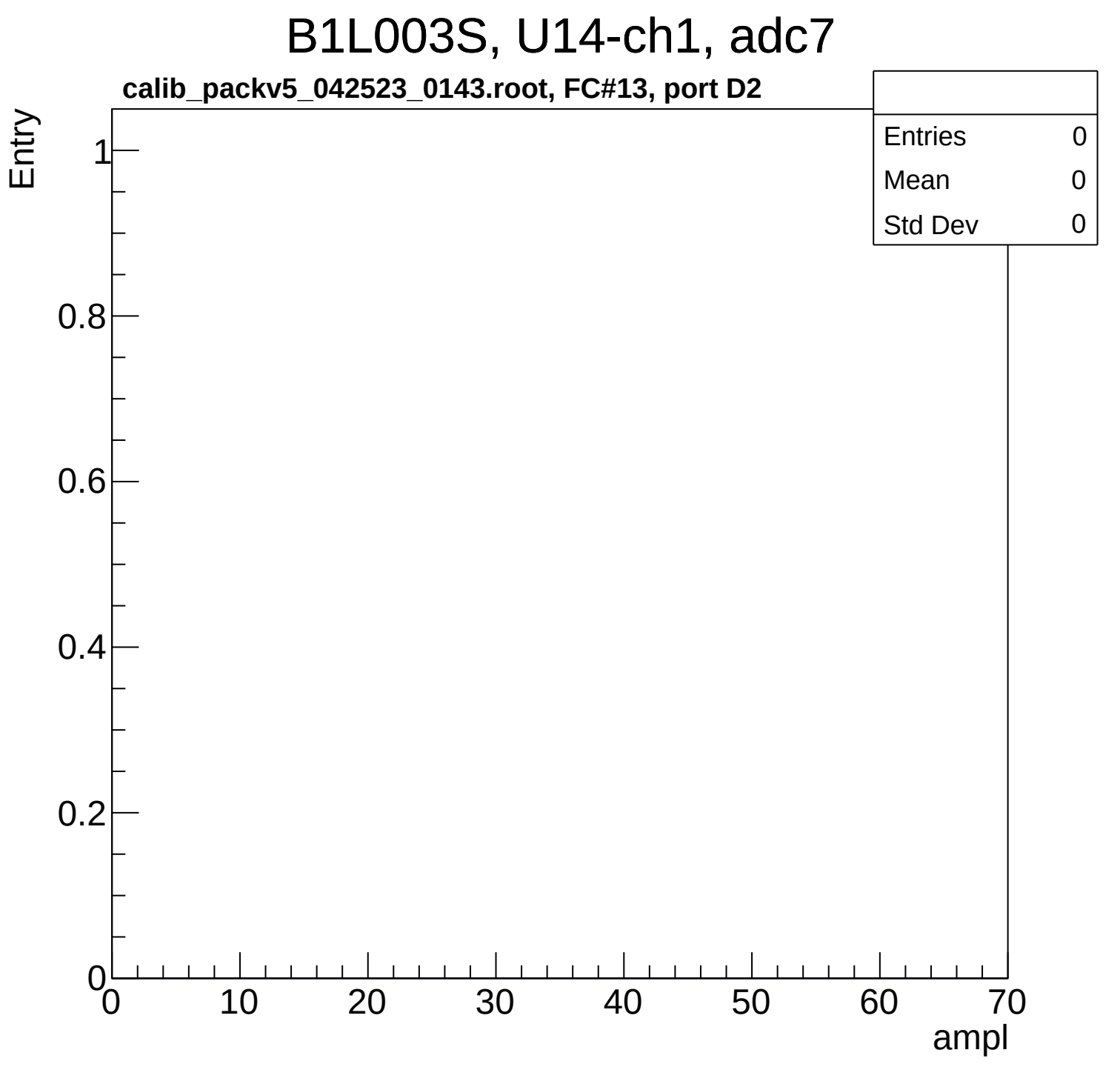

B1L003S, U14-ch1, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

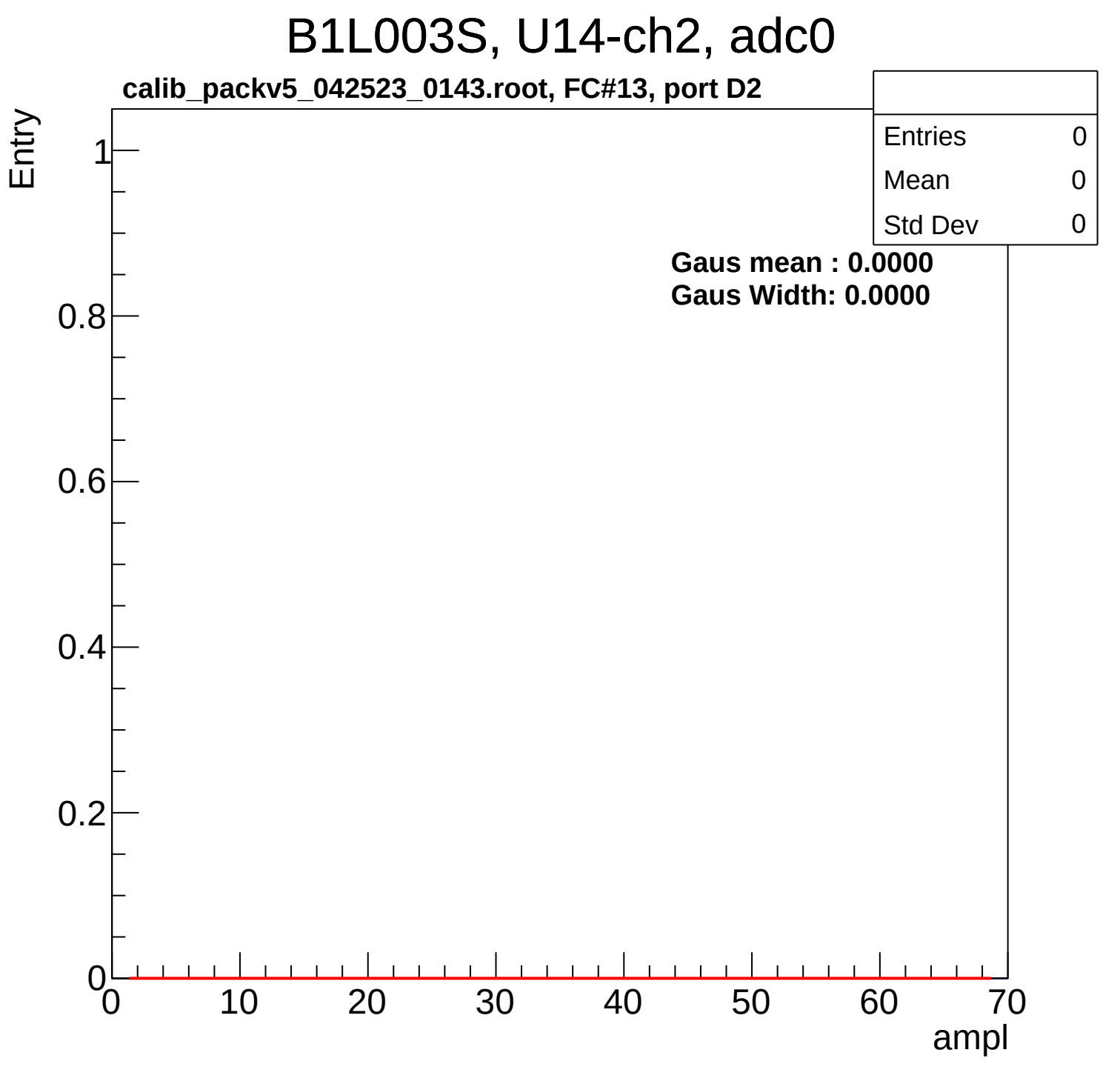

B1L003S, U14-ch1, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

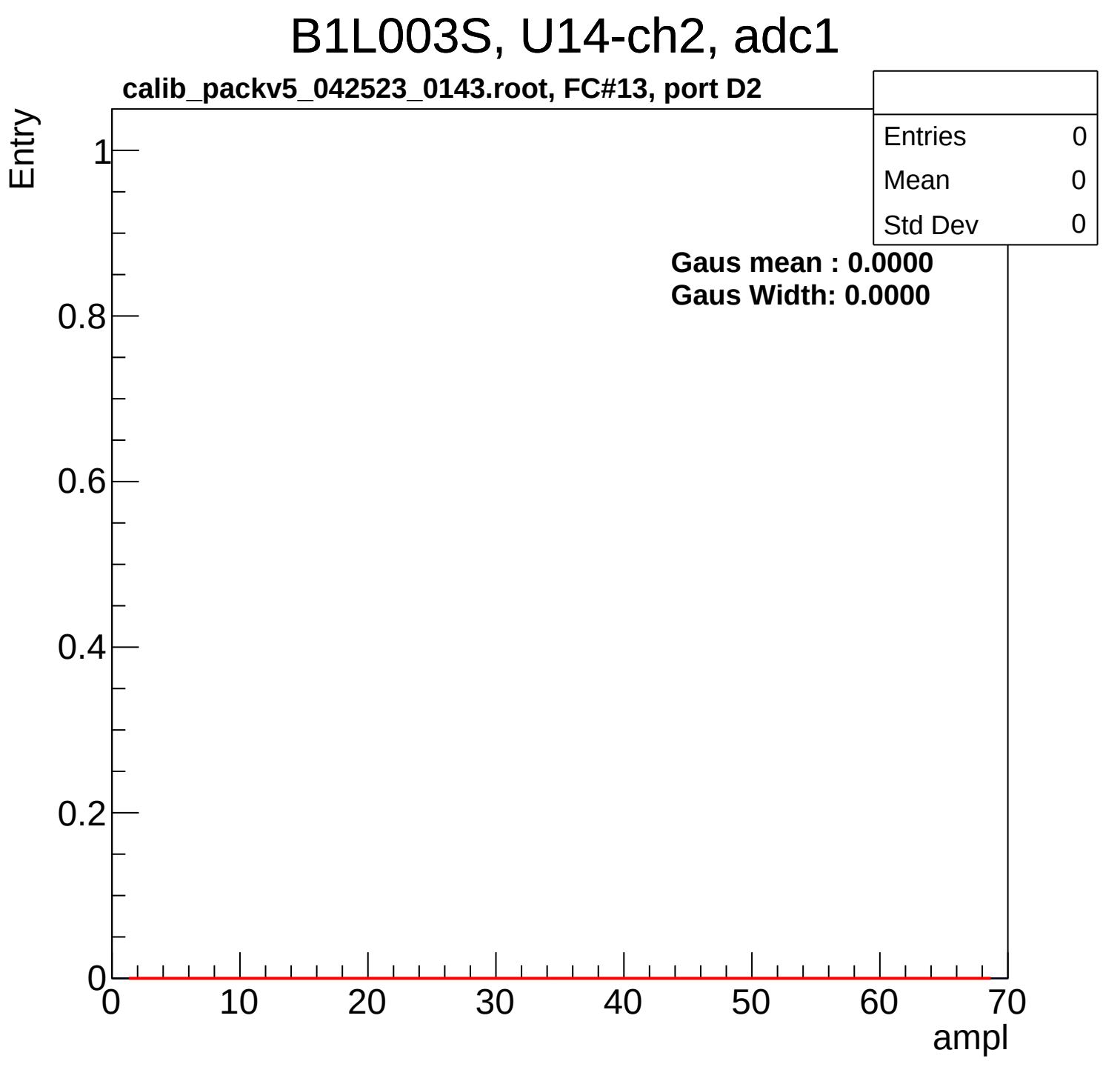

B1L003S, U14-ch1, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

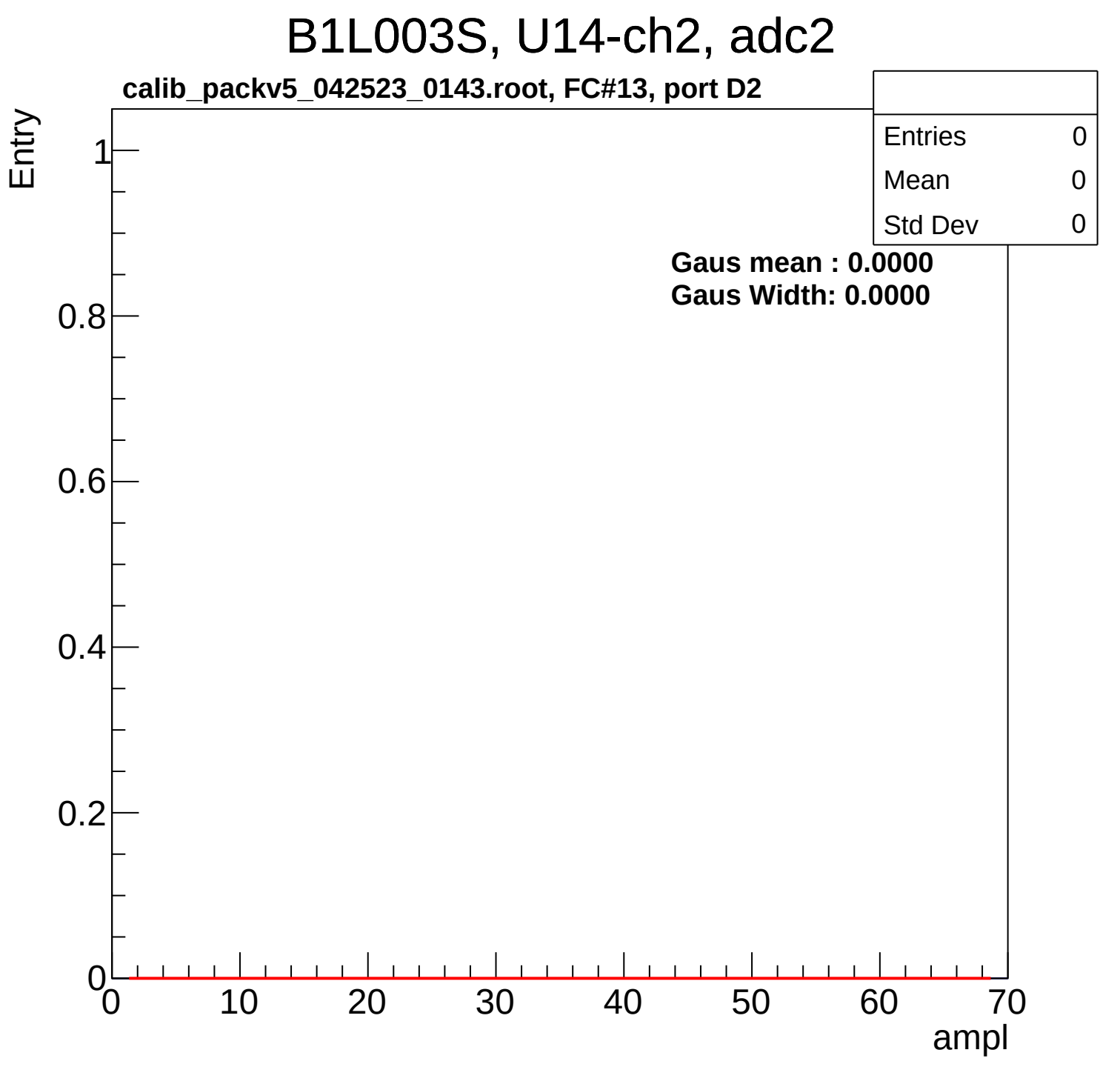

B1L003S, U14-ch1, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

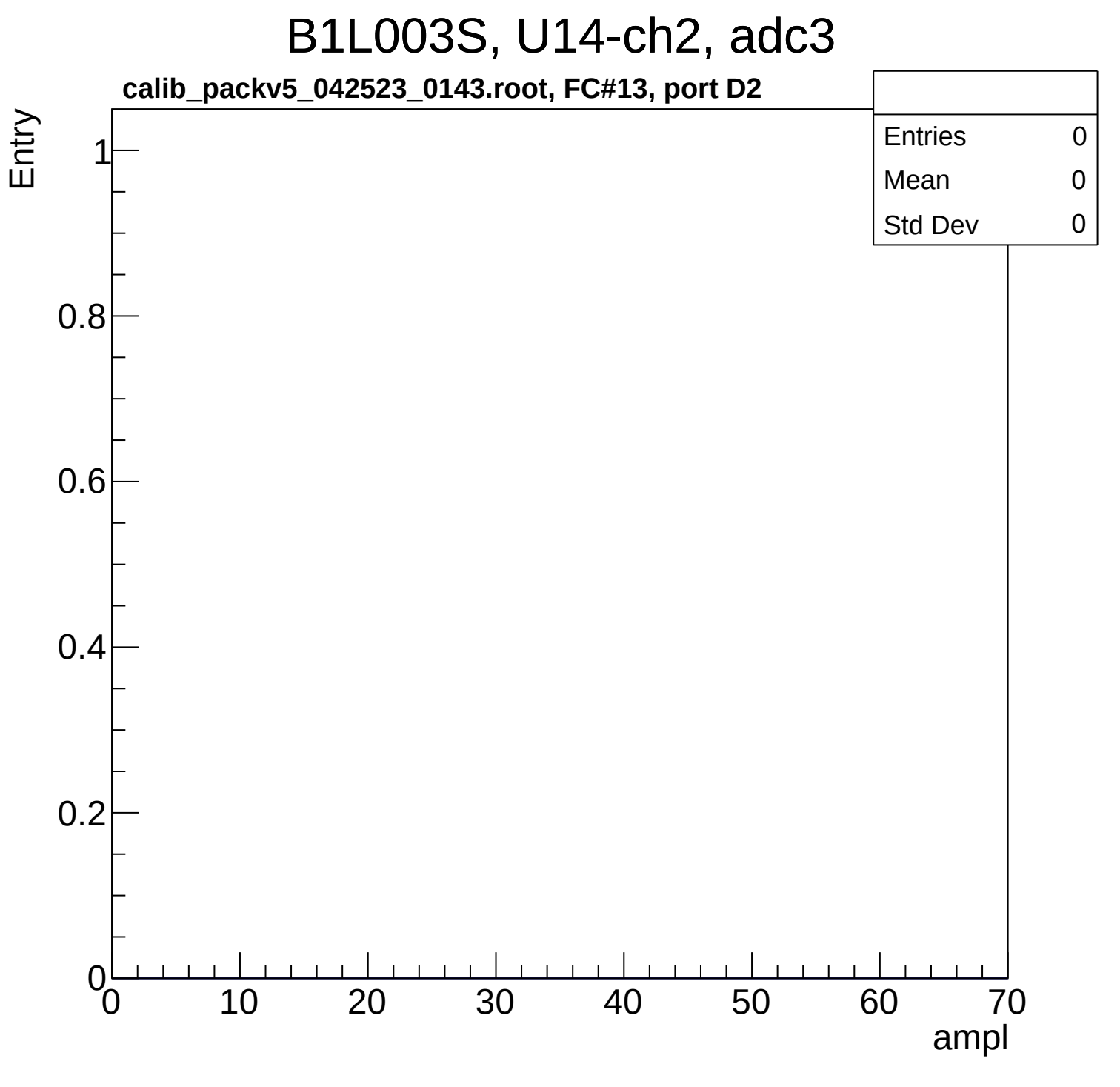

B1L003S, U14-ch1, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

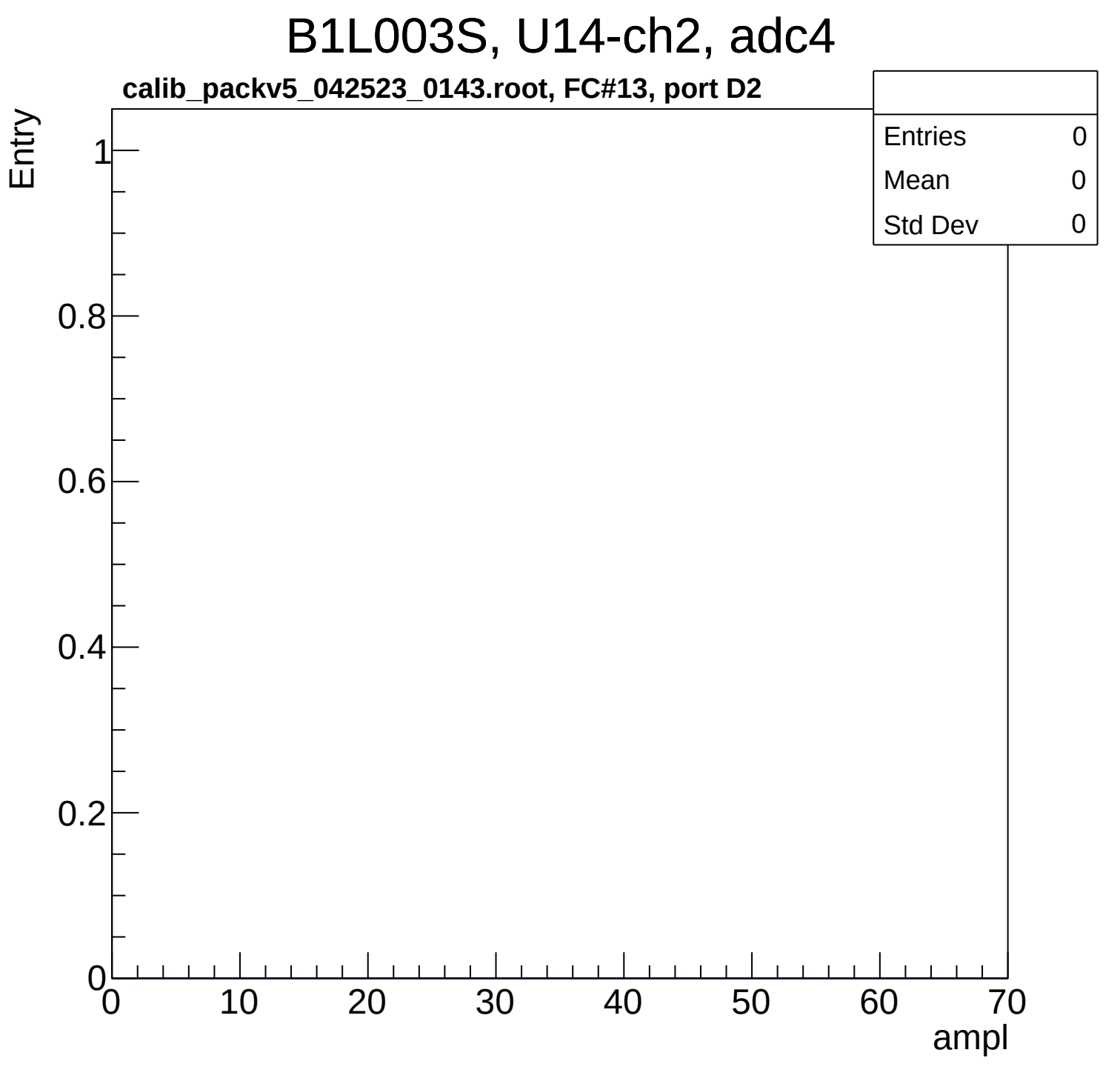

B1L003S, U14-ch1, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

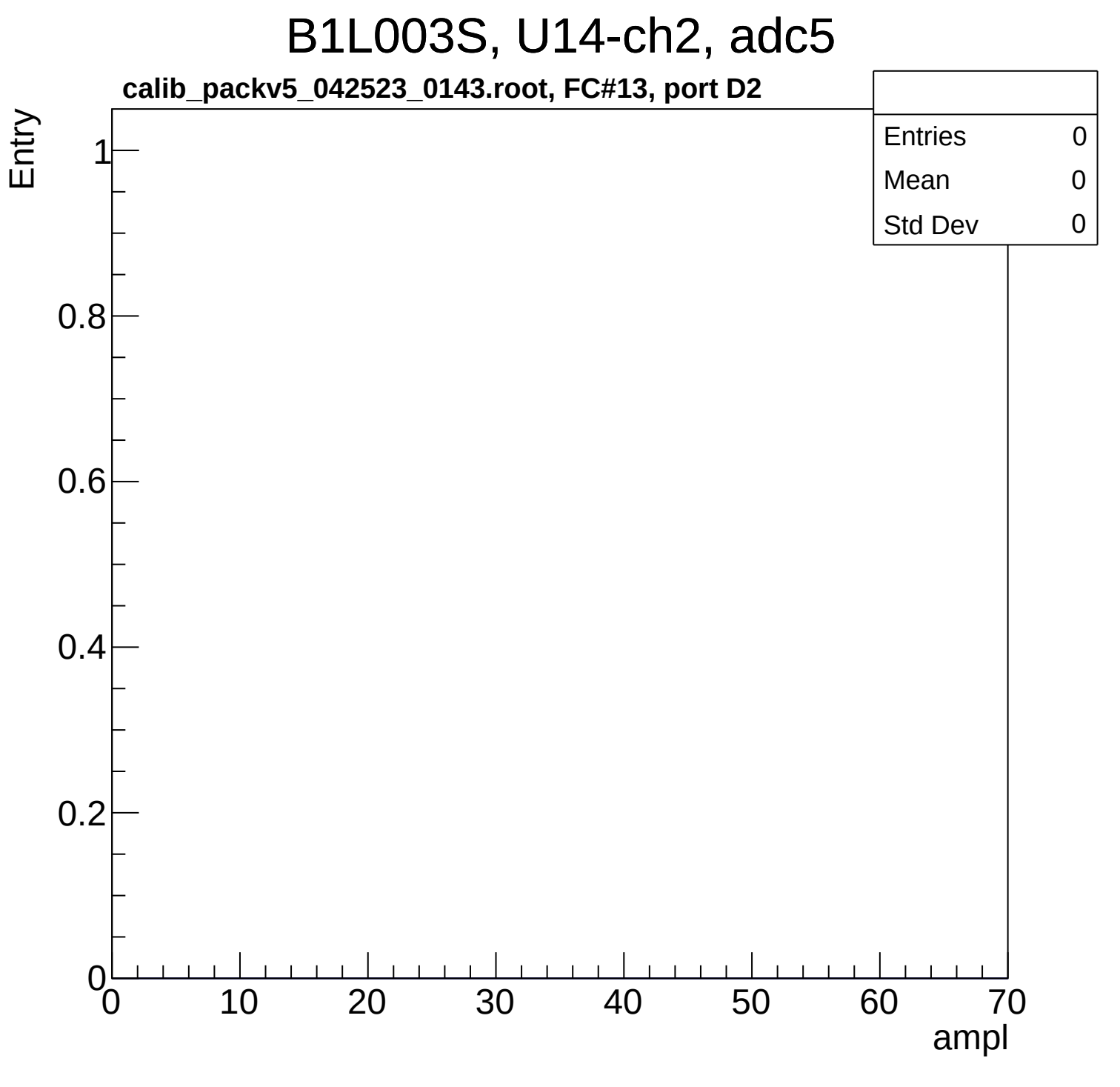

B1L003S, U14-ch2, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

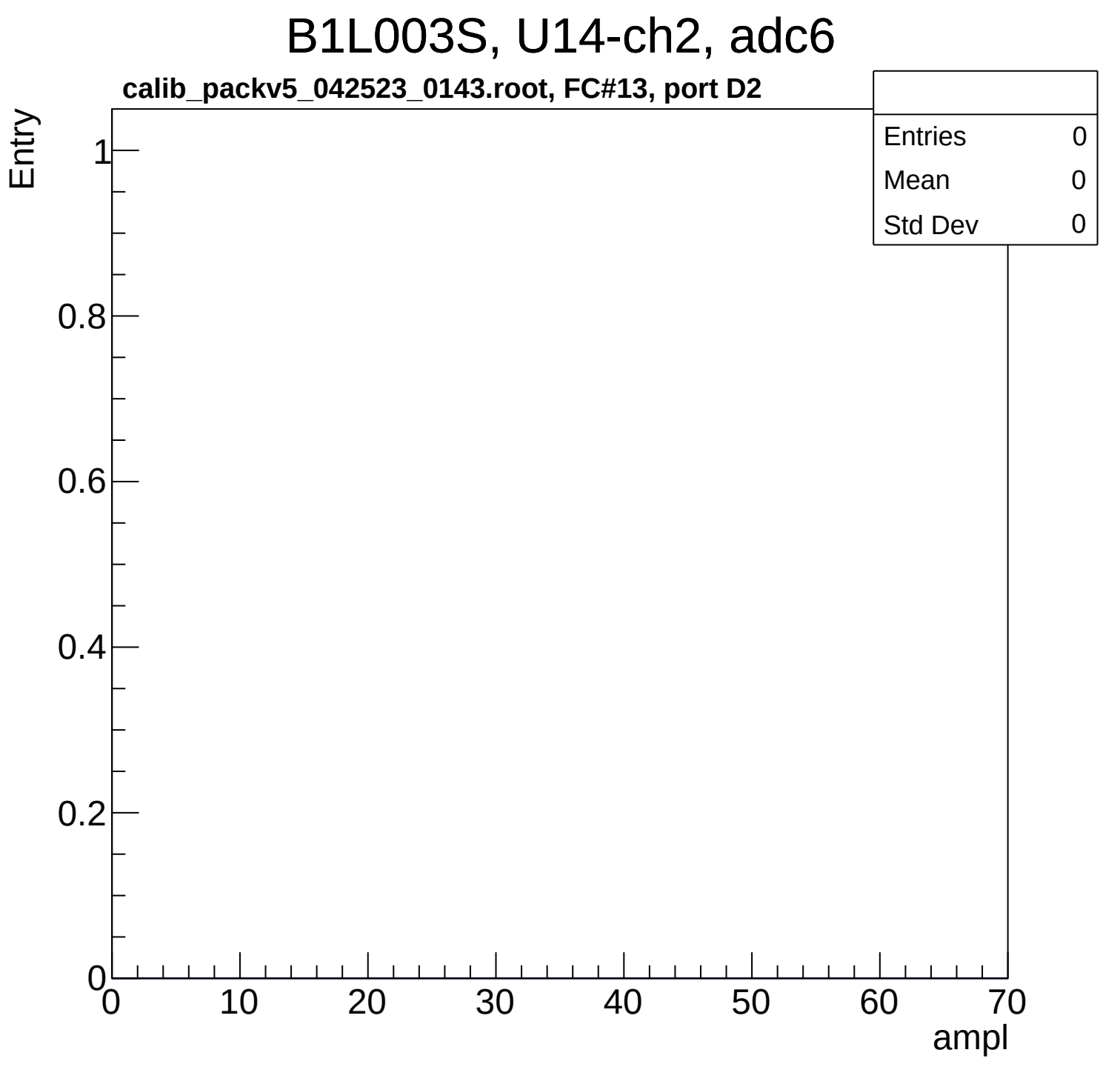

B1L003S, U14-ch2, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

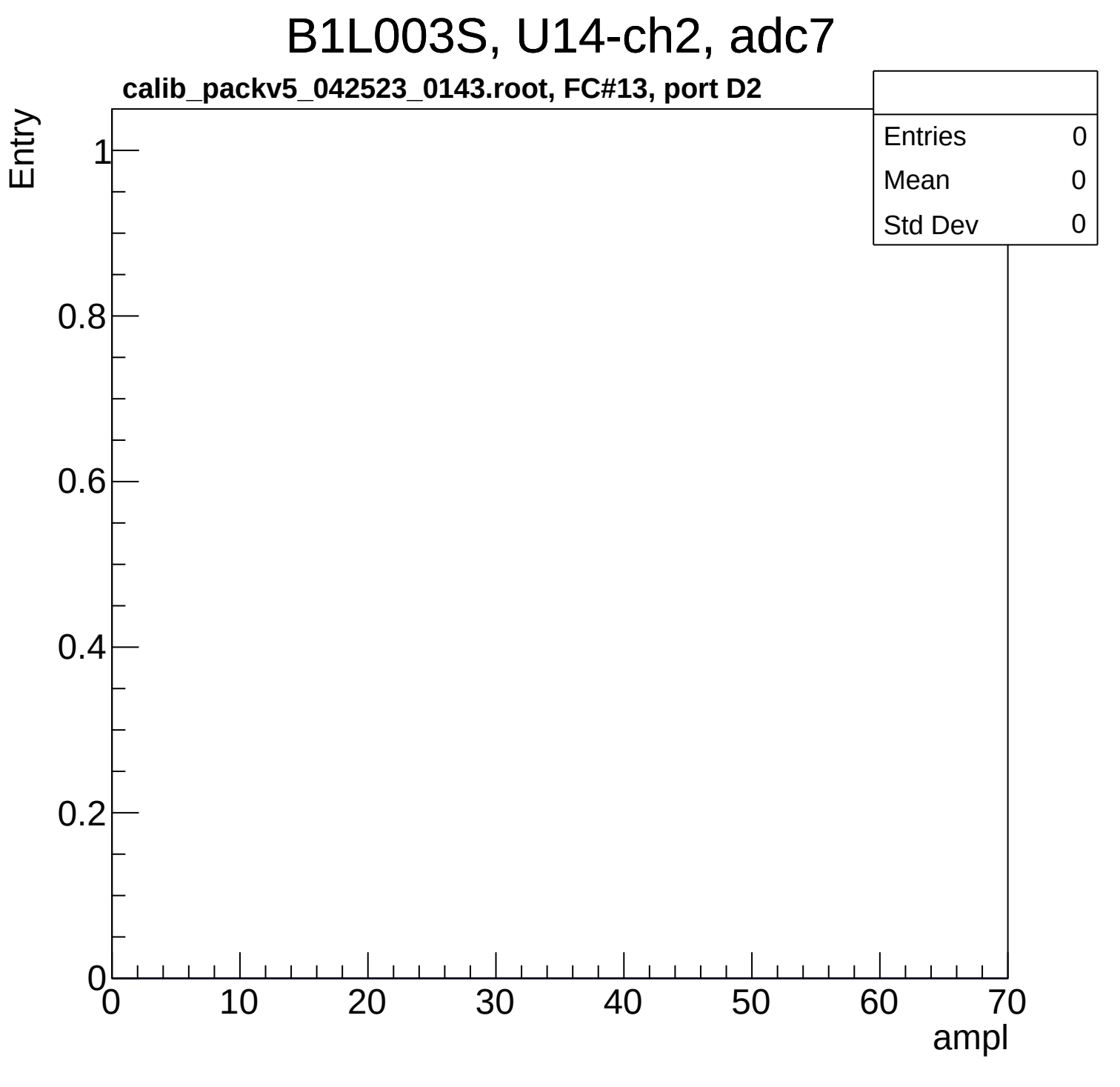

B1L003S, U14-ch2, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

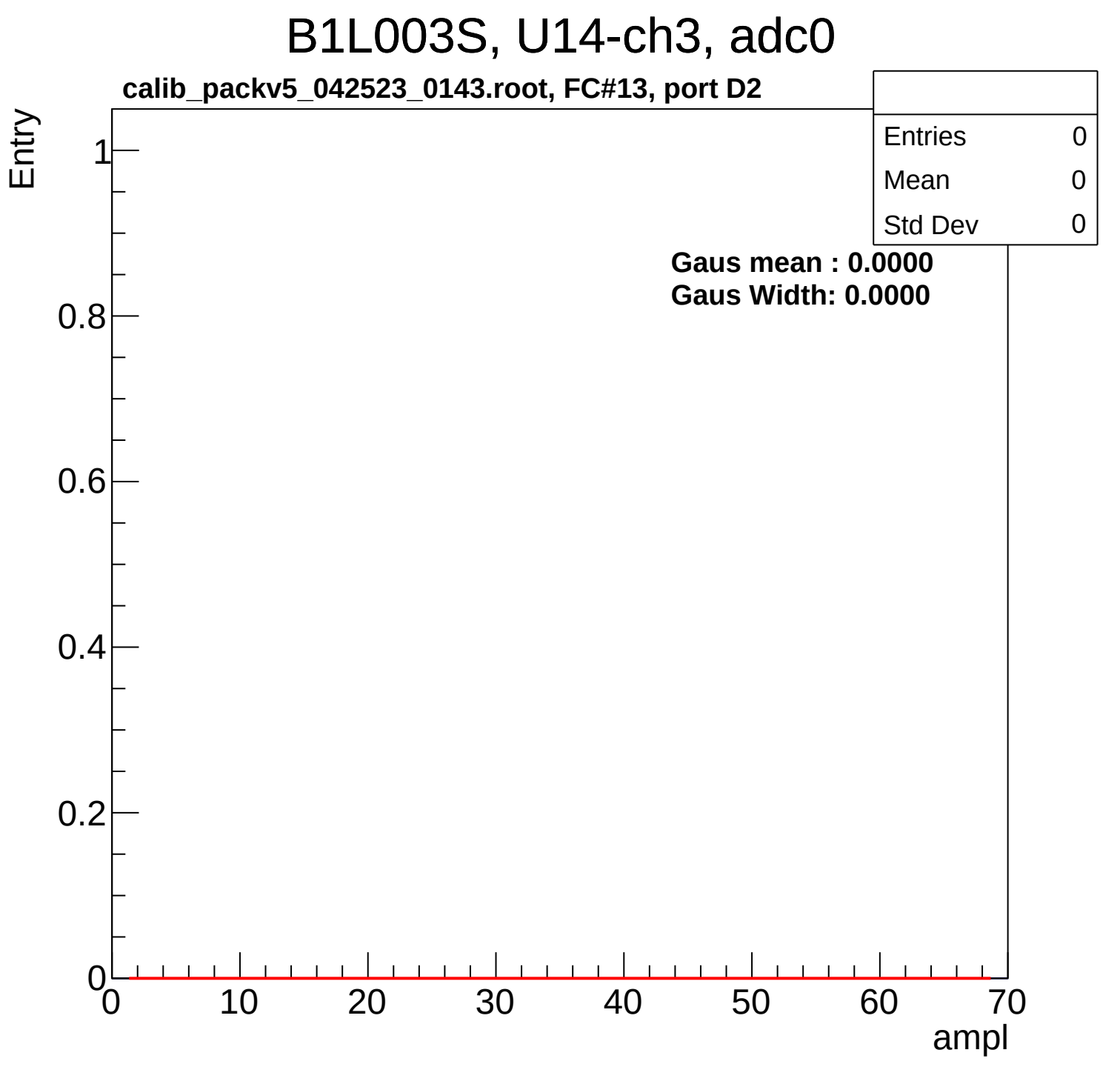

B1L003S, U14-ch2, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

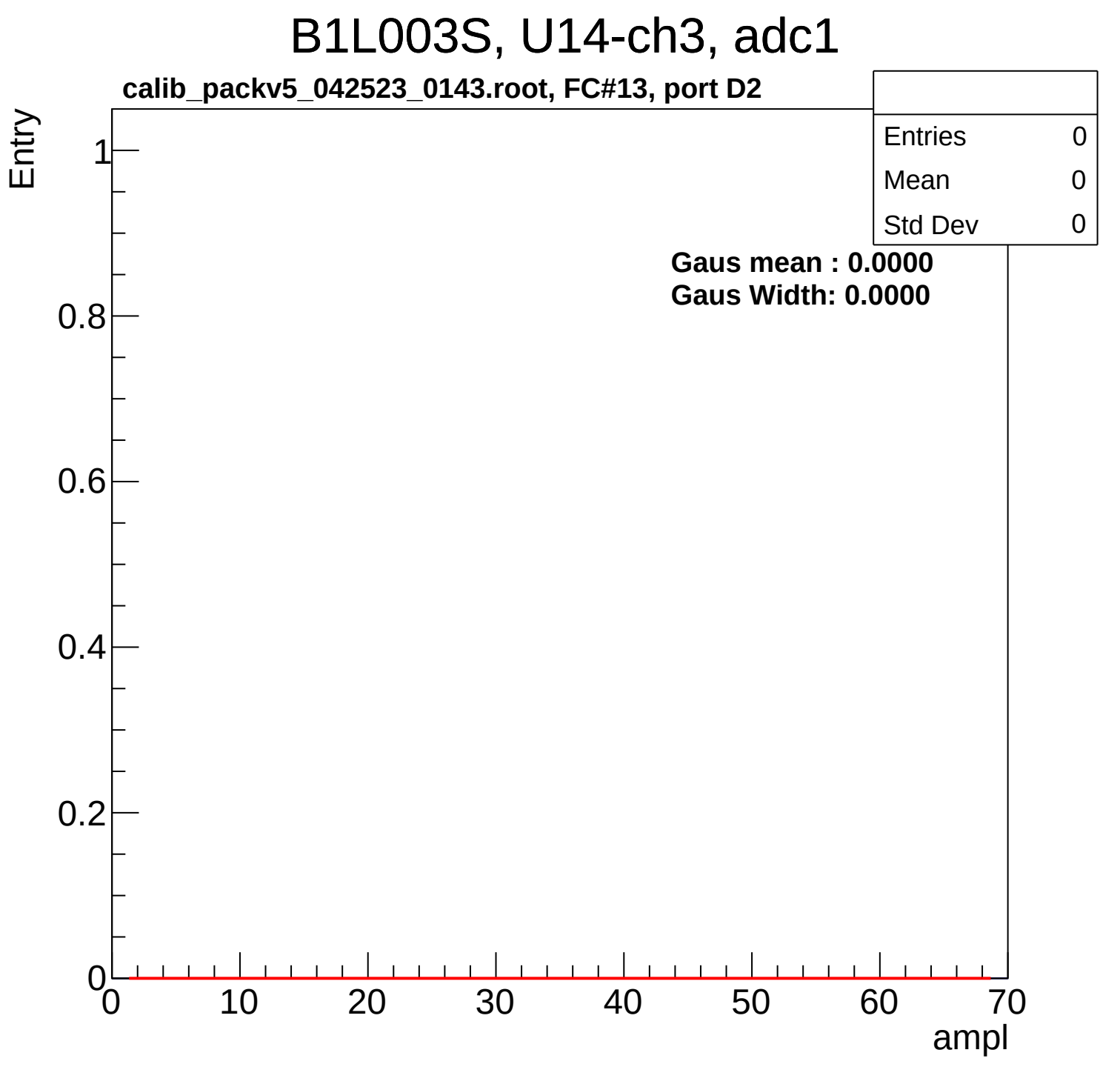

B1L003S, U14-ch2, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

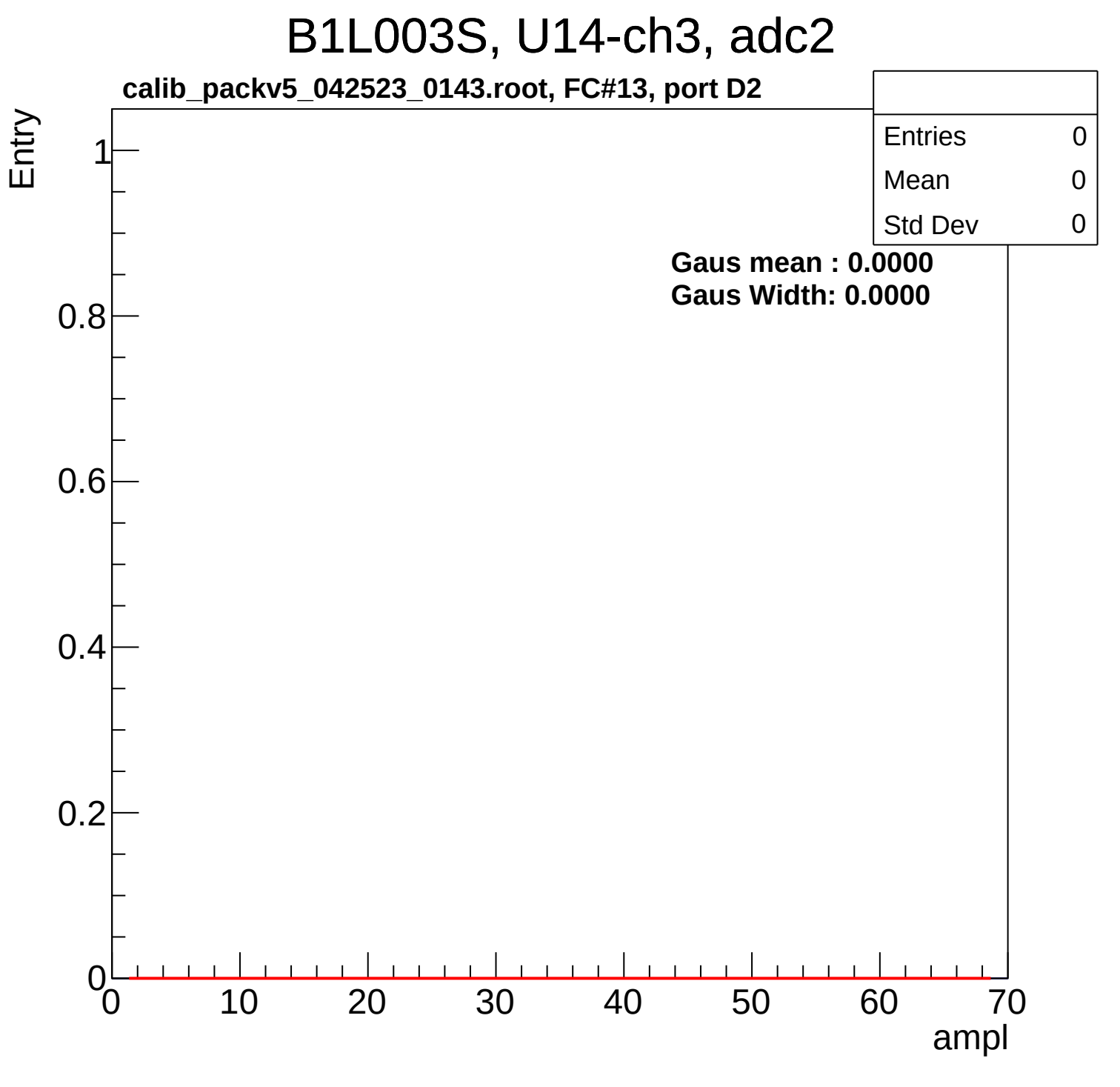

B1L003S, U14-ch2, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

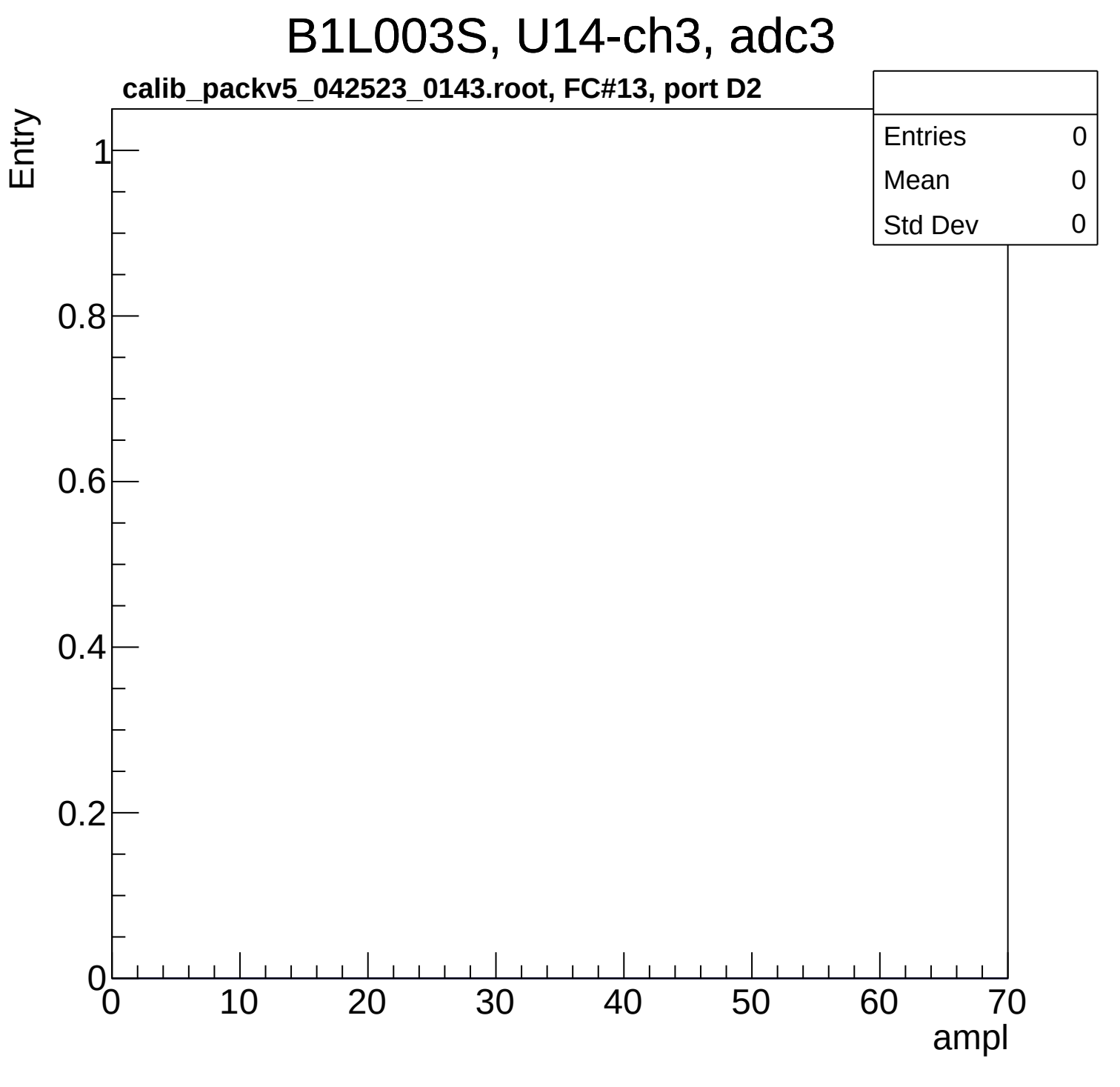

B1L003S, U14-ch2, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

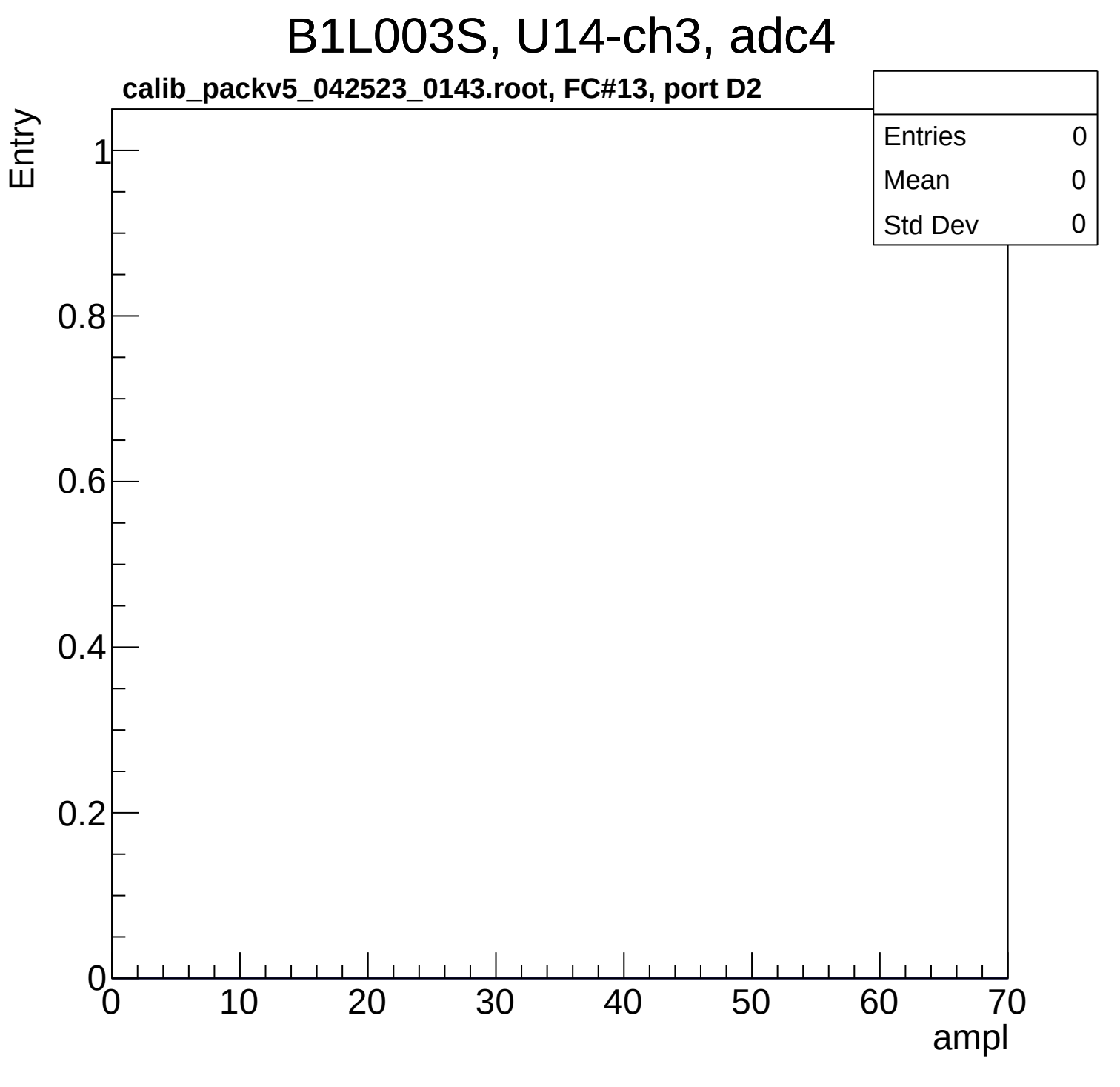

B1L003S, U14-ch2, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

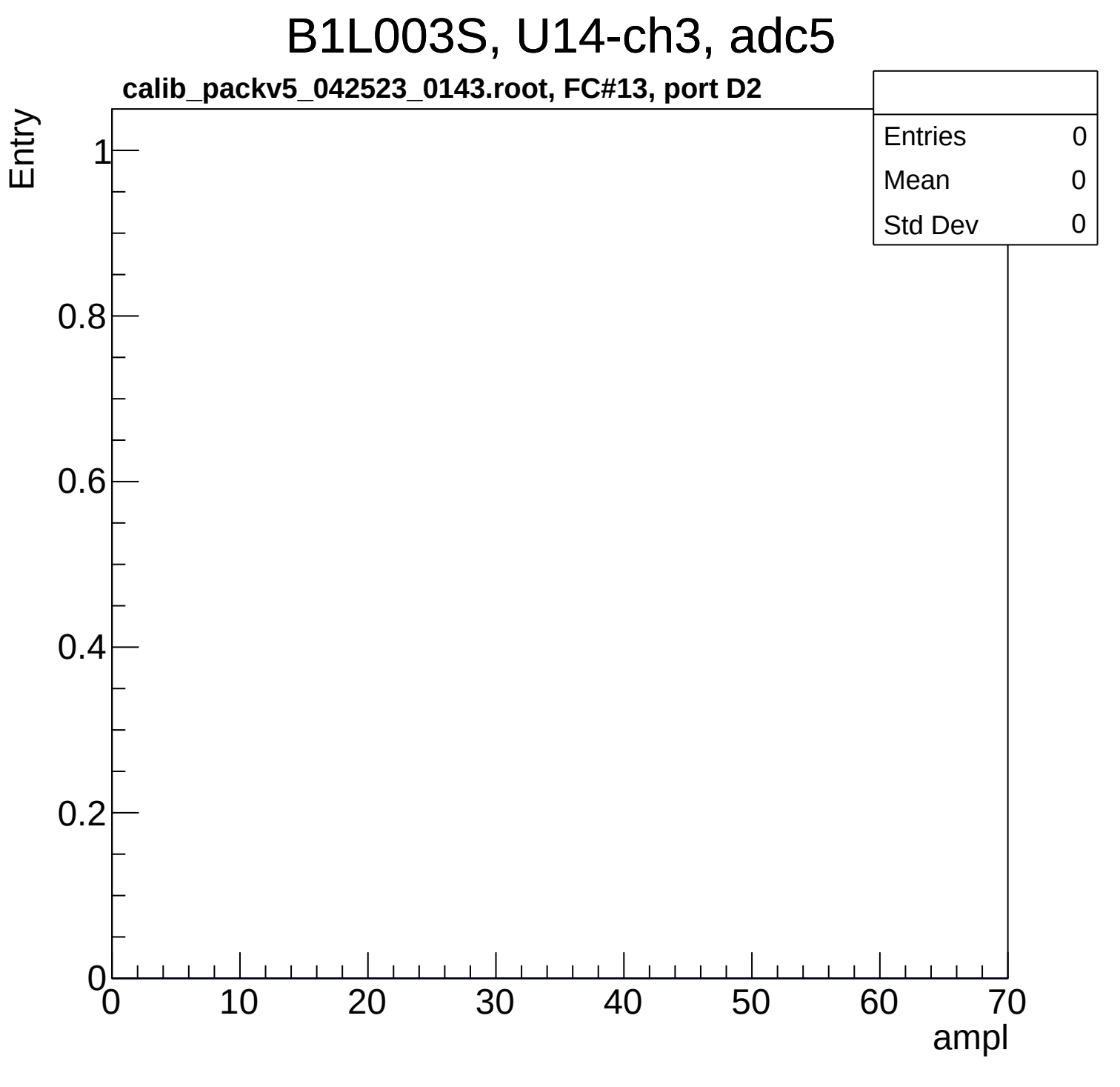

B1L003S, U14-ch3, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

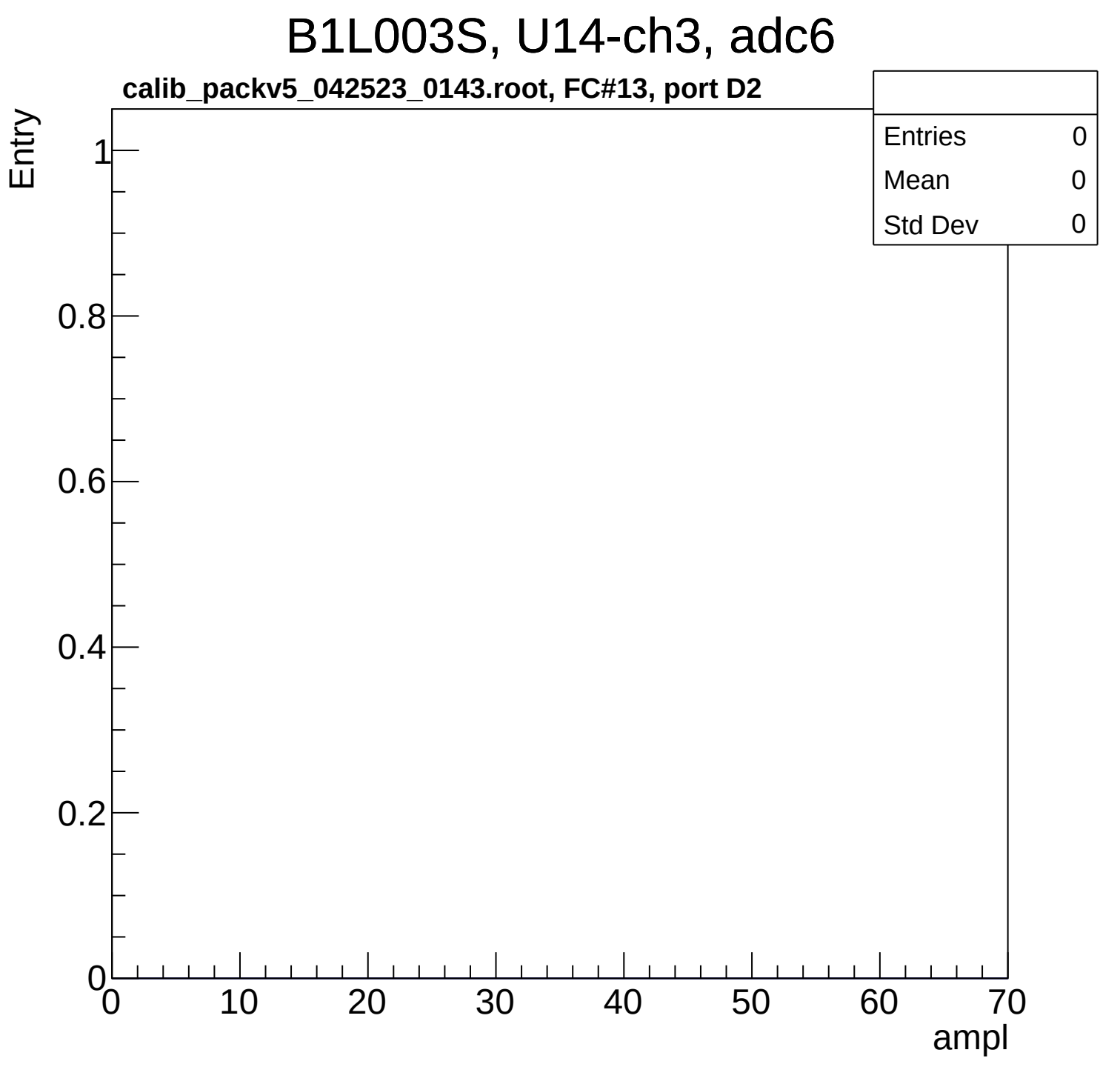

B1L003S, U14-ch3, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

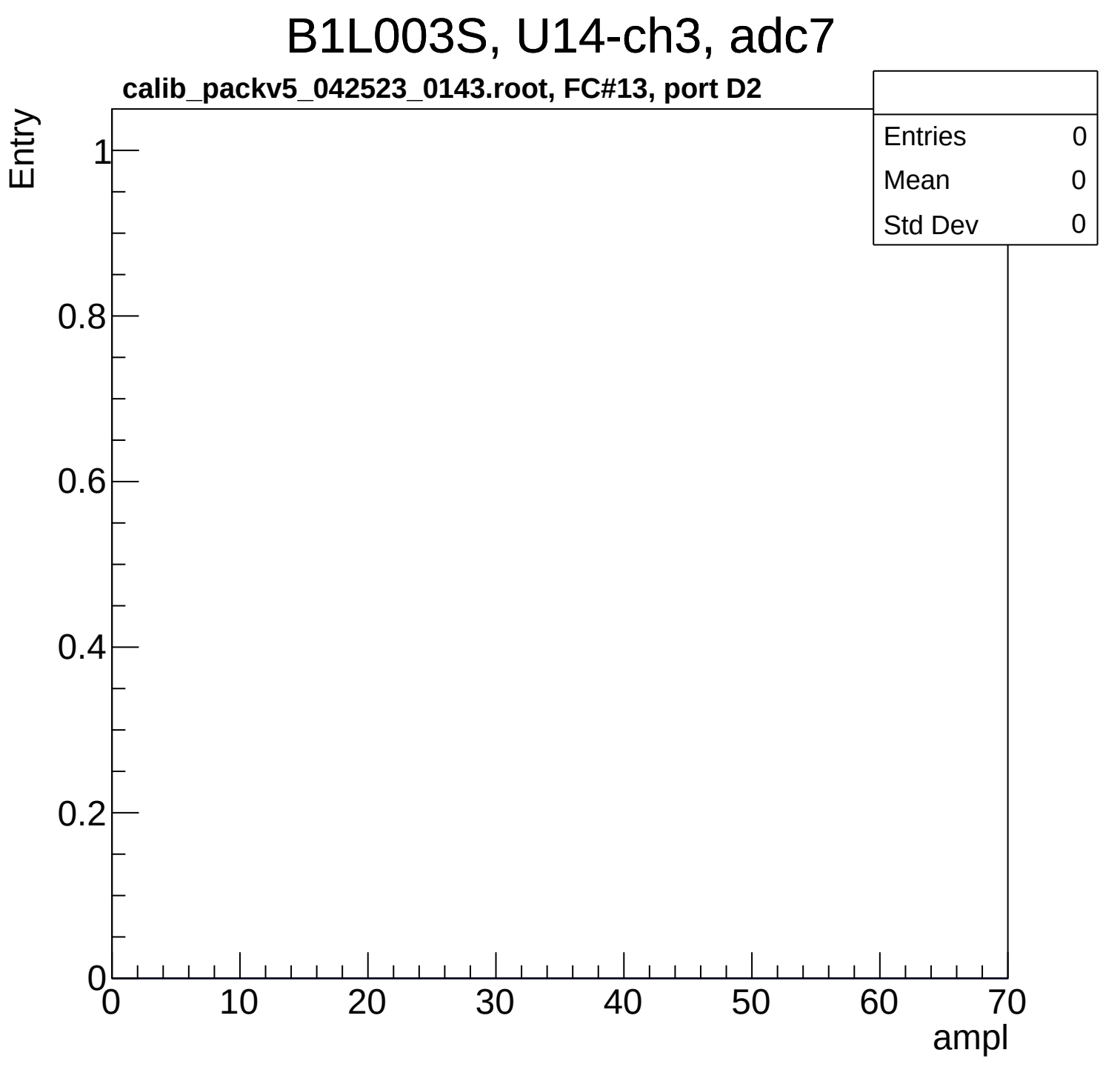

B1L003S, U14-ch3, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

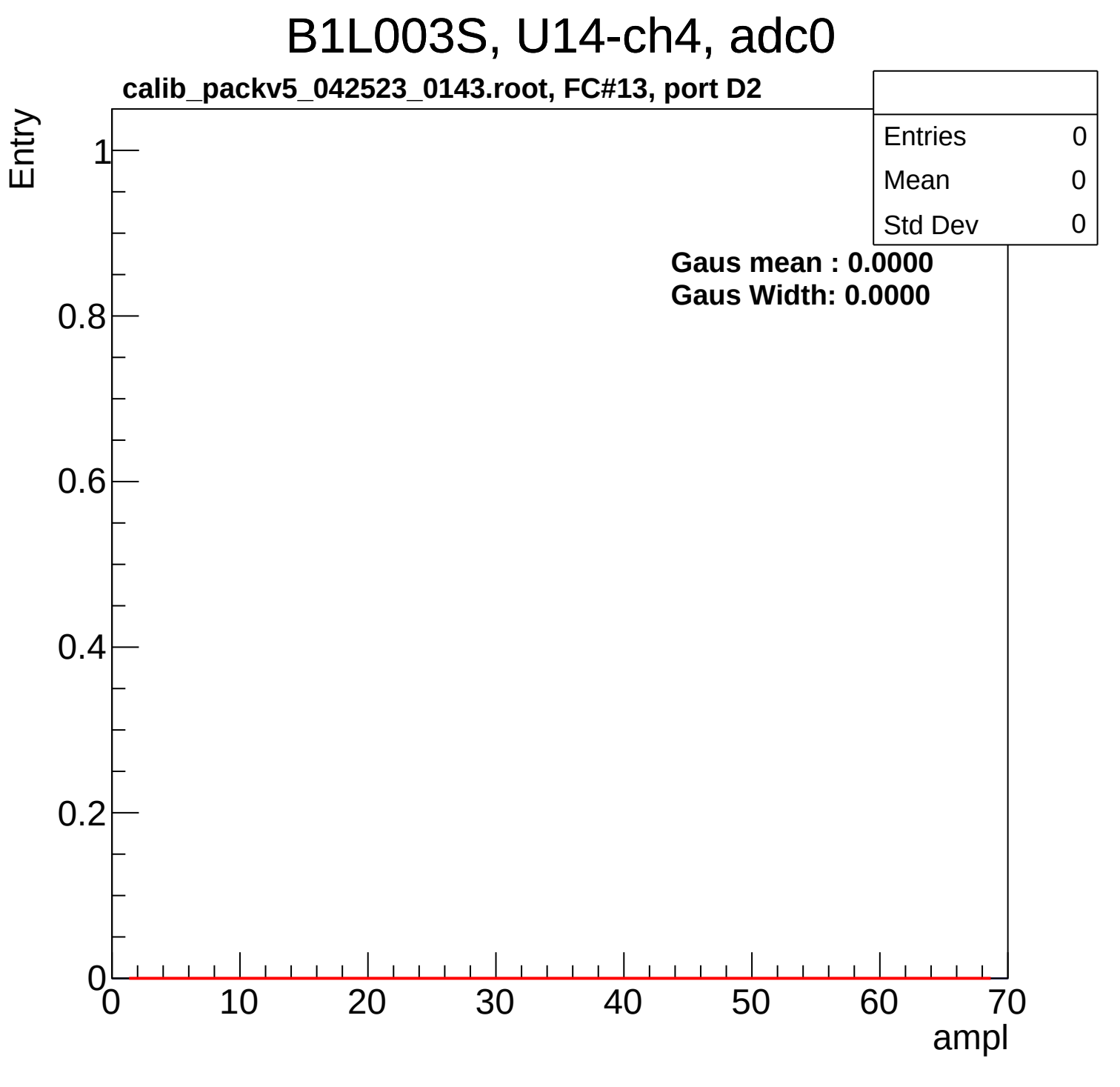

B1L003S, U14-ch3, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

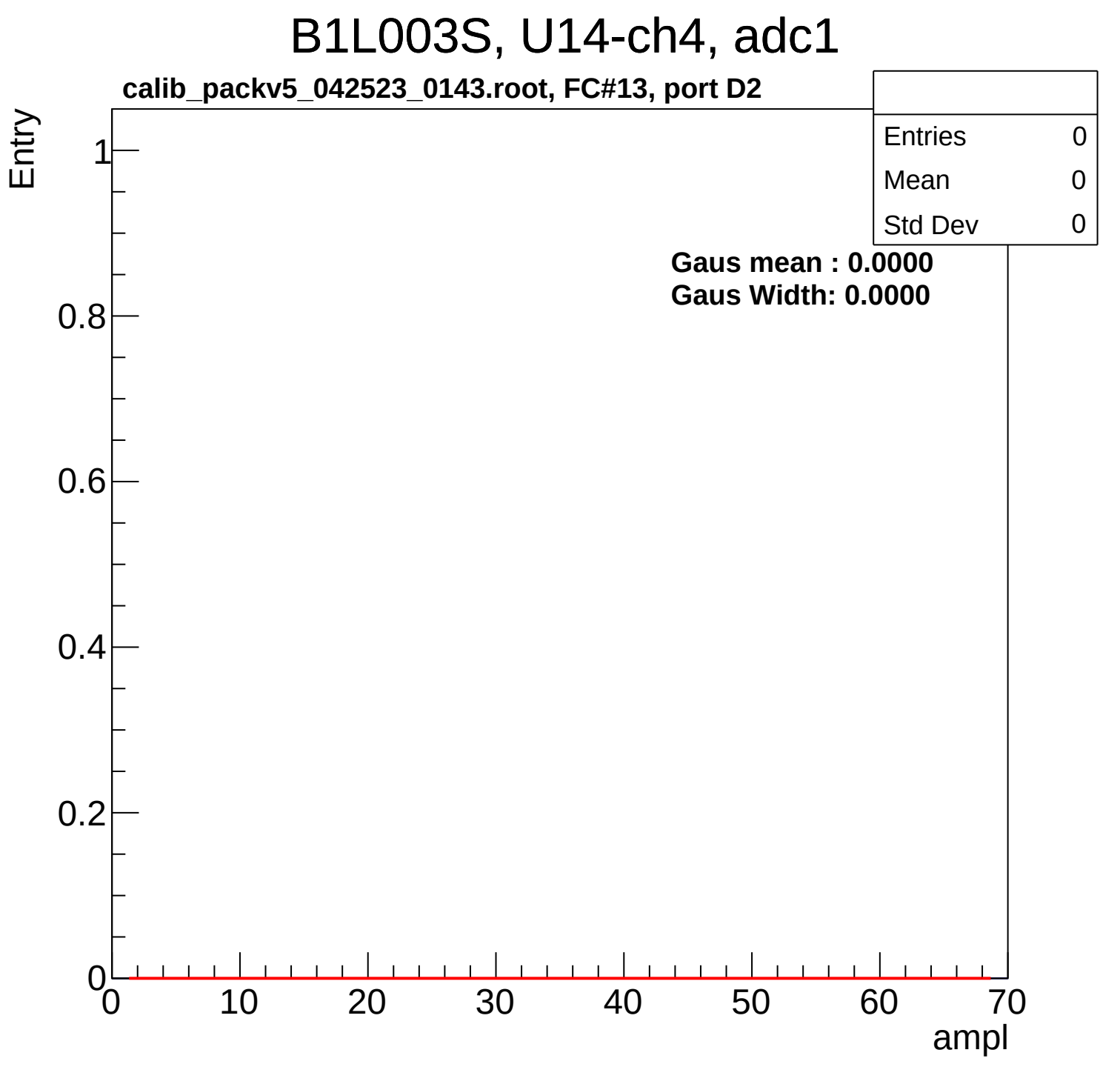

B1L003S, U14-ch3, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

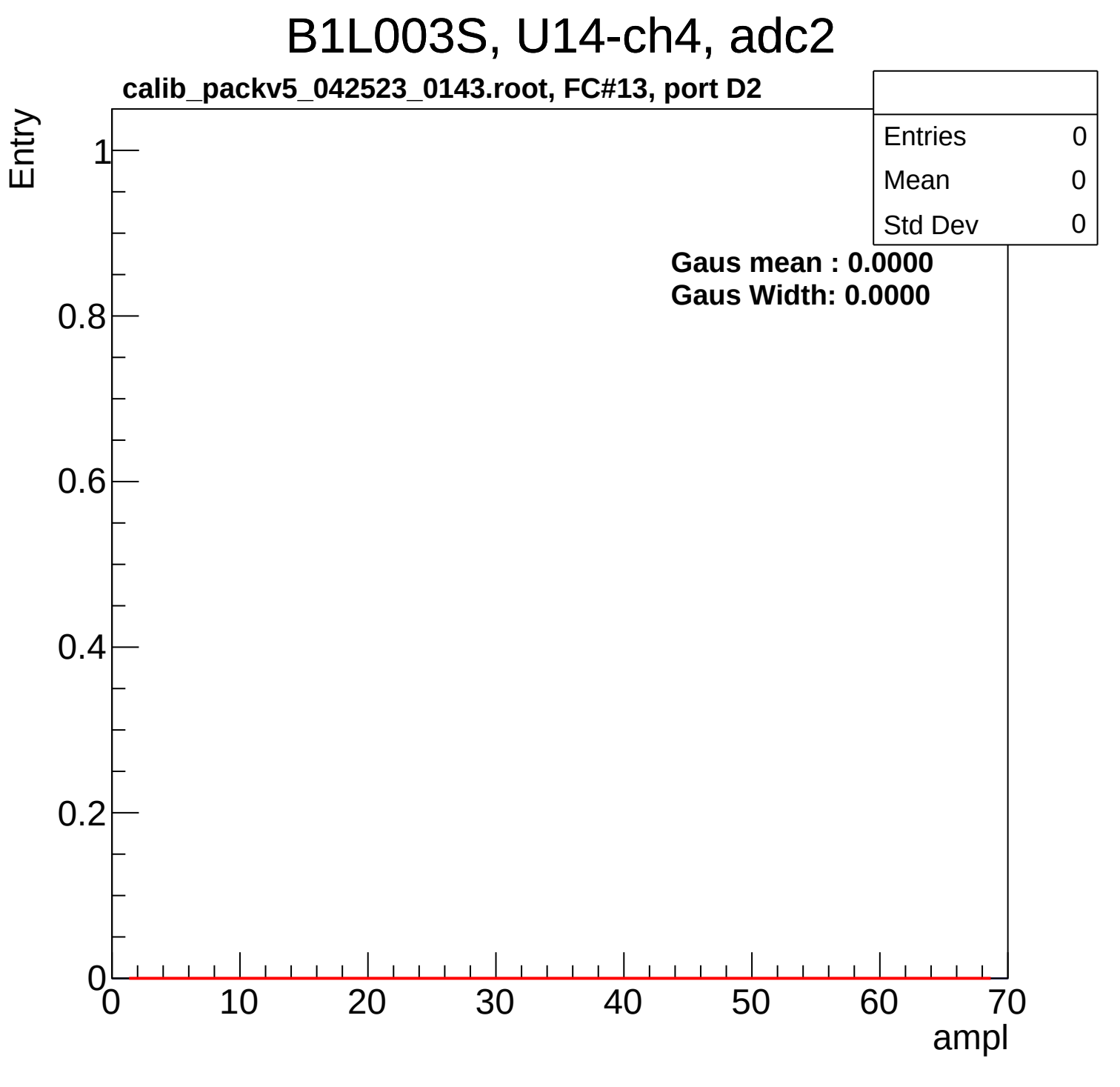

B1L003S, U14-ch3, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

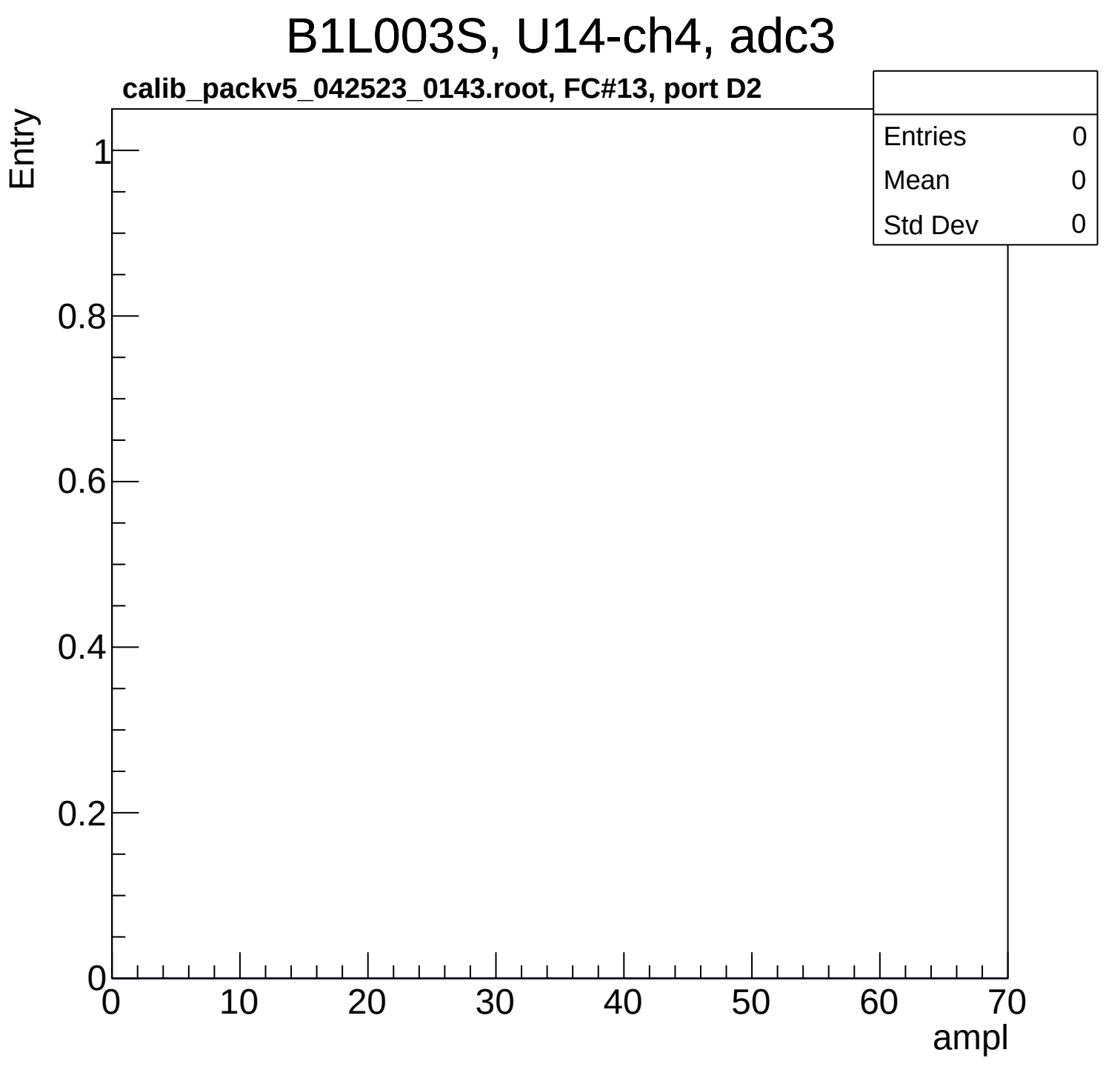

B1L003S, U14-ch3, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

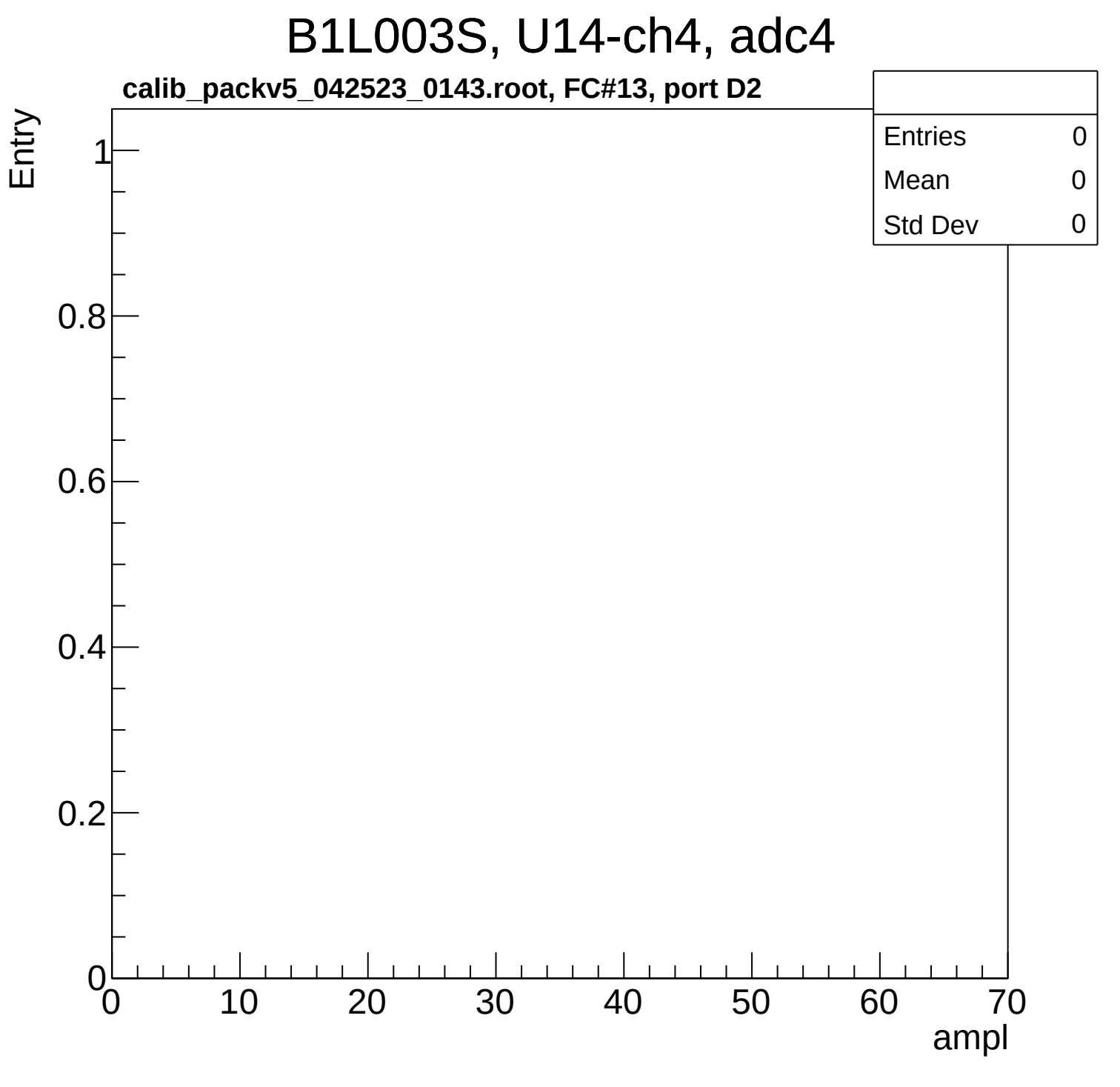

B1L003S, U14-ch3, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

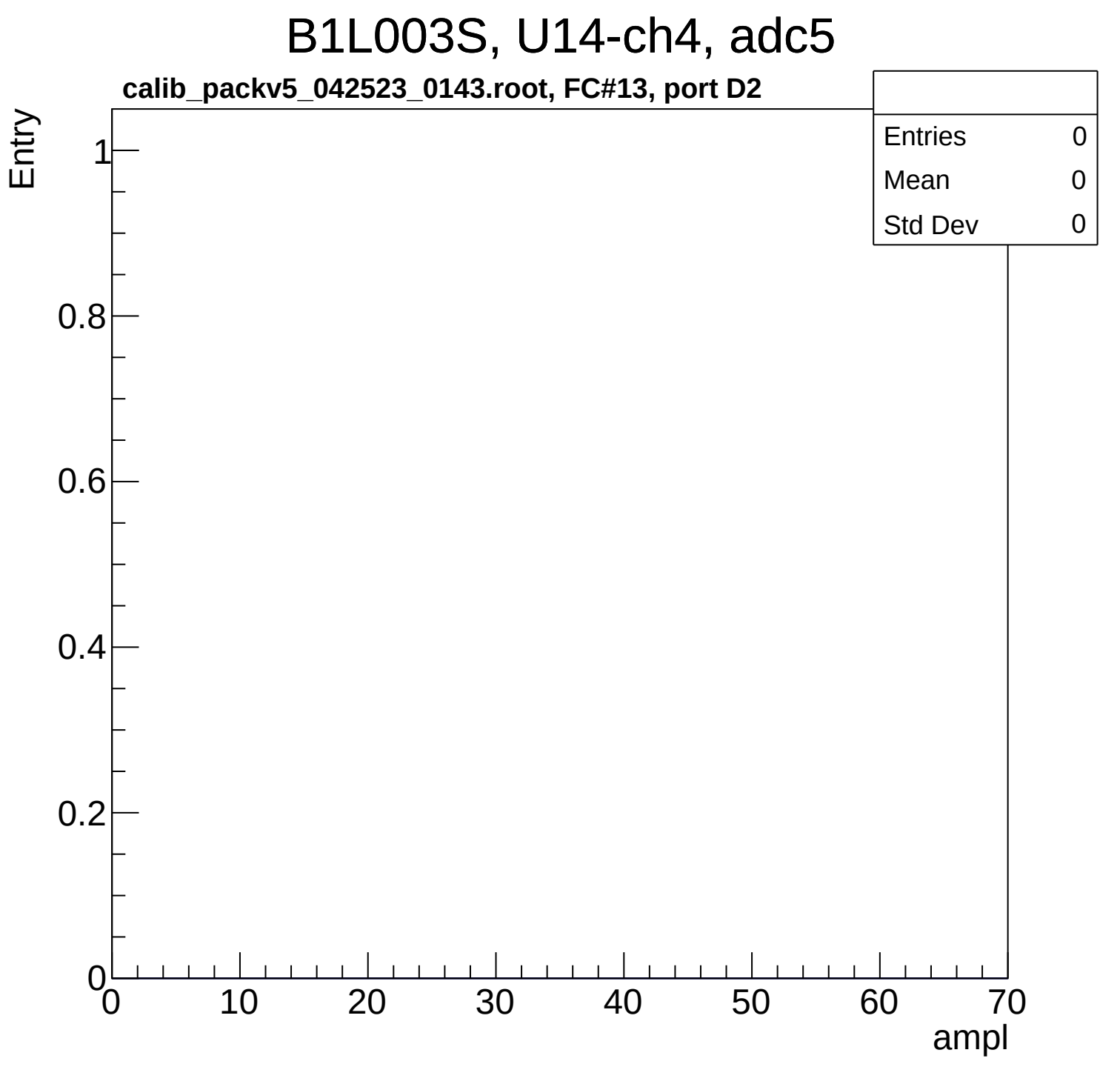

B1L003S, U14-ch4, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

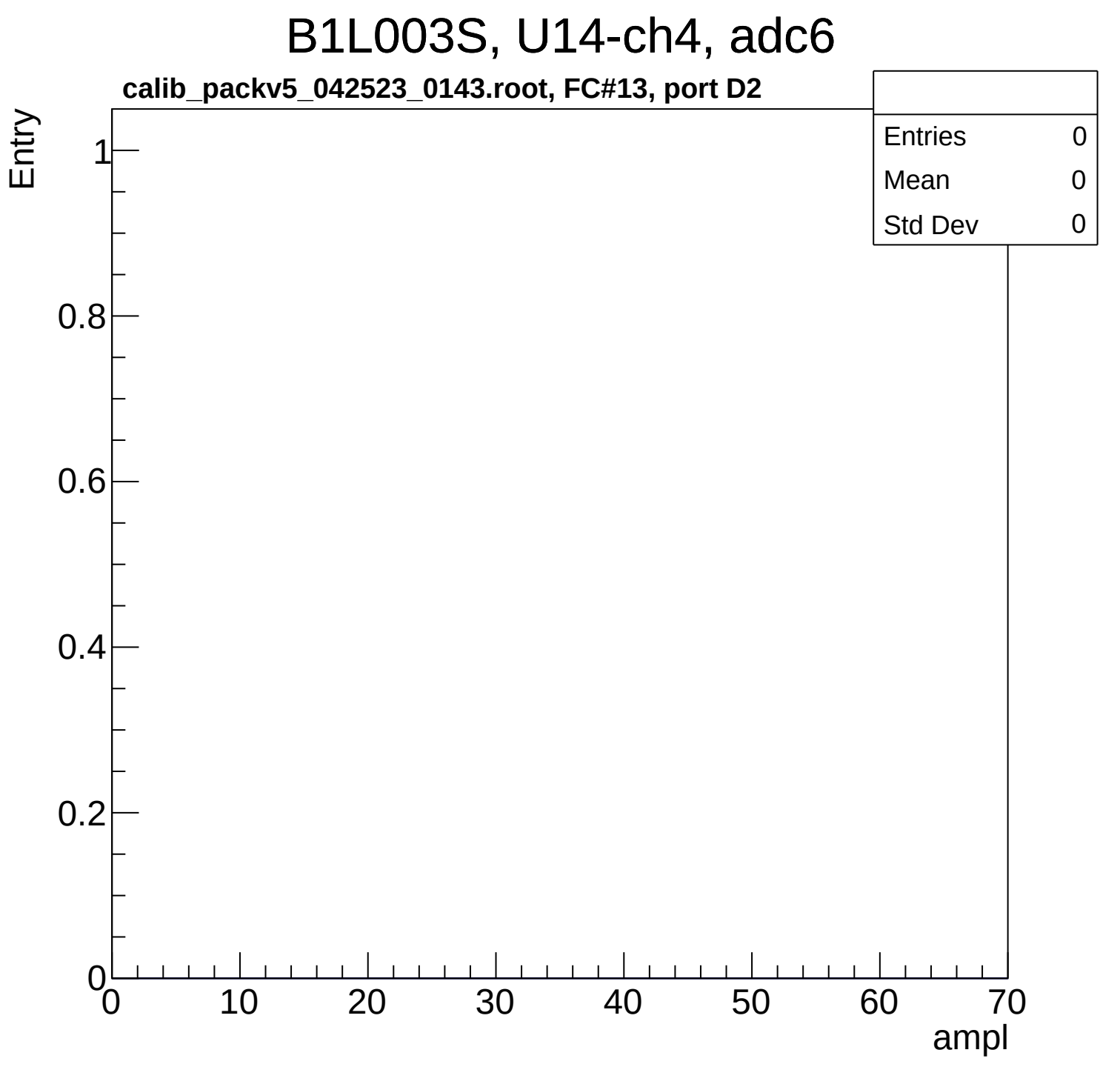

B1L003S, U14-ch4, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

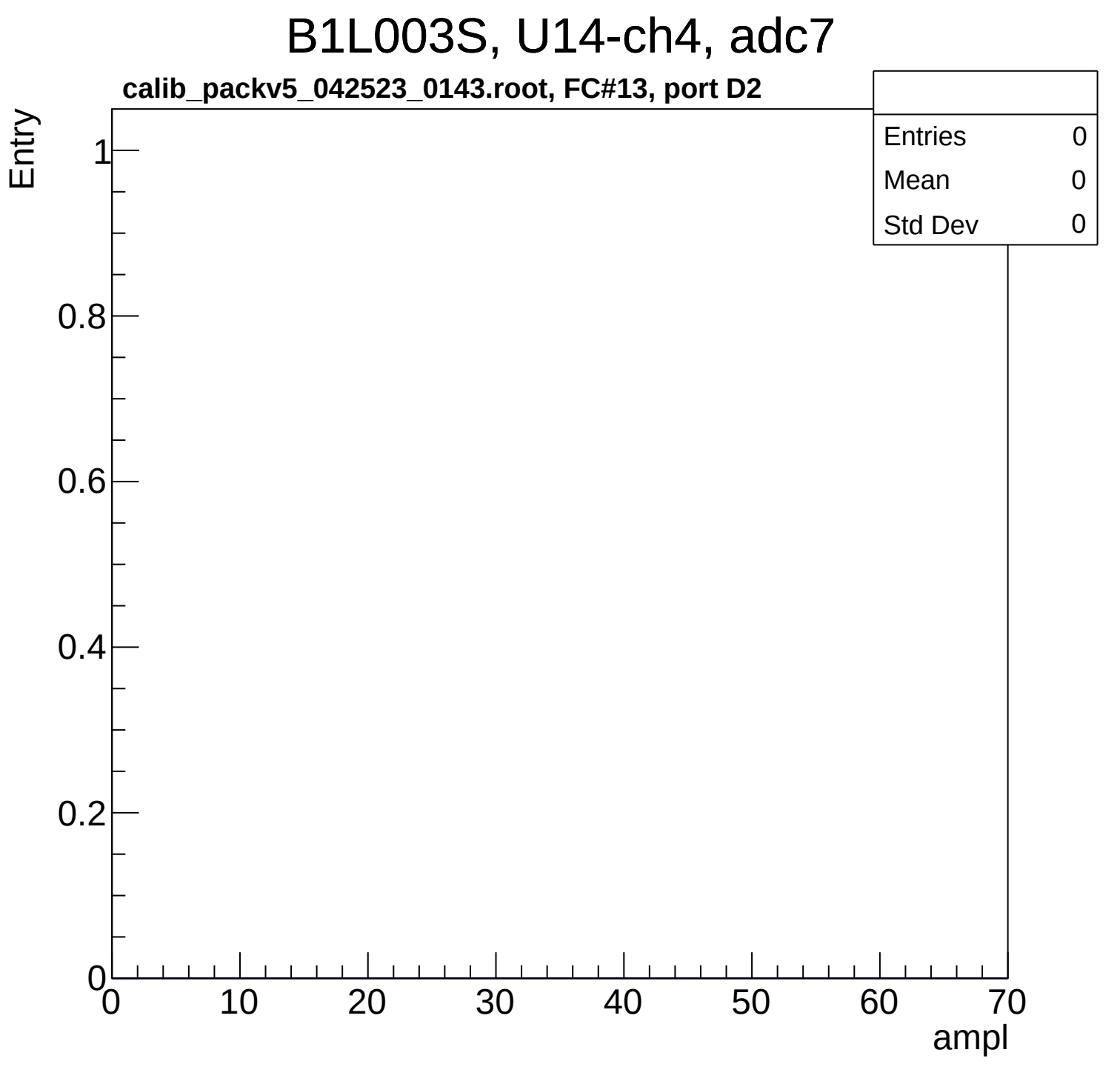

B1L003S, U14-ch4, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

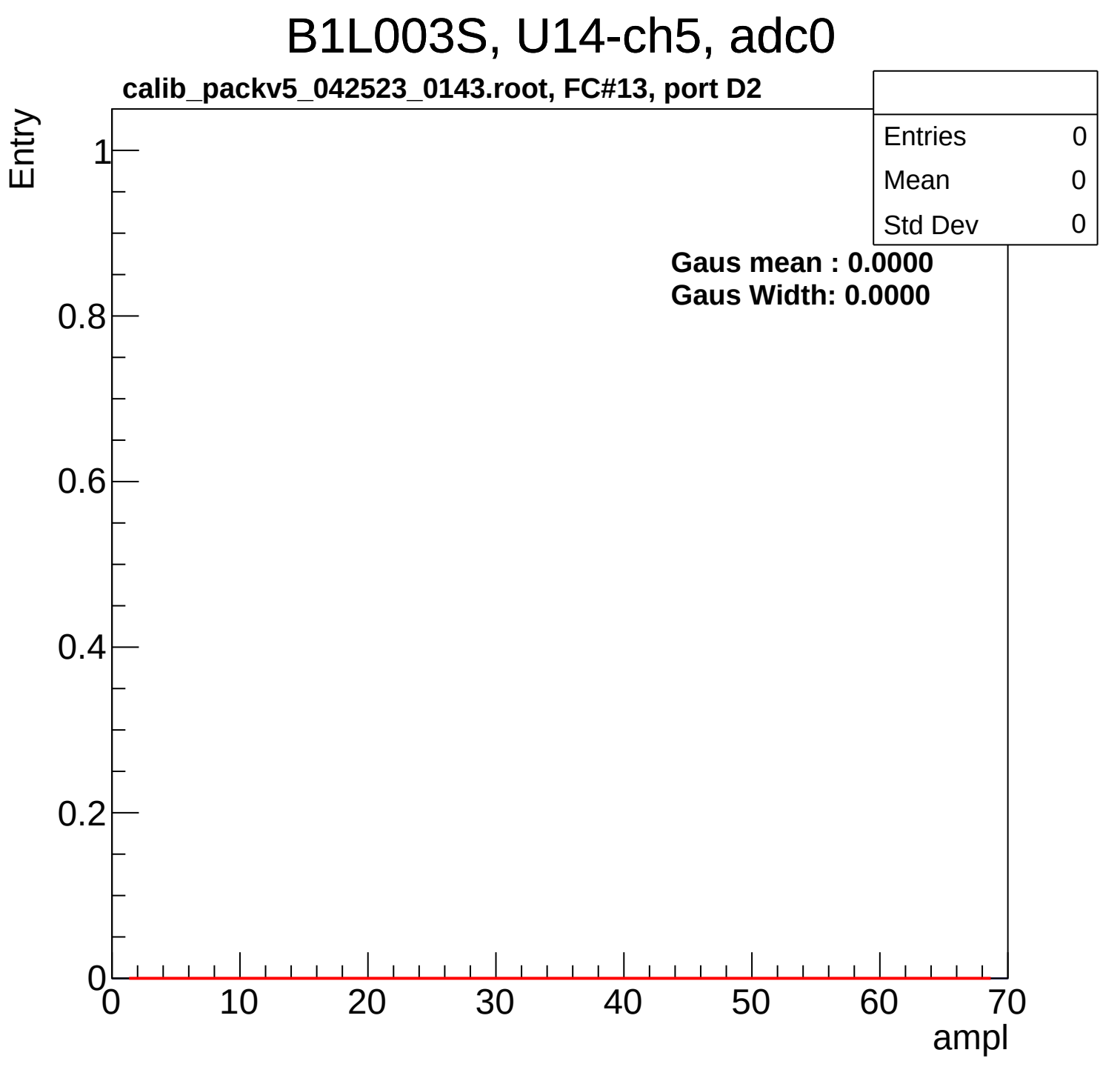

B1L003S, U14-ch4, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

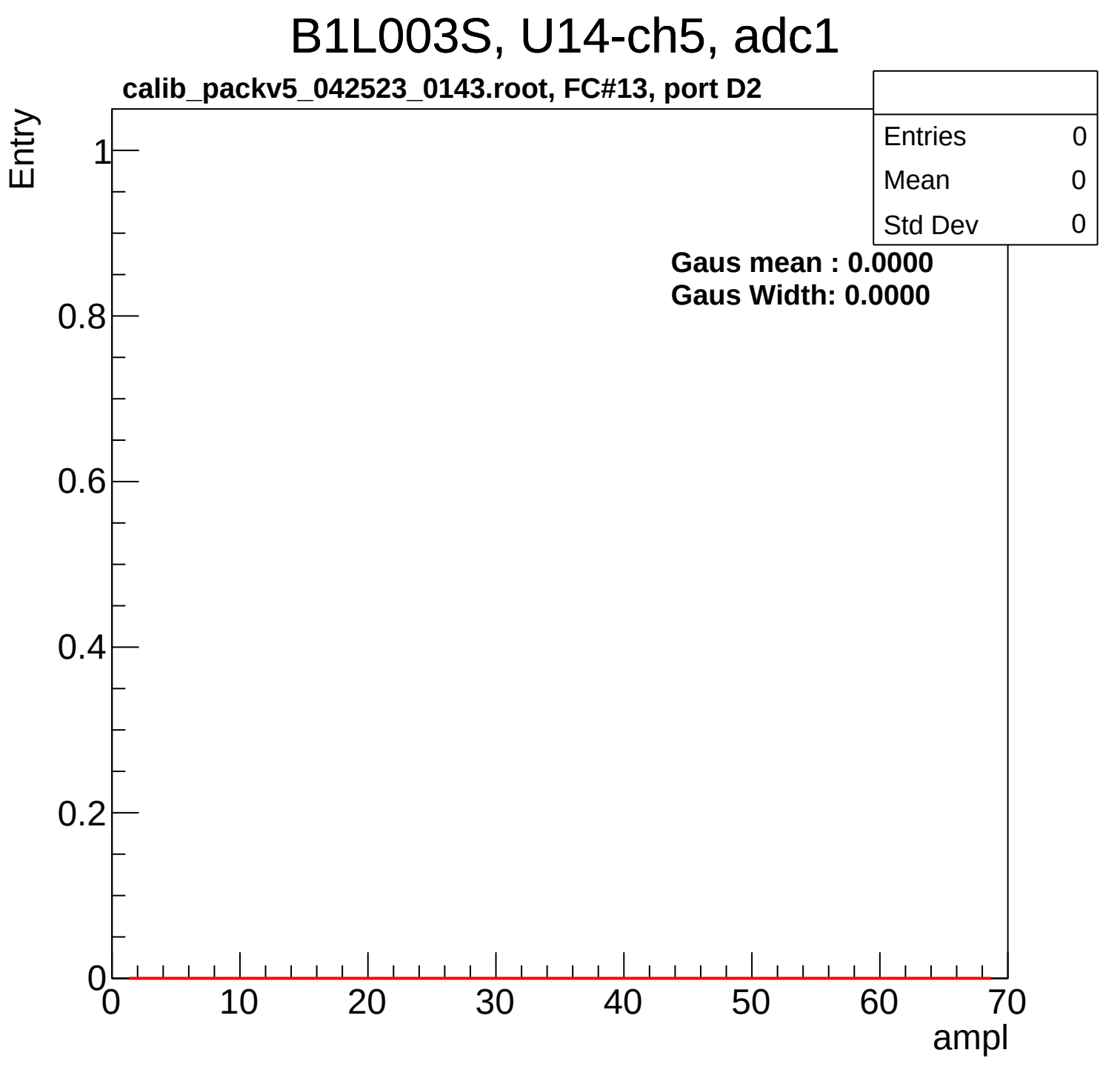

B1L003S, U14-ch4, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

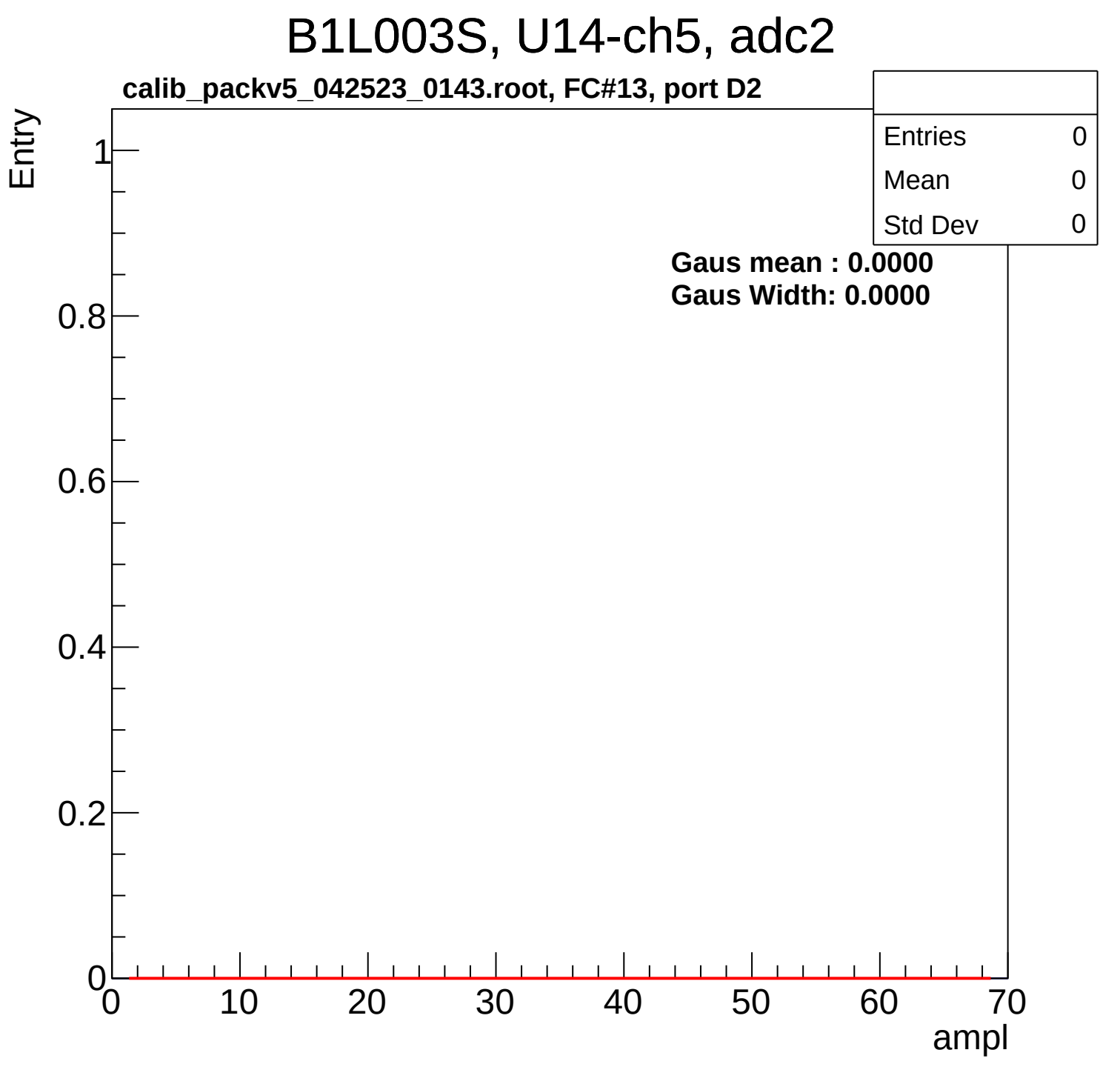

B1L003S, U14-ch4, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

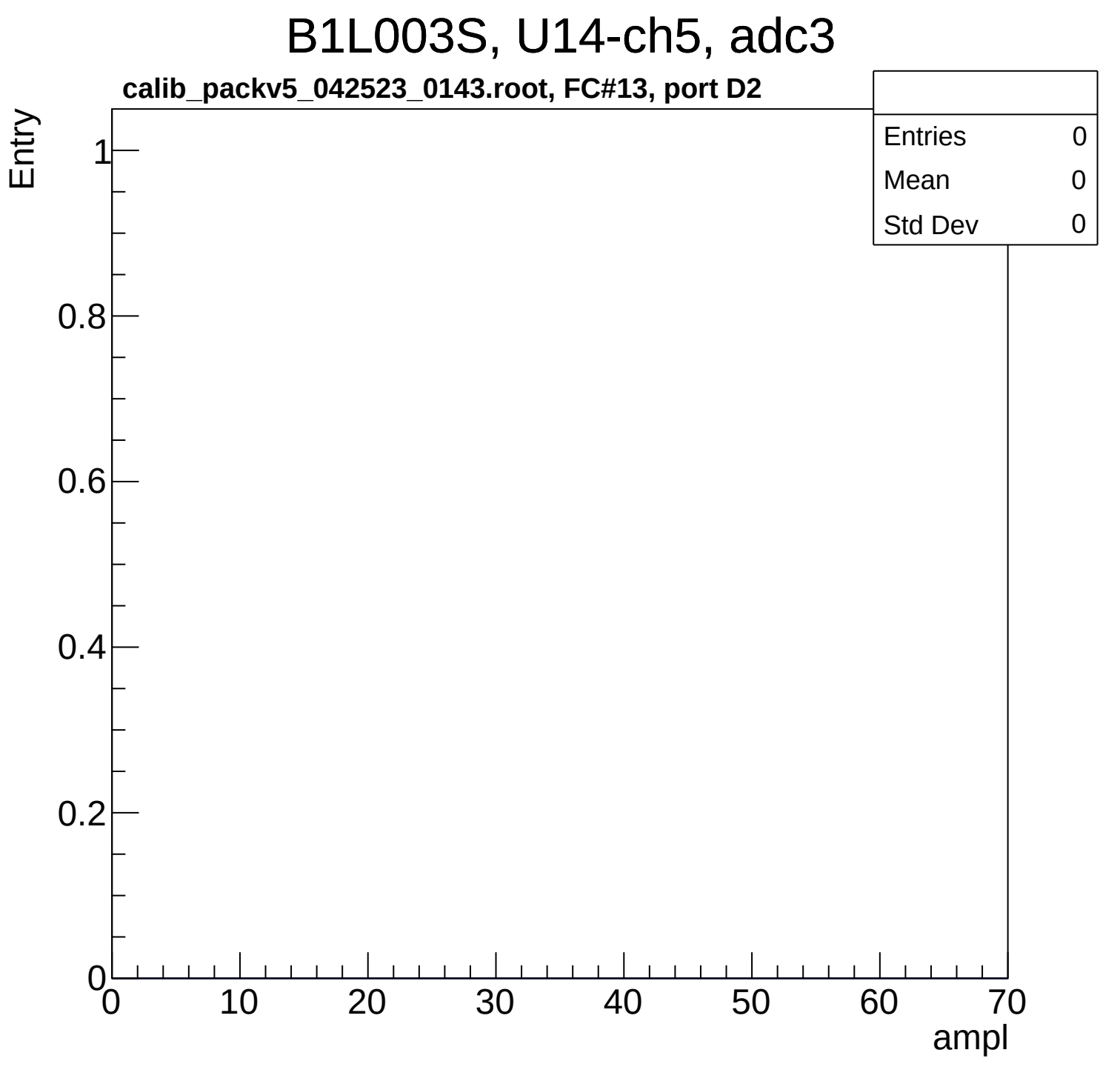

B1L003S, U14-ch4, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

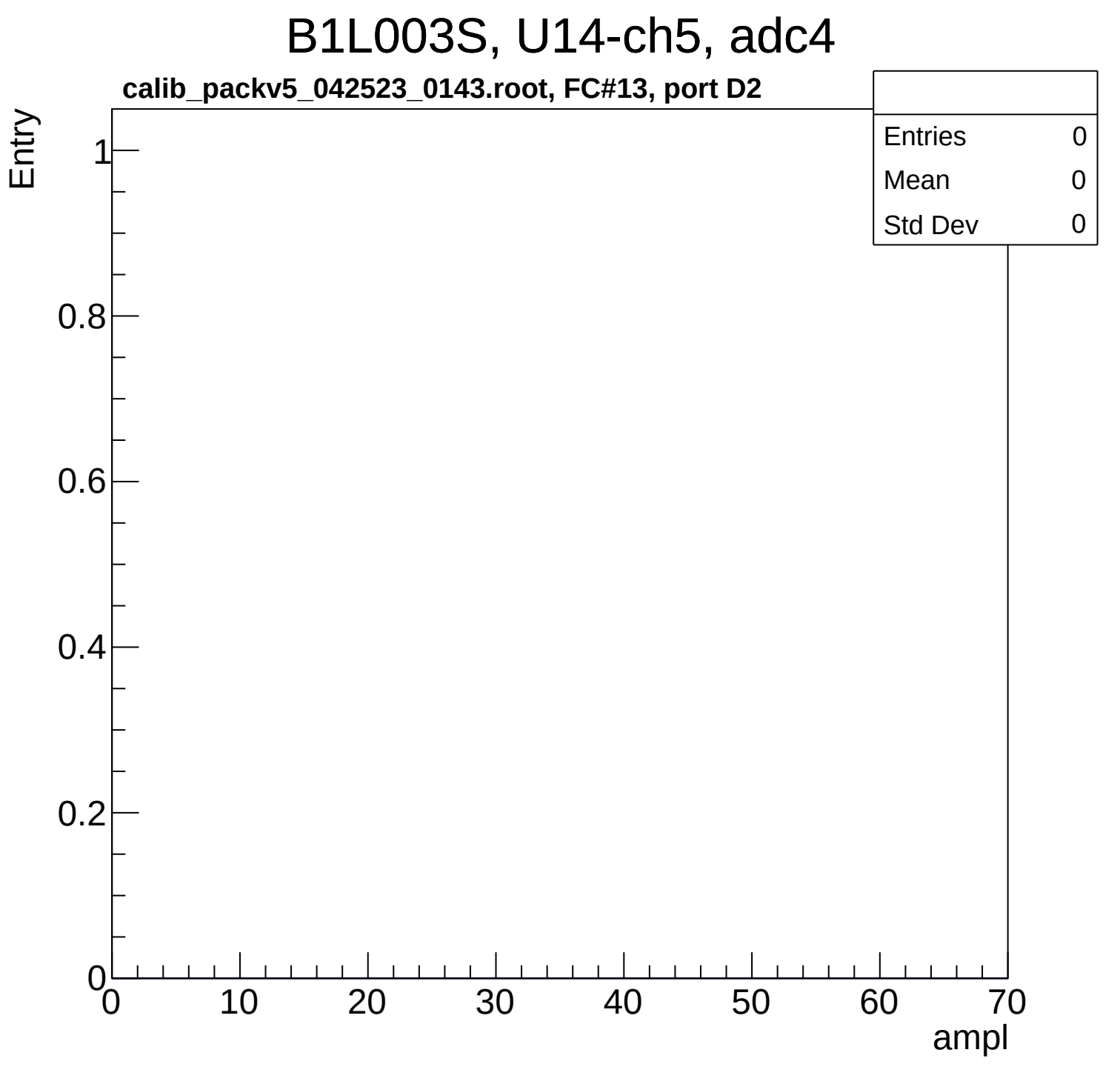

B1L003S, U14-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

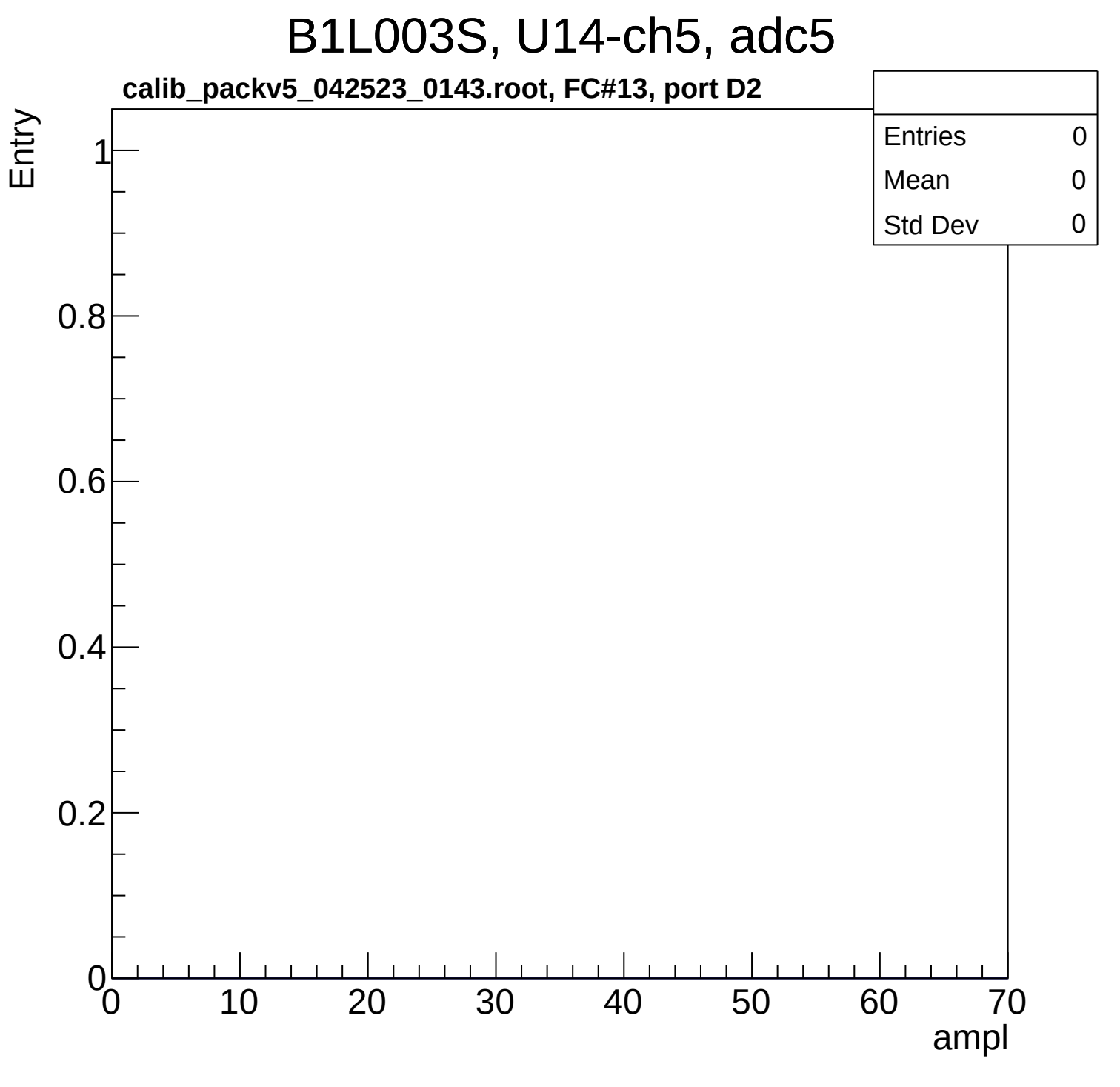

B1L003S, U14-ch5, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

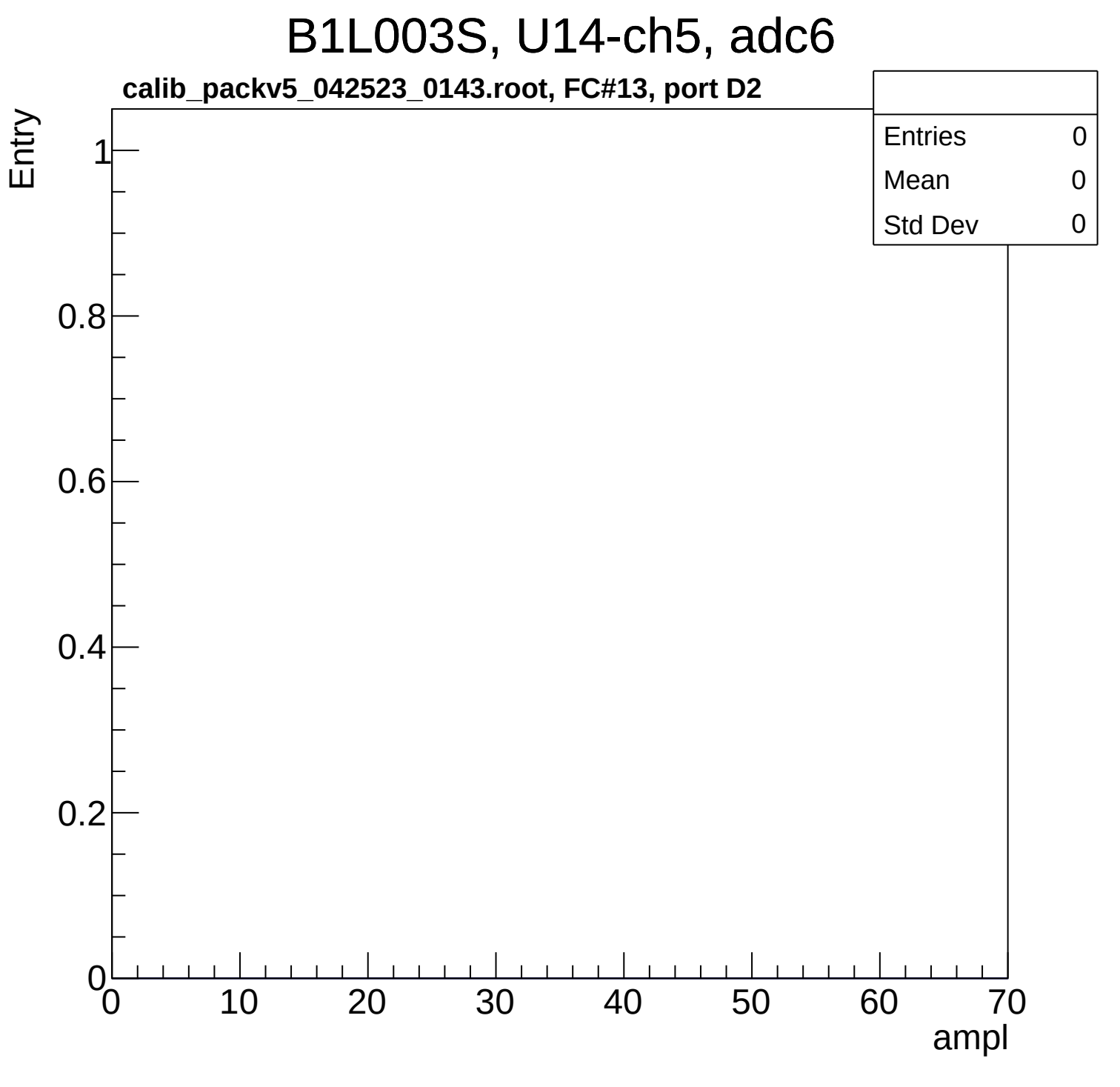

B1L003S, U14-ch5, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

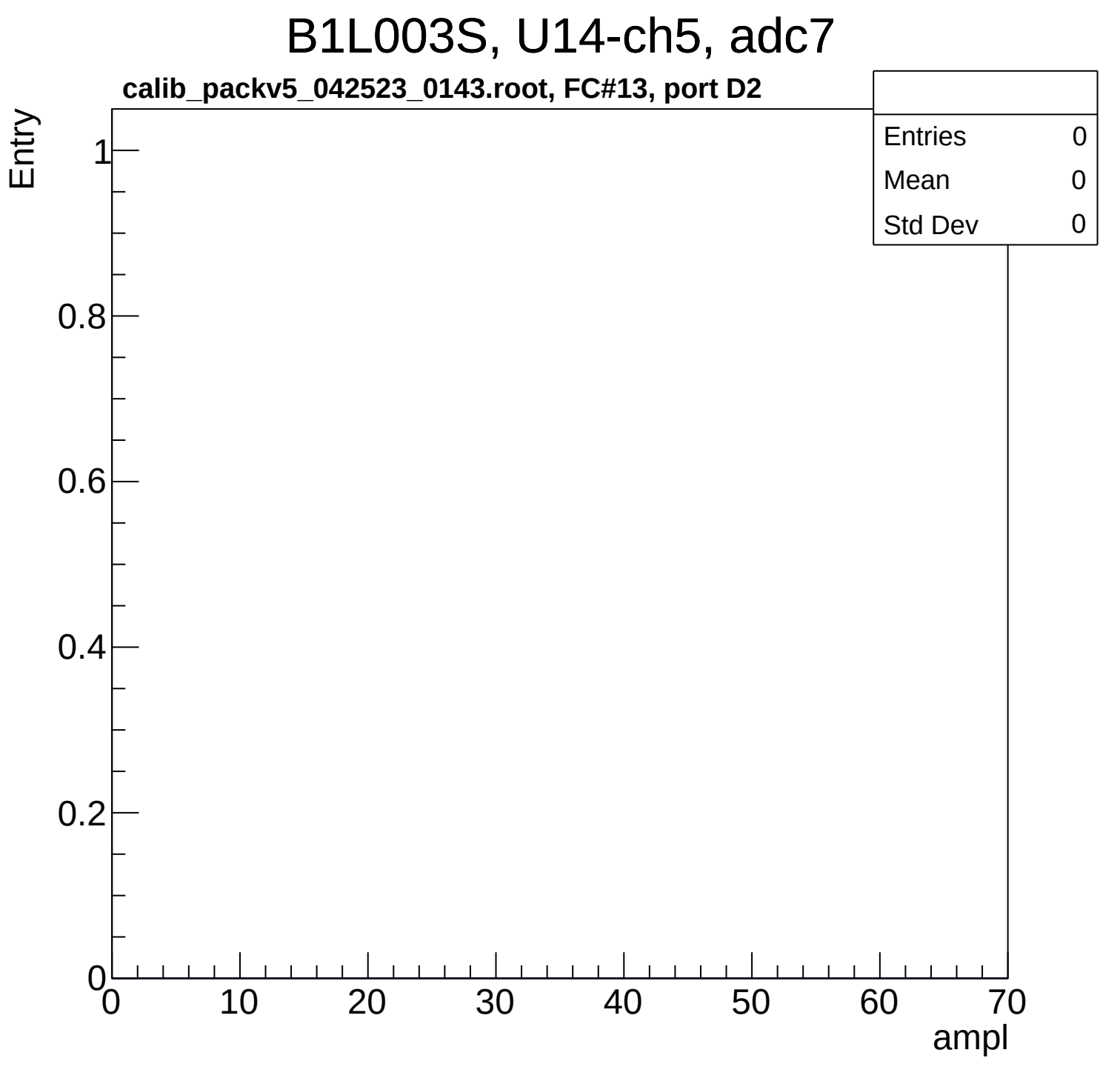

B1L003S, U14-ch5, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

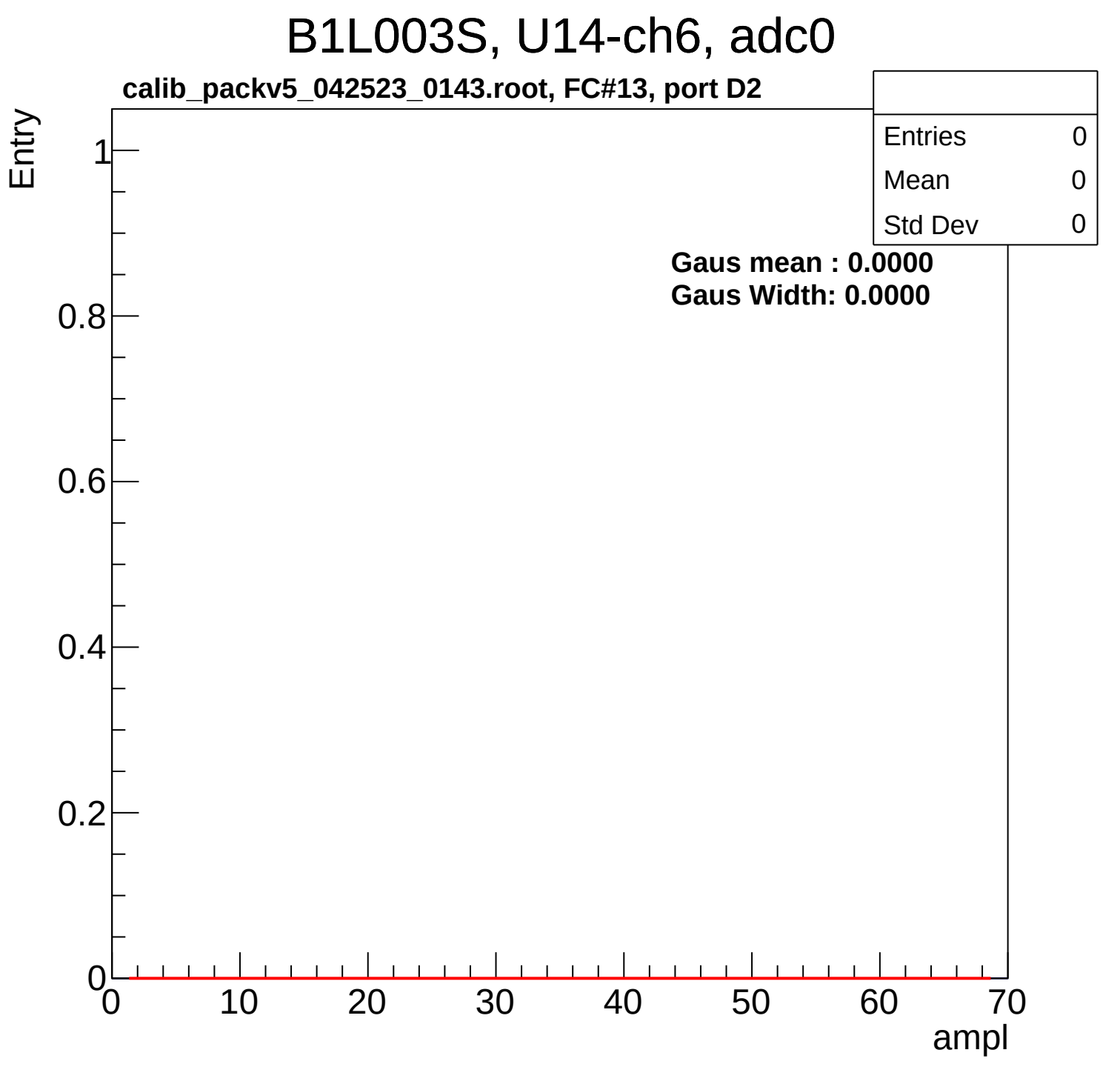

B1L003S, U14-ch5, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

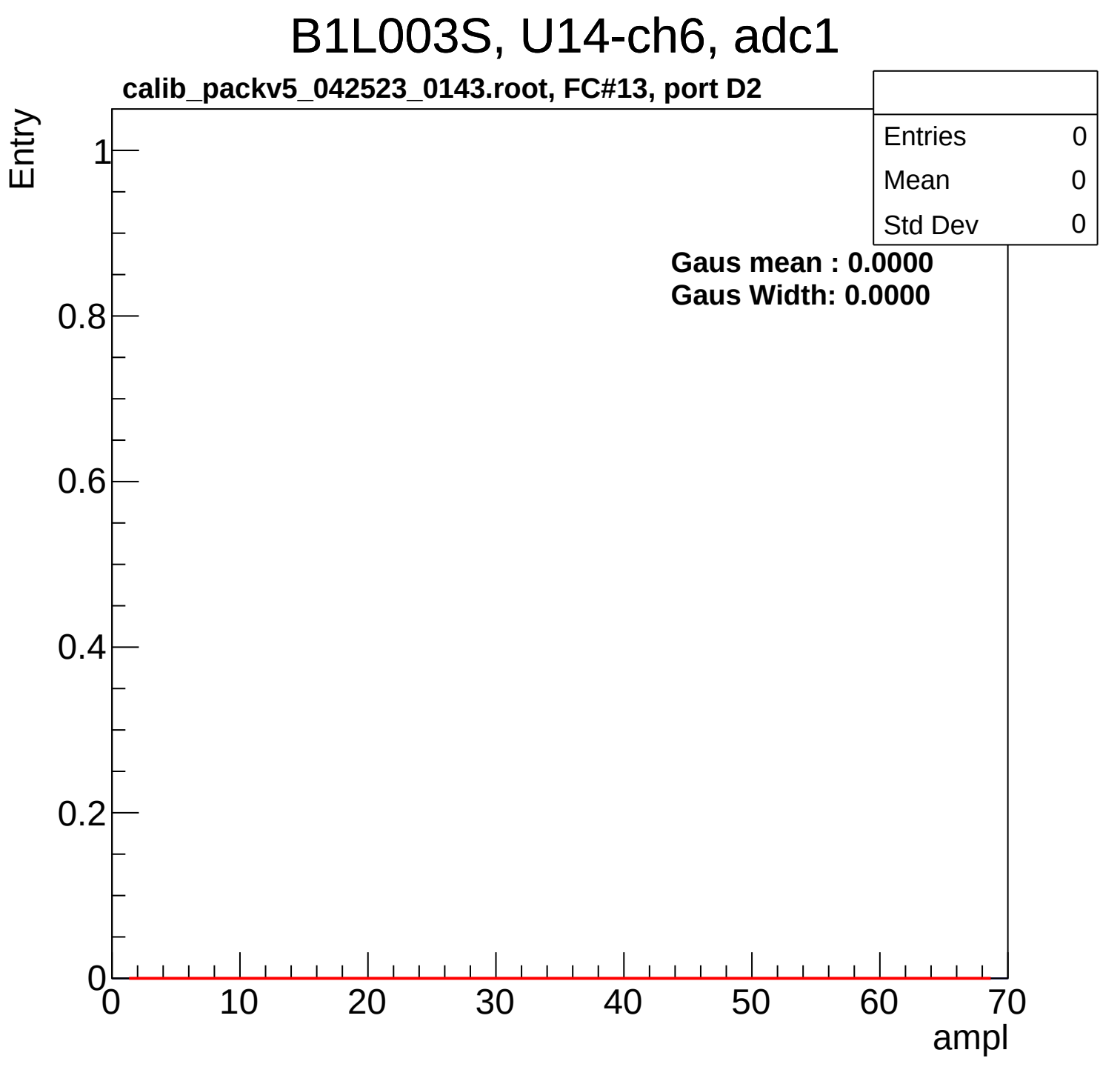

B1L003S, U14-ch5, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

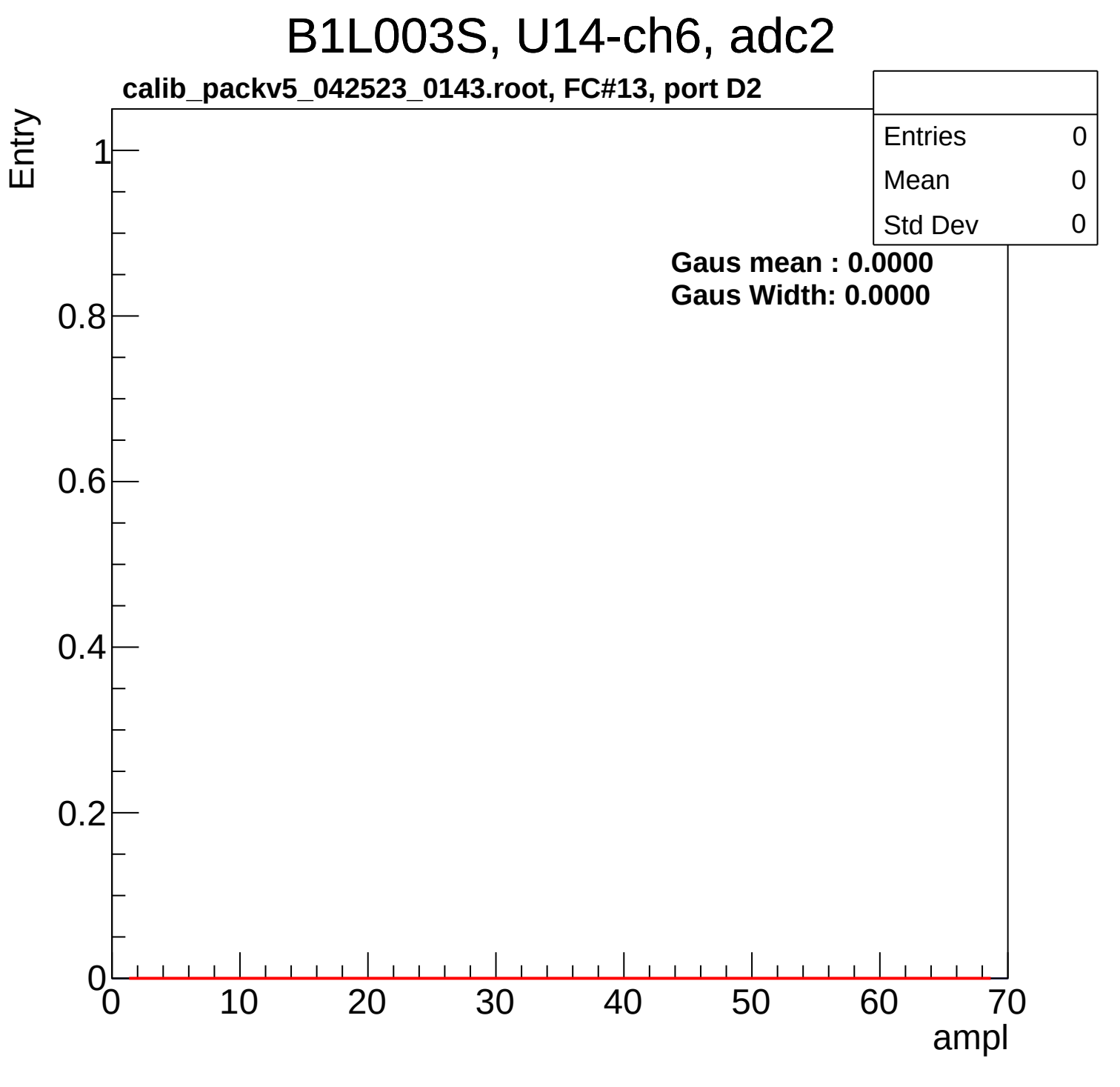

B1L003S, U14-ch5, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

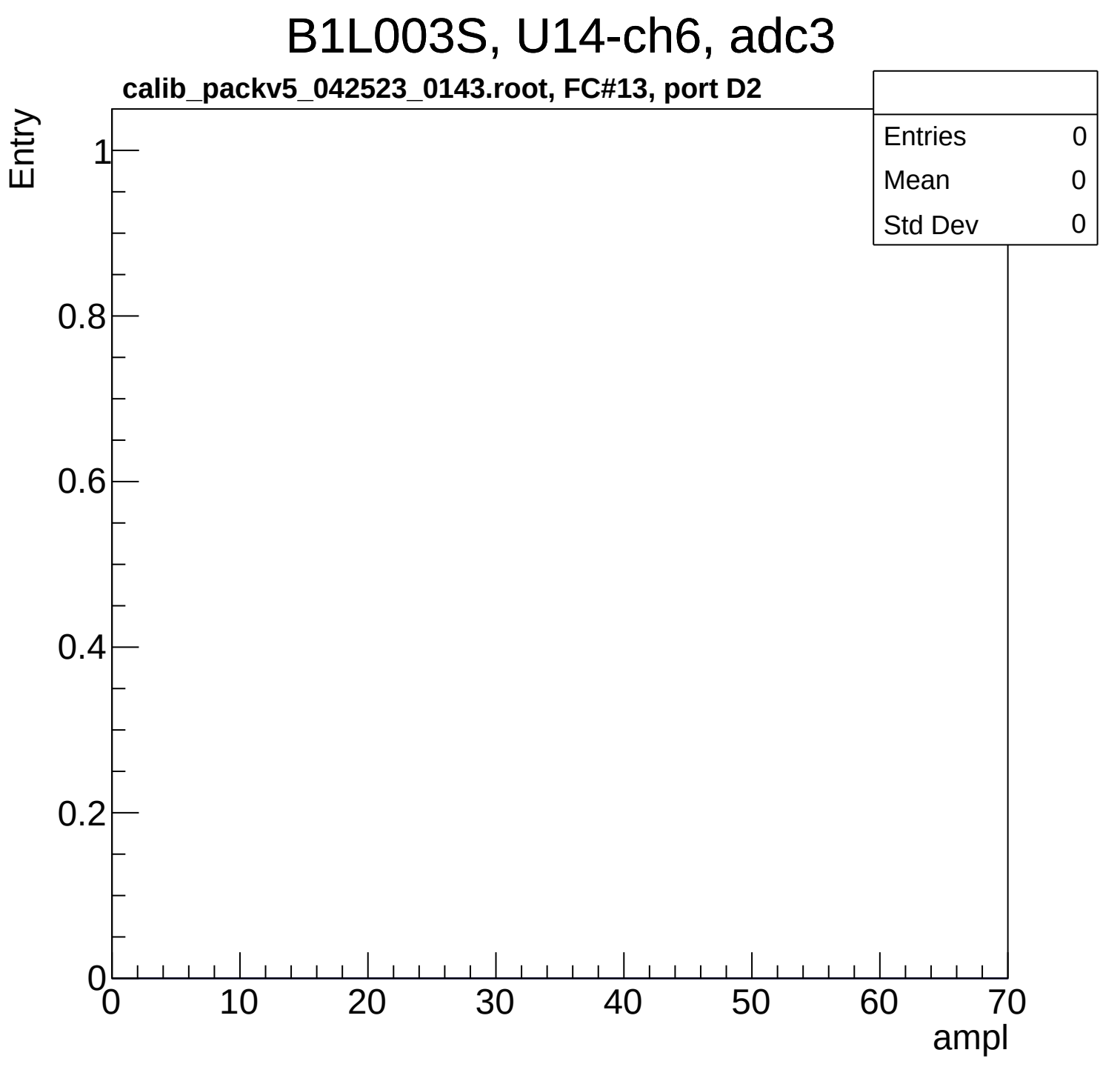

B1L003S, U14-ch5, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

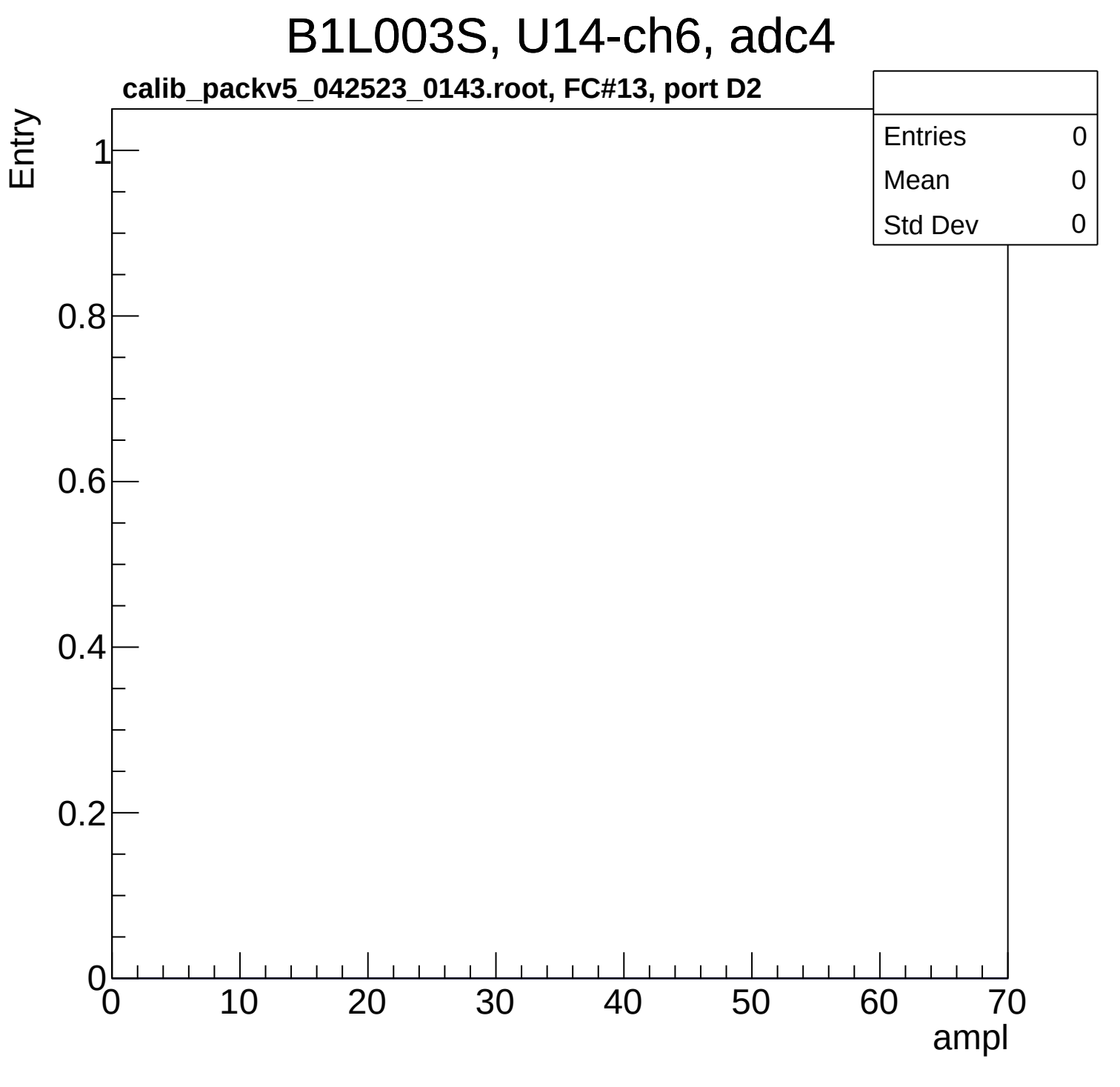

B1L003S, U14-ch5, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

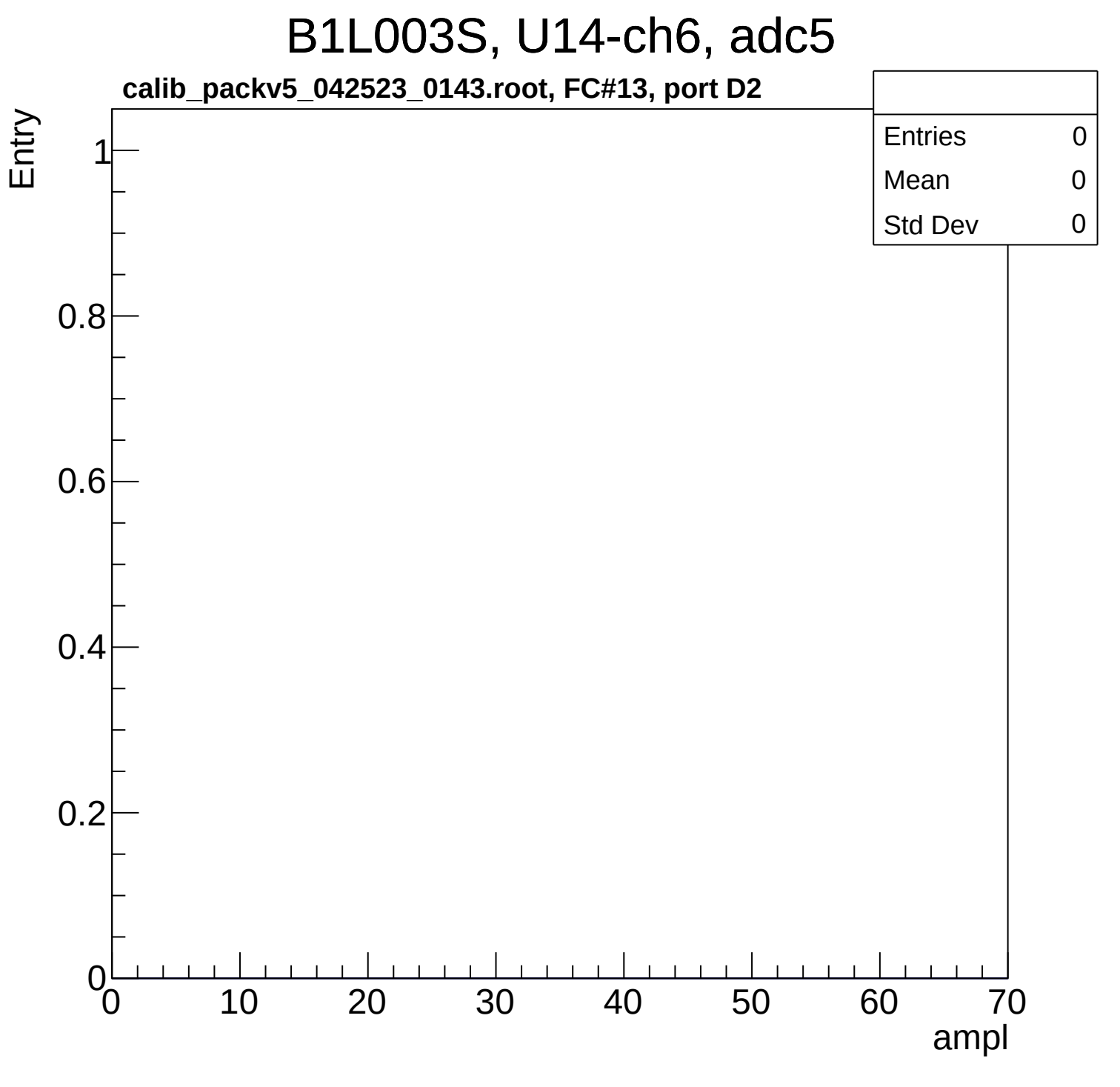

B1L003S, U14-ch6, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

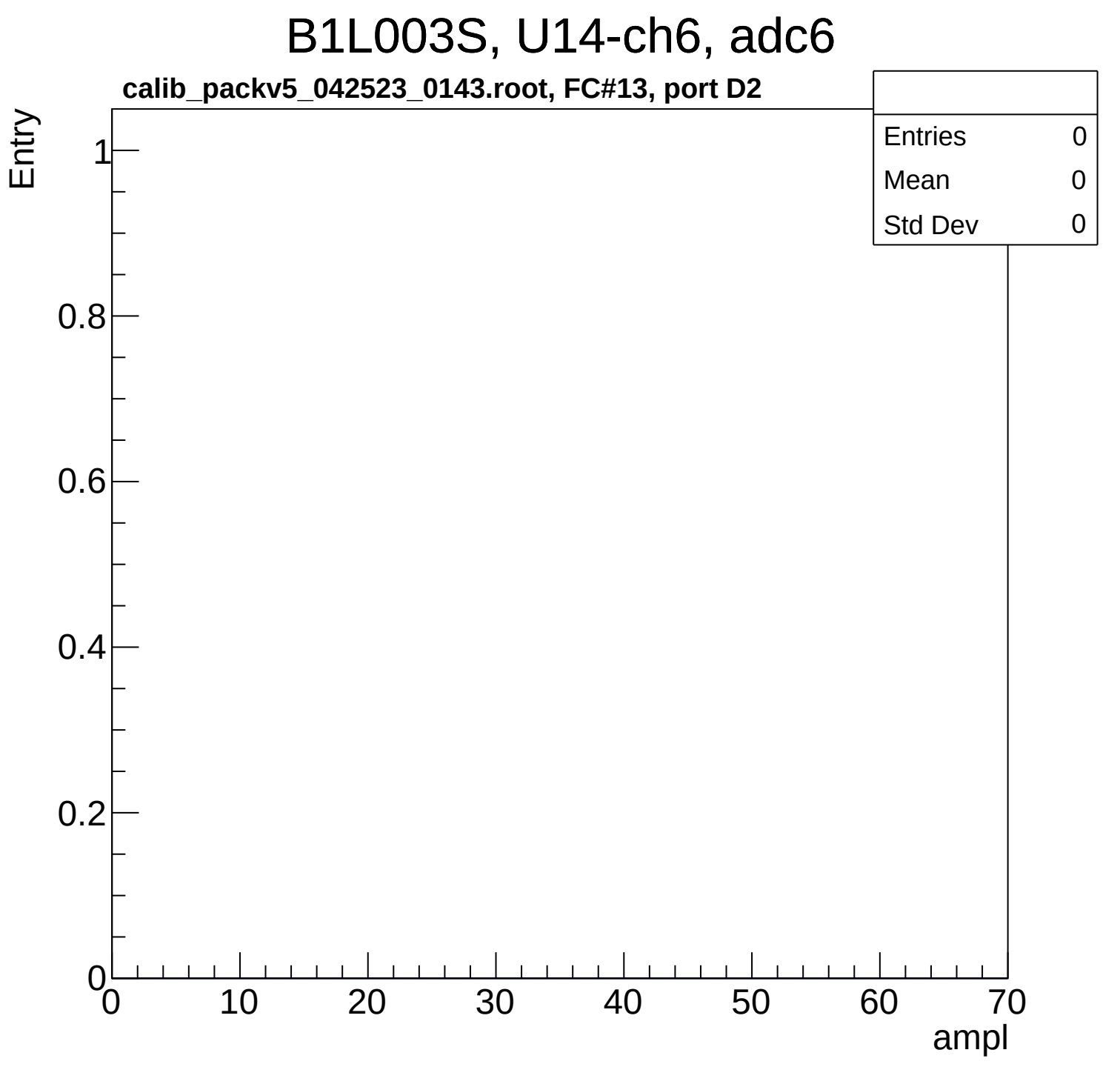

B1L003S, U14-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

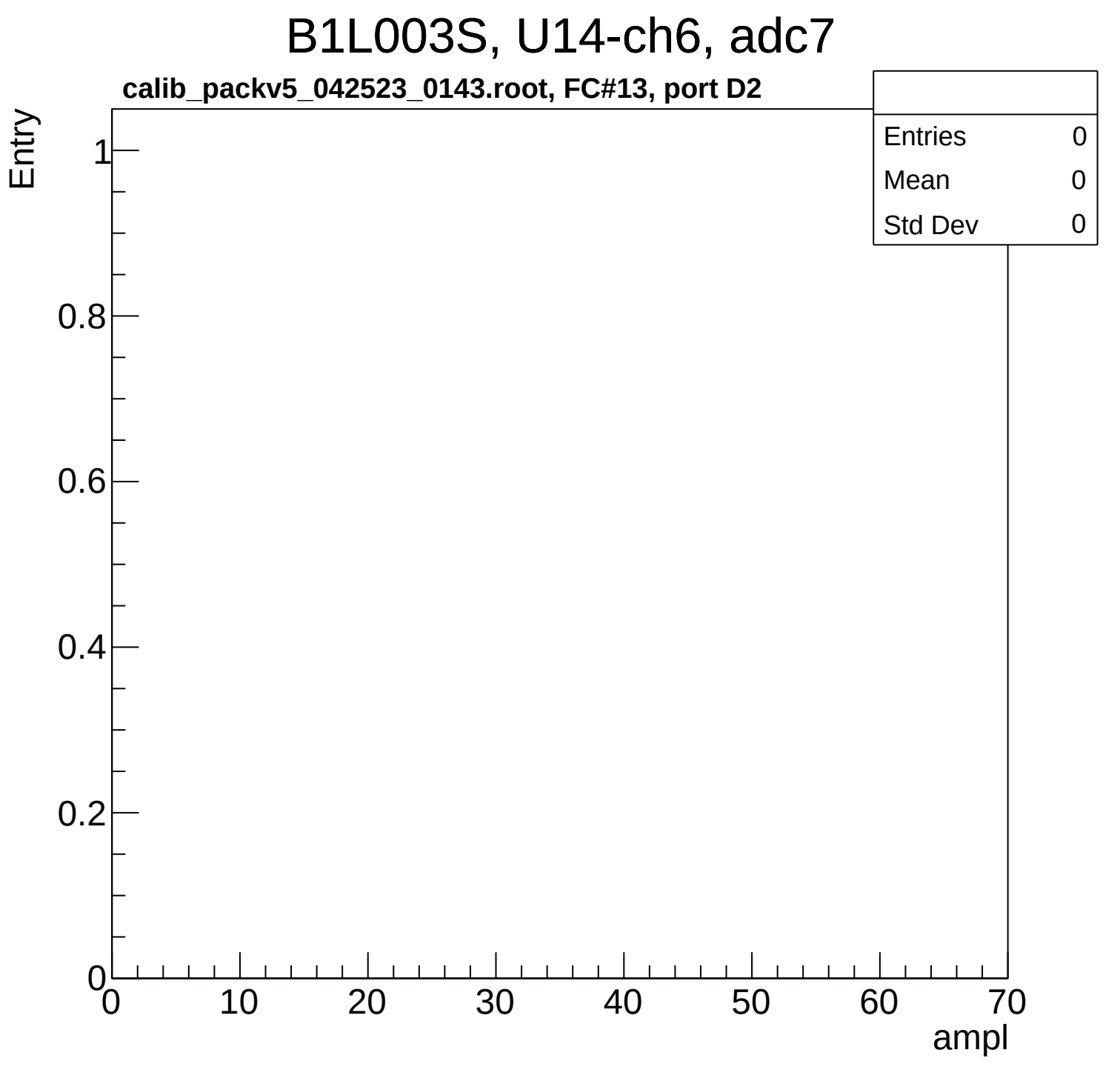

B1L003S, U14-ch6, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

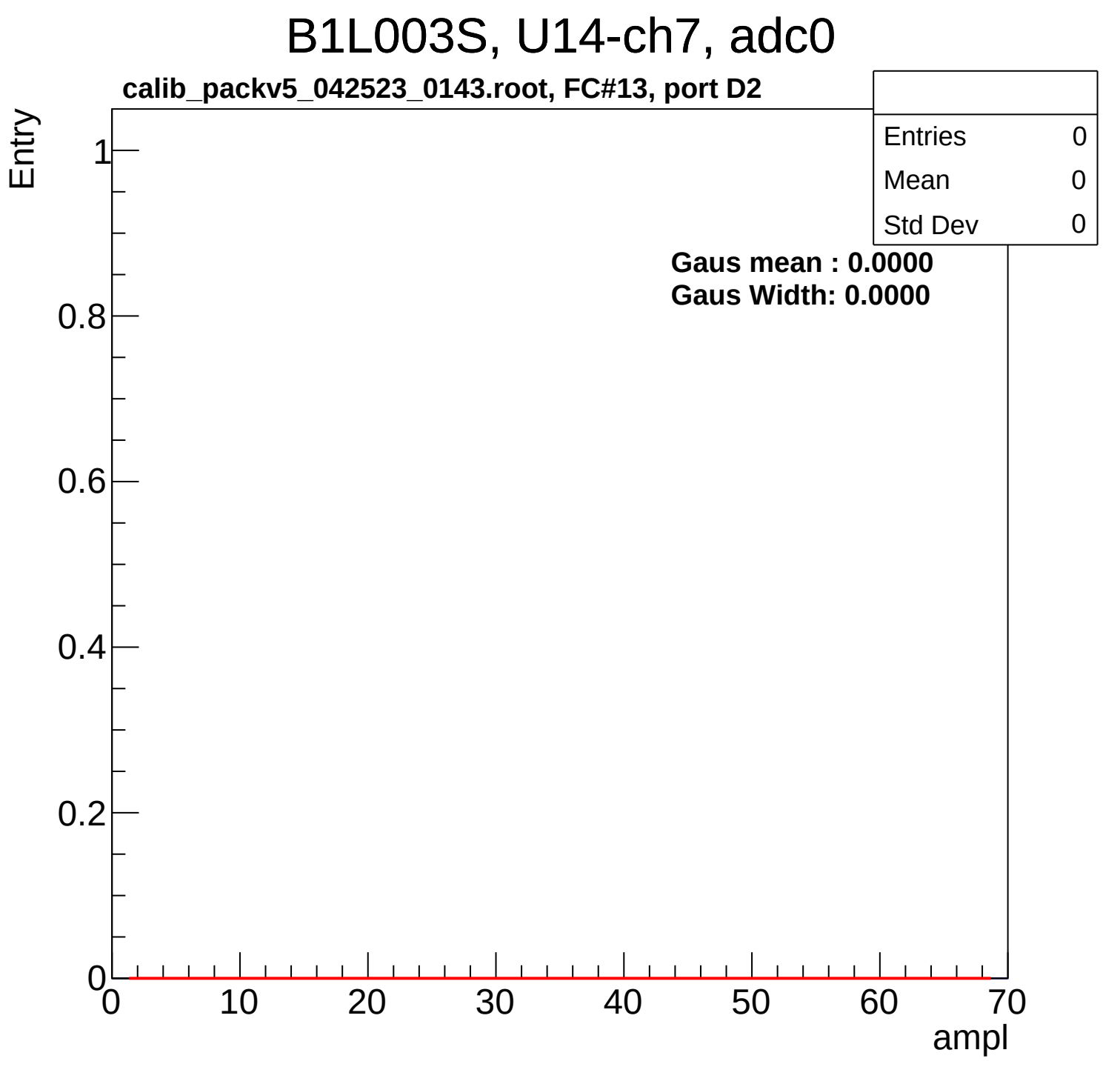

B1L003S, U14-ch6, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

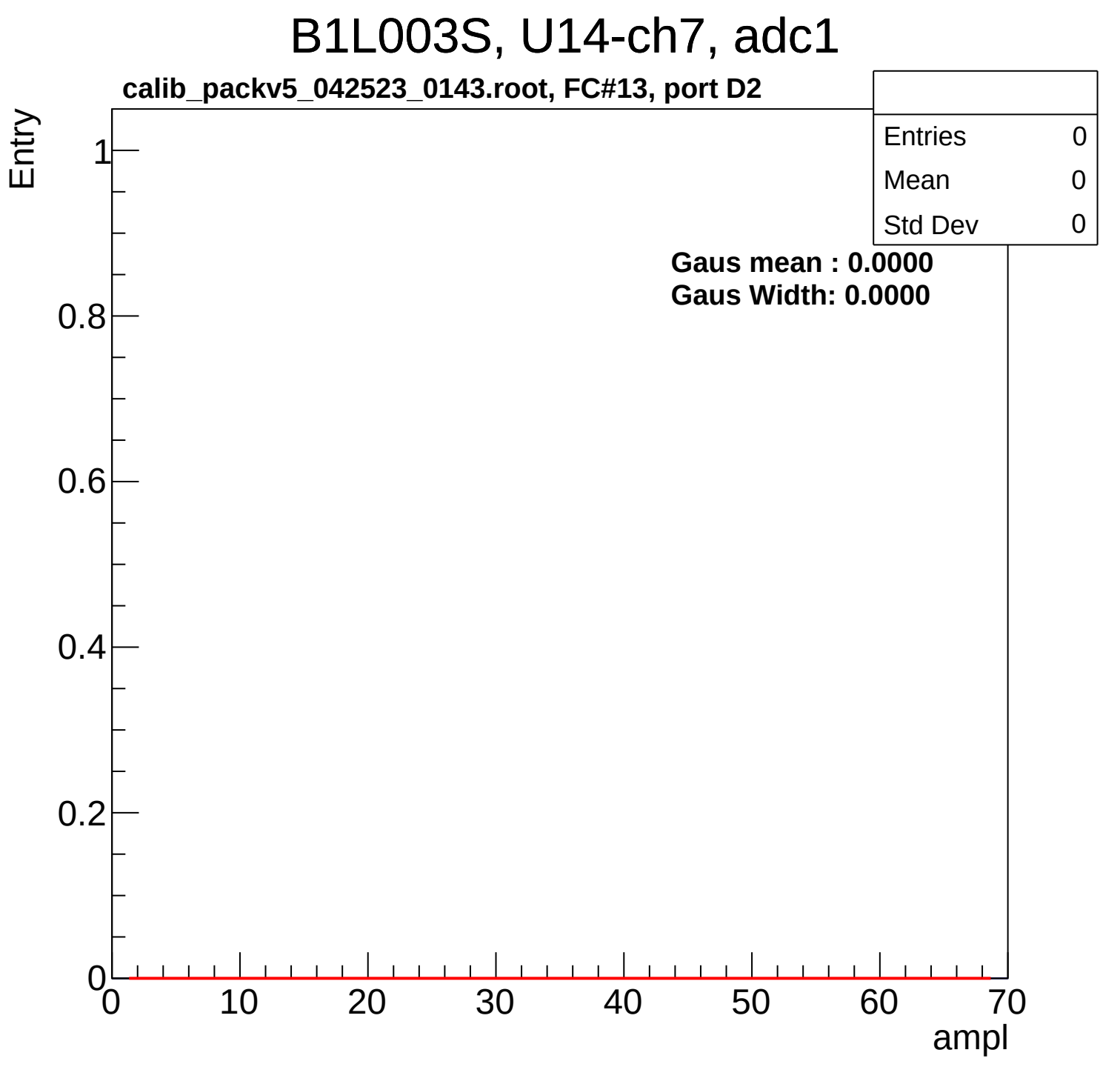

B1L003S, U14-ch6, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

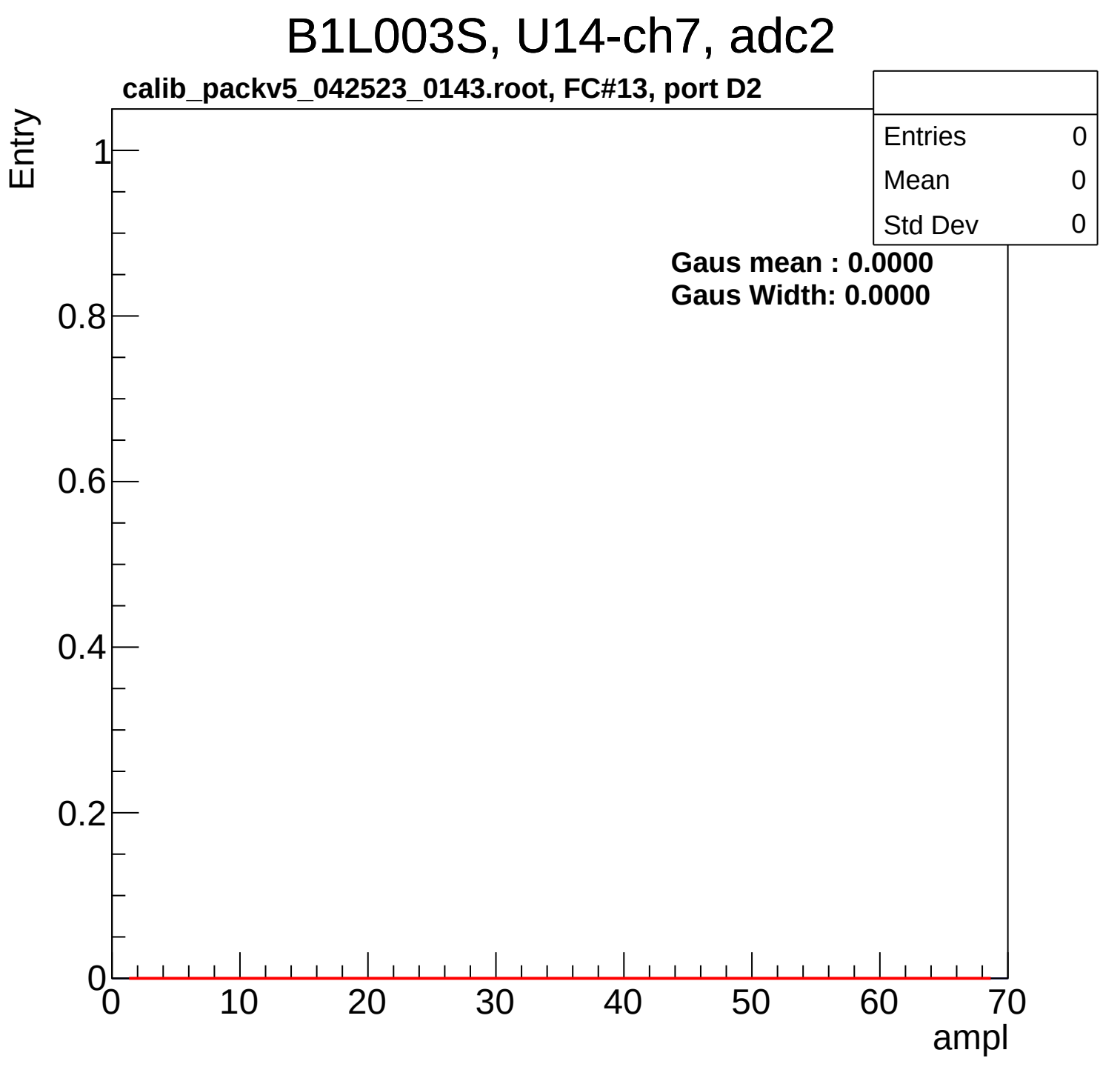

B1L003S, U14-ch6, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

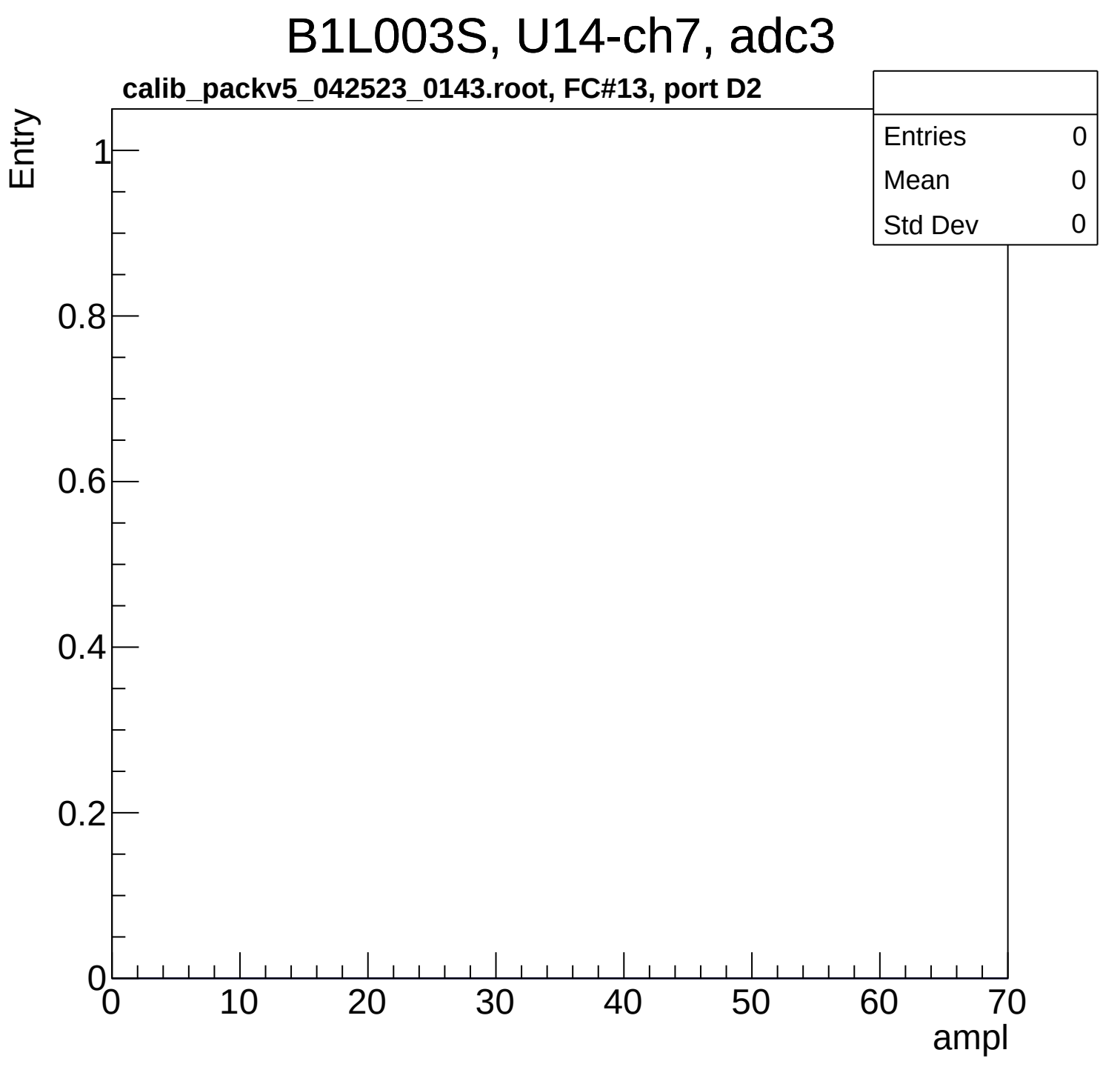

B1L003S, U14-ch6, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

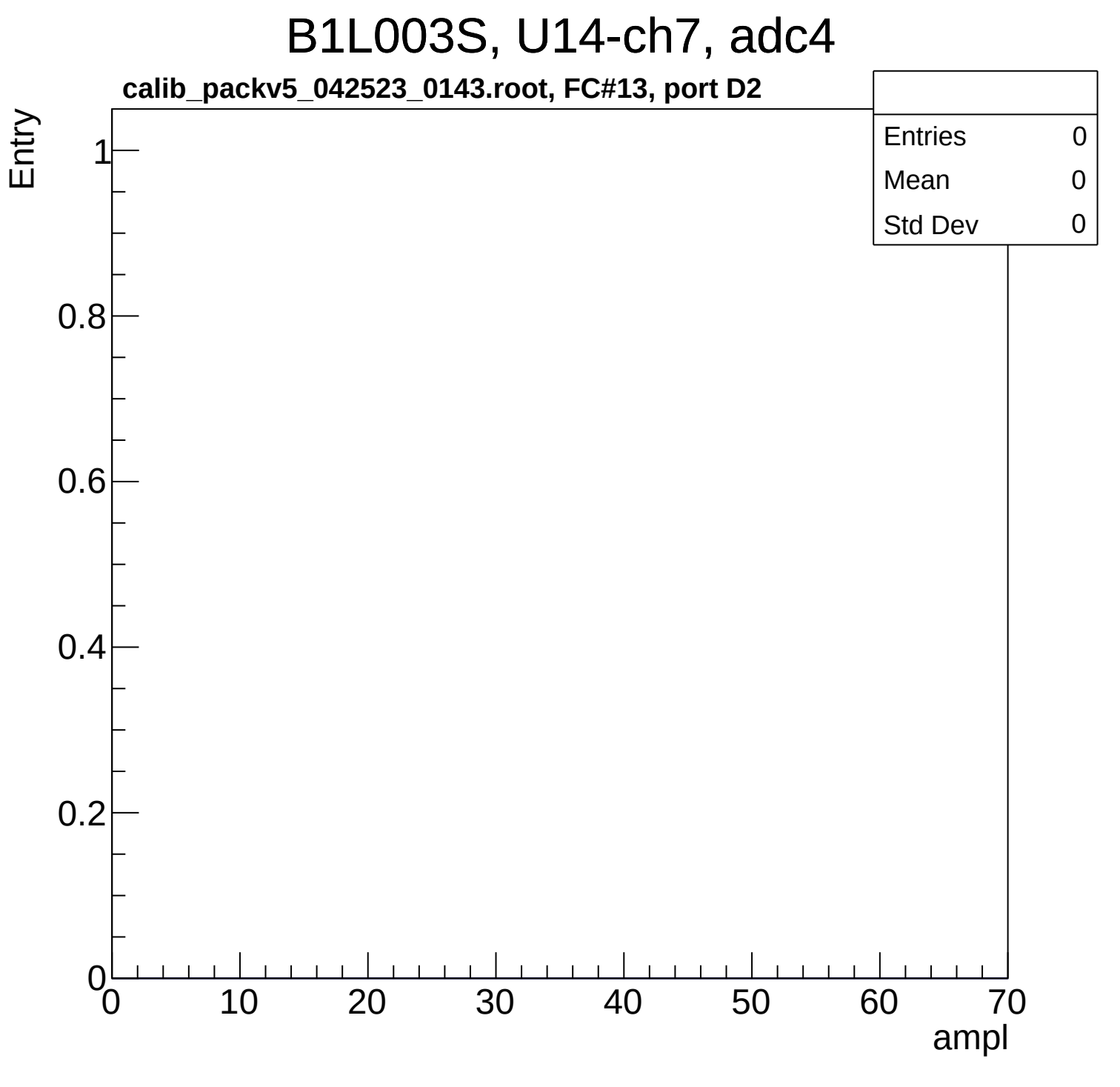

B1L003S, U14-ch6, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

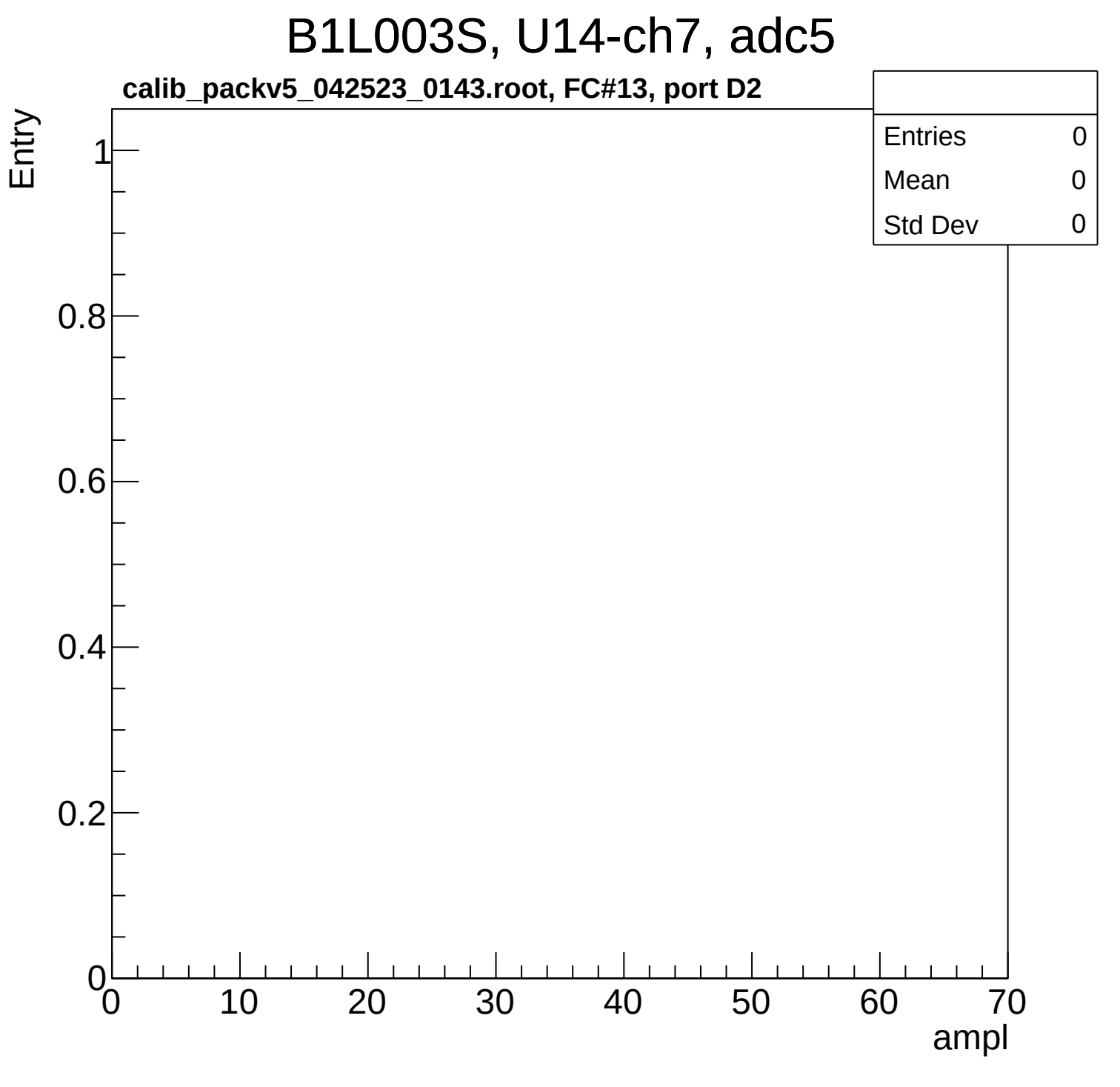

B1L003S, U14-ch7, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

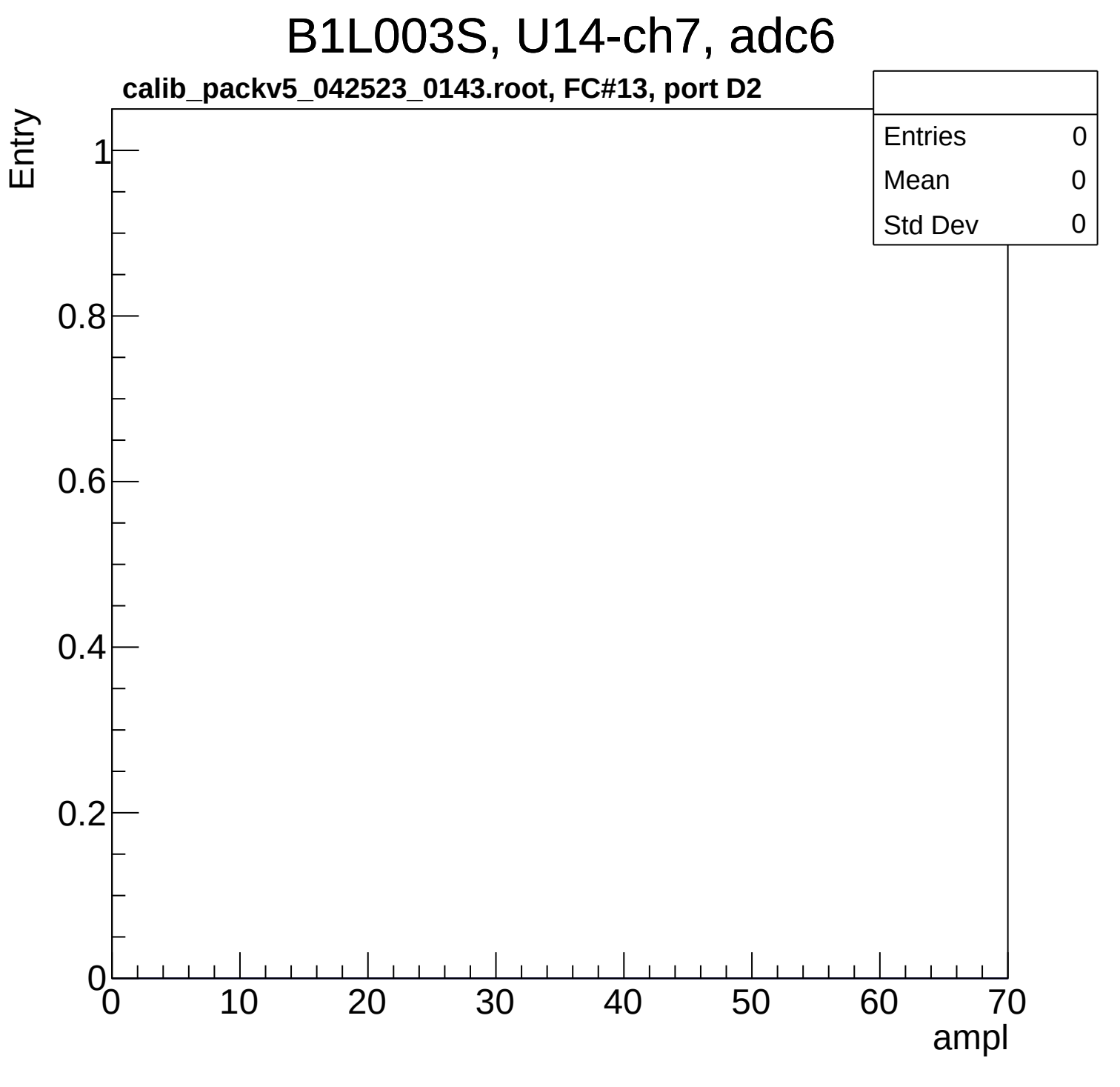

B1L003S, U14-ch7, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

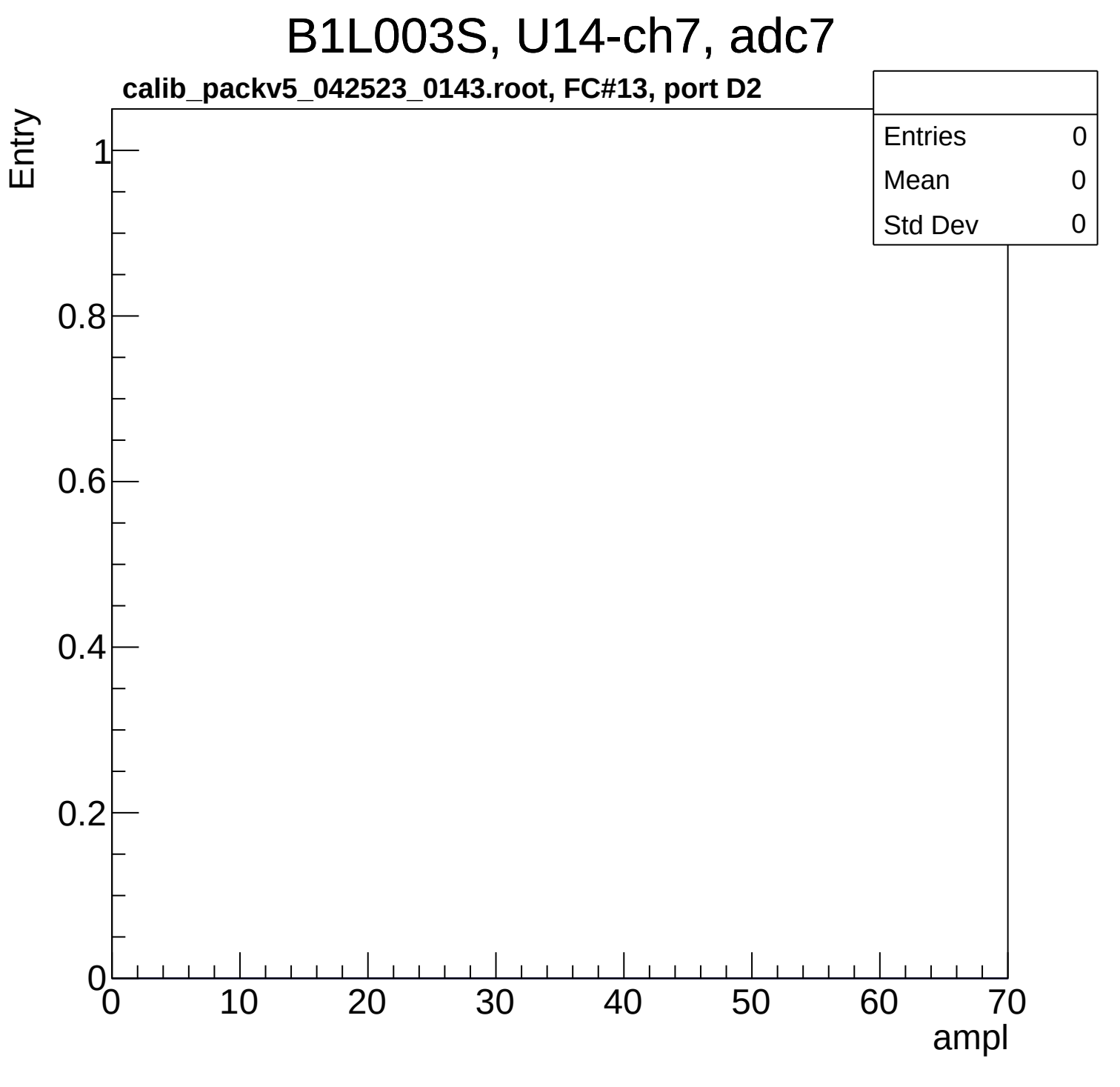

B1L003S, U14-ch7, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

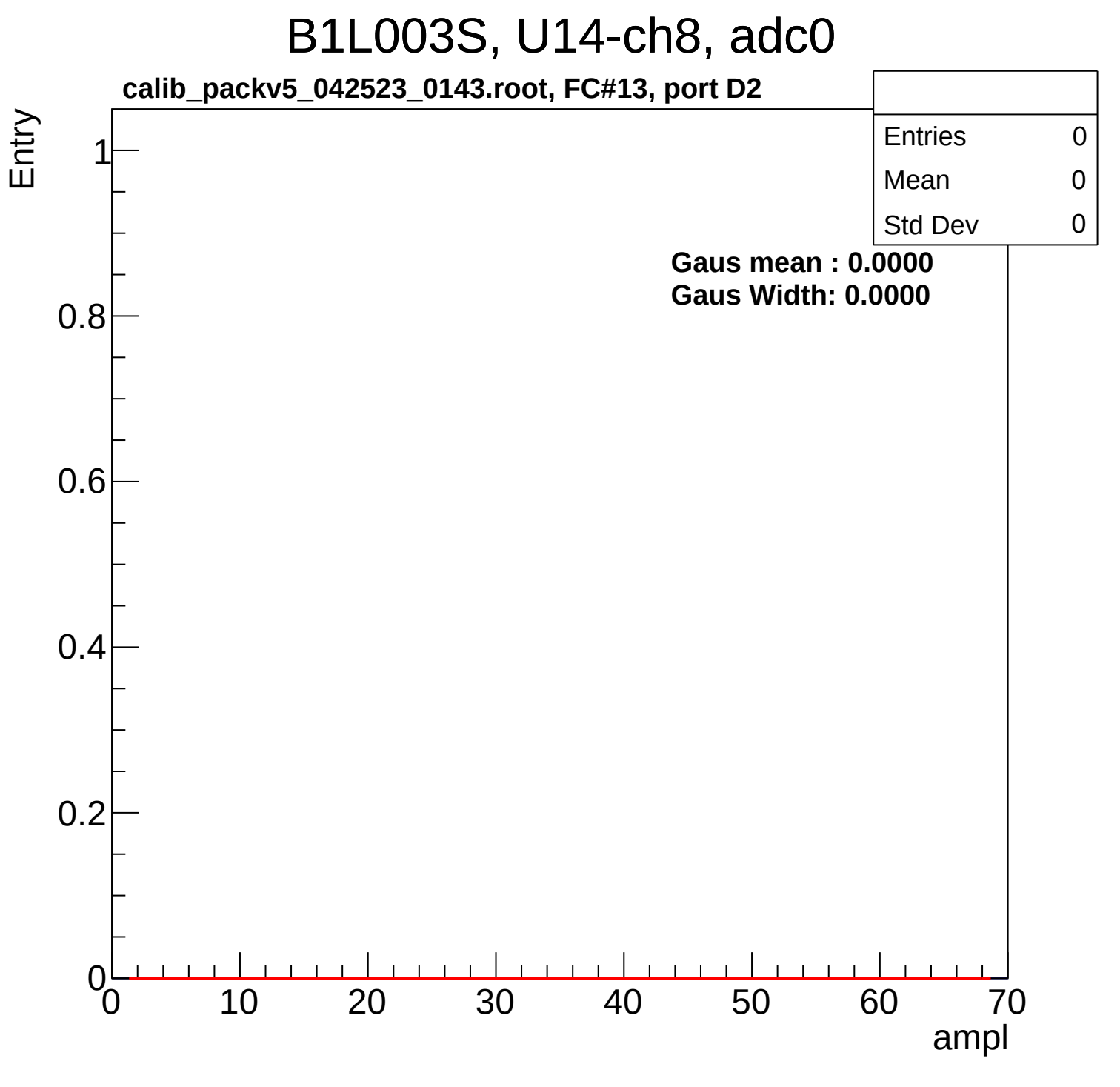

B1L003S, U14-ch7, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

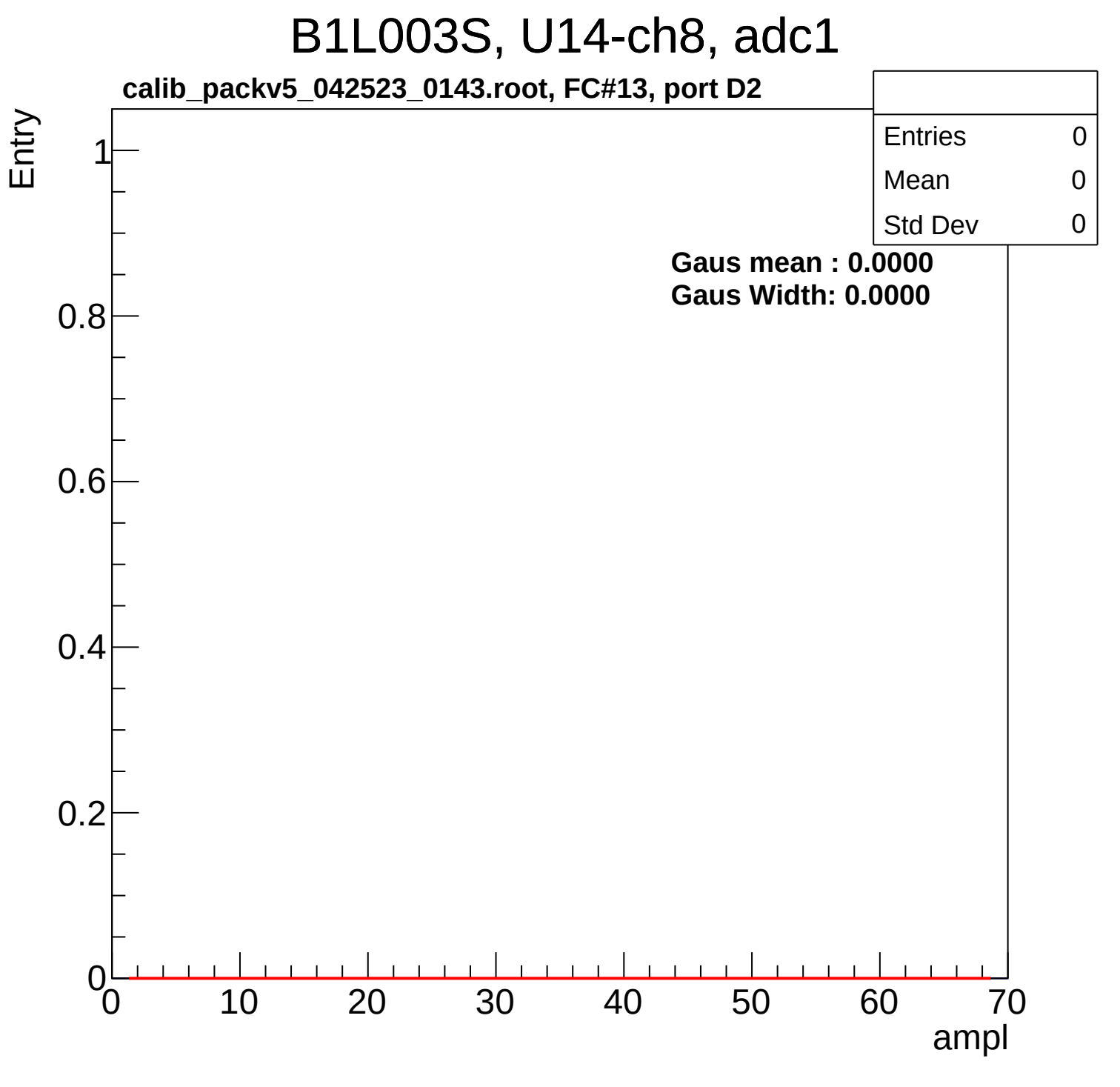

B1L003S, U14-ch7, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

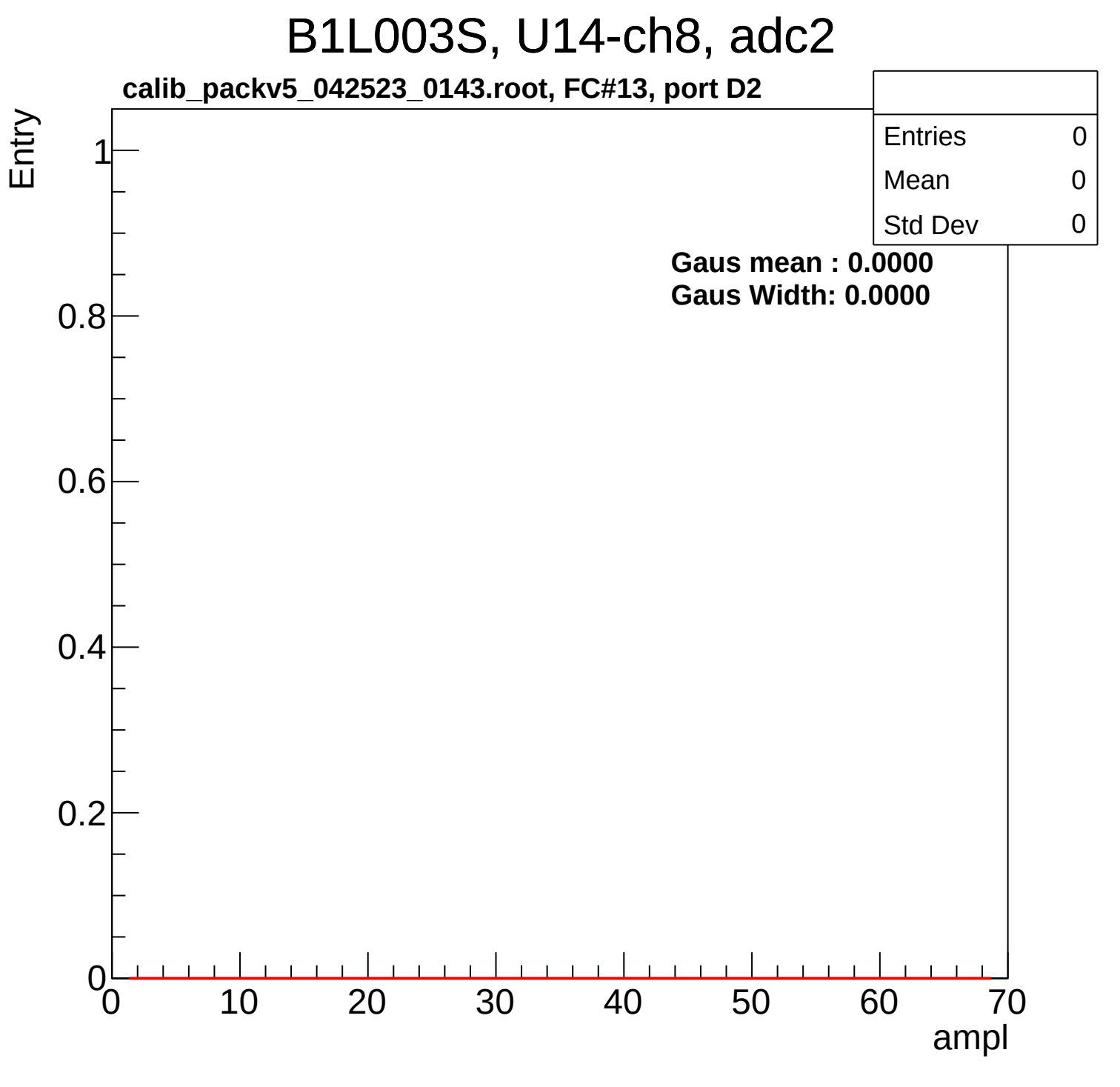

B1L003S, U14-ch7, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

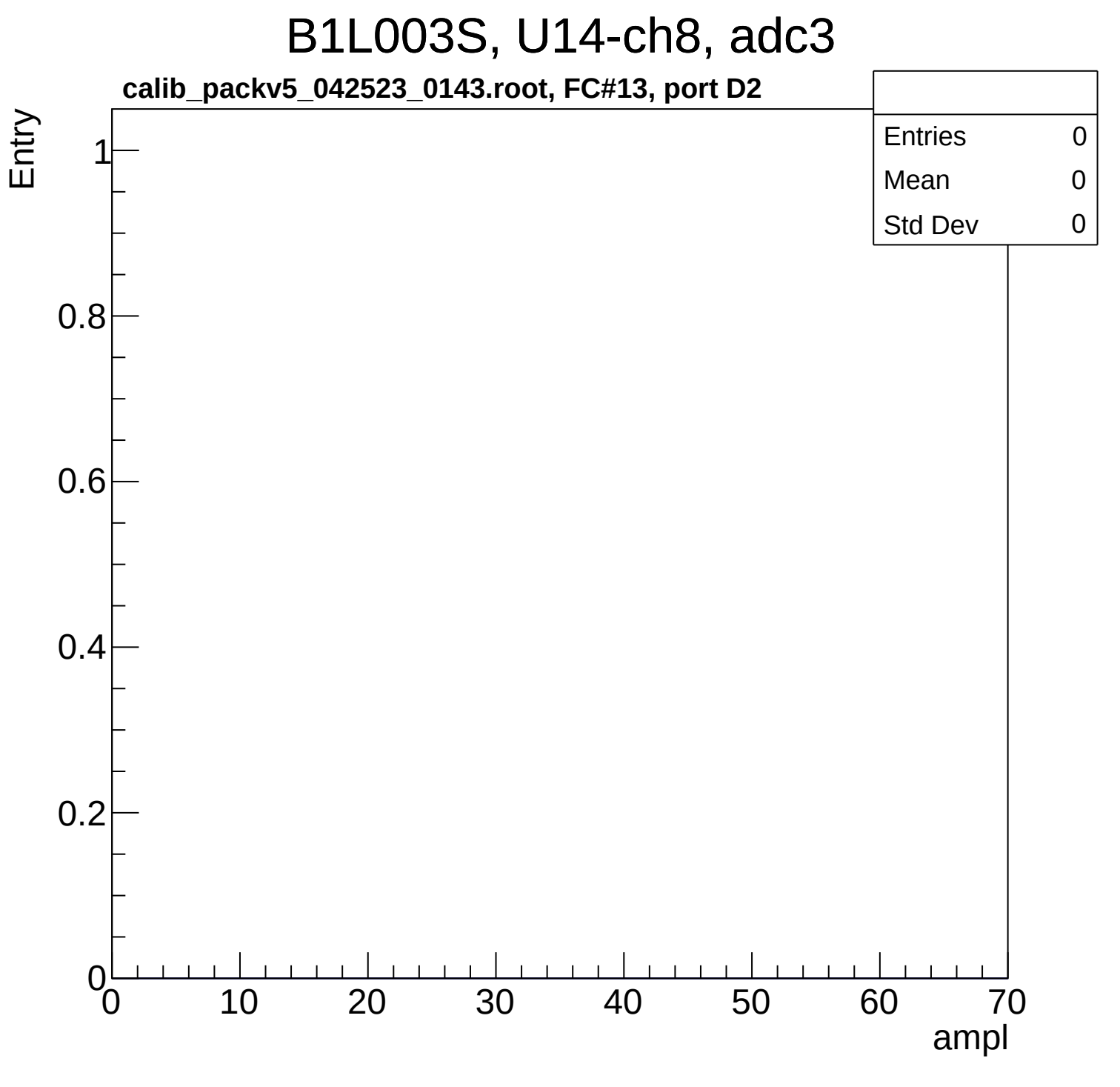

B1L003S, U14-ch7, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

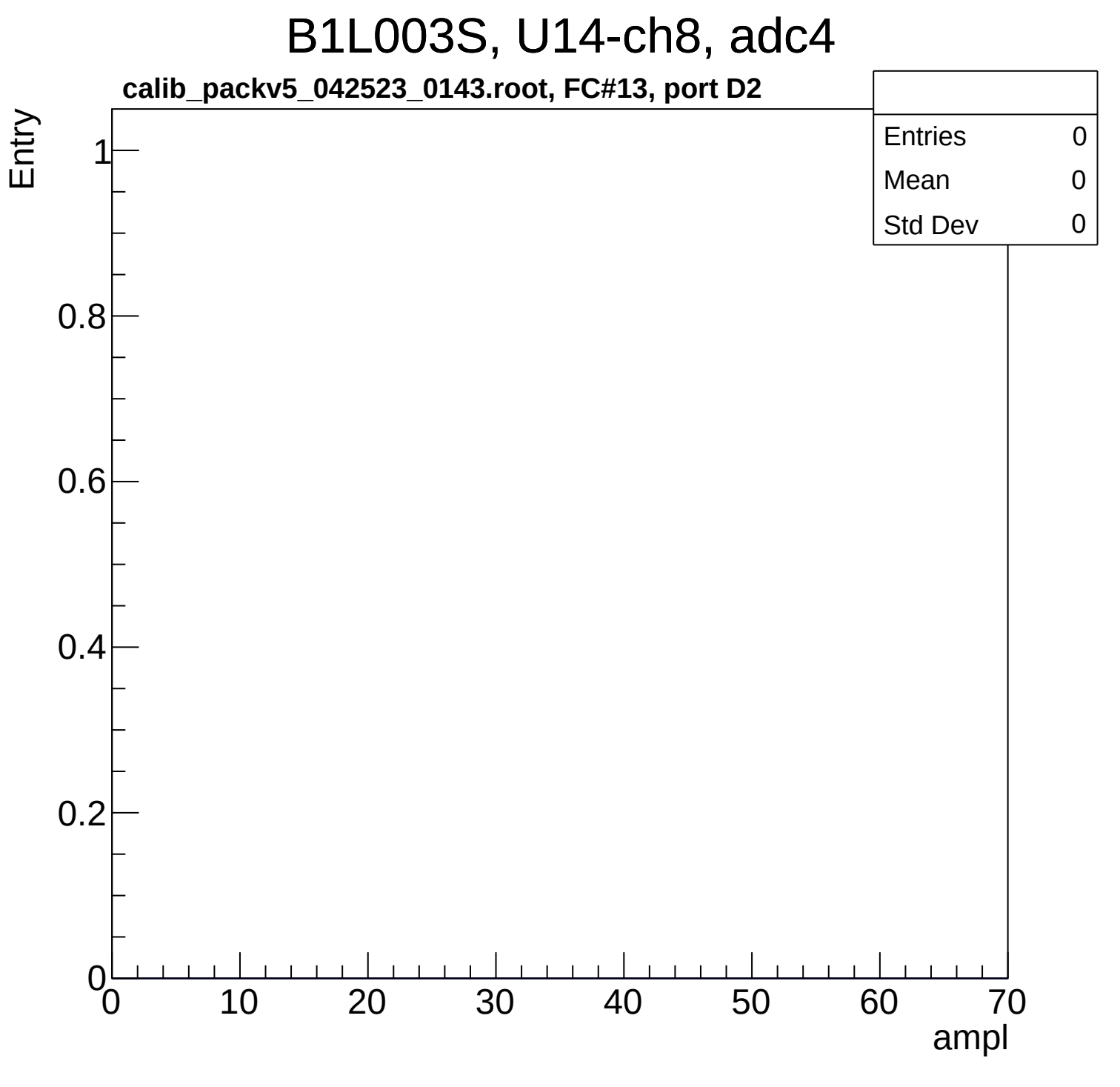

B1L003S, U14-ch7, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

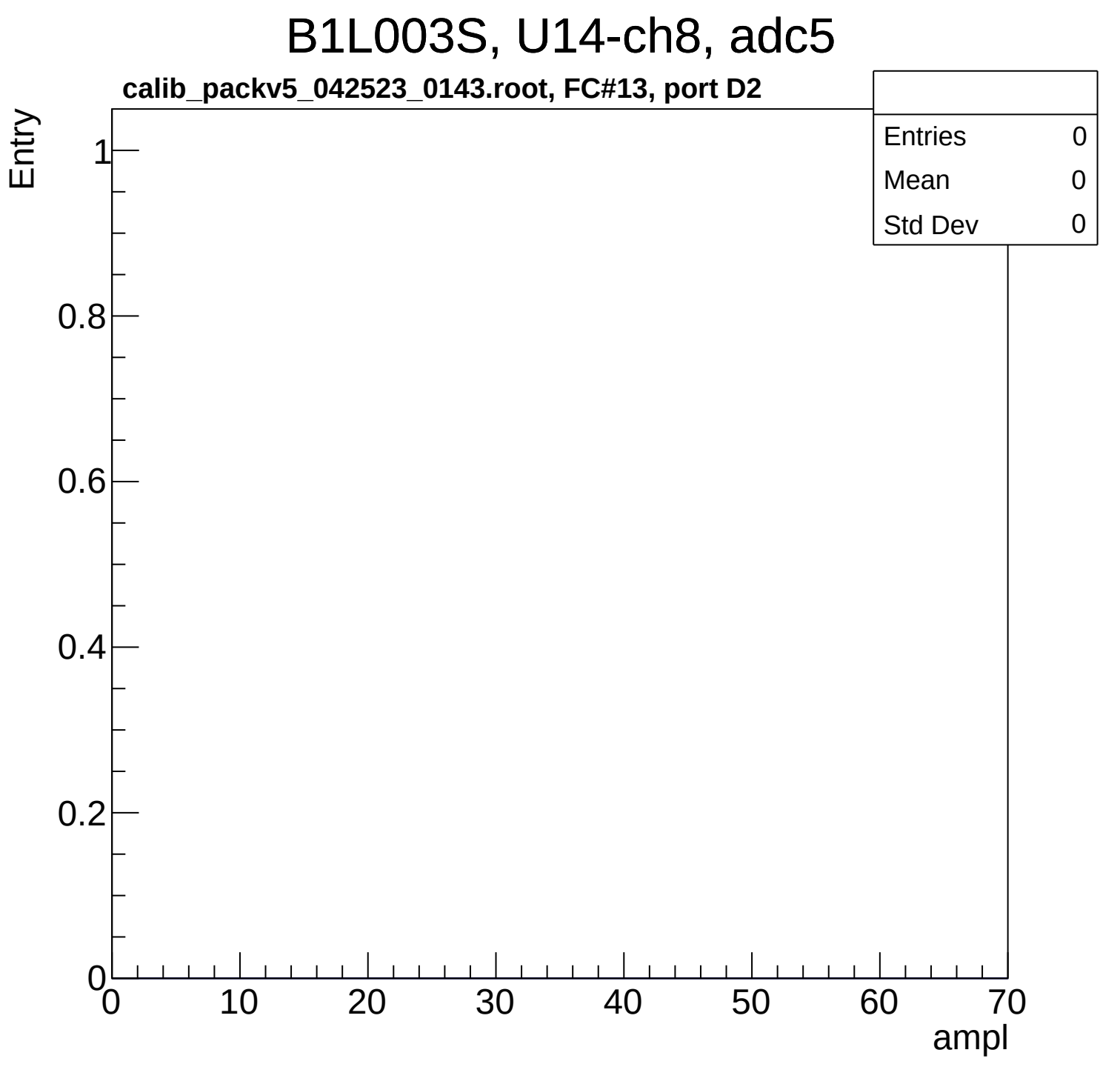

B1L003S, U14-ch8, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

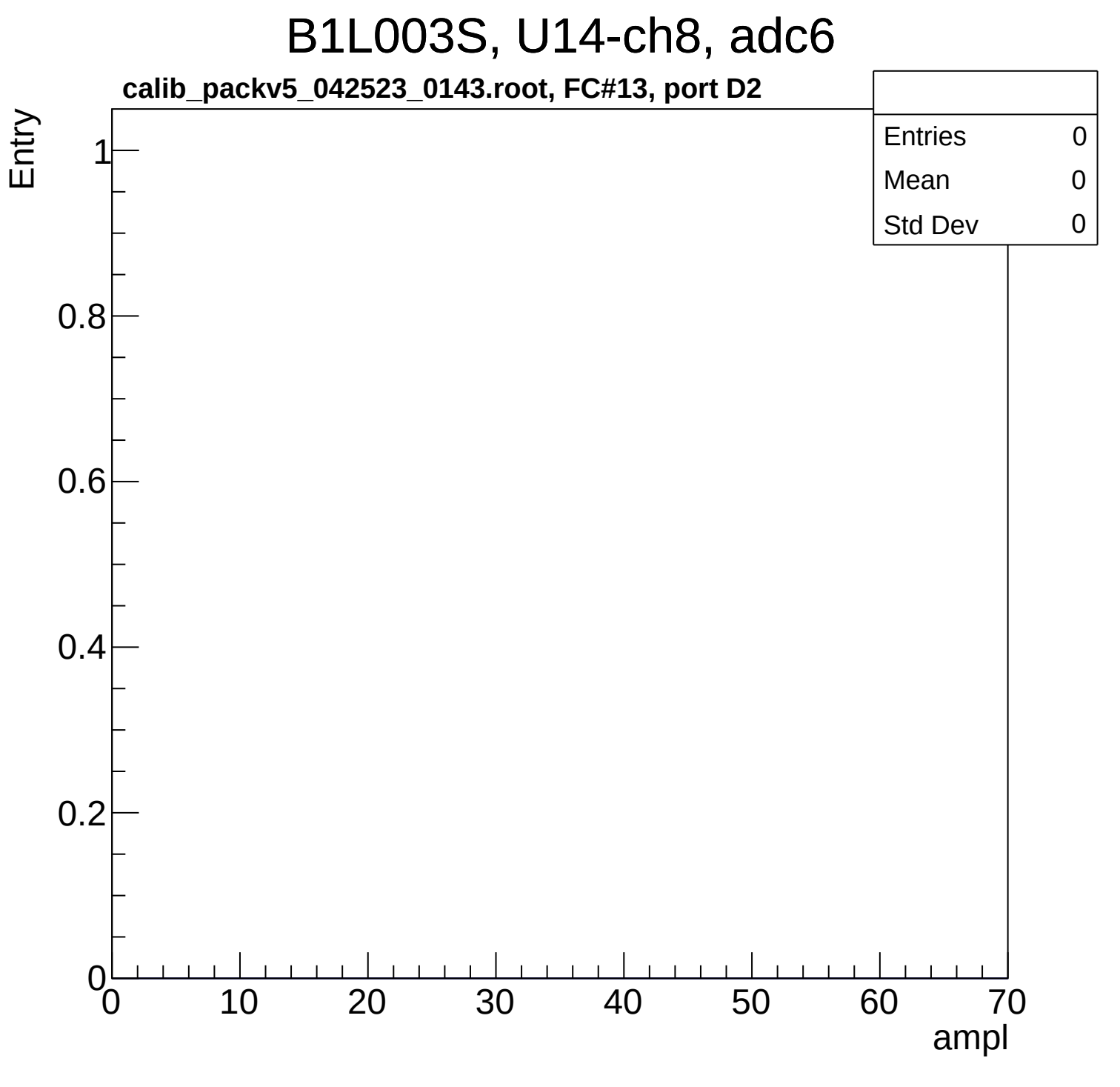

B1L003S, U14-ch8, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

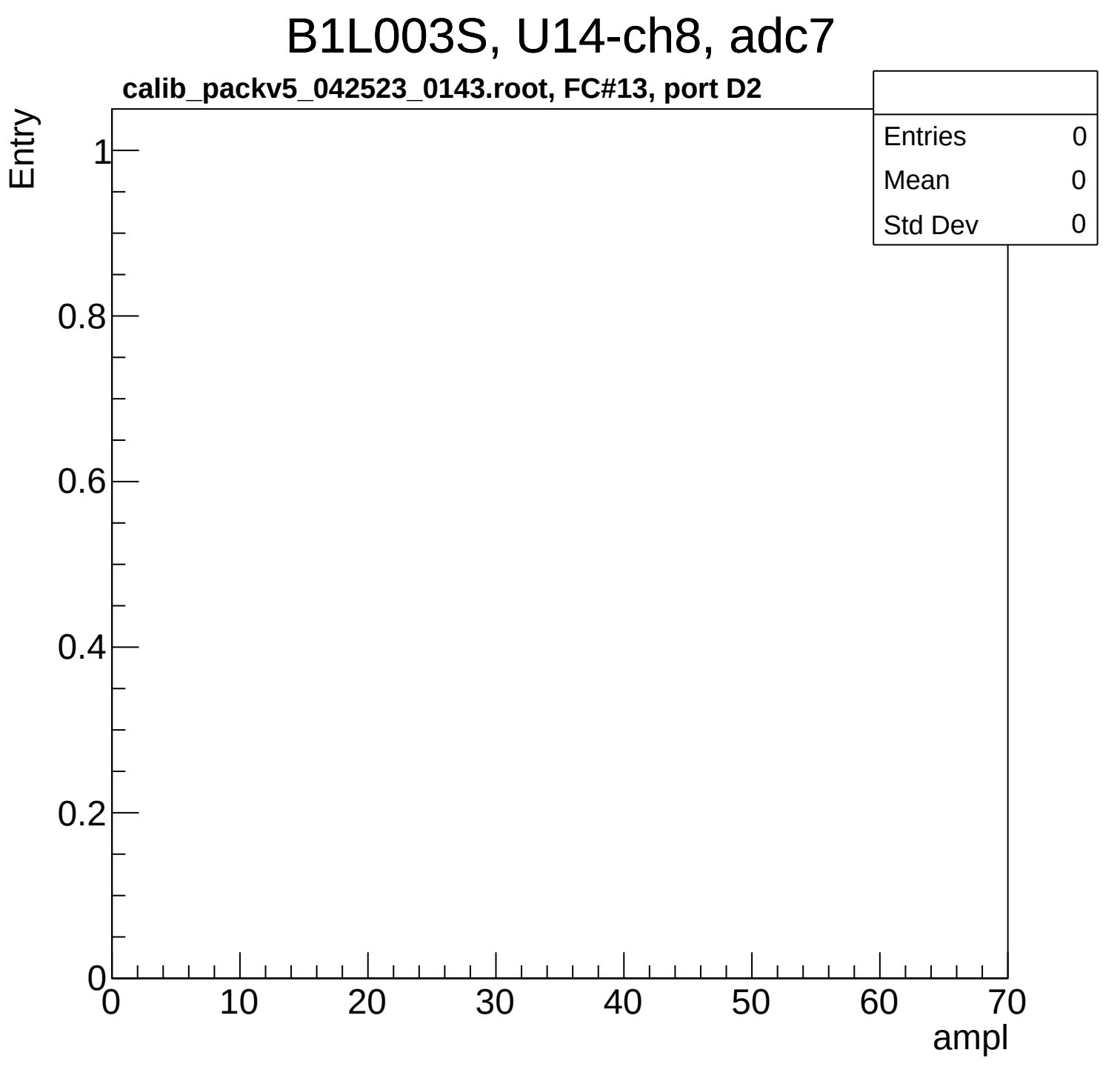

B1L003S, U14-ch8, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

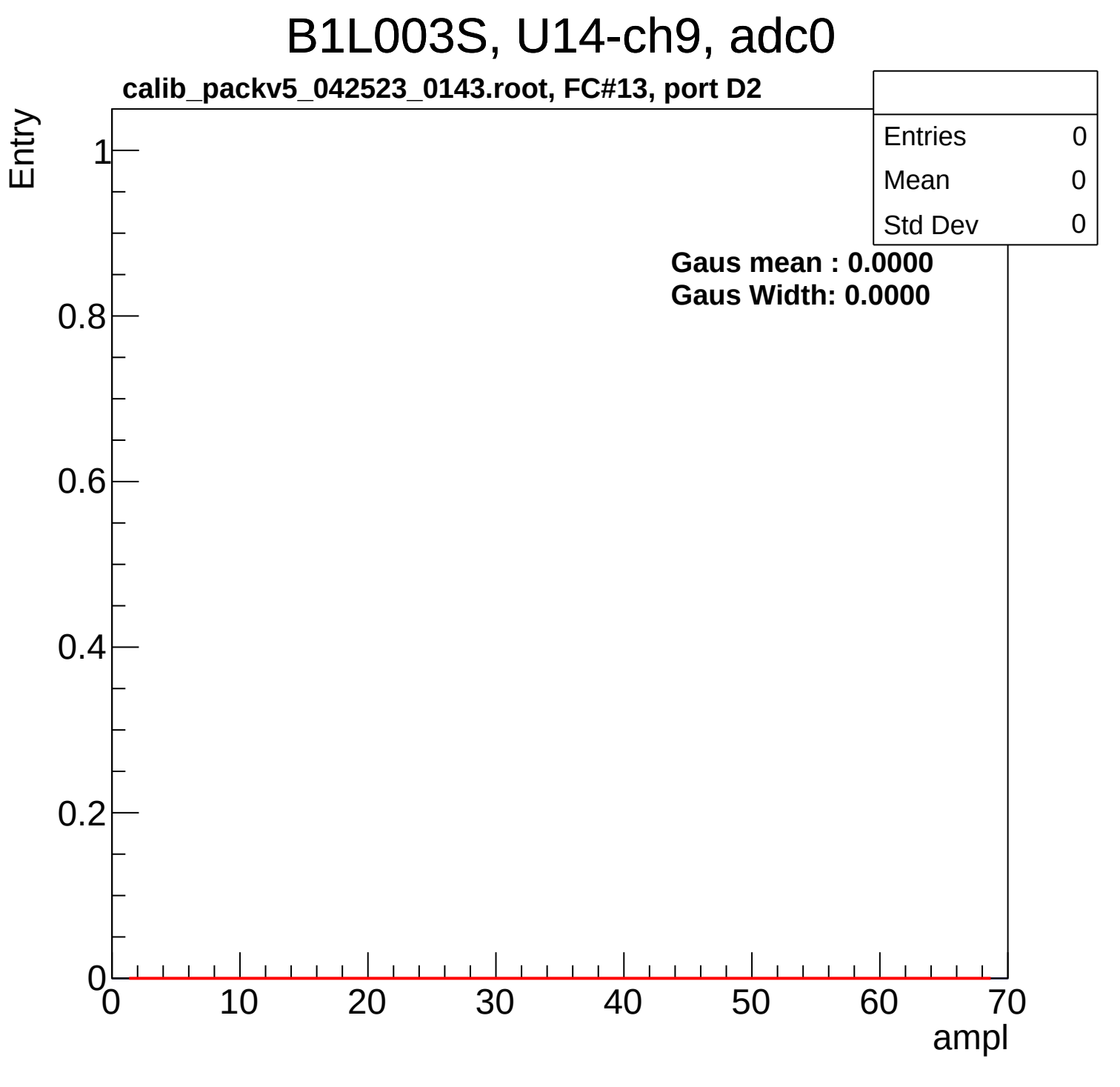

B1L003S, U14-ch8, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

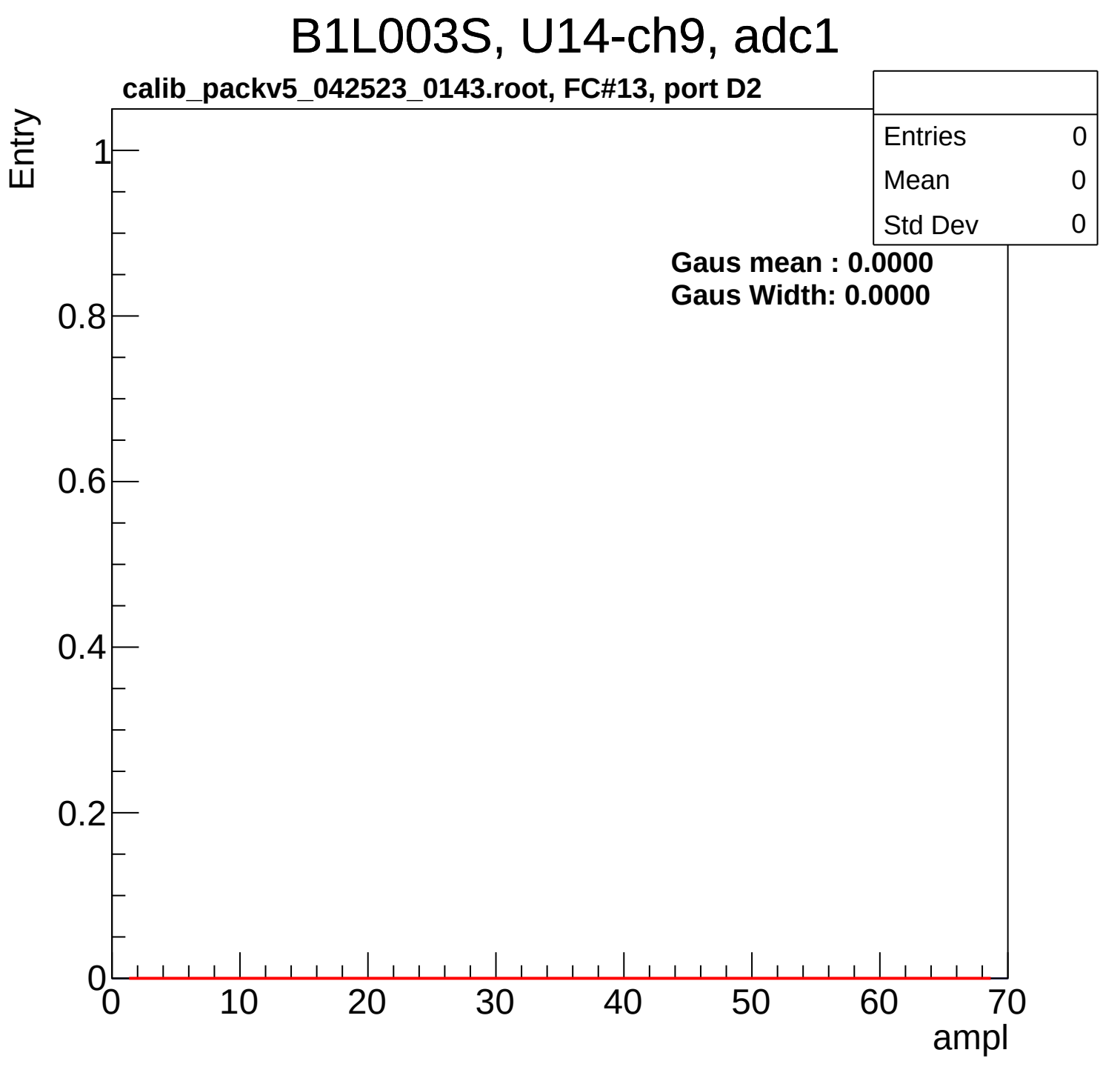

B1L003S, U14-ch8, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

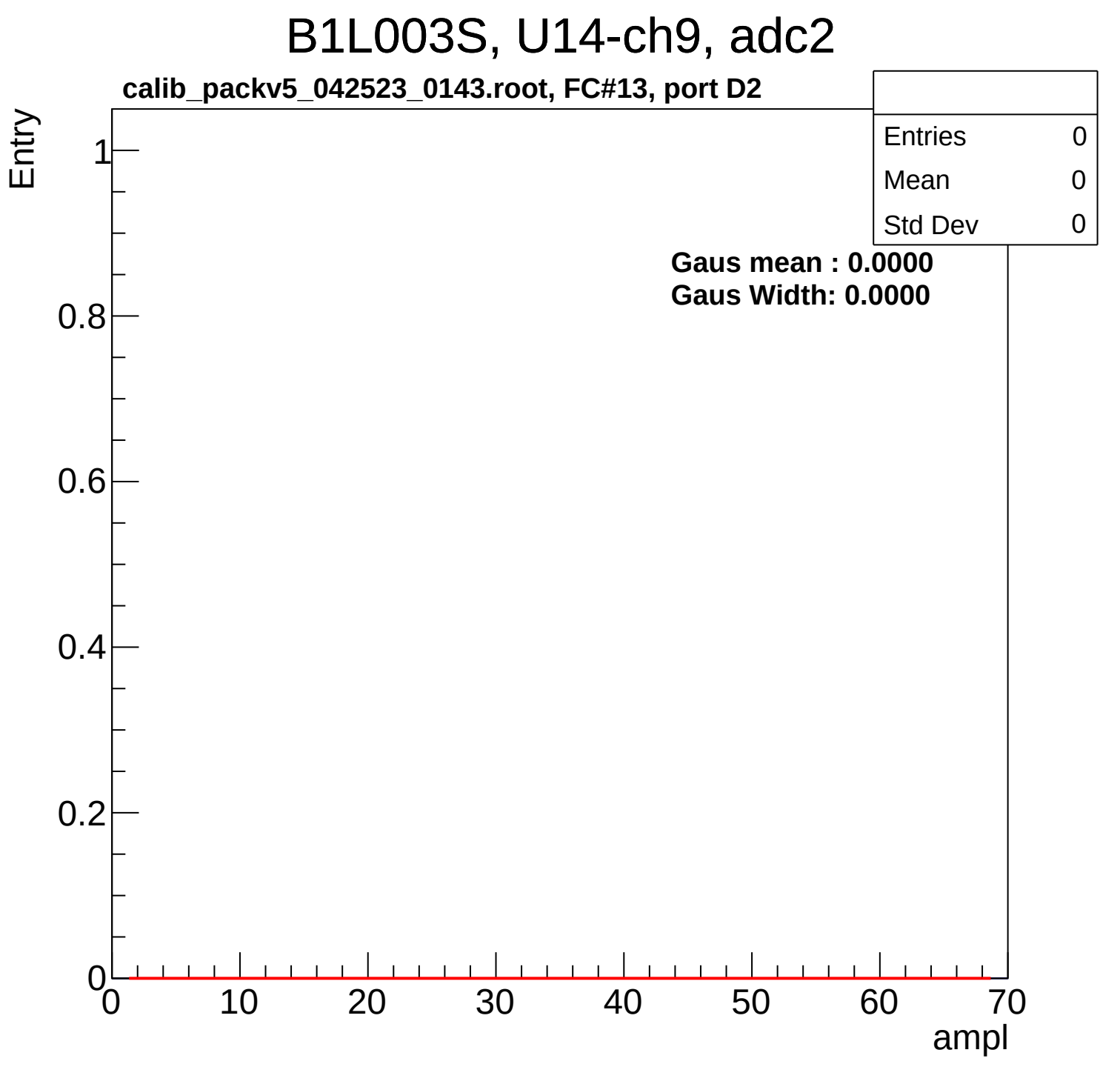

B1L003S, U14-ch8, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

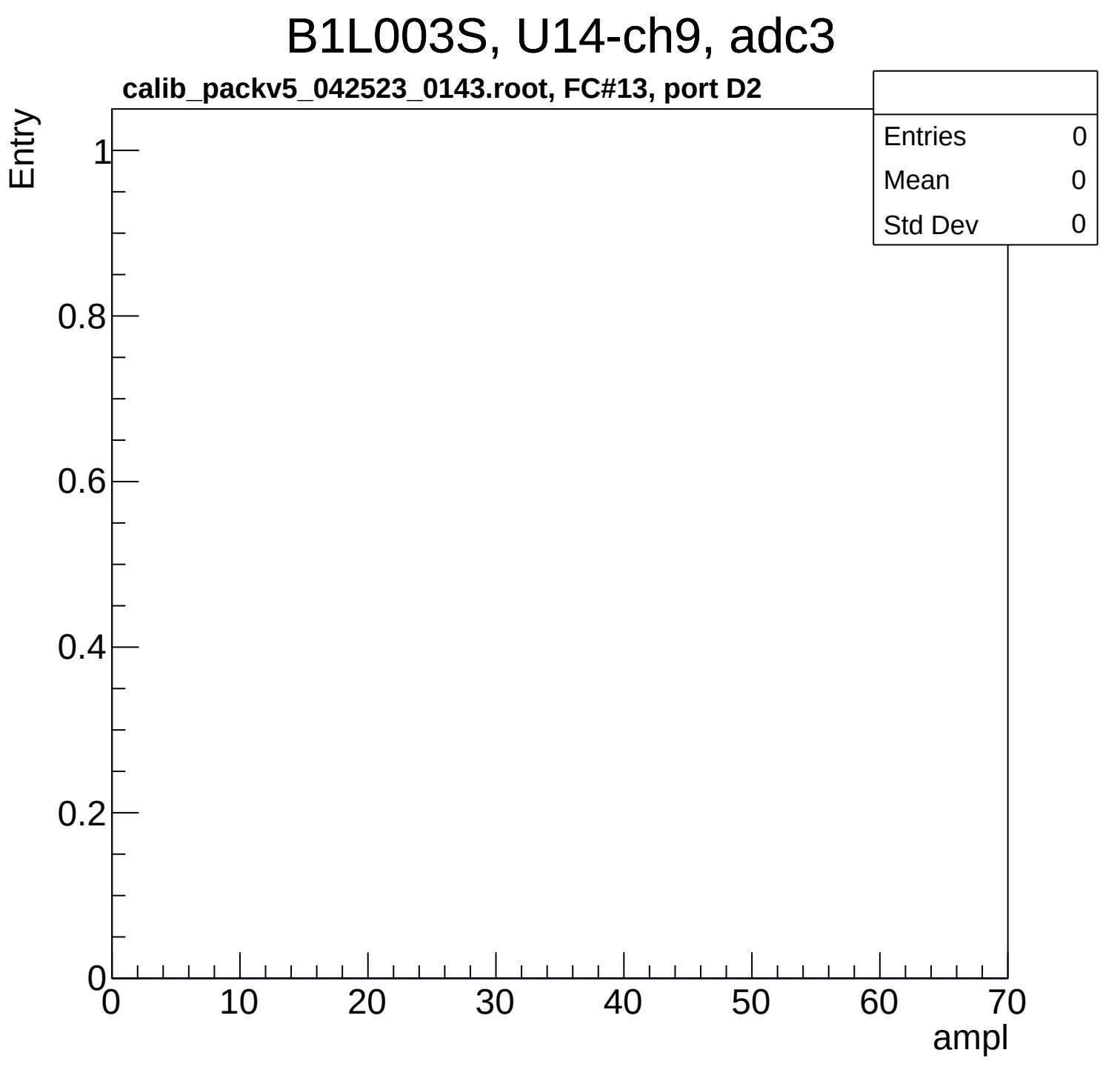

B1L003S, U14-ch8, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

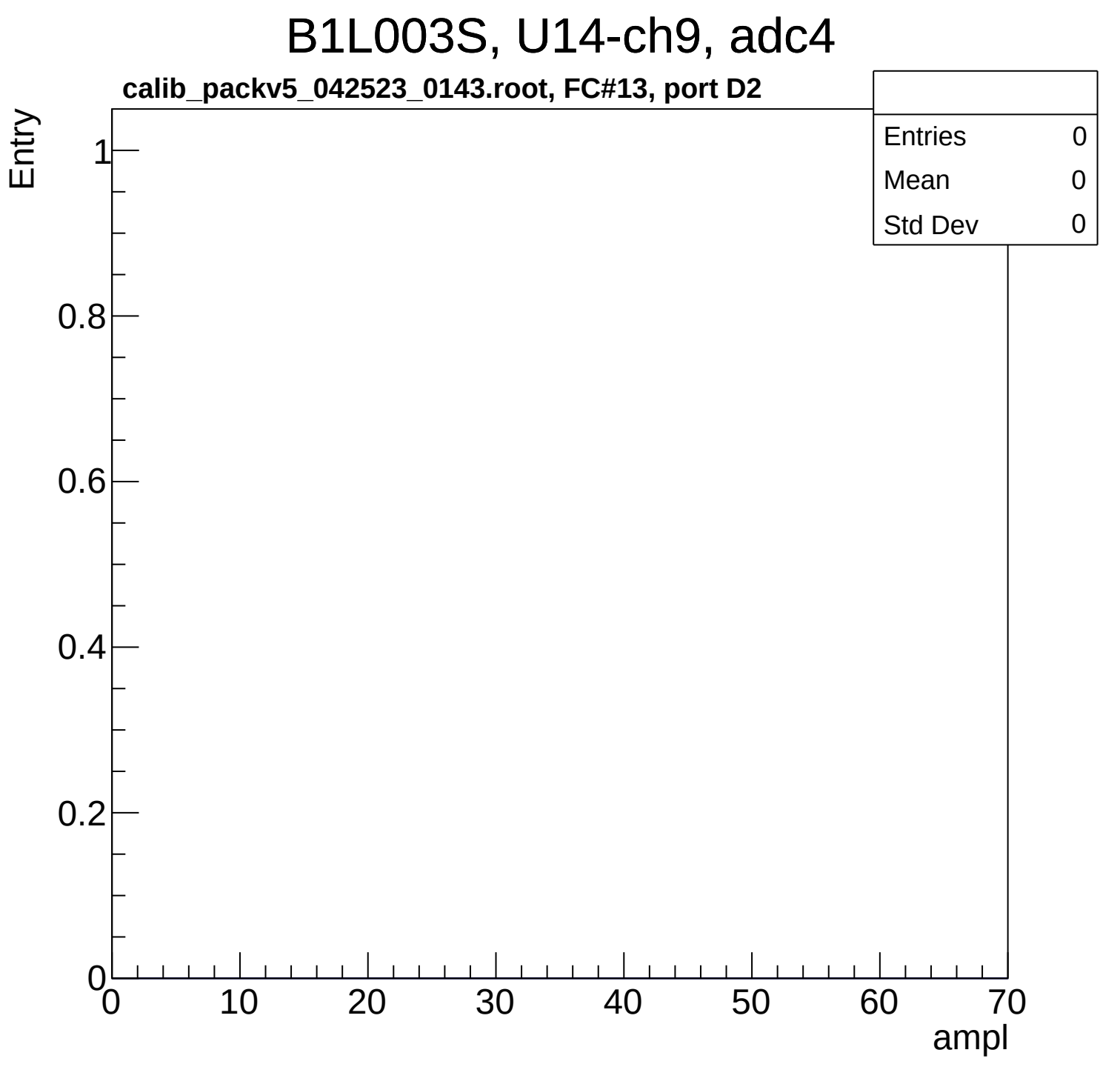

B1L003S, U14-ch8, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

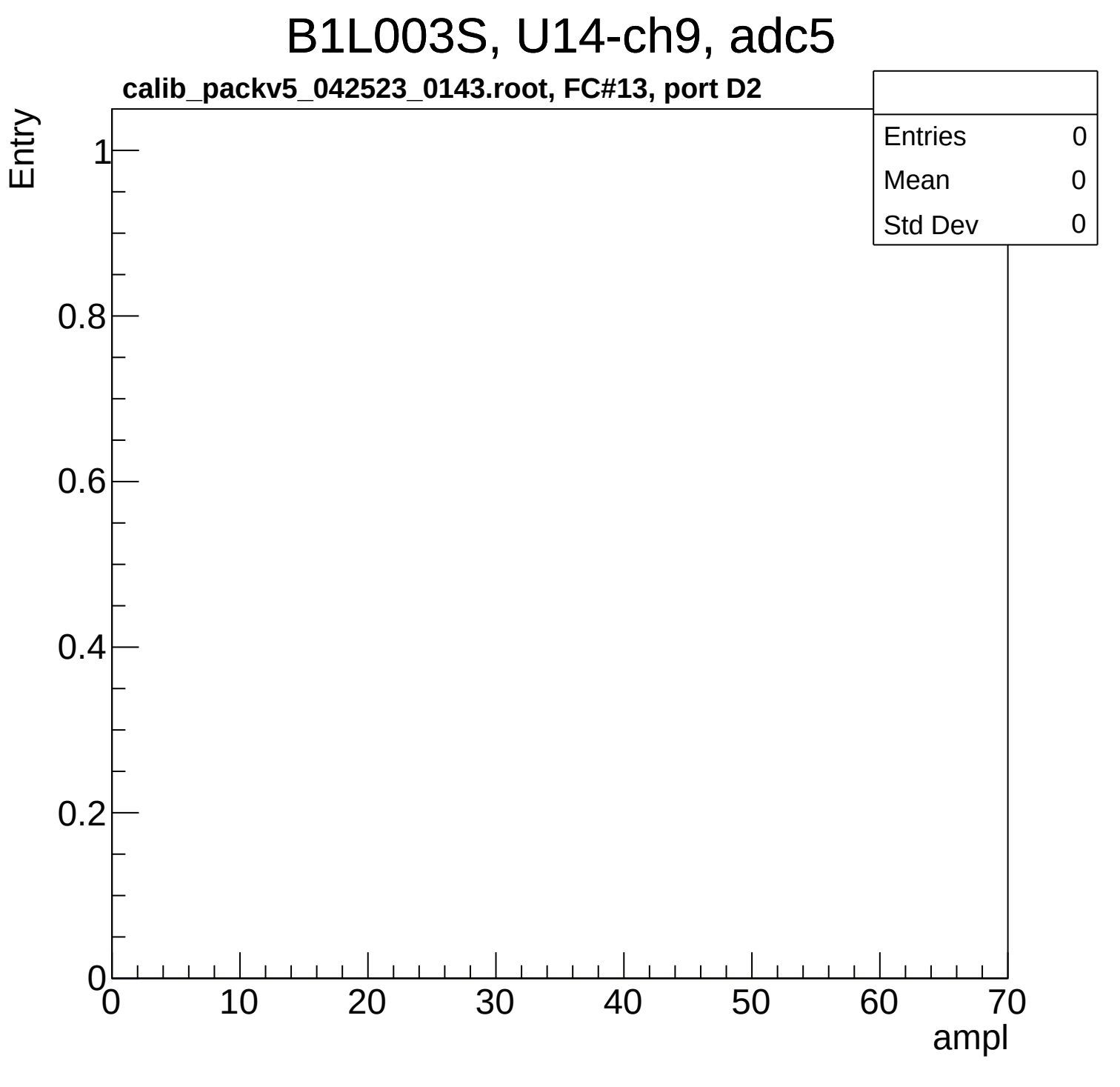

B1L003S, U14-ch9, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

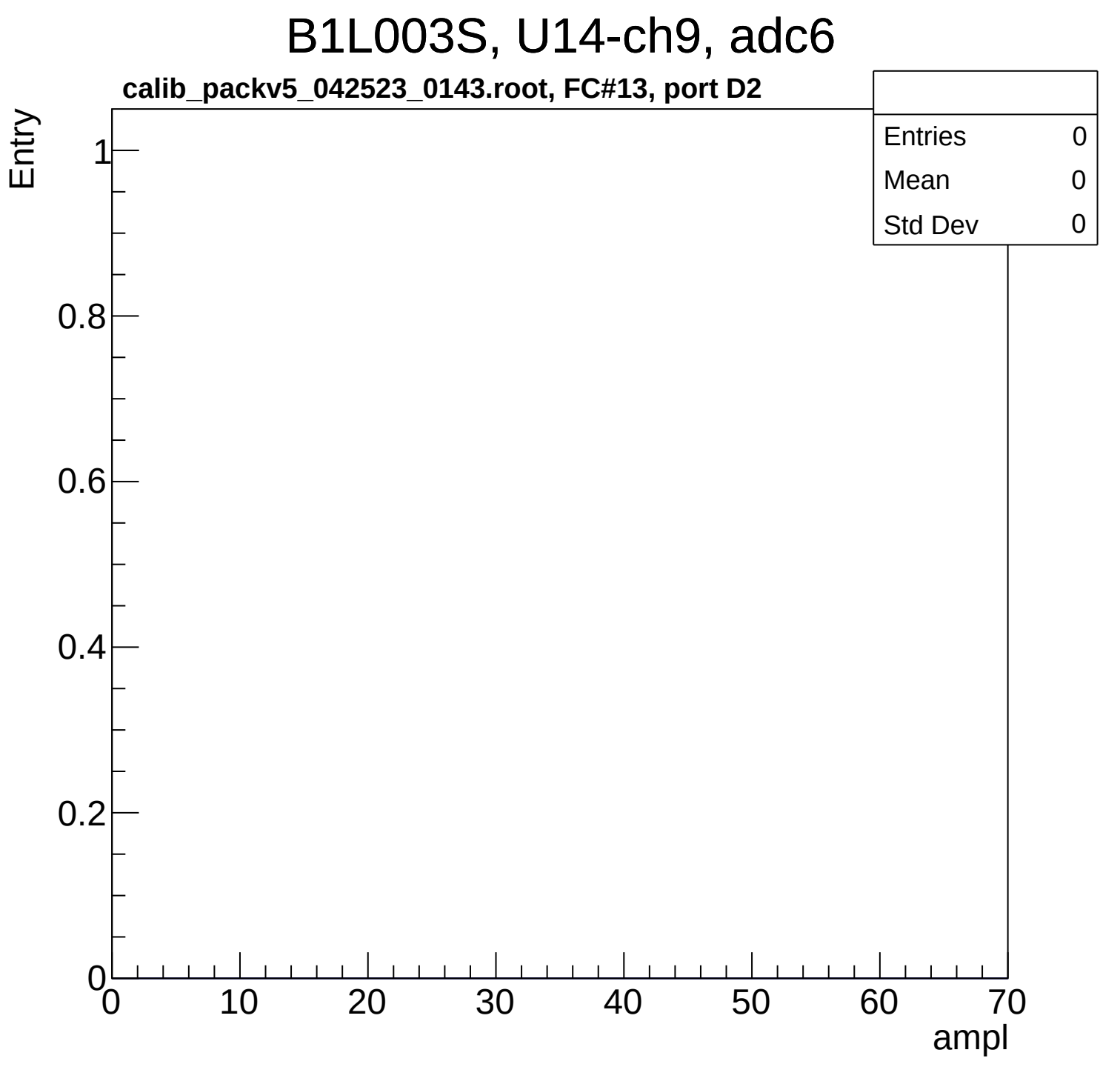

B1L003S, U14-ch9, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

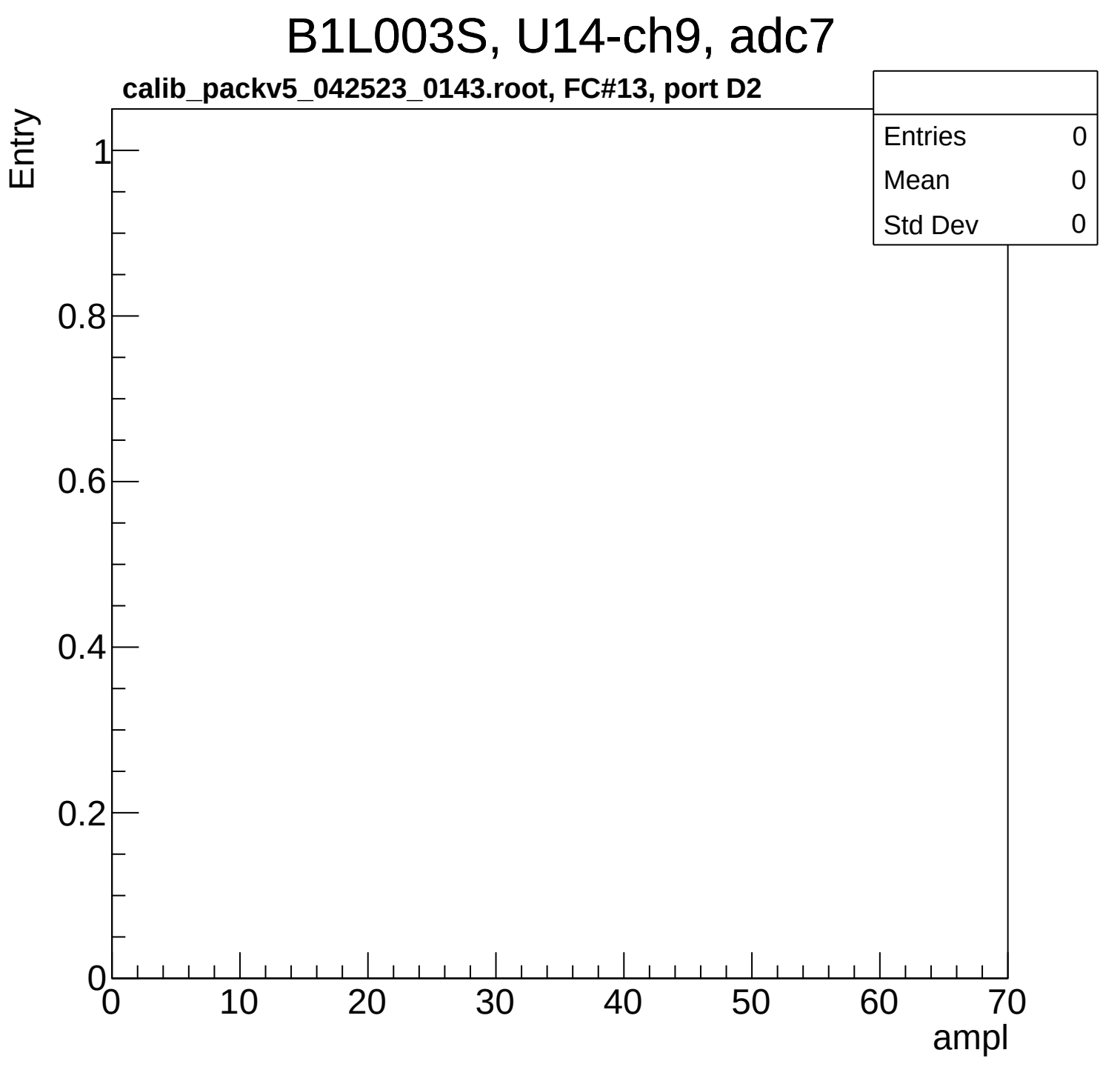

B1L003S, U14-ch9, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

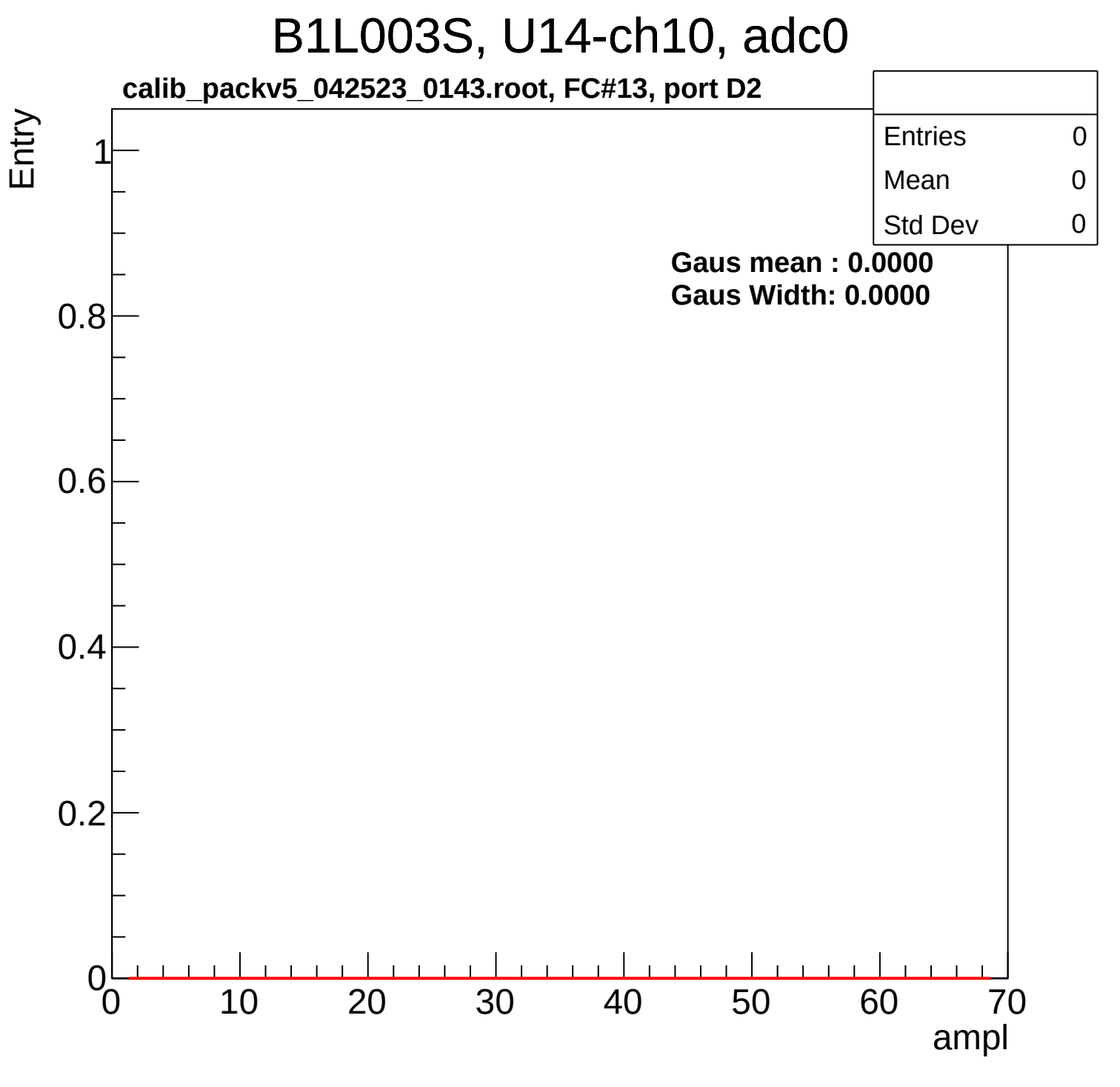

B1L003S, U14-ch9, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

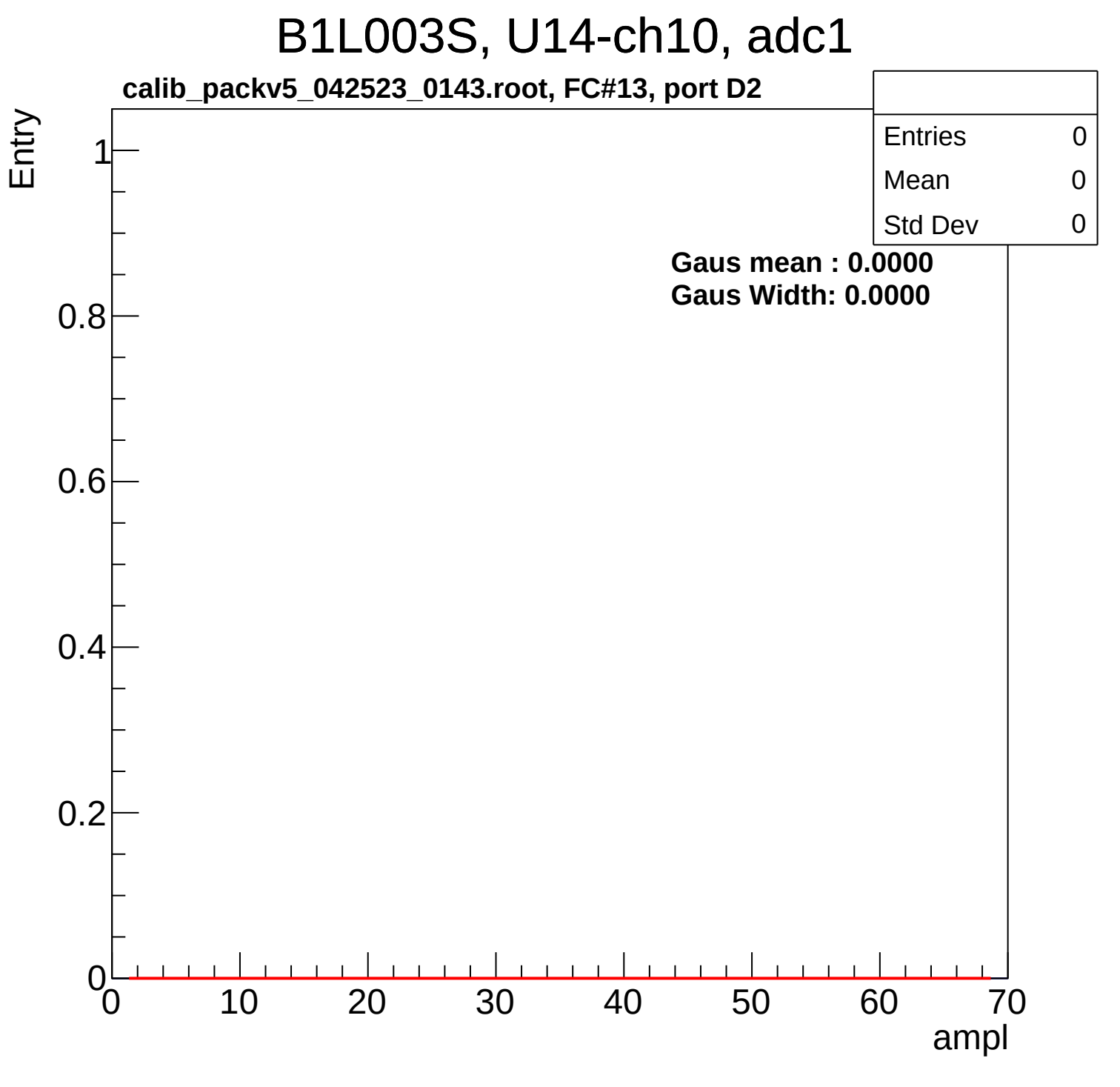

B1L003S, U14-ch9, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

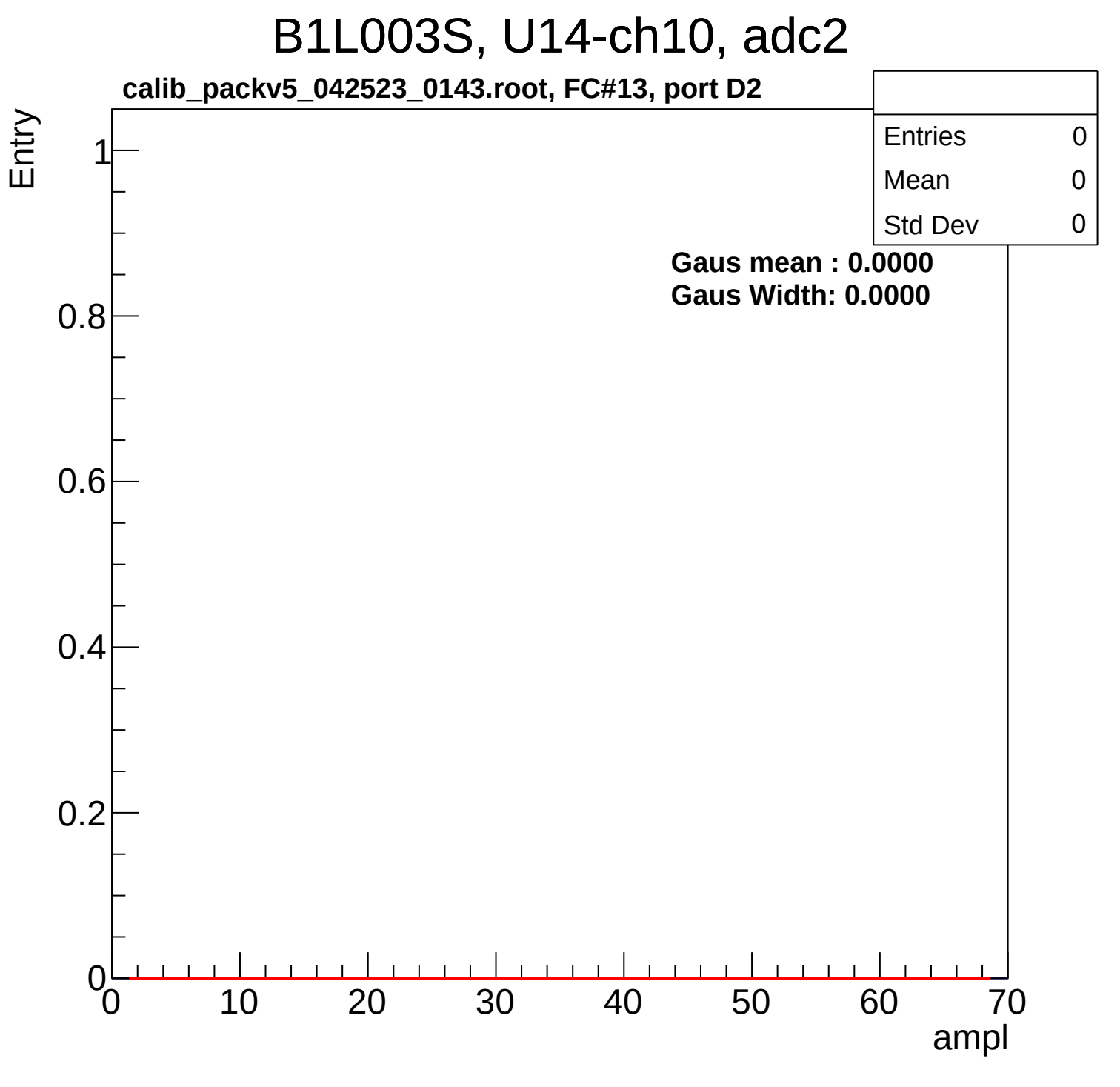

B1L003S, U14-ch9, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

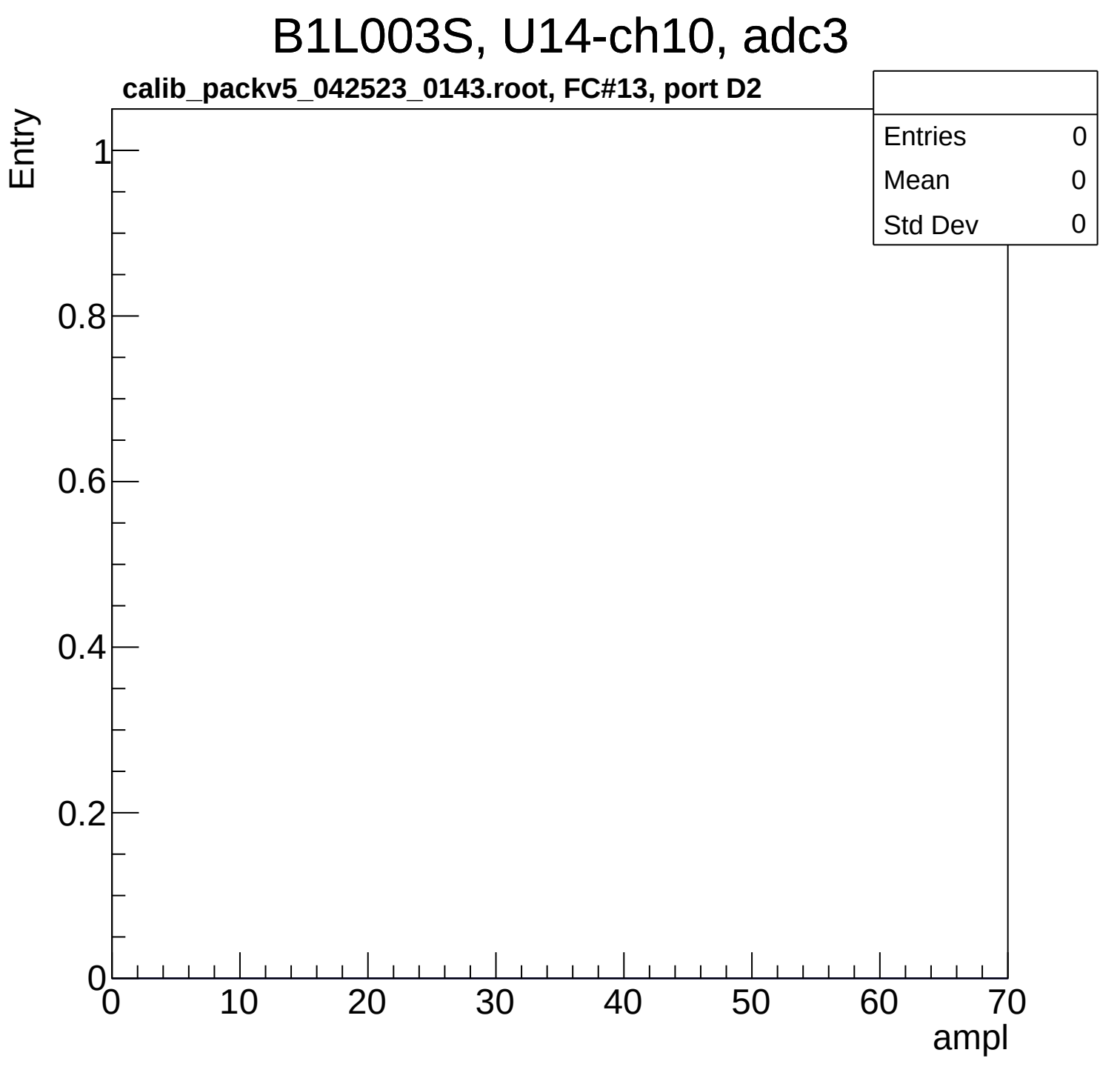

B1L003S, U14-ch9, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

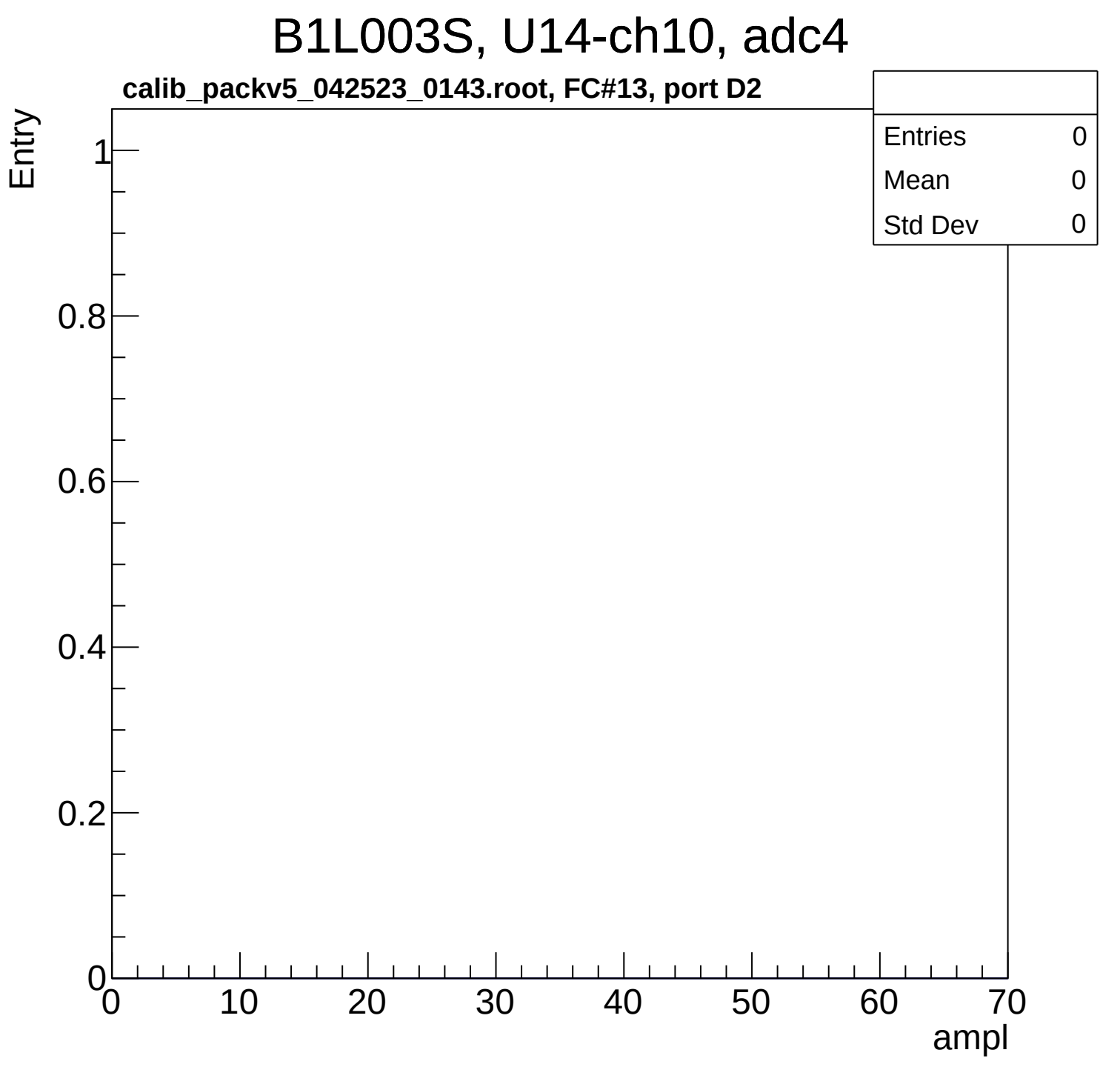

B1L003S, U14-ch9, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

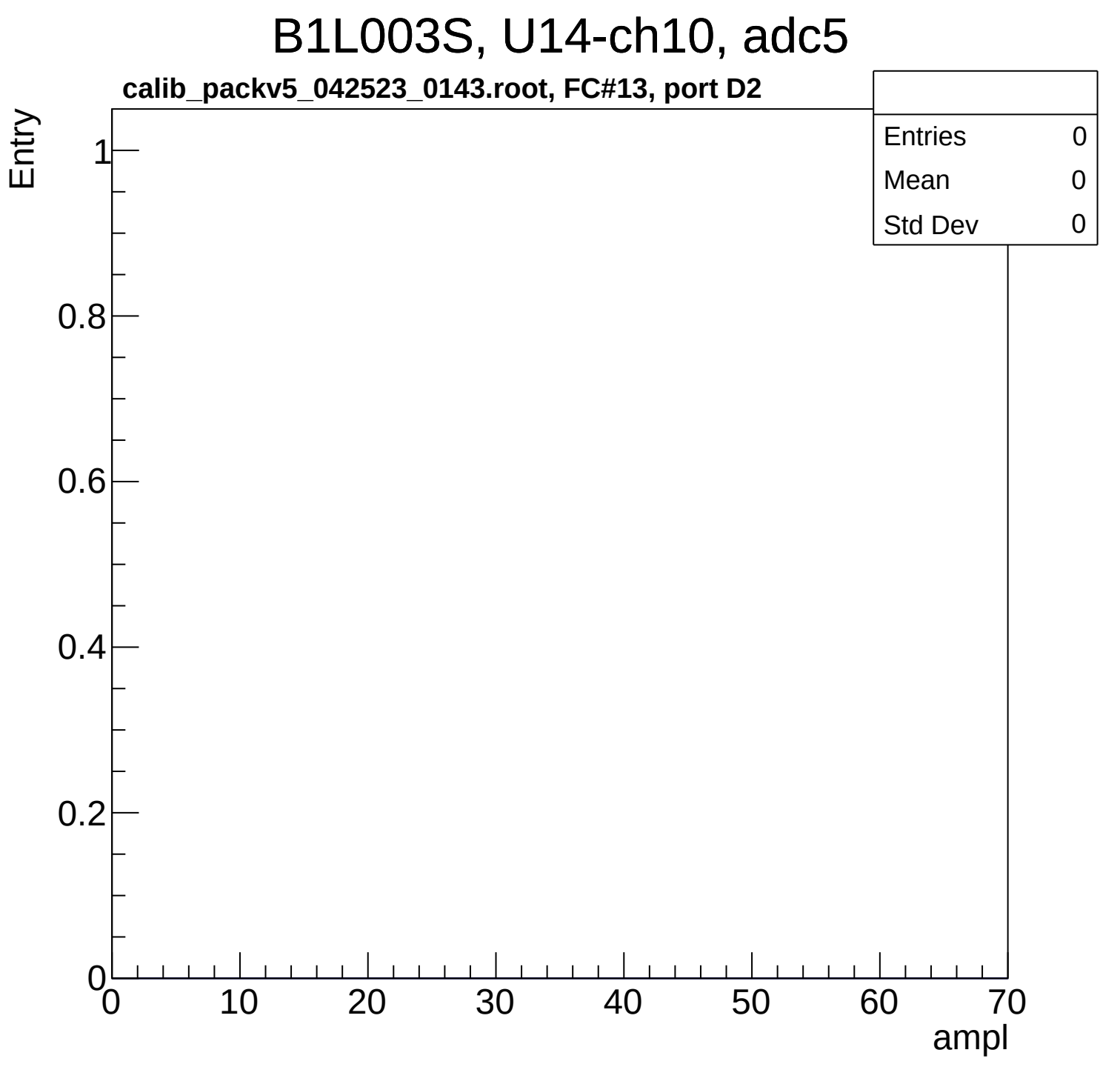

B1L003S, U14-ch10, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

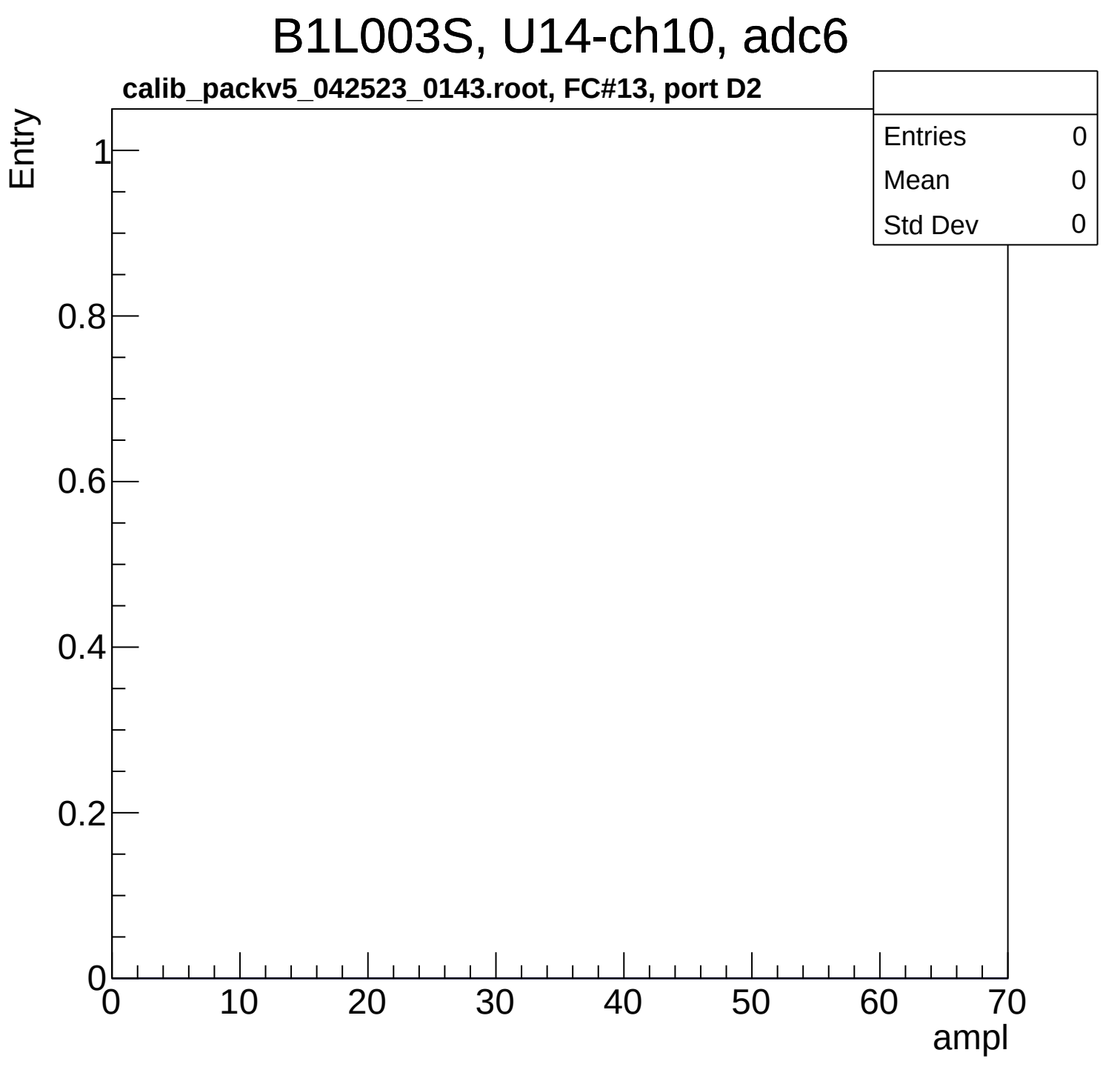

B1L003S, U14-ch10, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

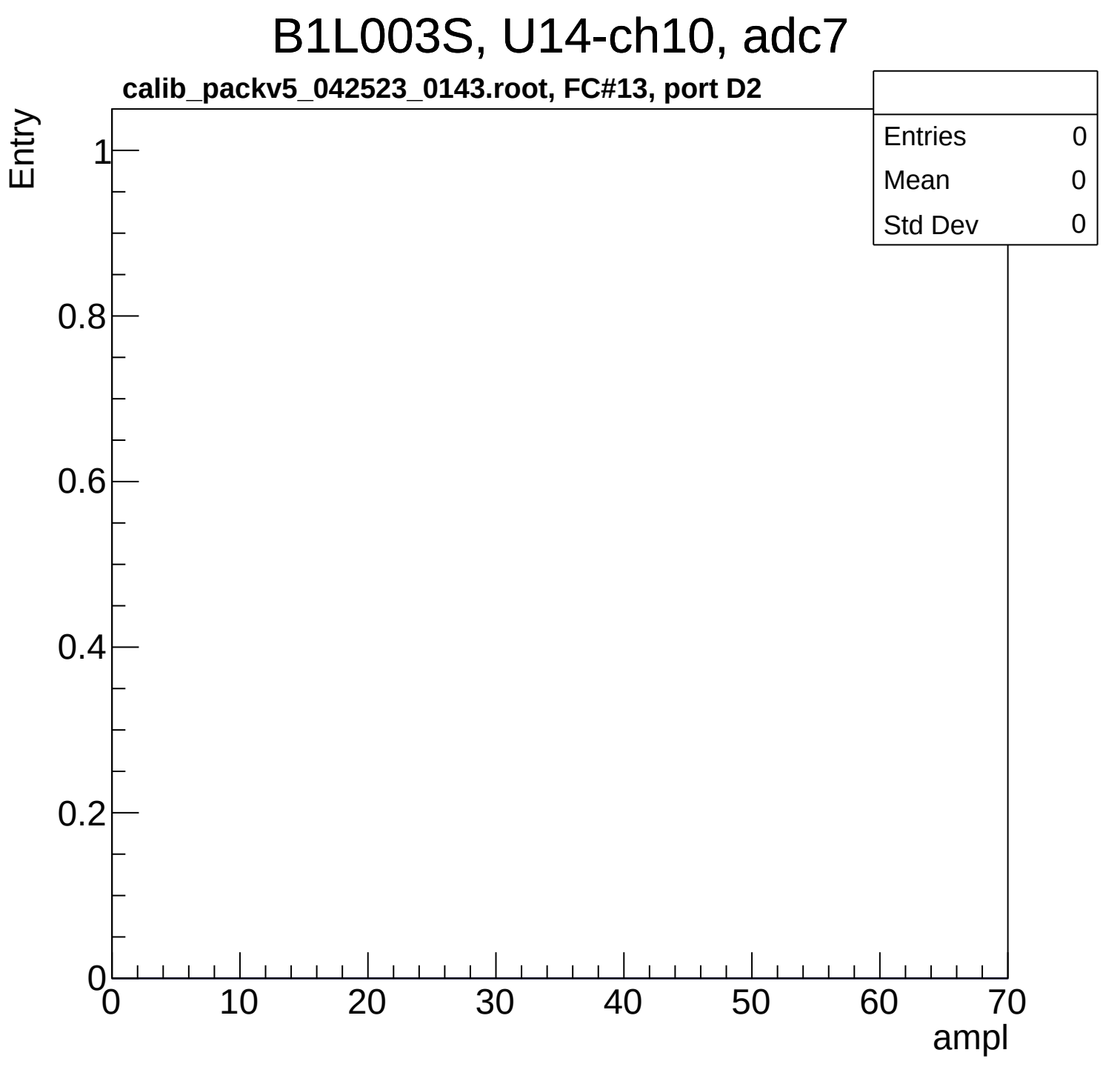

B1L003S, U14-ch10, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

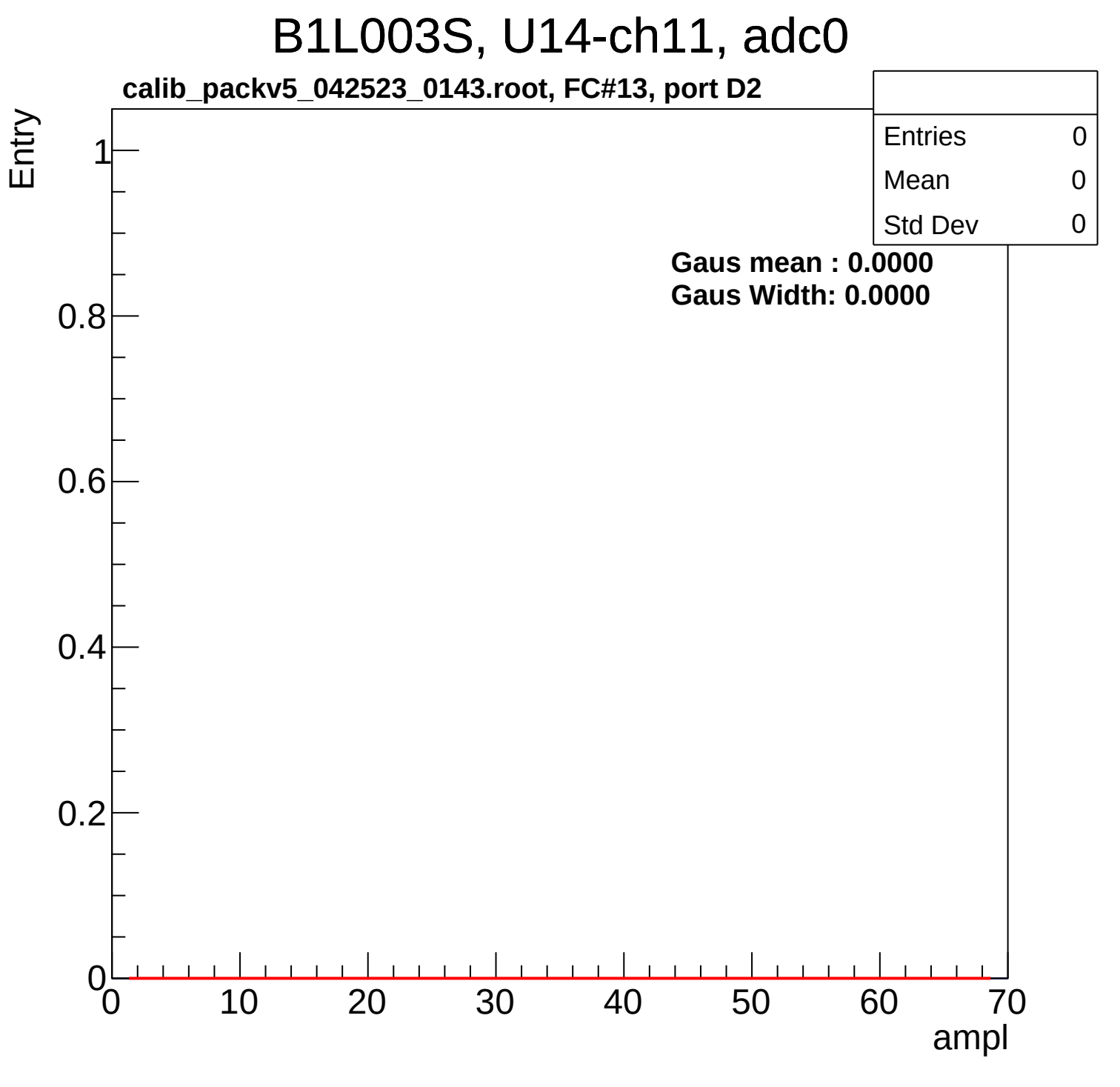

B1L003S, U14-ch10, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

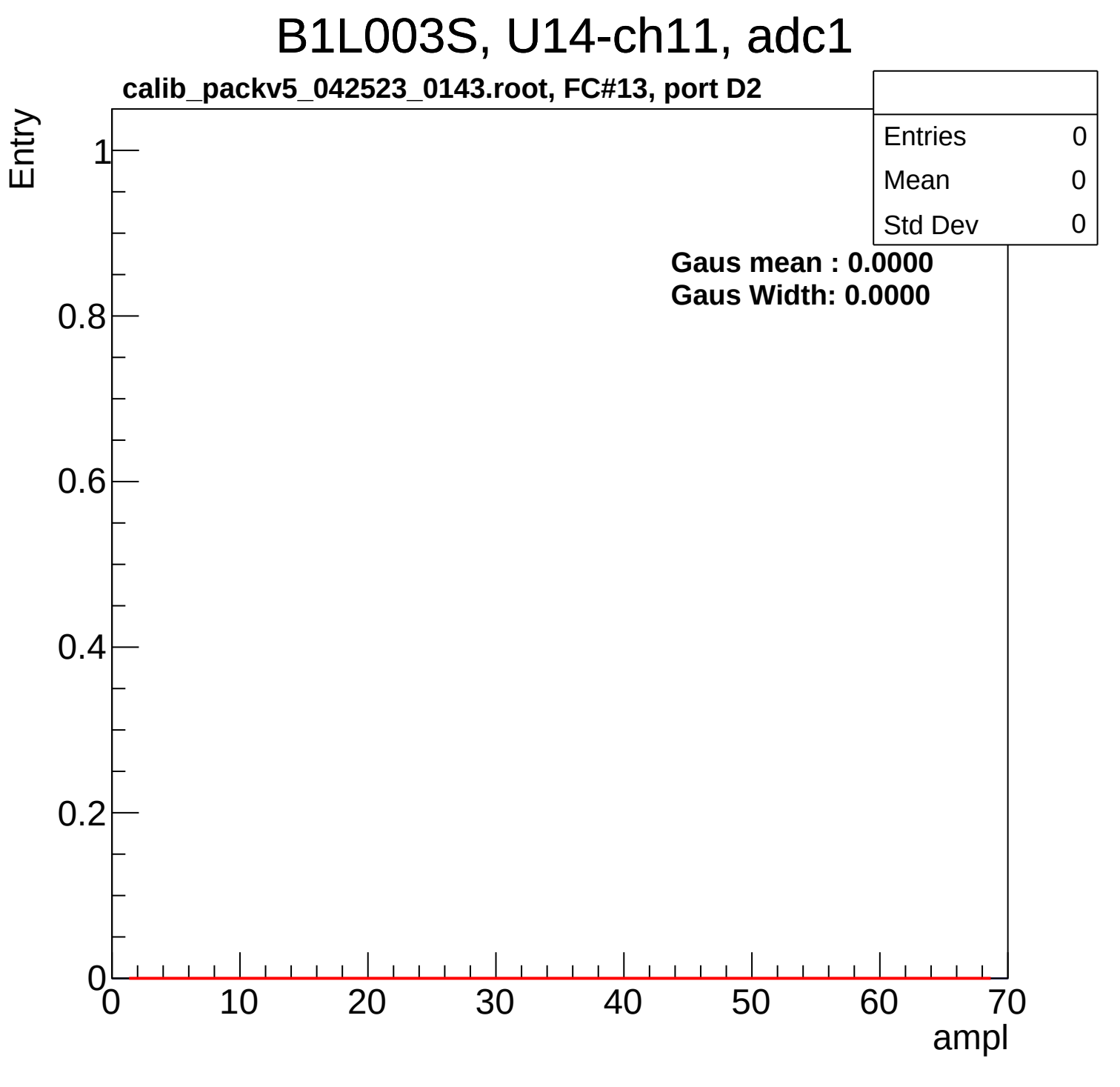

B1L003S, U14-ch10, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

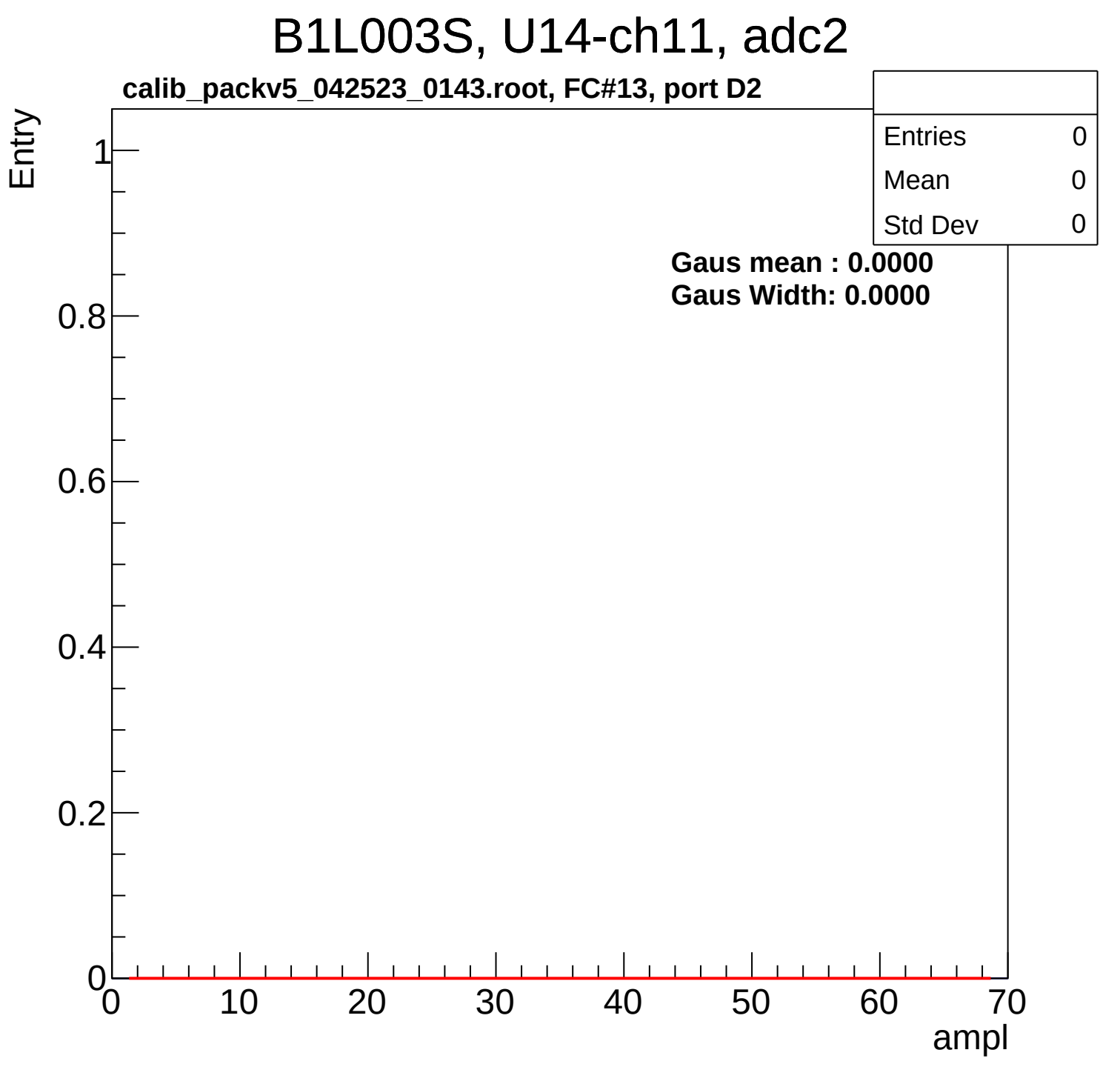

B1L003S, U14-ch10, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

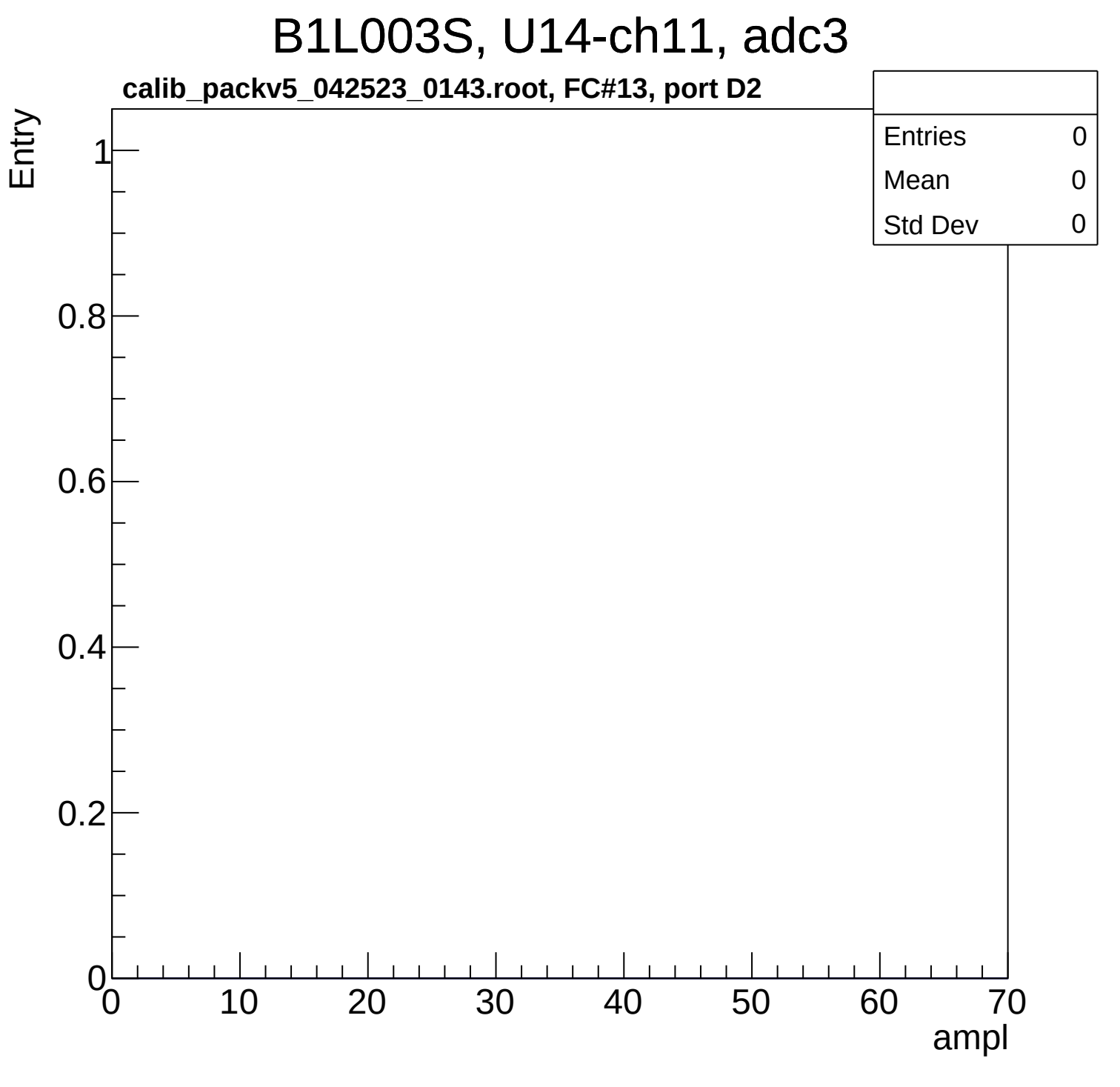

B1L003S, U14-ch10, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

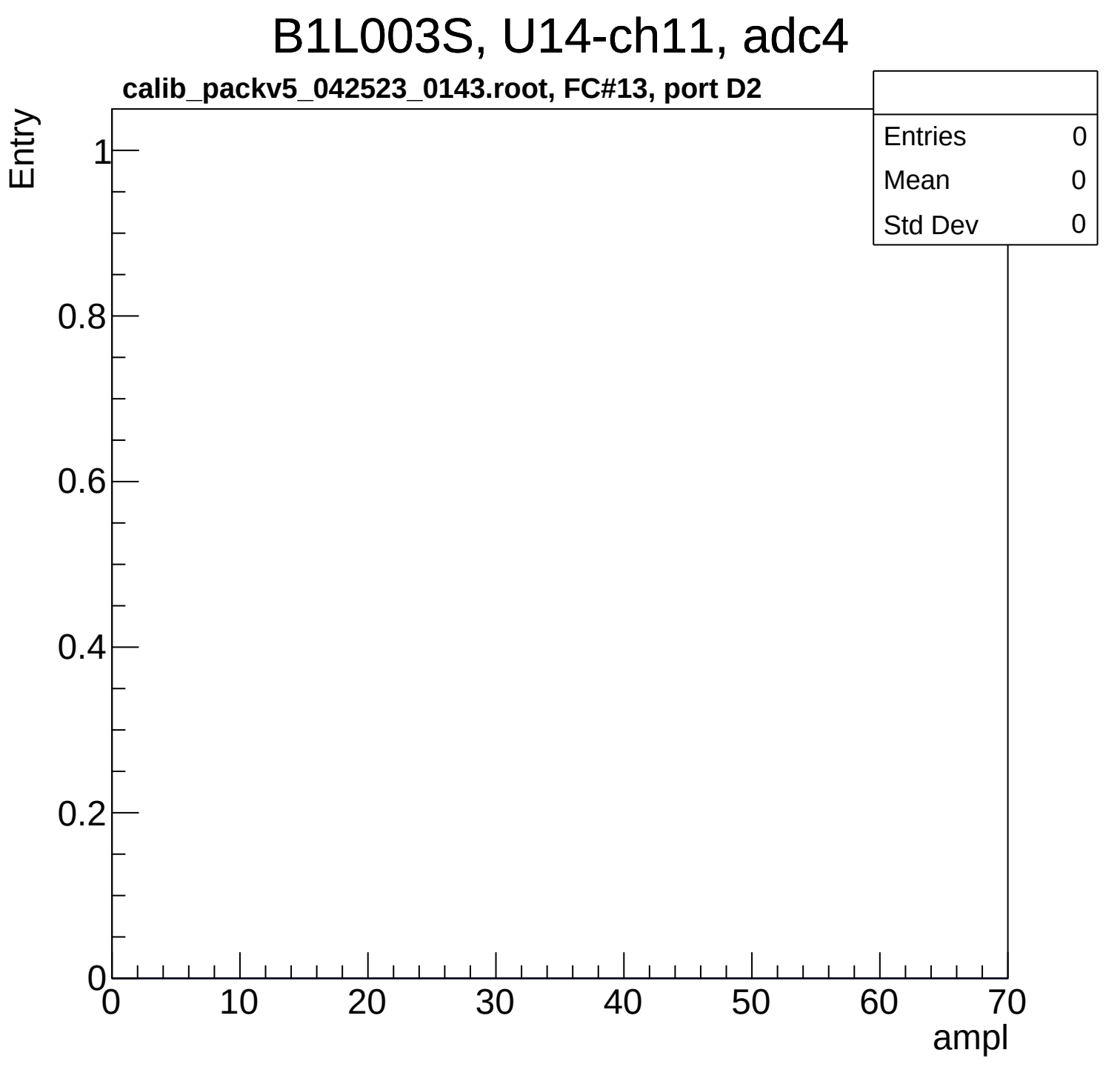

B1L003S, U14-ch10, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

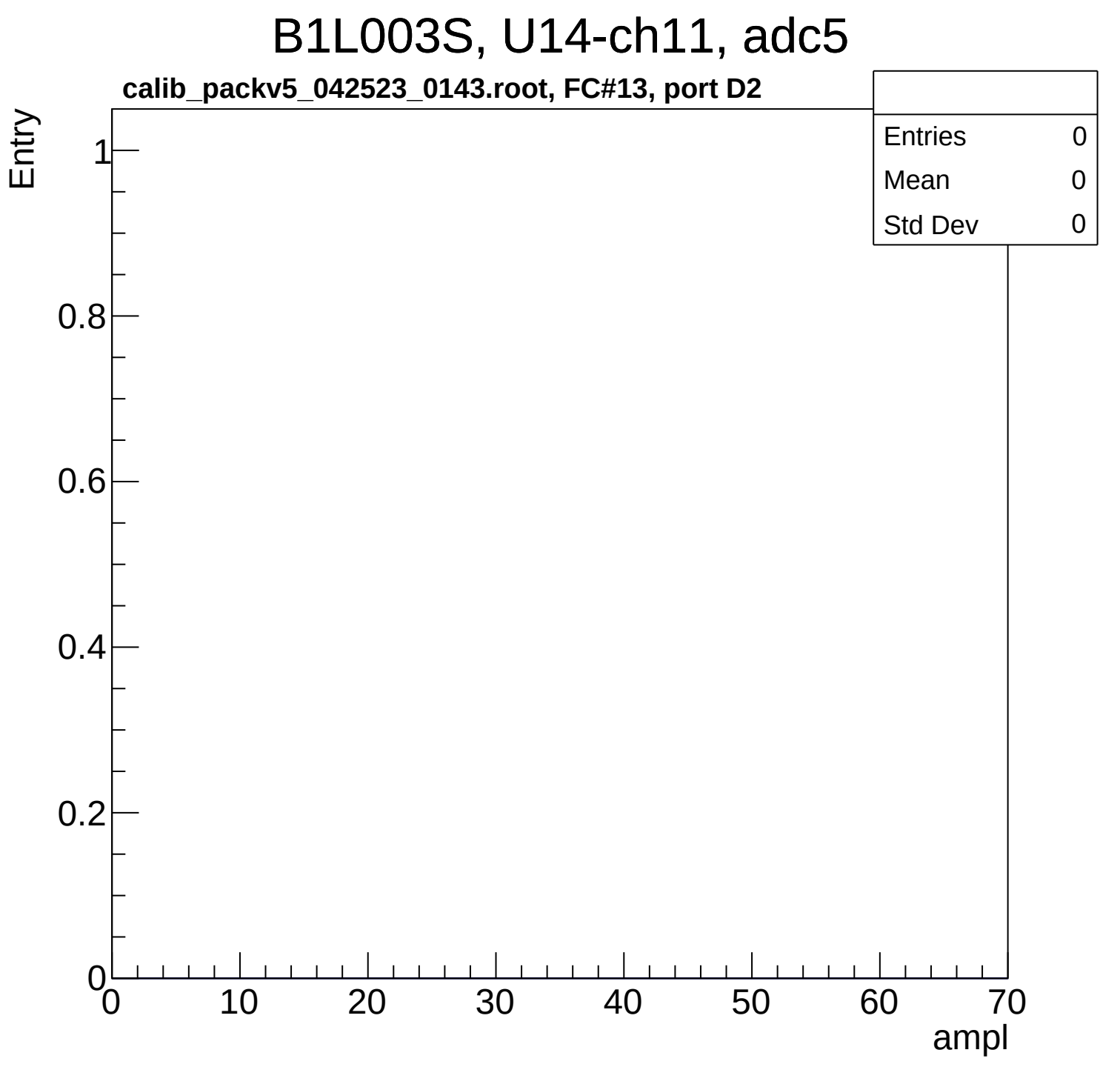

B1L003S, U14-ch11, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

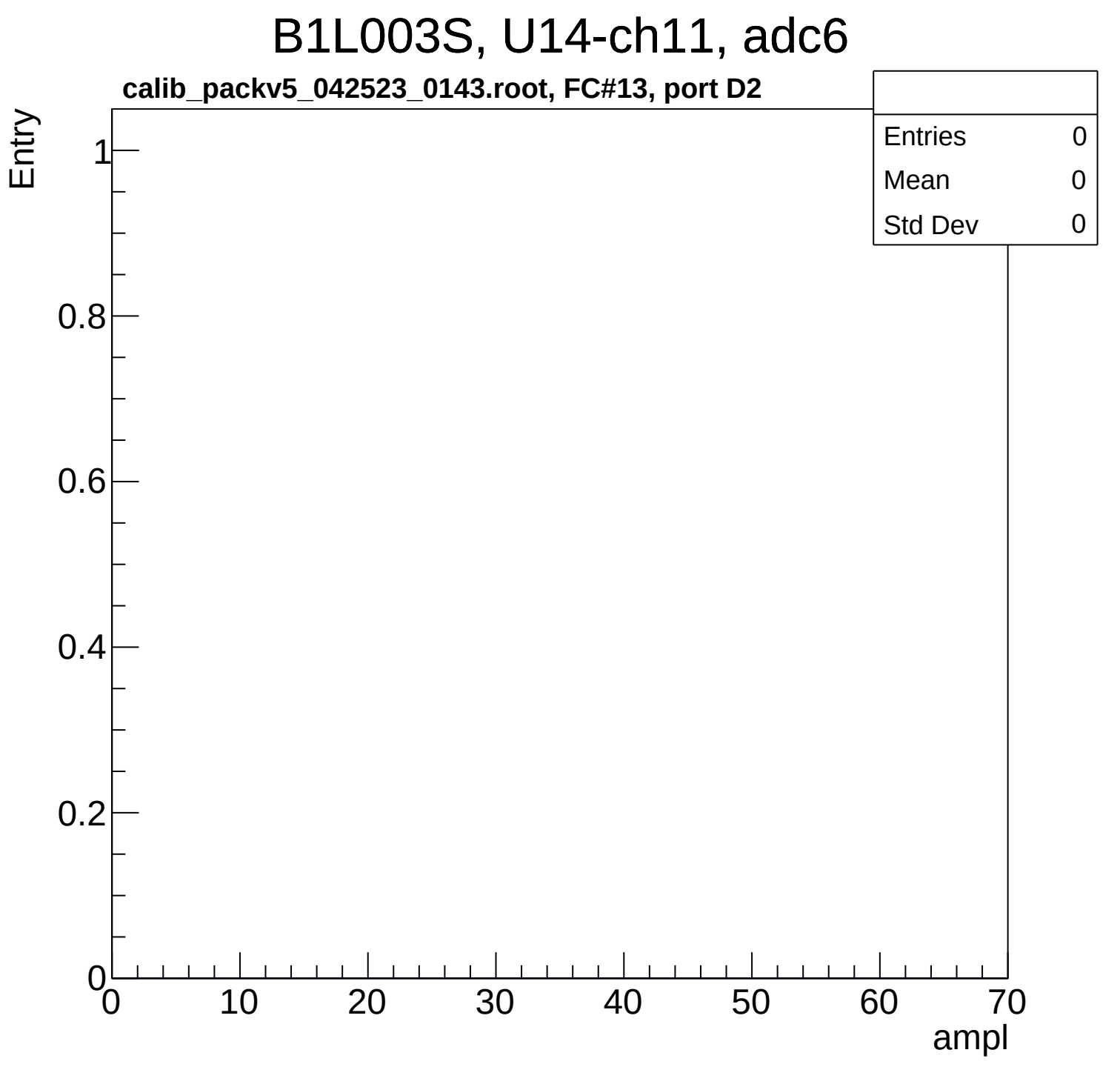

B1L003S, U14-ch11, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

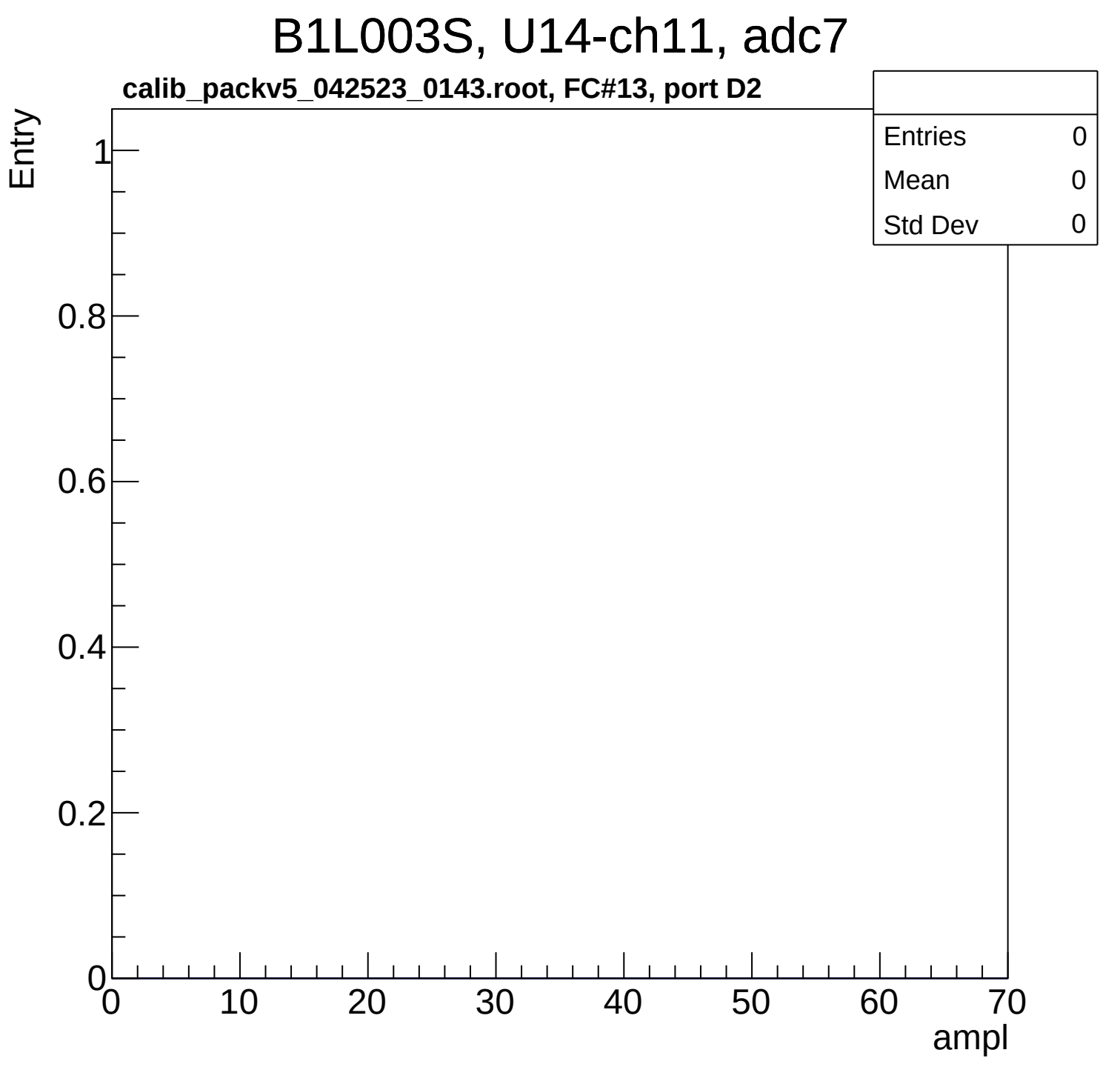

B1L003S, U14-ch11, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

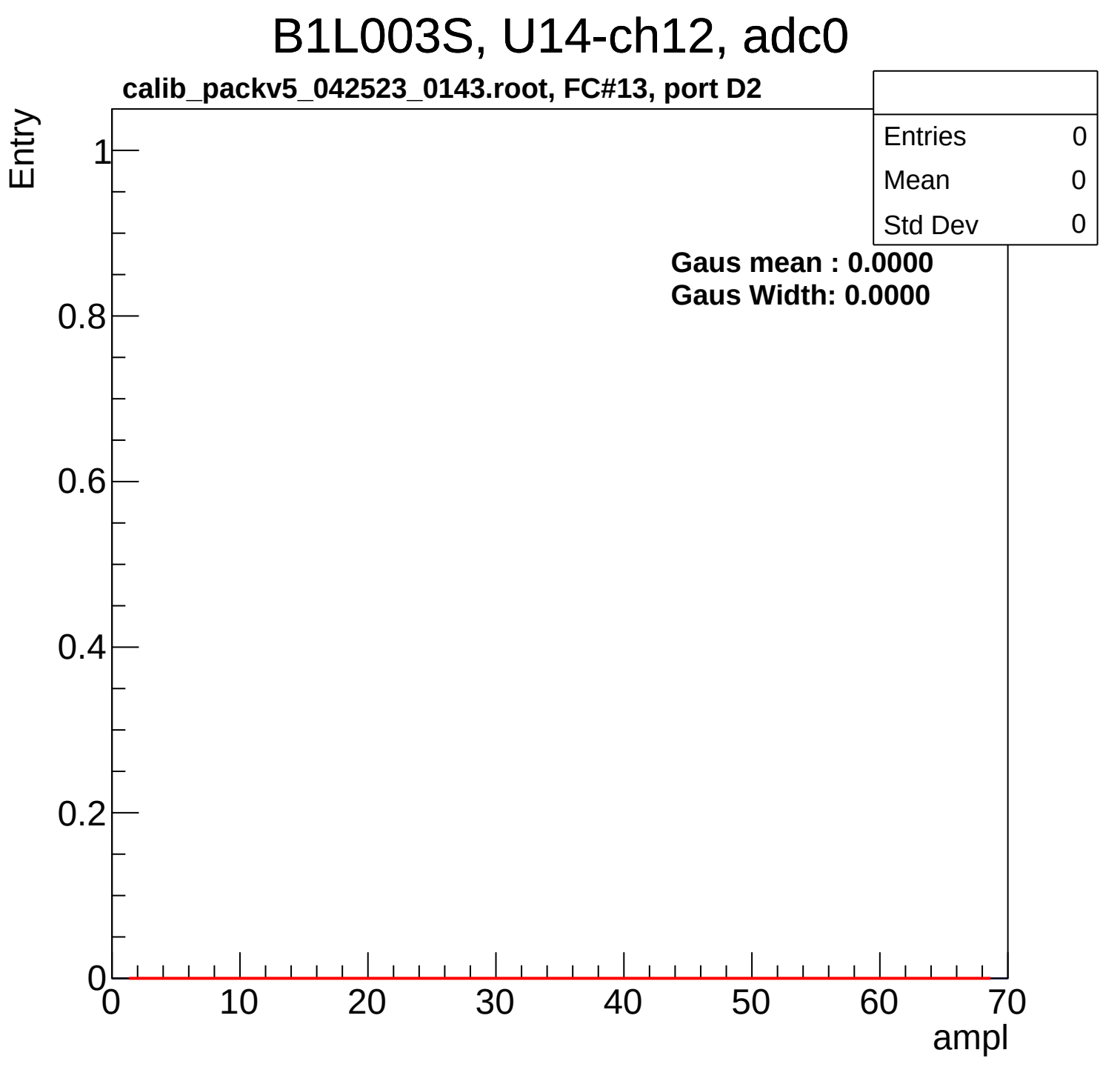

B1L003S, U14-ch11, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

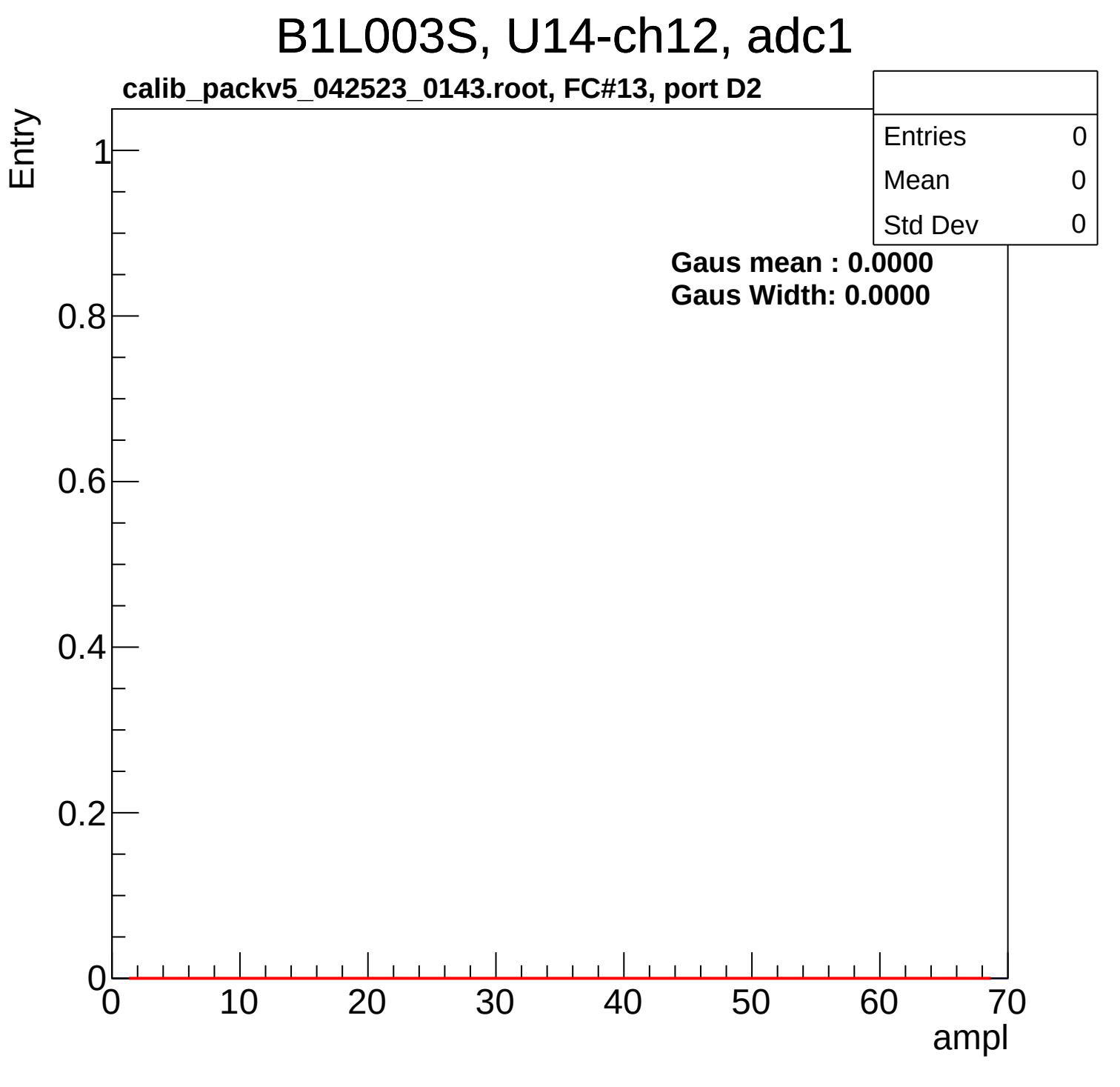

B1L003S, U14-ch11, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

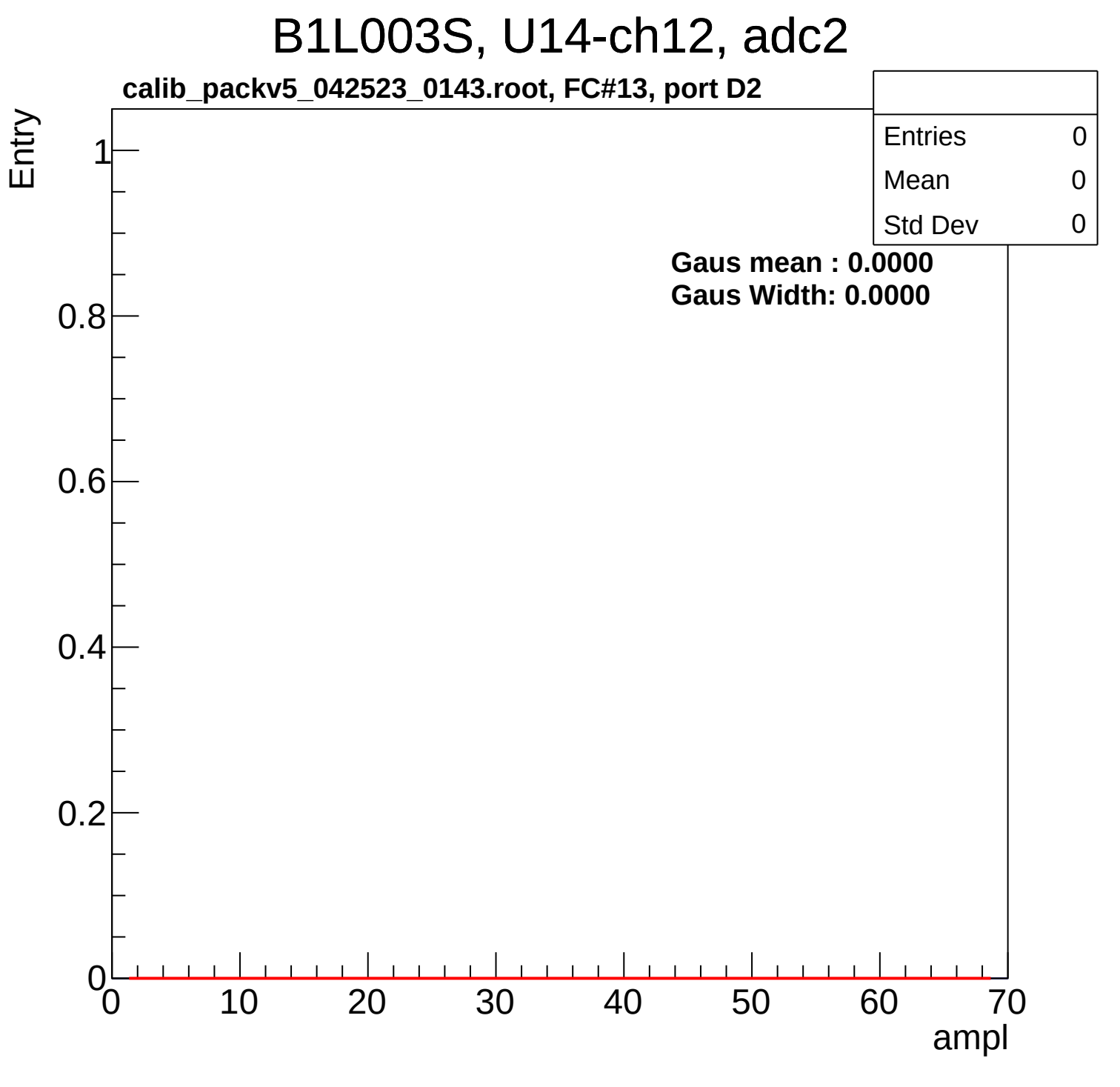

B1L003S, U14-ch11, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

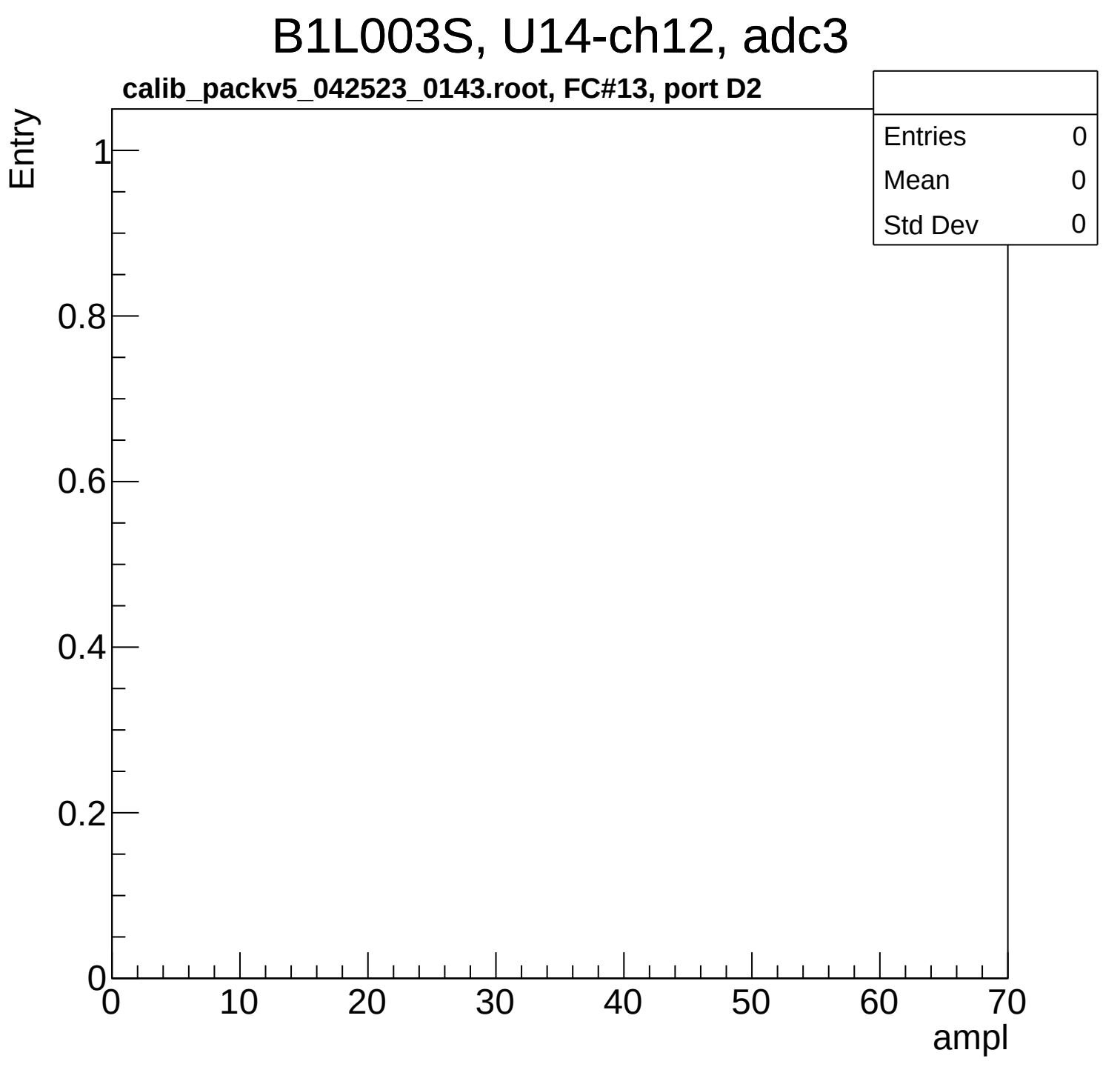

B1L003S, U14-ch11, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

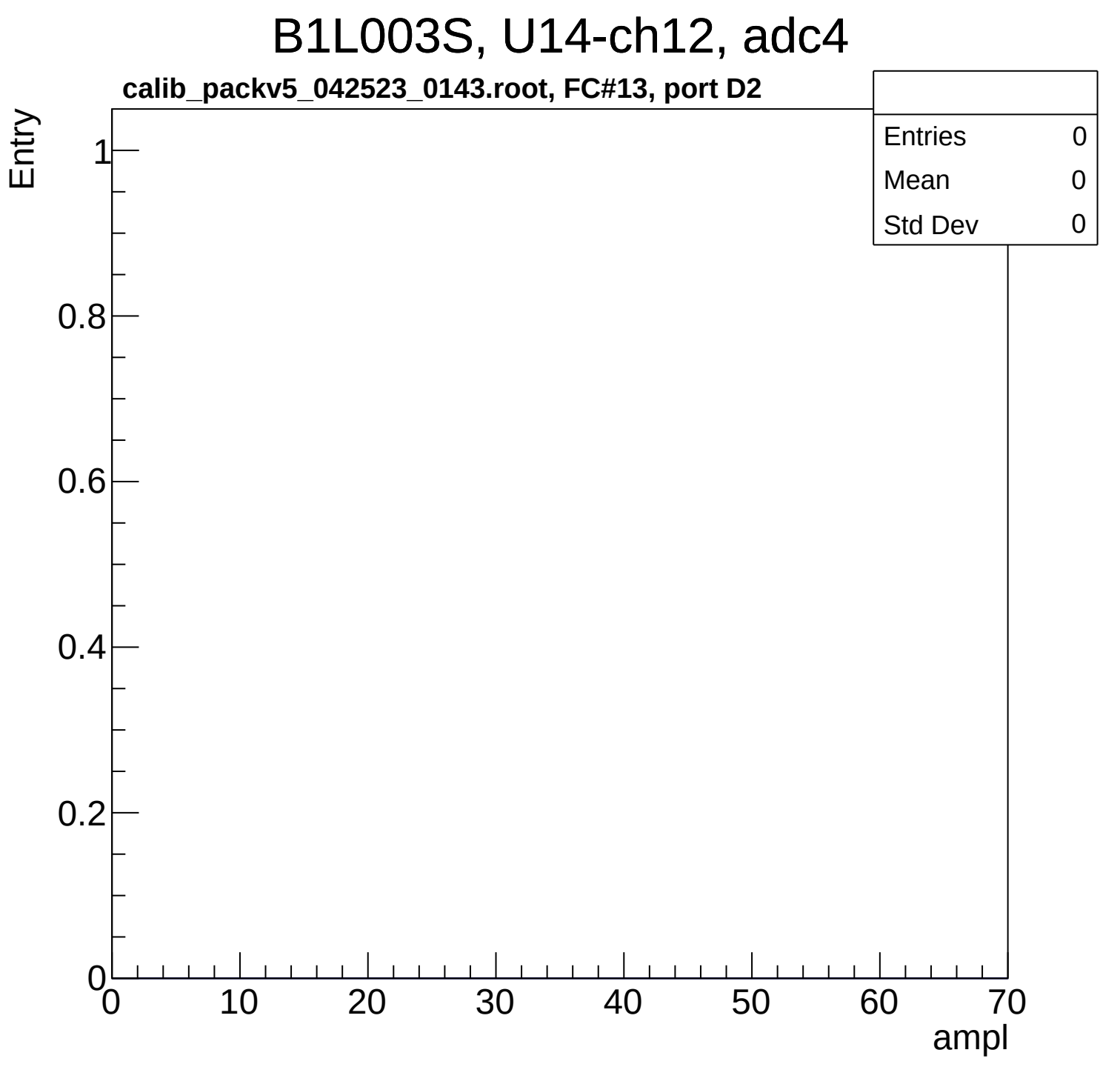

B1L003S, U14-ch11, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

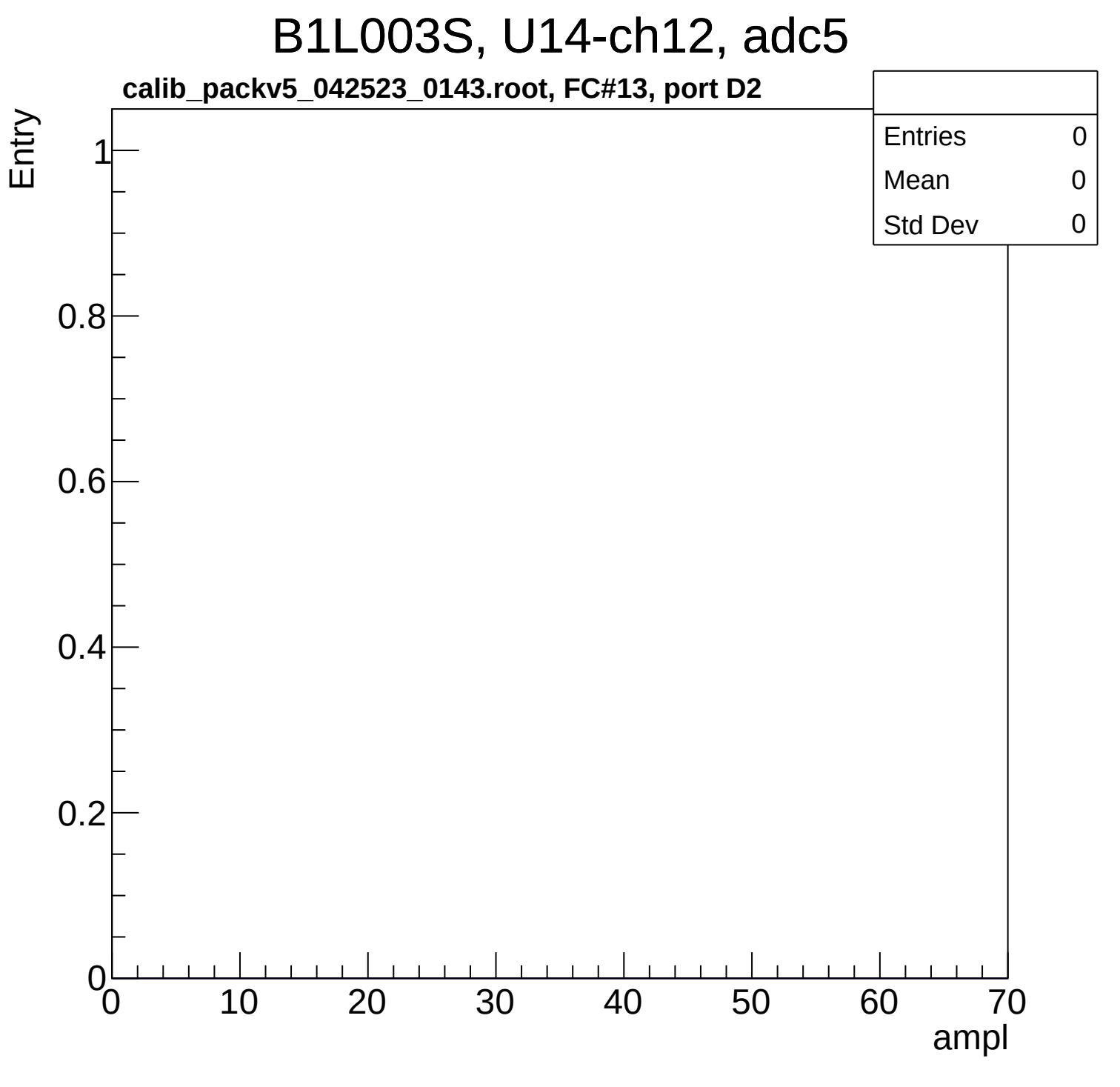

B1L003S, U14-ch12, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

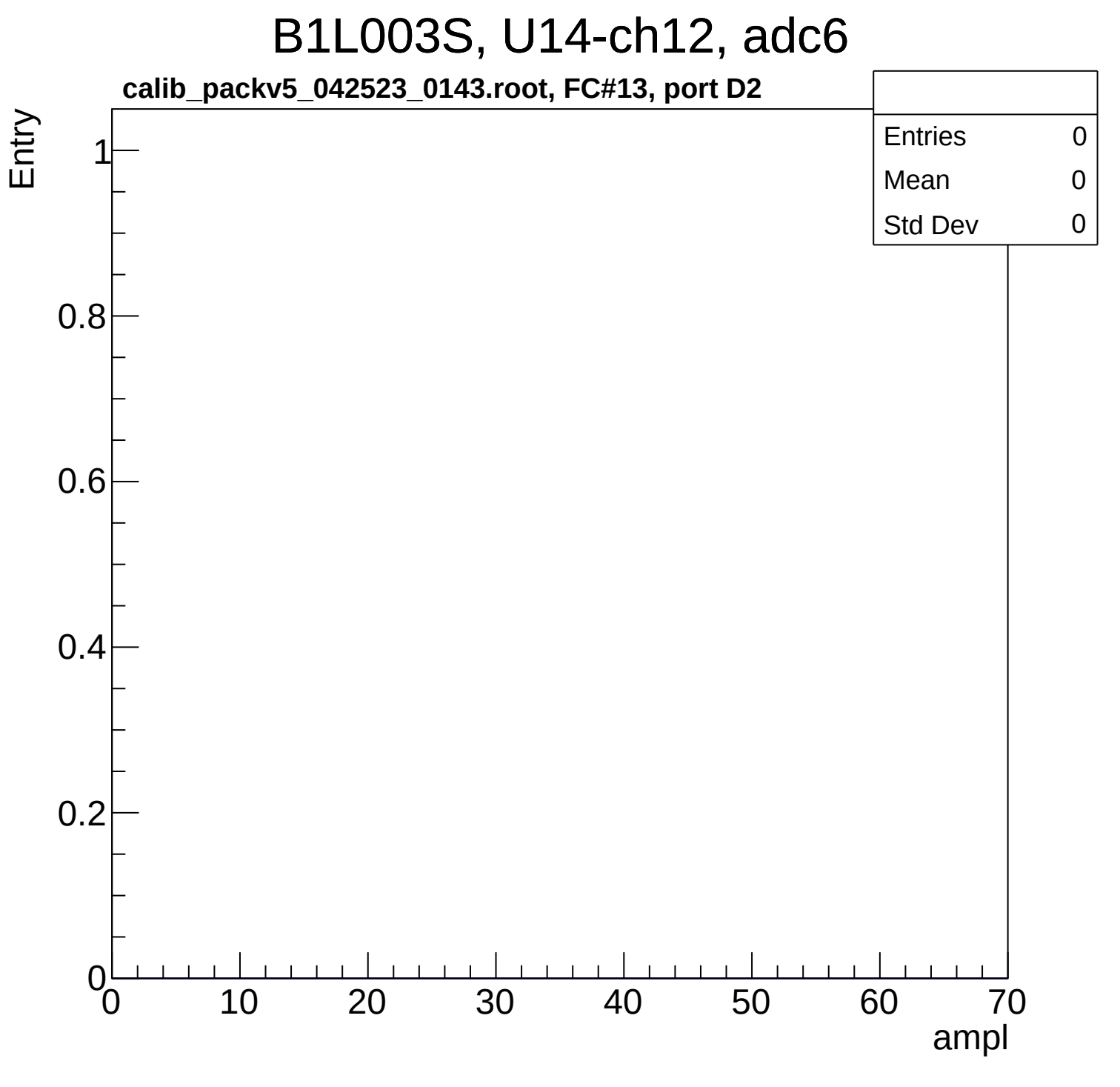

B1L003S, U14-ch12, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

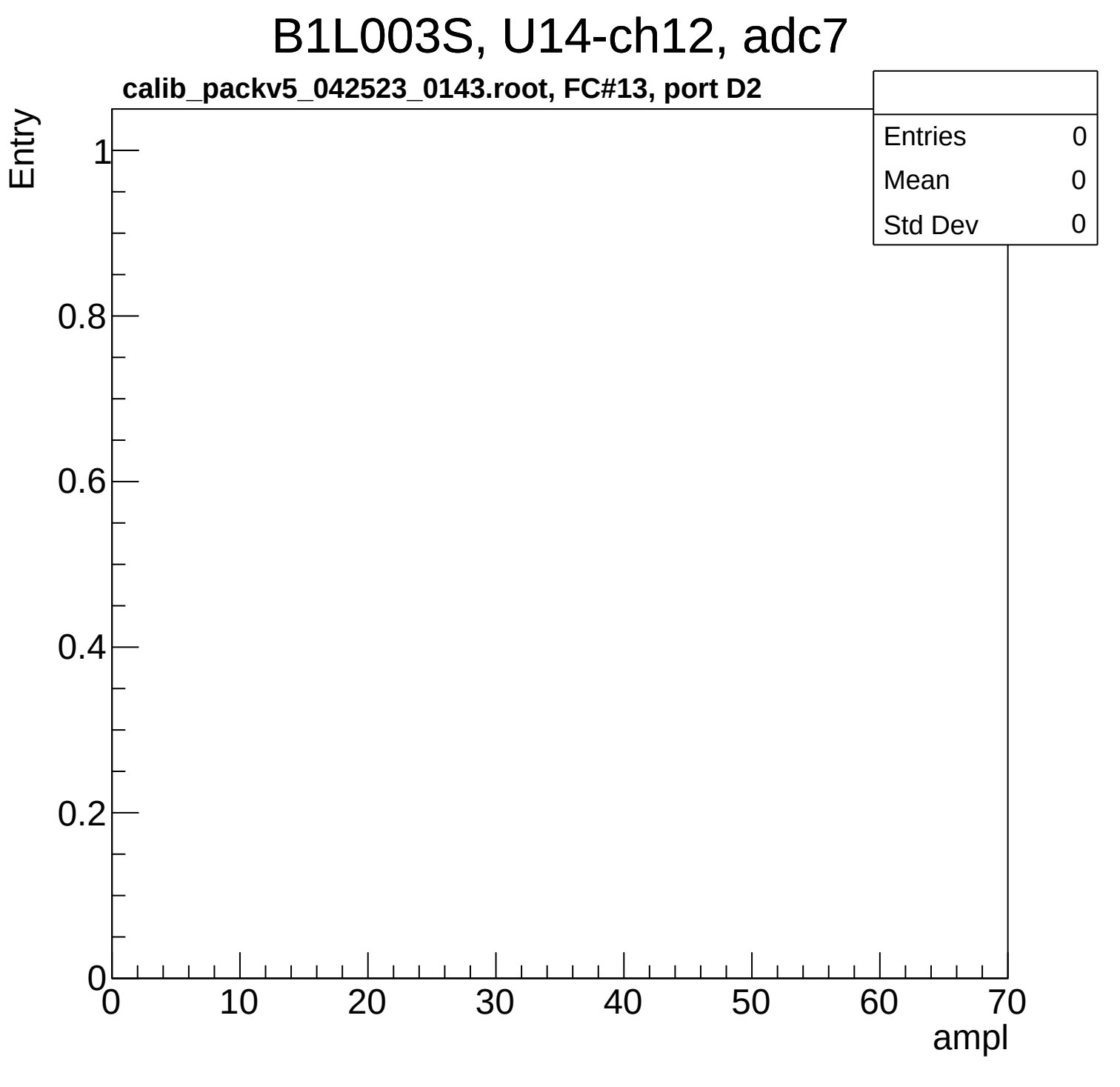

B1L003S, U14-ch12, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

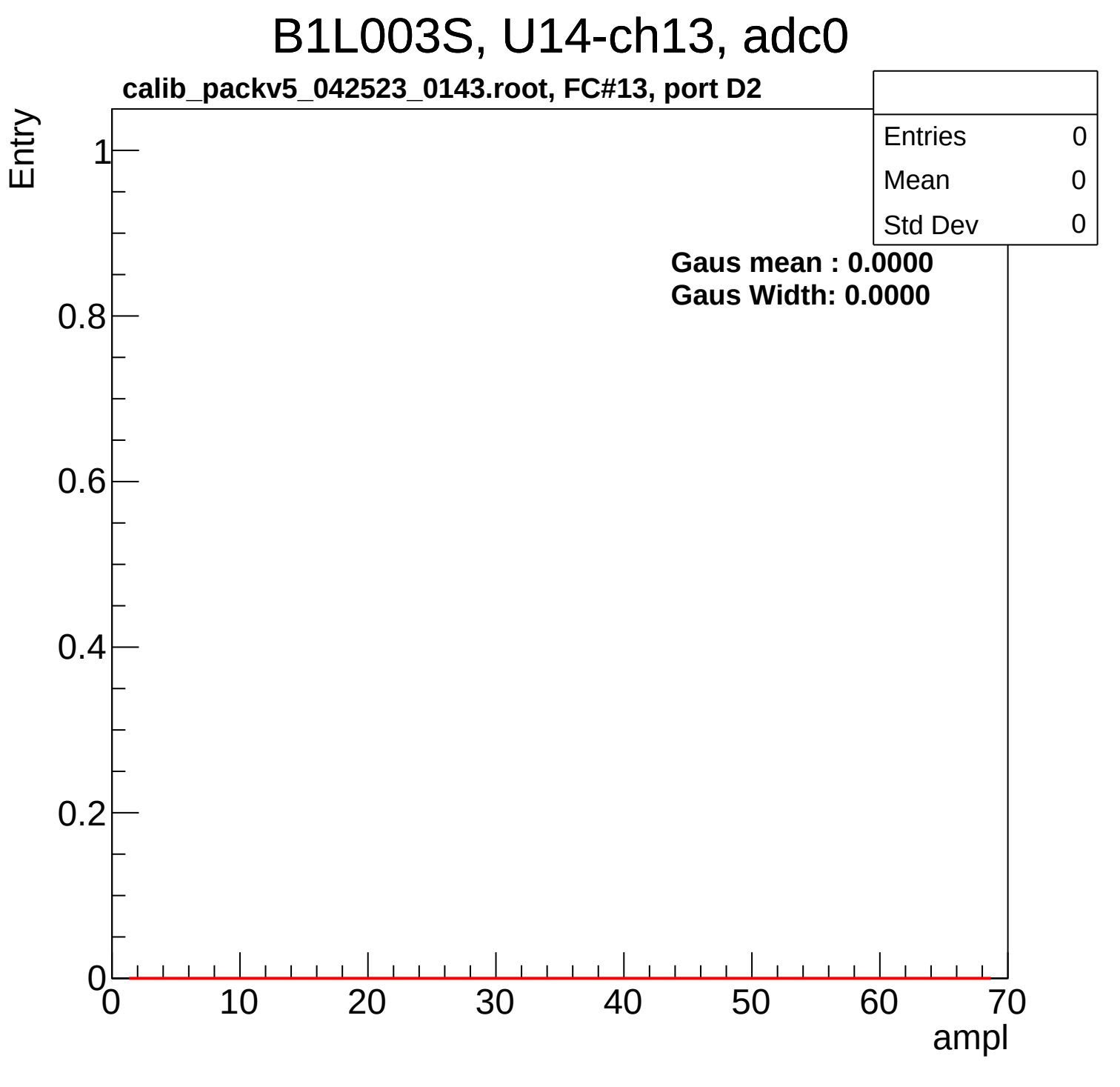

B1L003S, U14-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

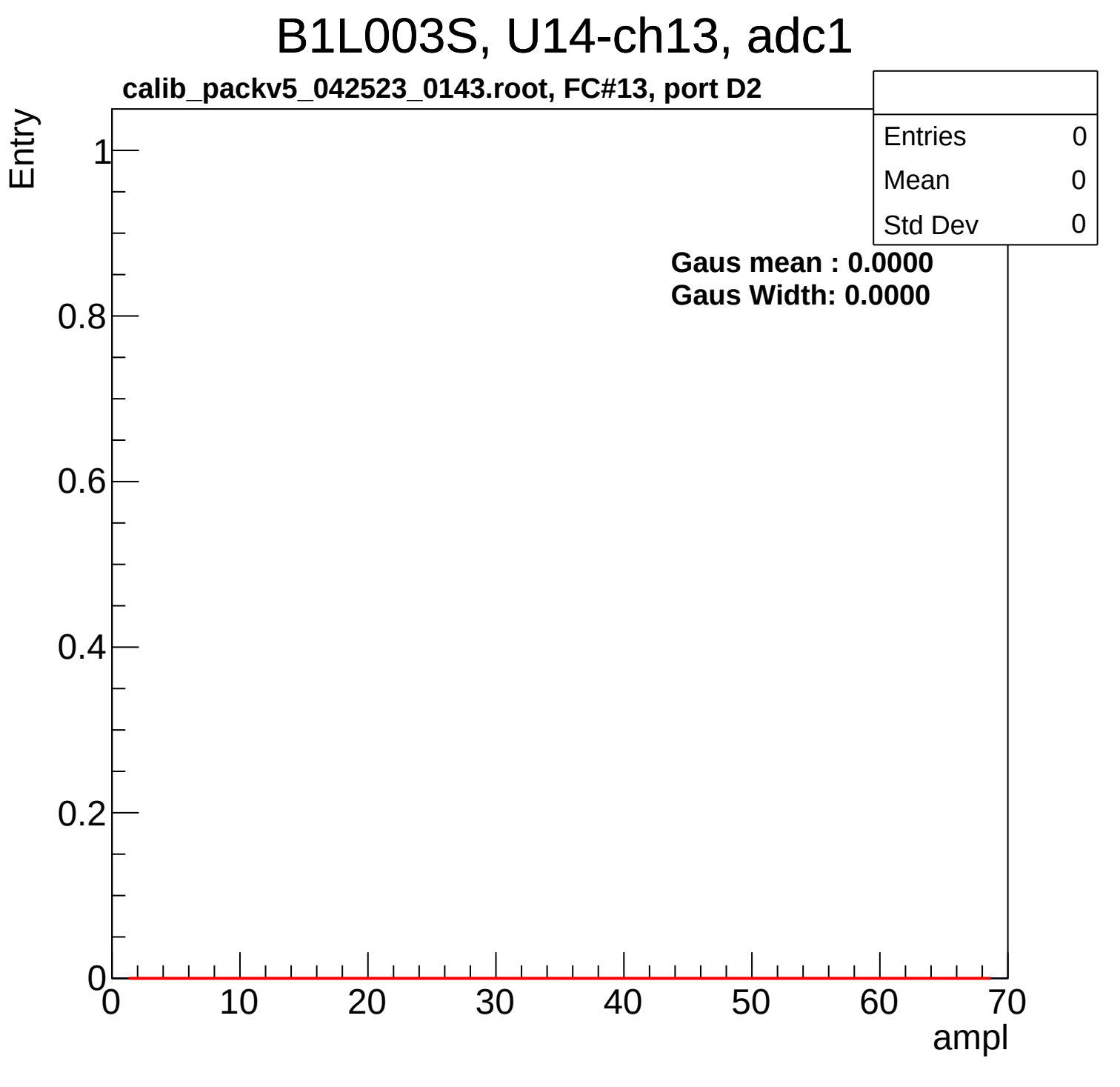

B1L003S, U14-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

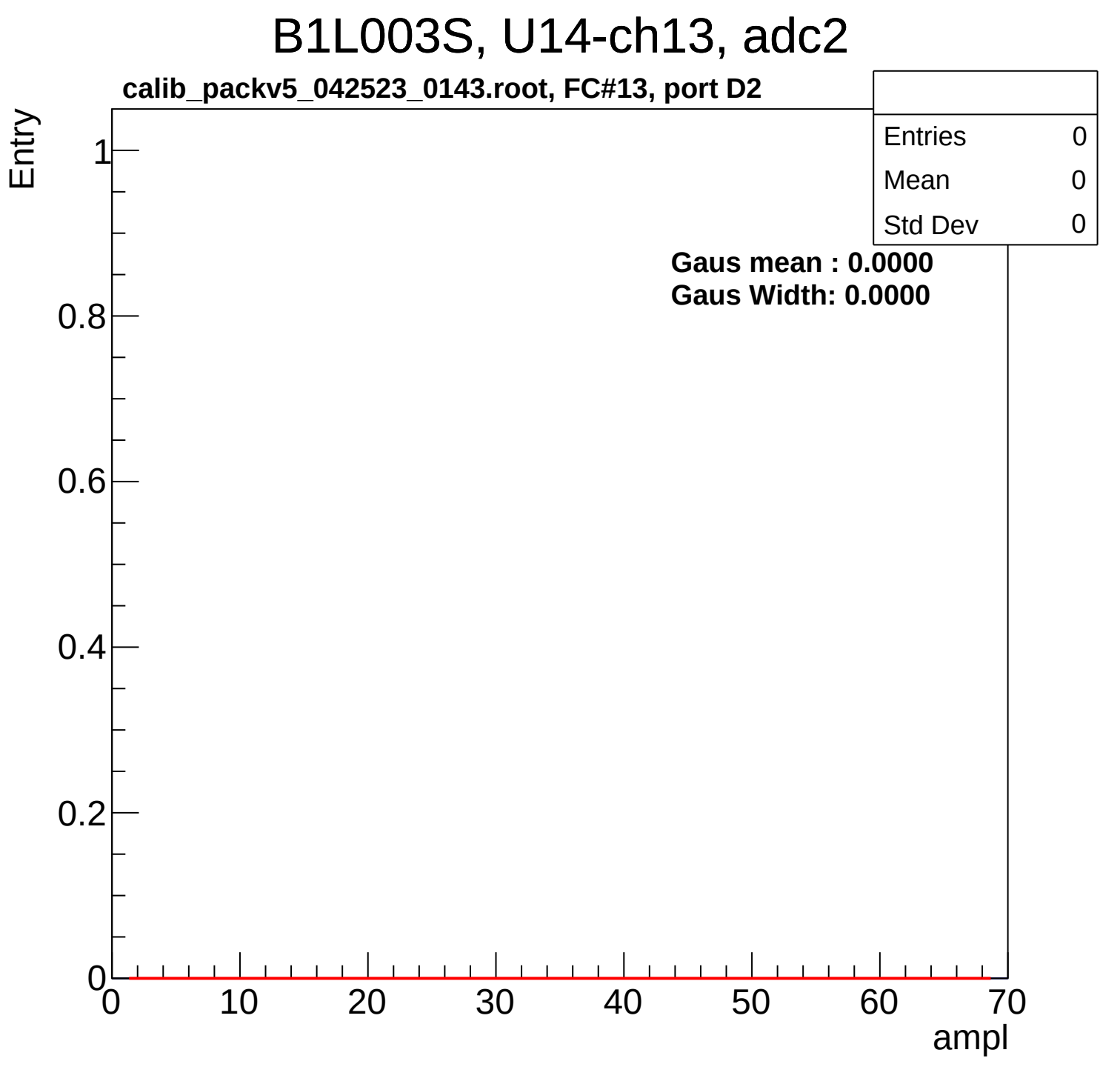

B1L003S, U14-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

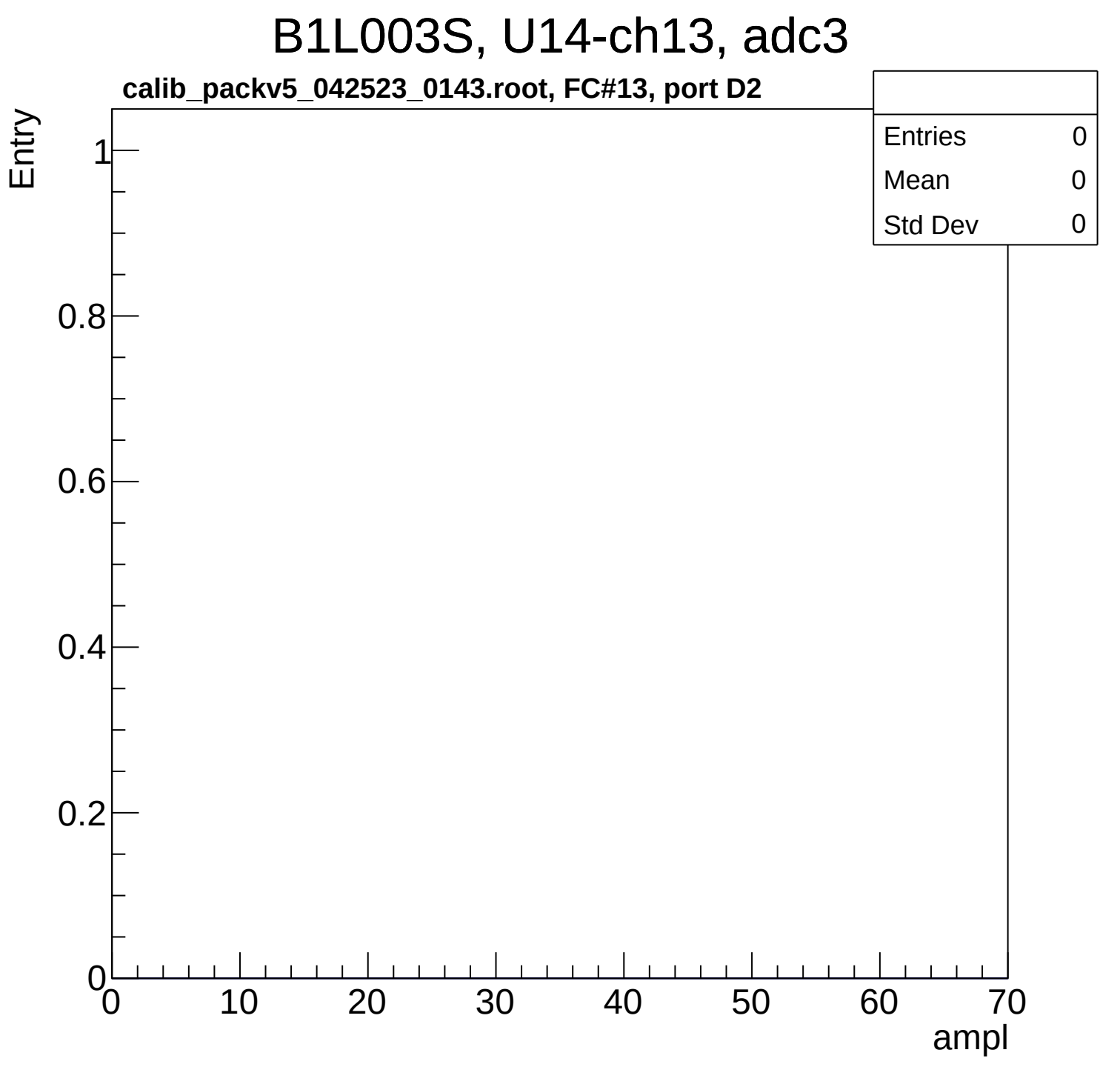

B1L003S, U14-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

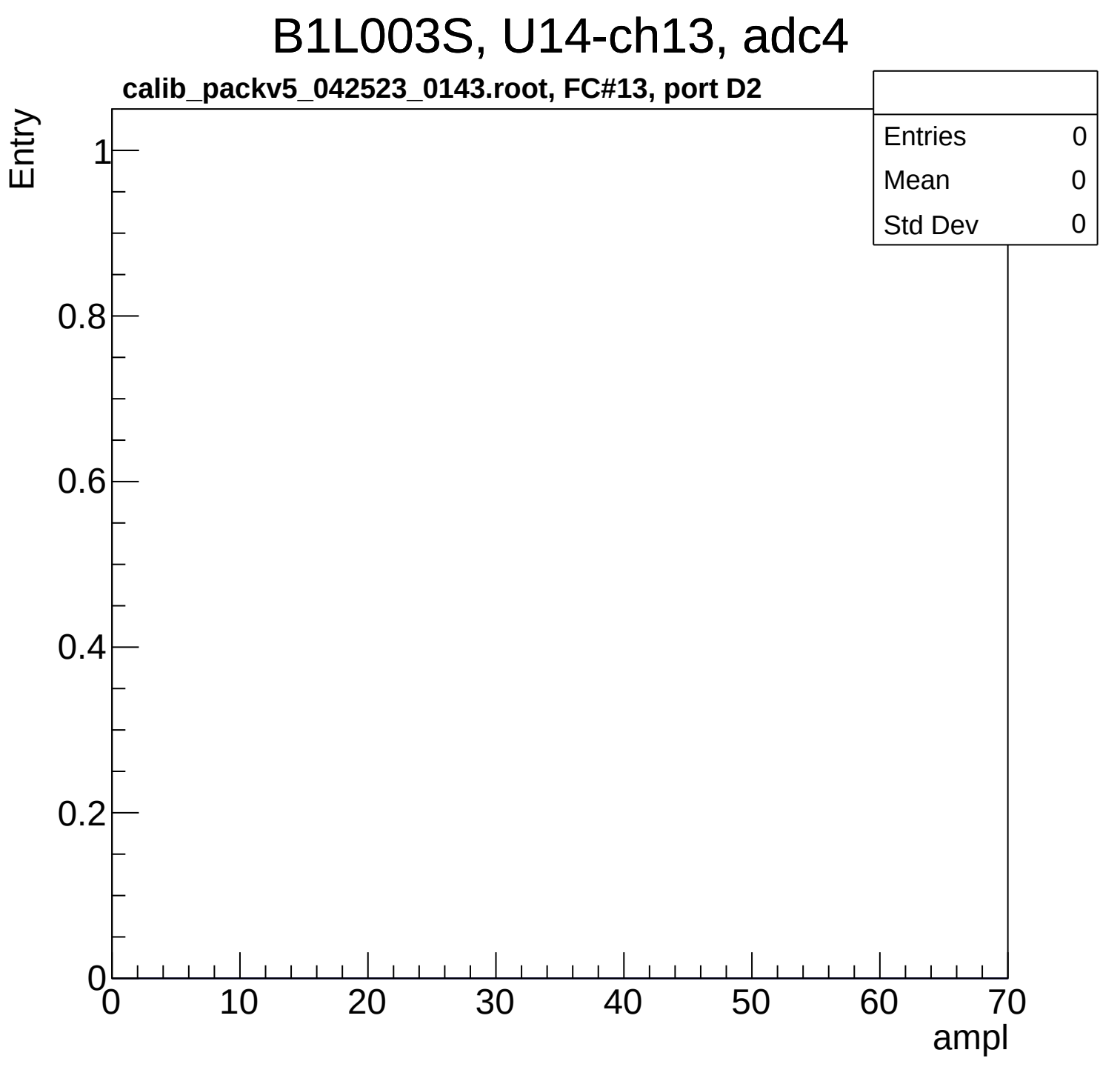

B1L003S, U14-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

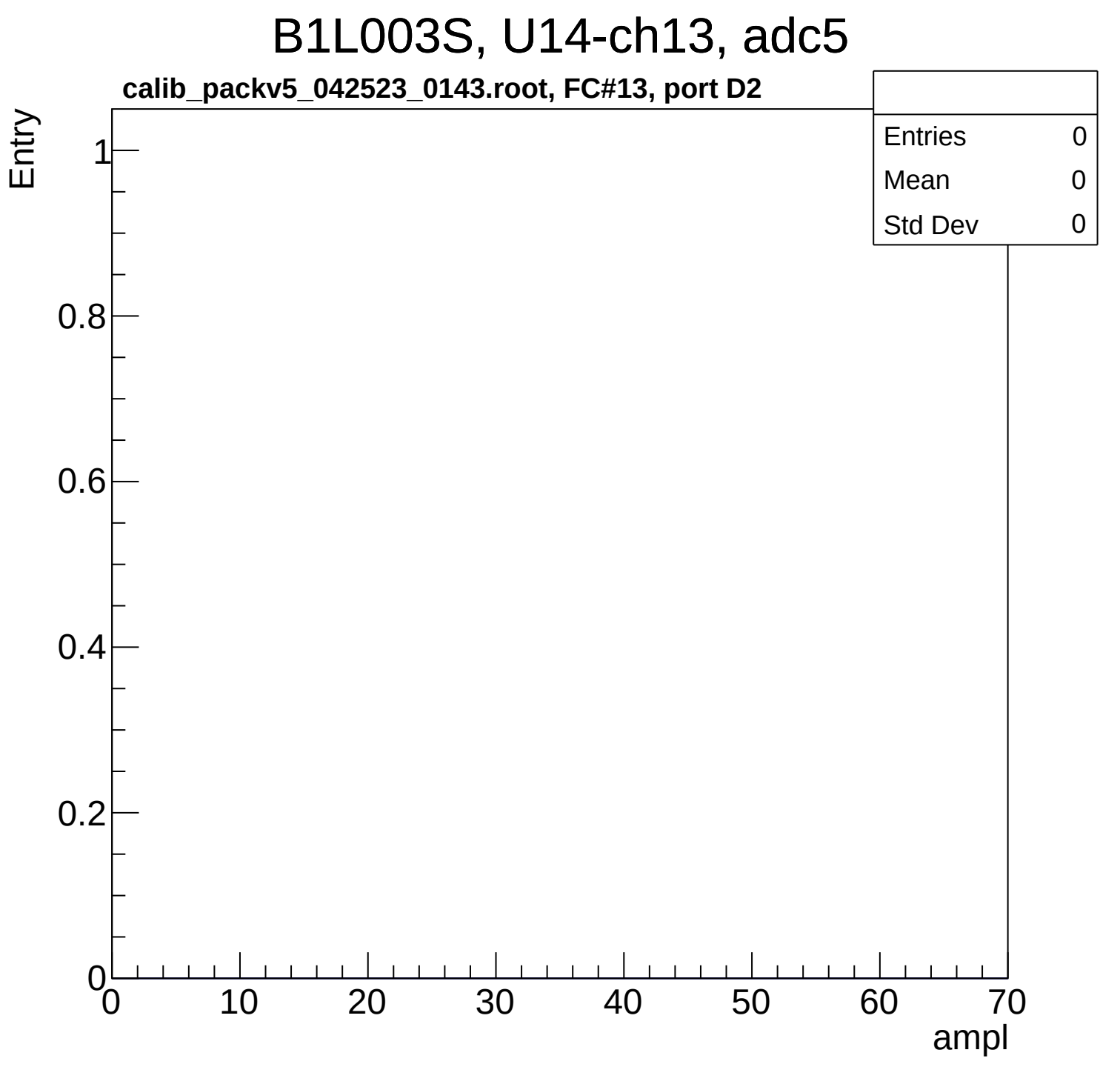

B1L003S, U14-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

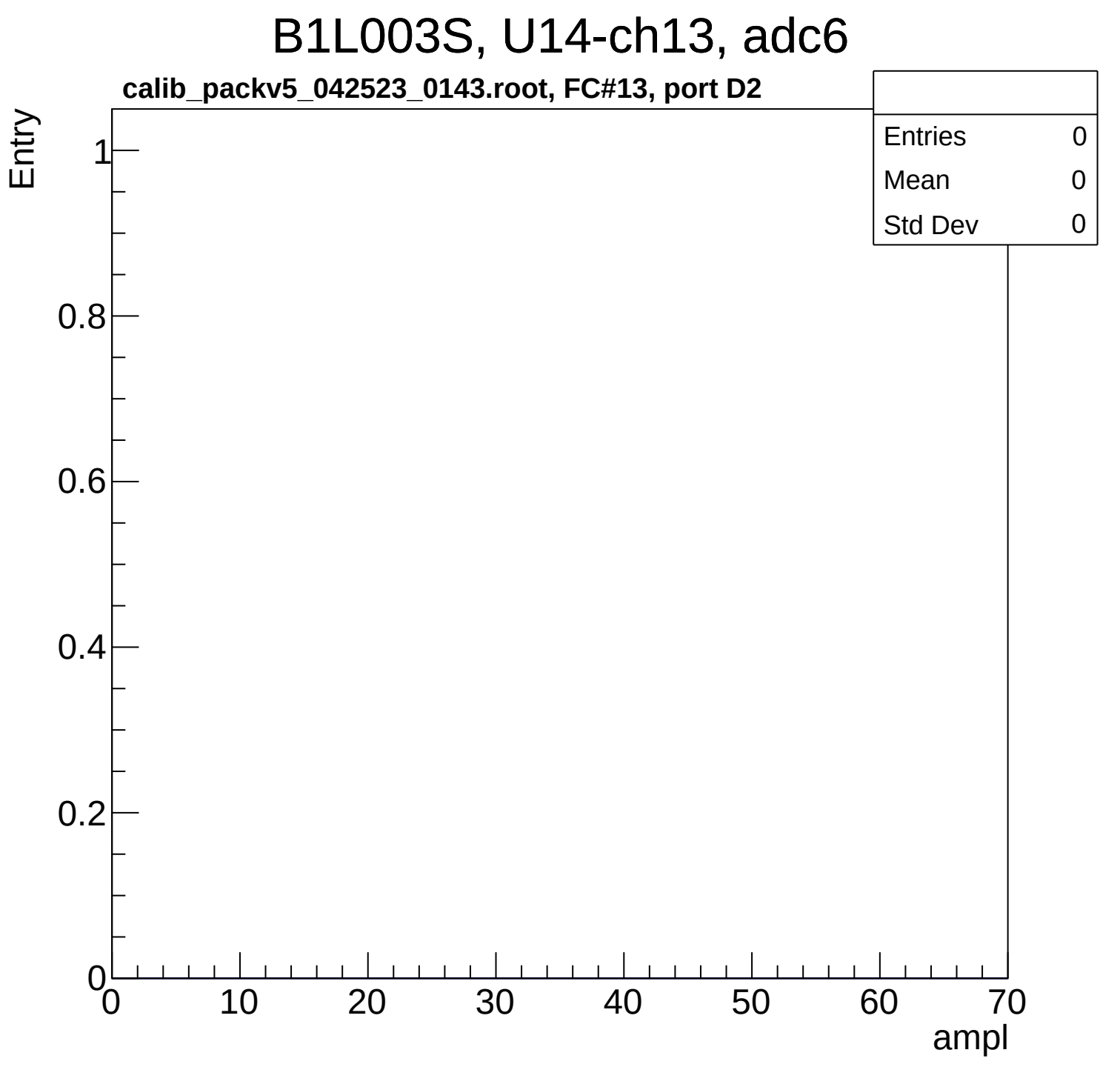

B1L003S, U14-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

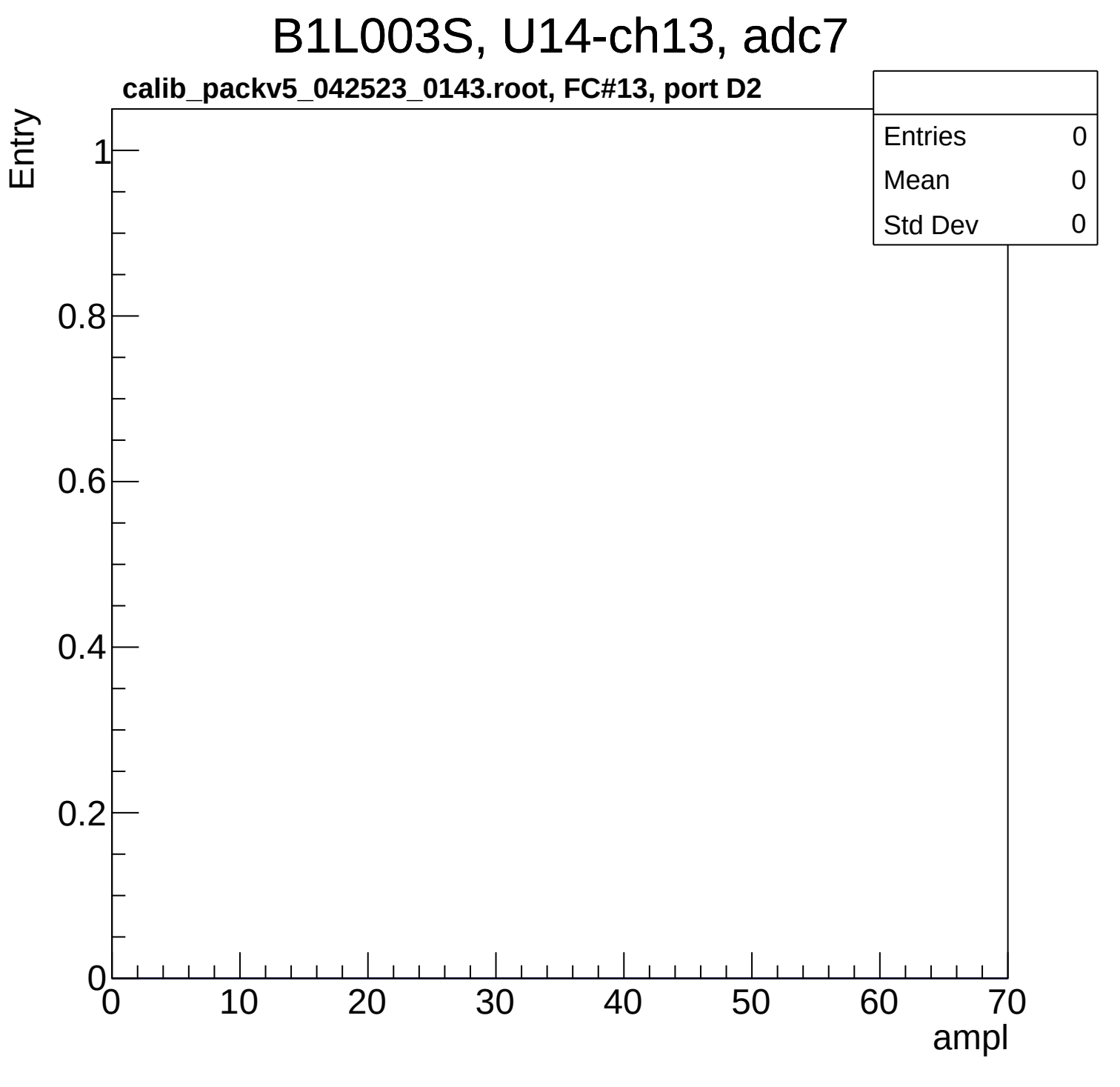

B1L003S, U14-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

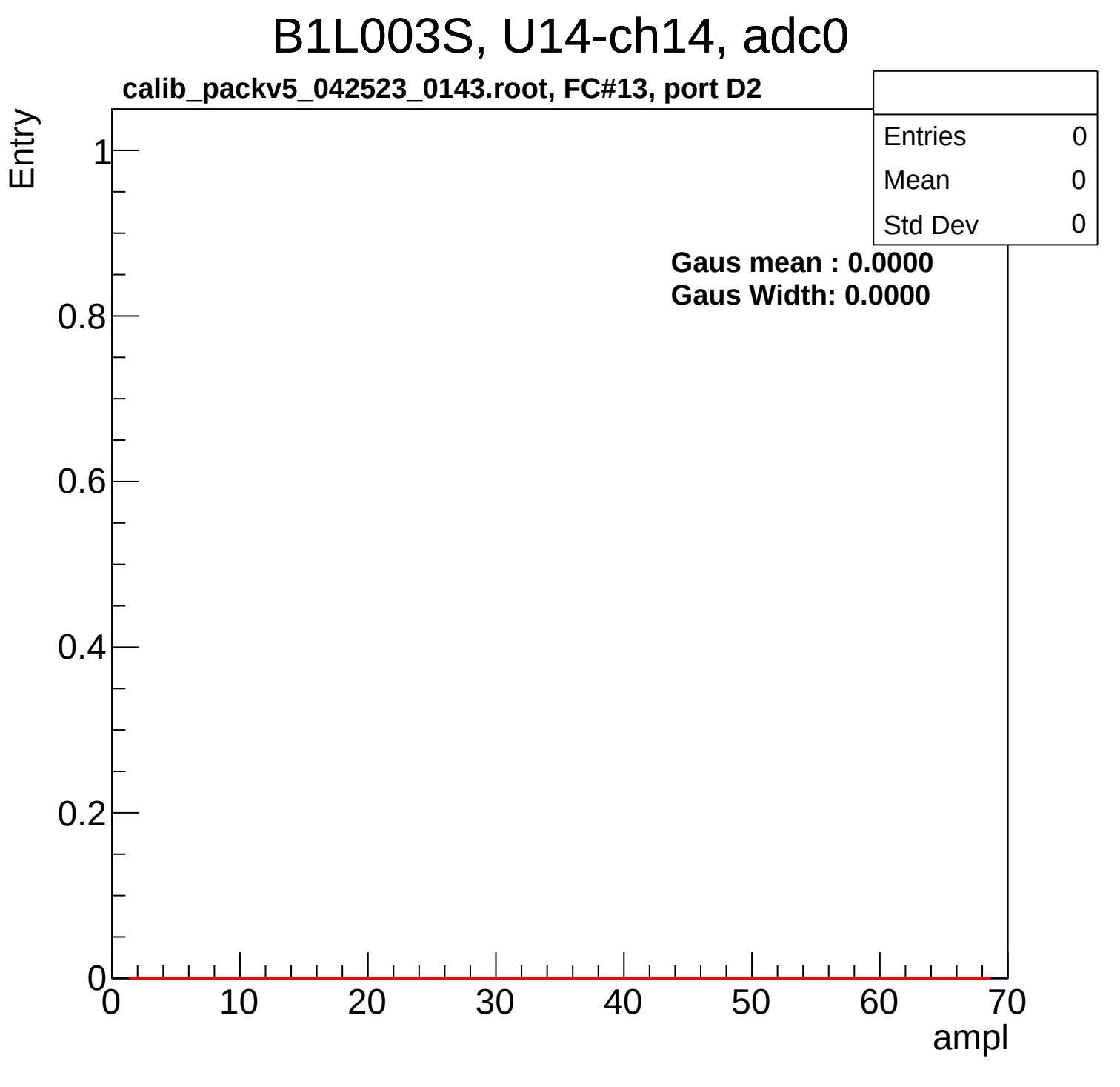

B1L003S, U14-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

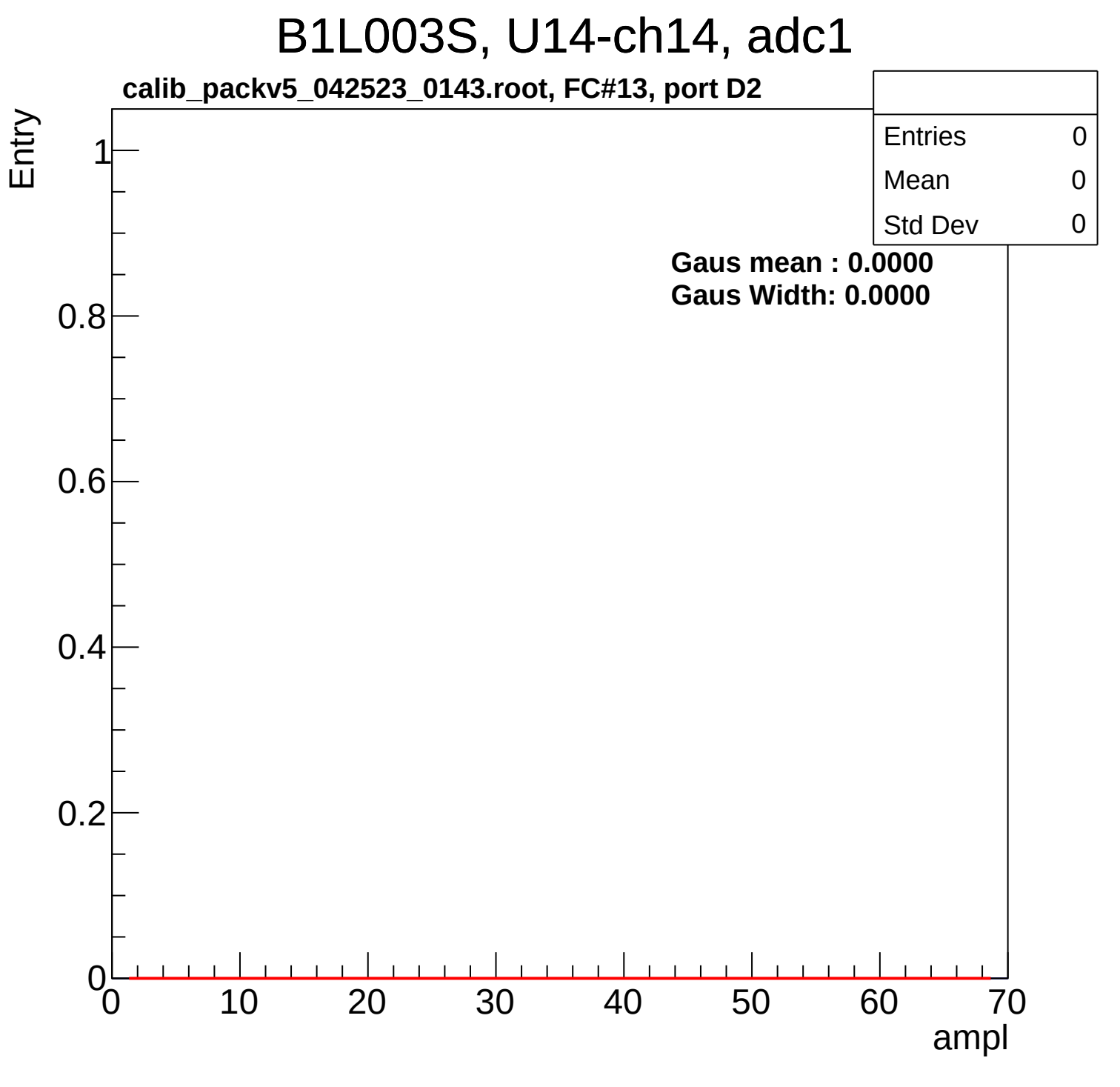

B1L003S, U14-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

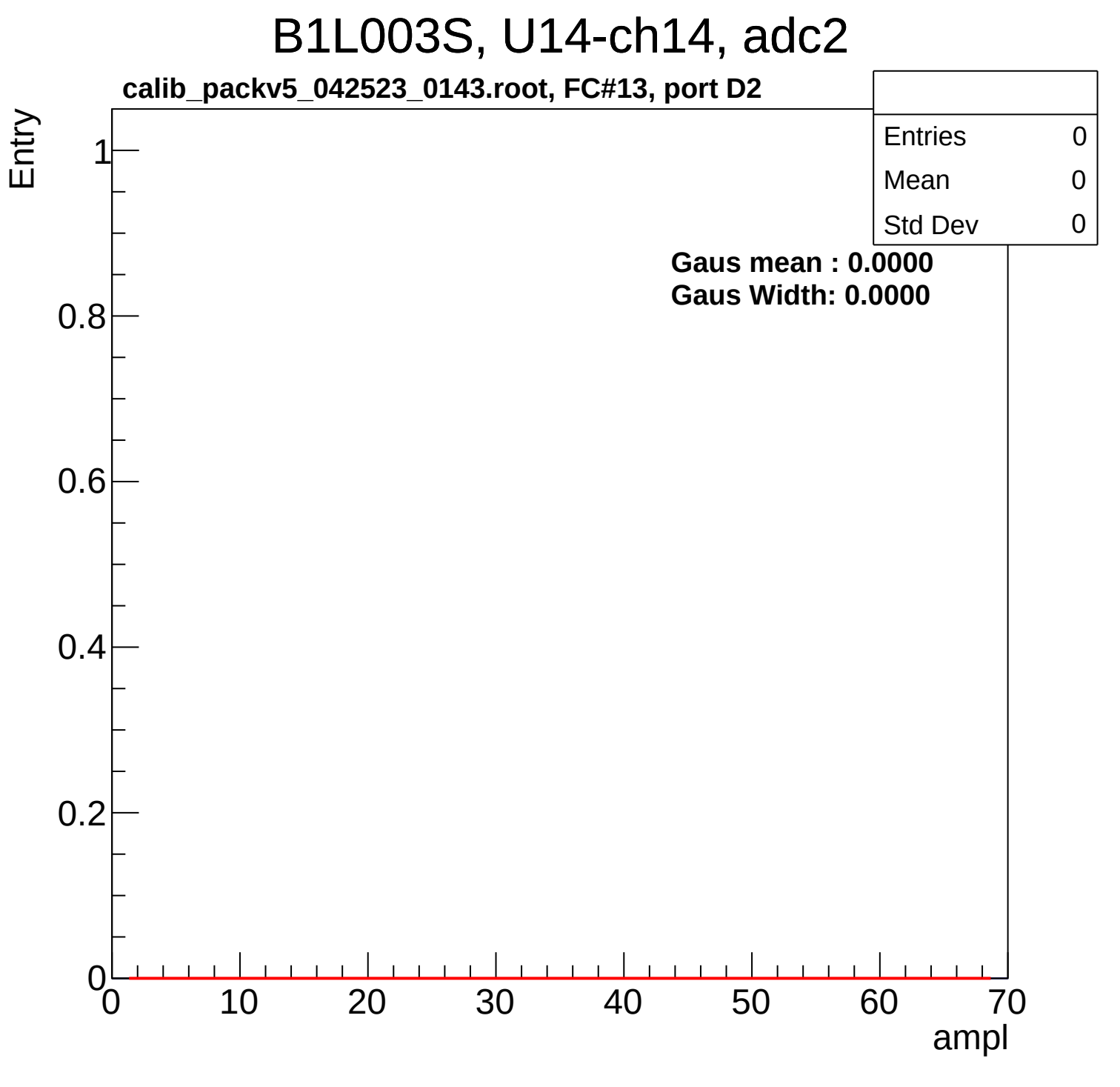

B1L003S, U14-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

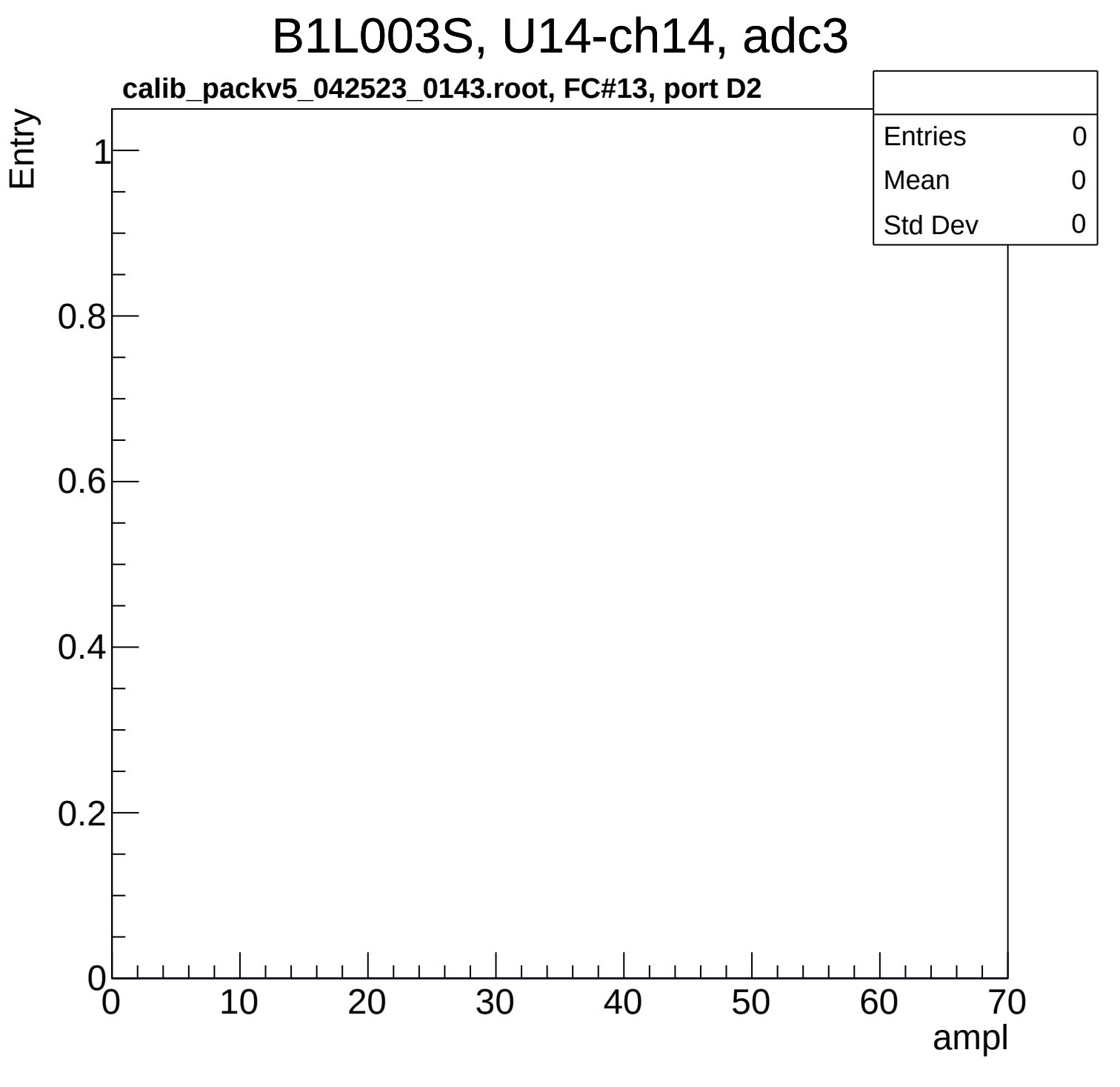

B1L003S, U14-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

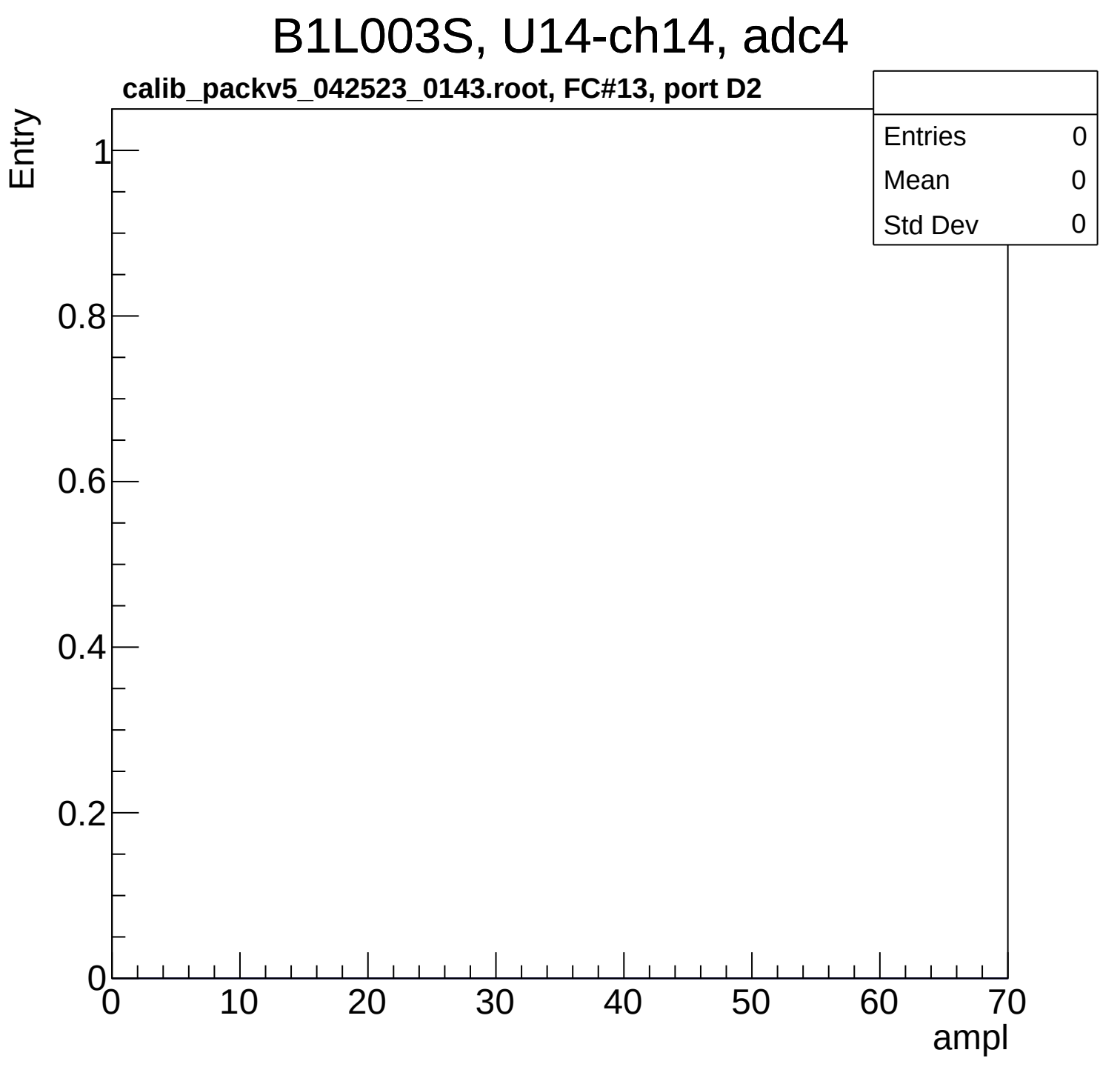

B1L003S, U14-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

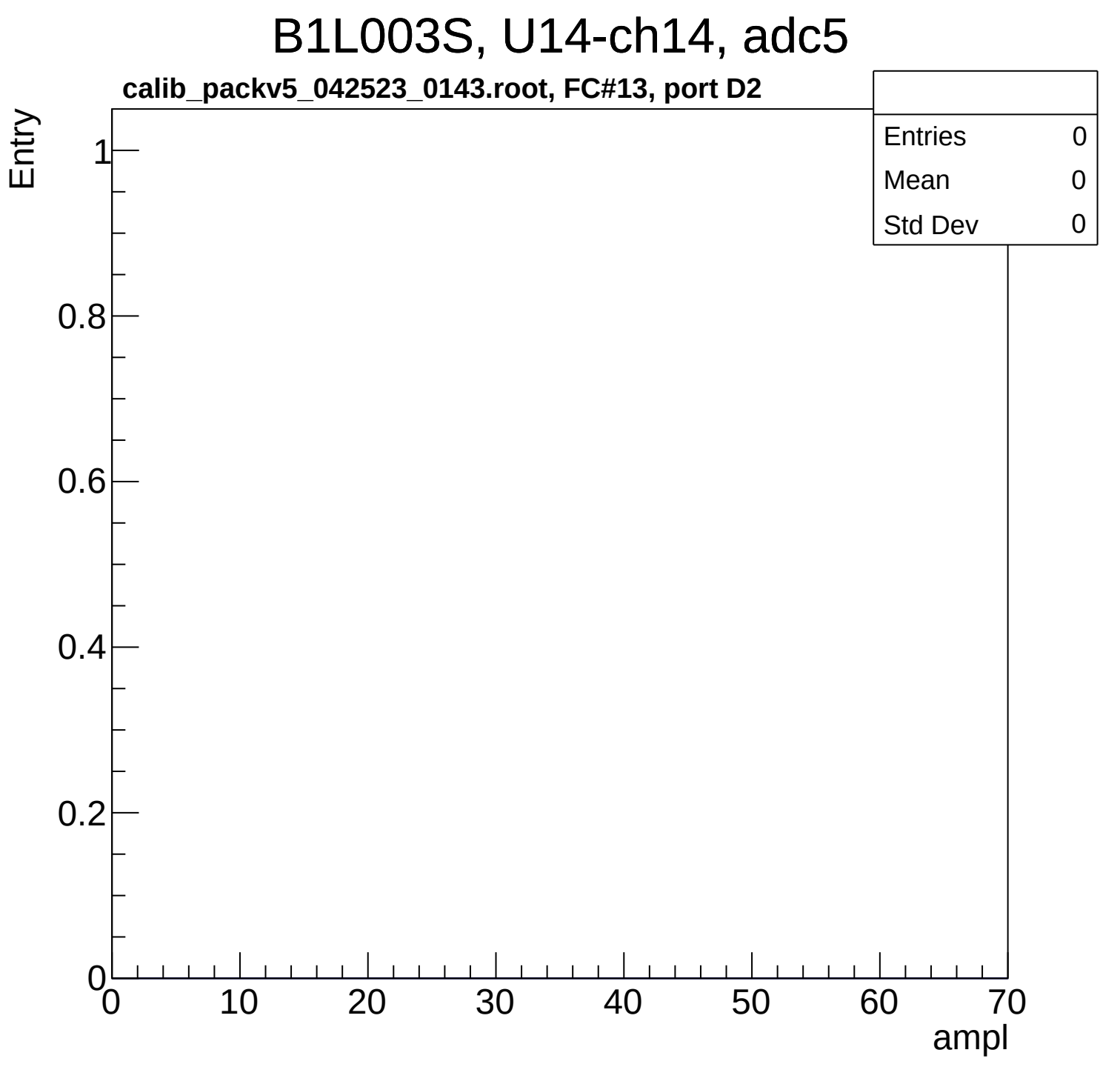

B1L003S, U14-ch14, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

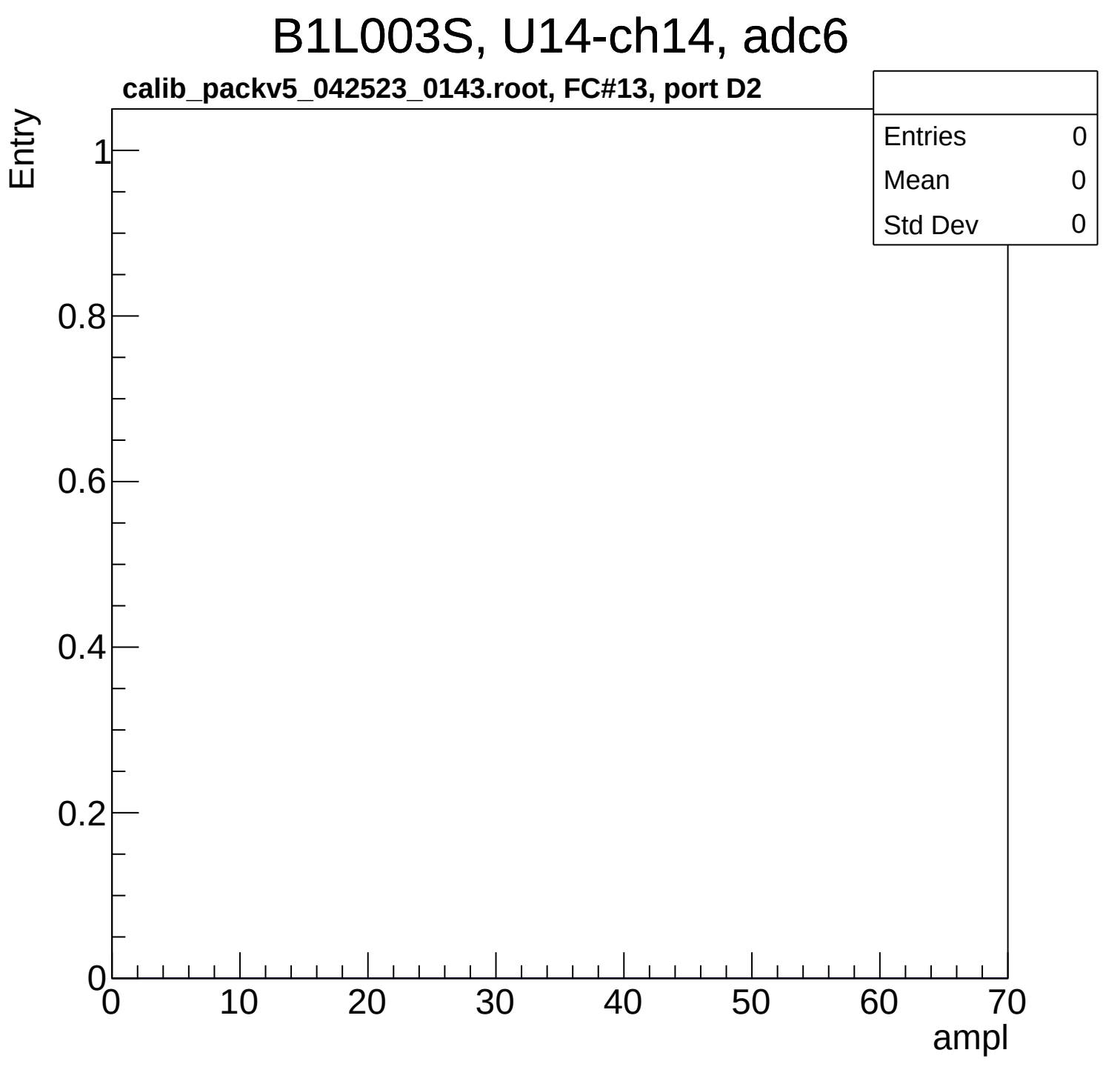

B1L003S, U14-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

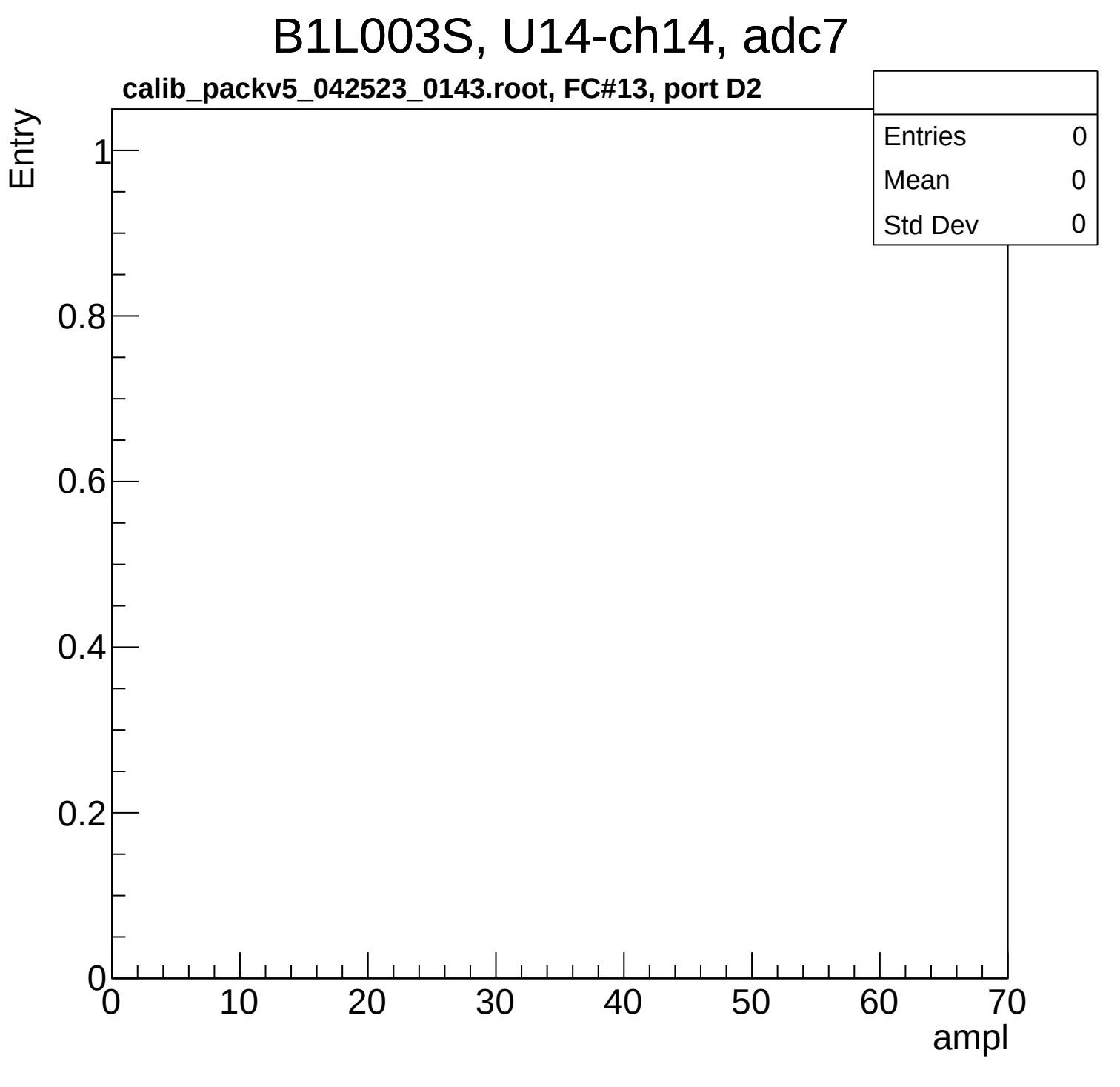

B1L003S, U14-ch14, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

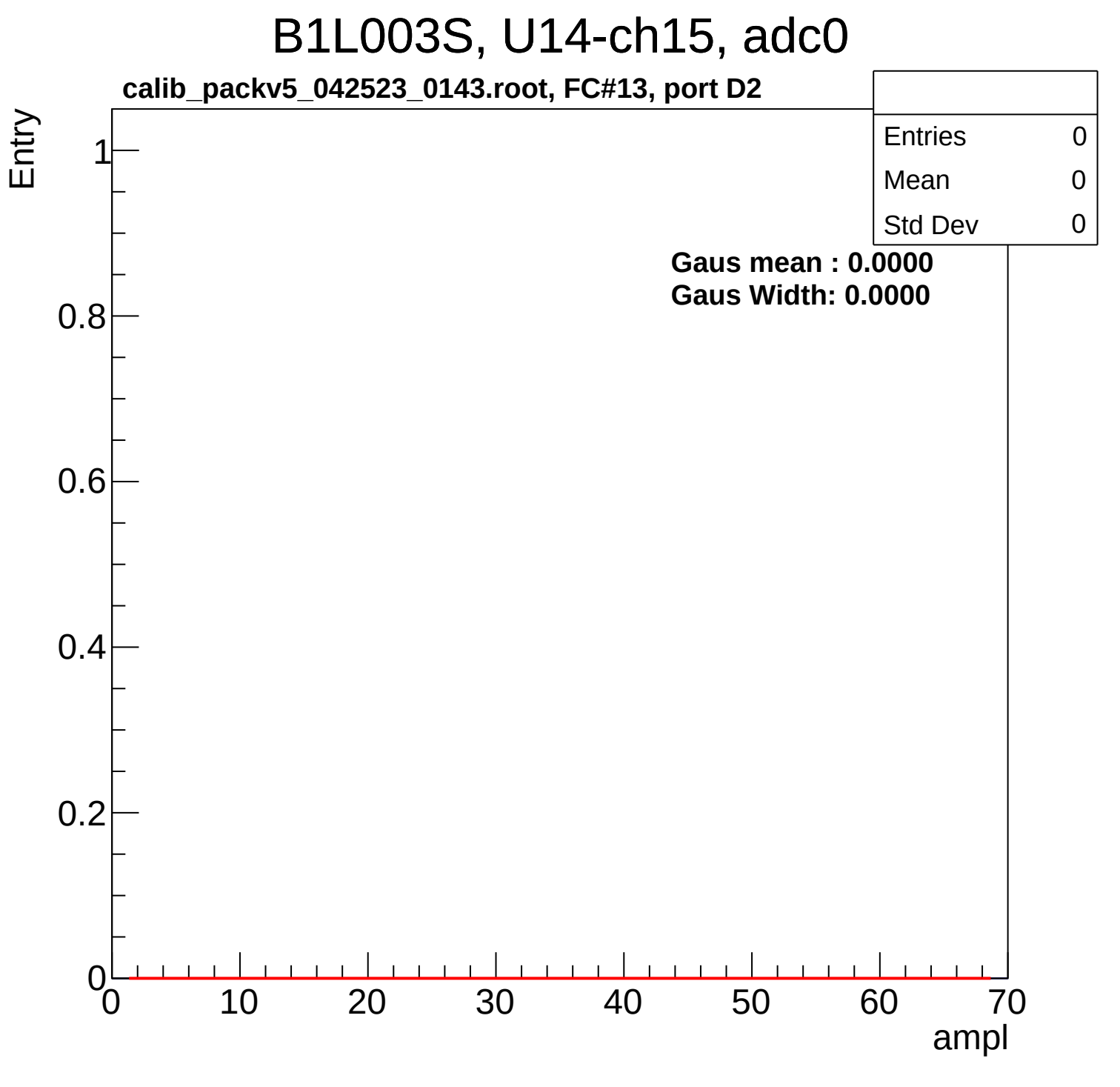

B1L003S, U14-ch14, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

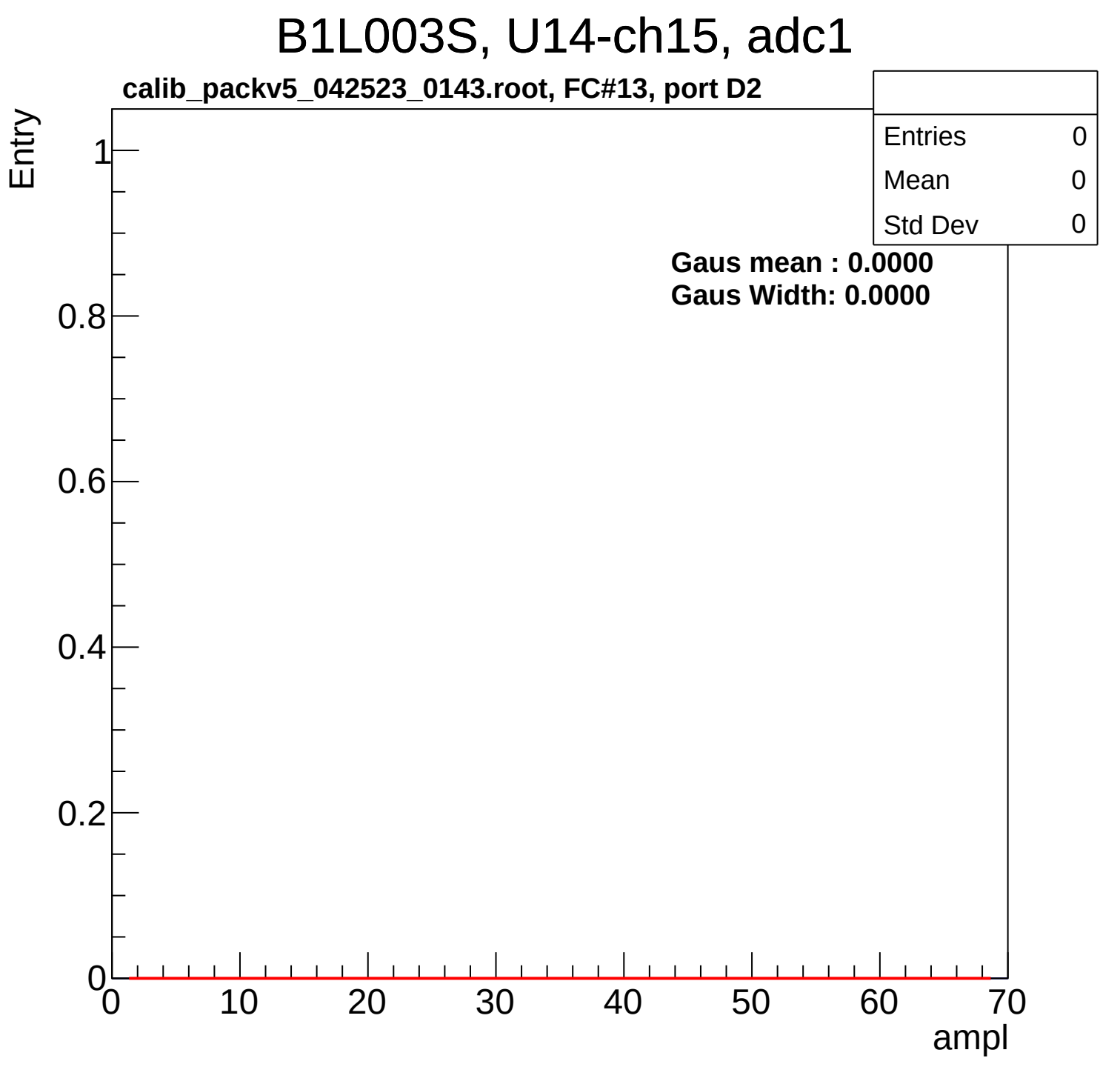

B1L003S, U14-ch14, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

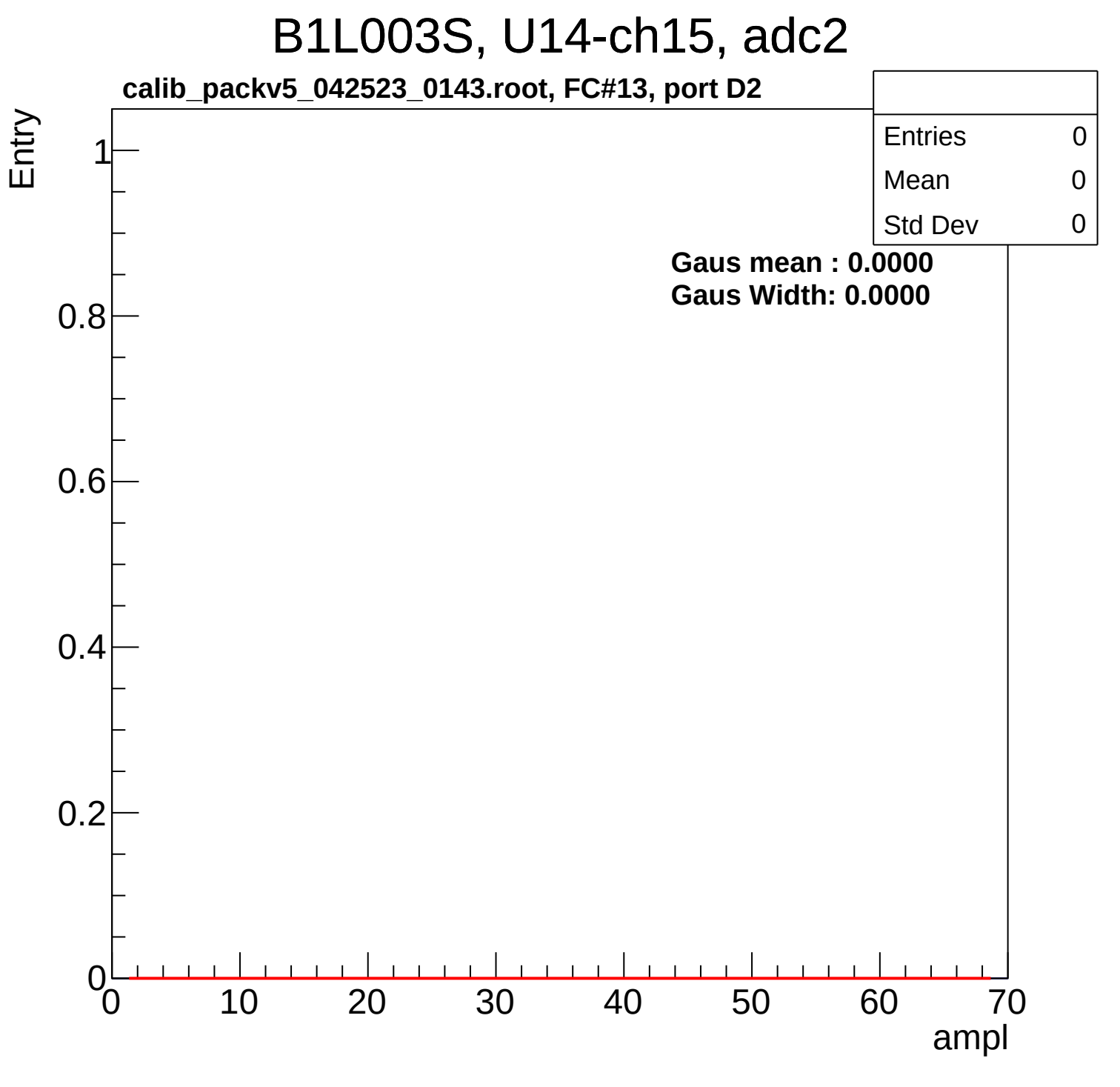

B1L003S, U14-ch14, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

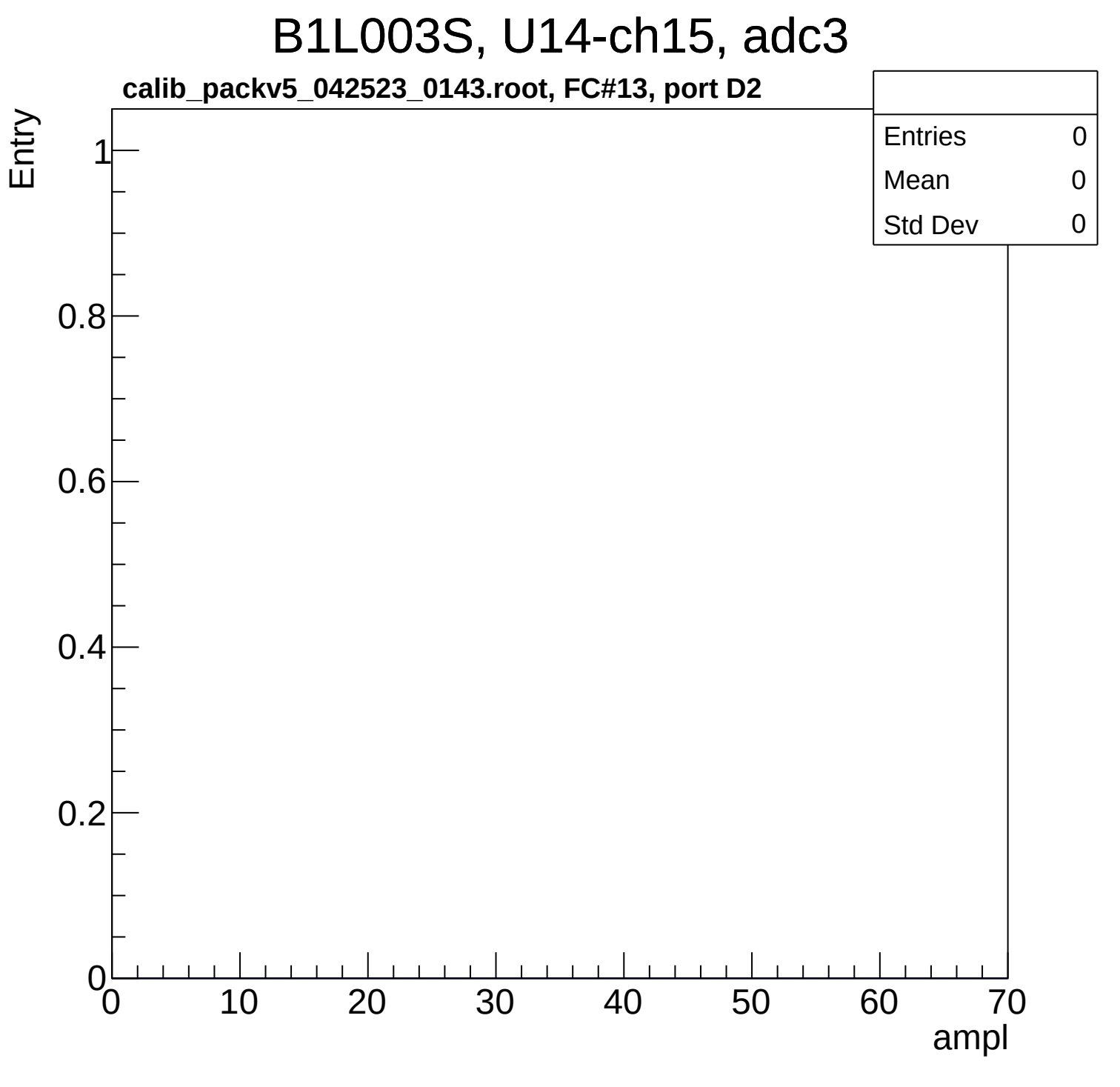

B1L003S, U14-ch14, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

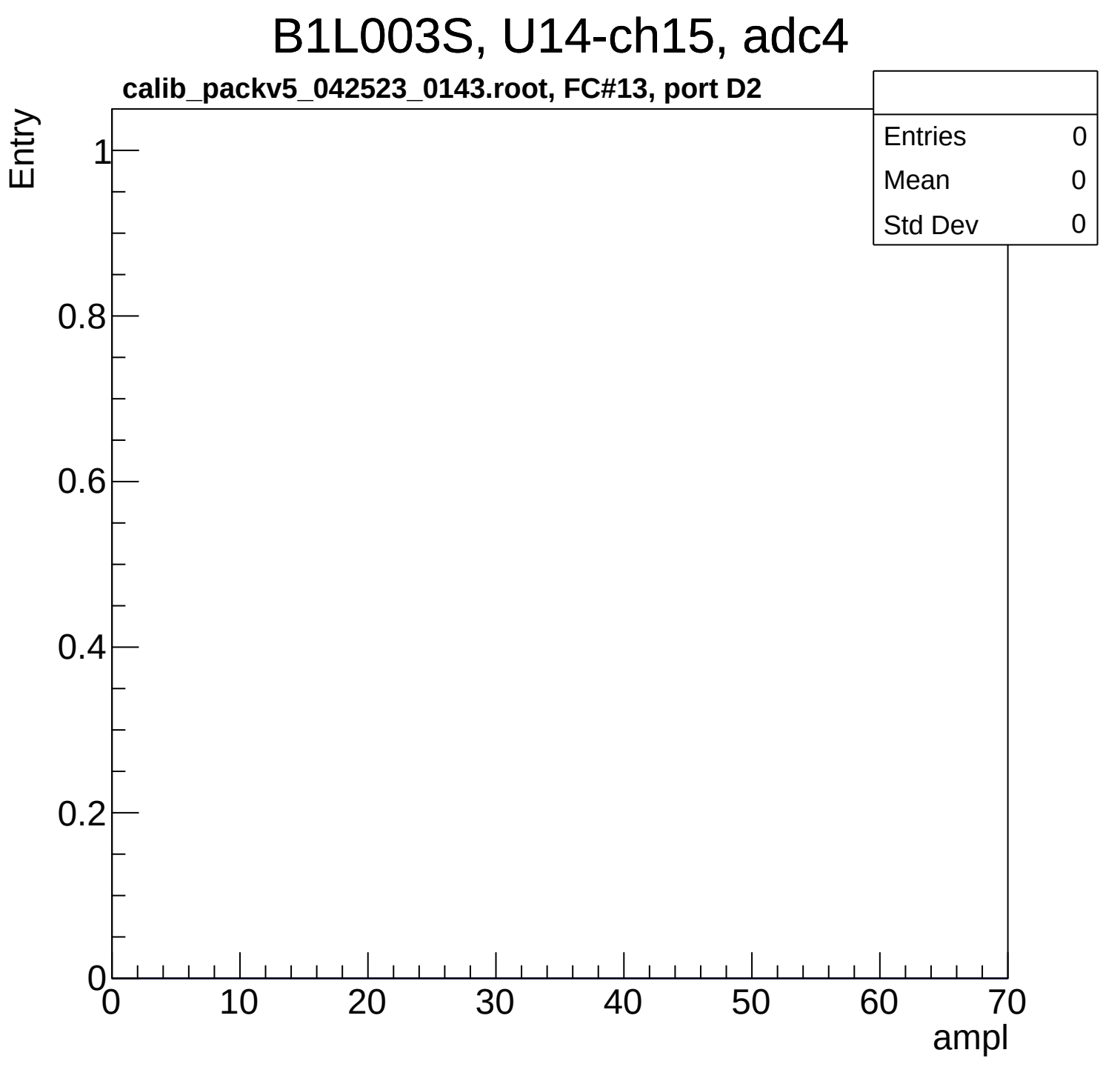

B1L003S, U14-ch14, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

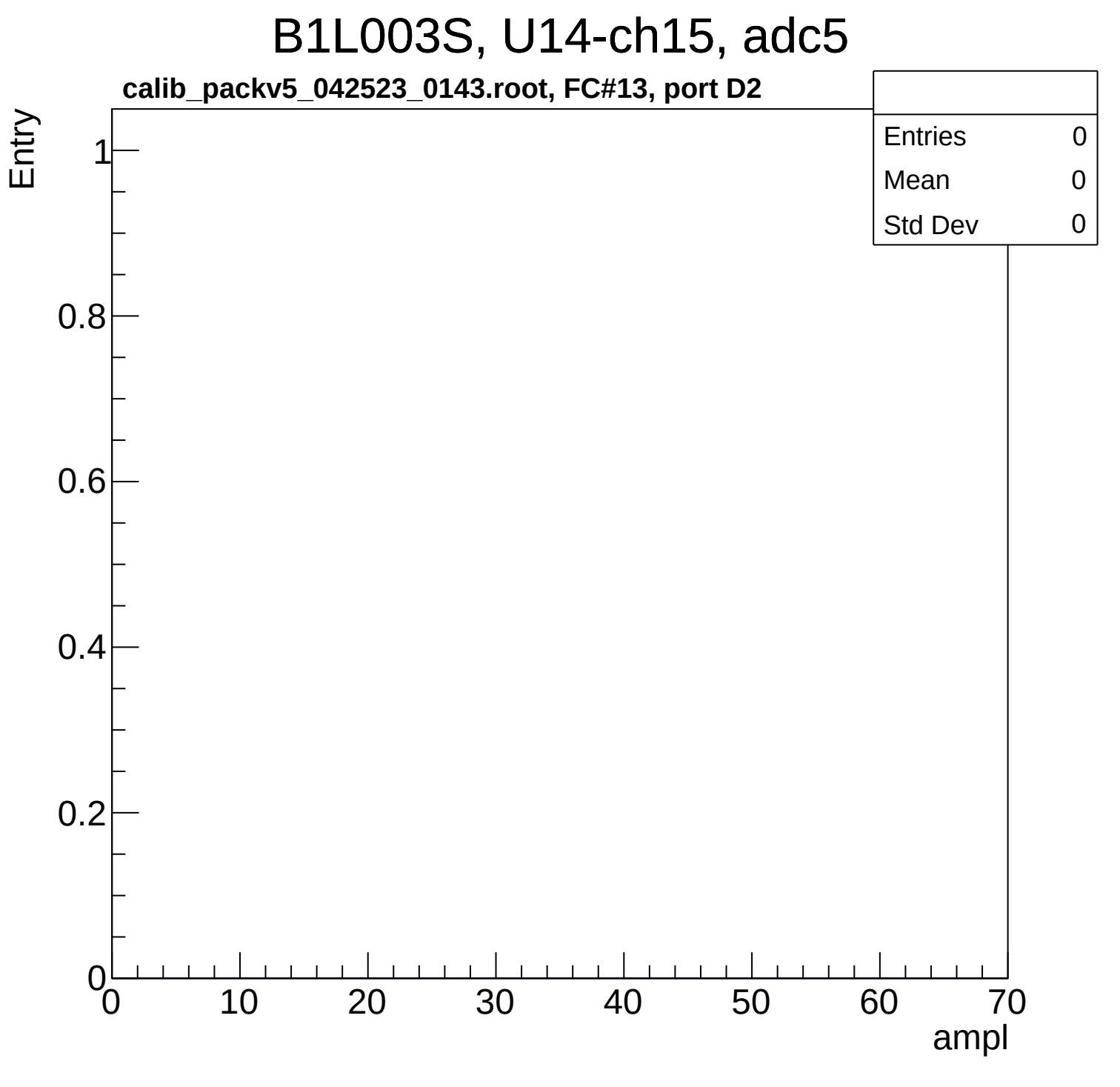

B1L003S, U14-ch15, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

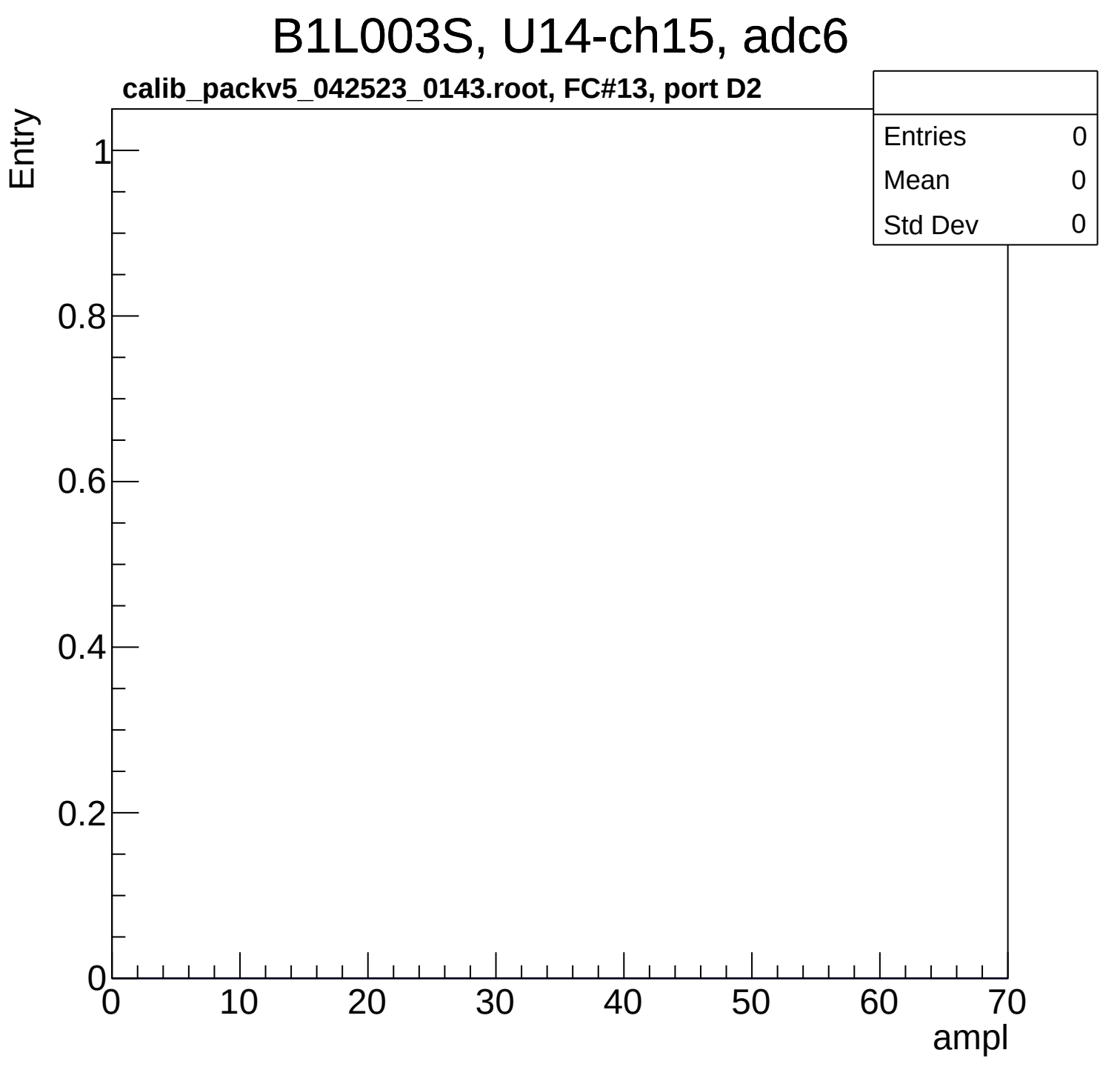

B1L003S, U14-ch15, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

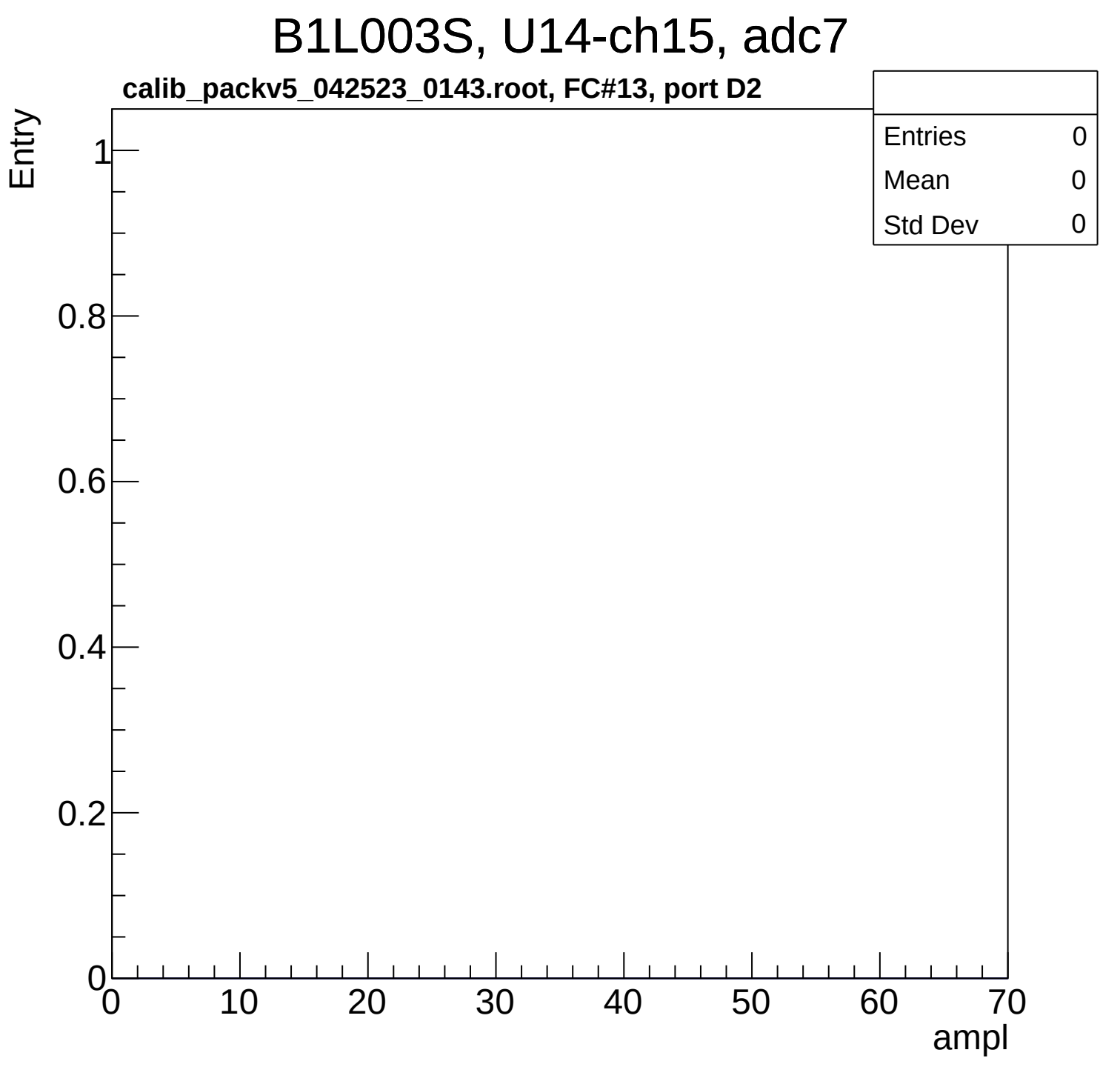

B1L003S, U14-ch15, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

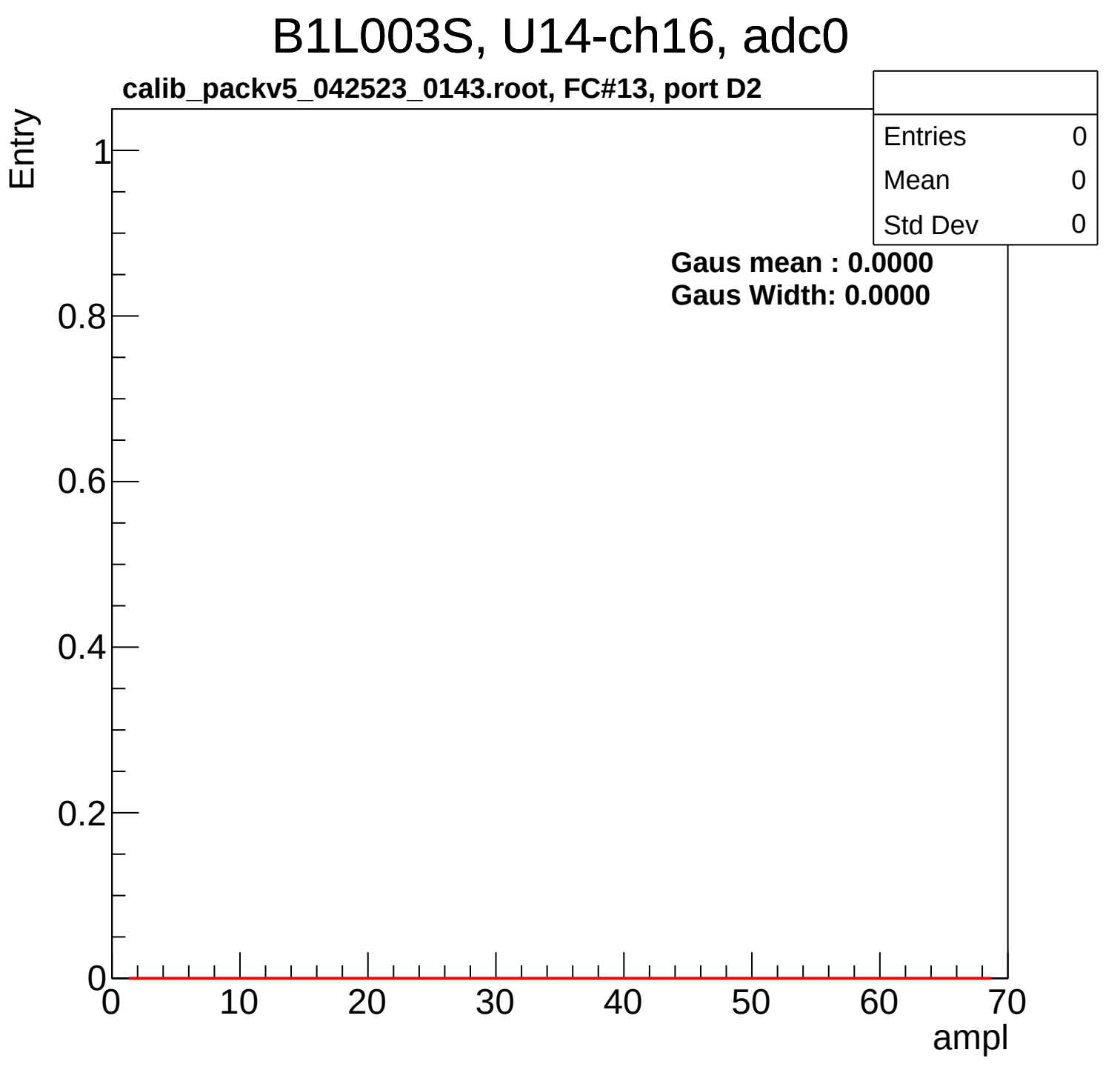

B1L003S, U14-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

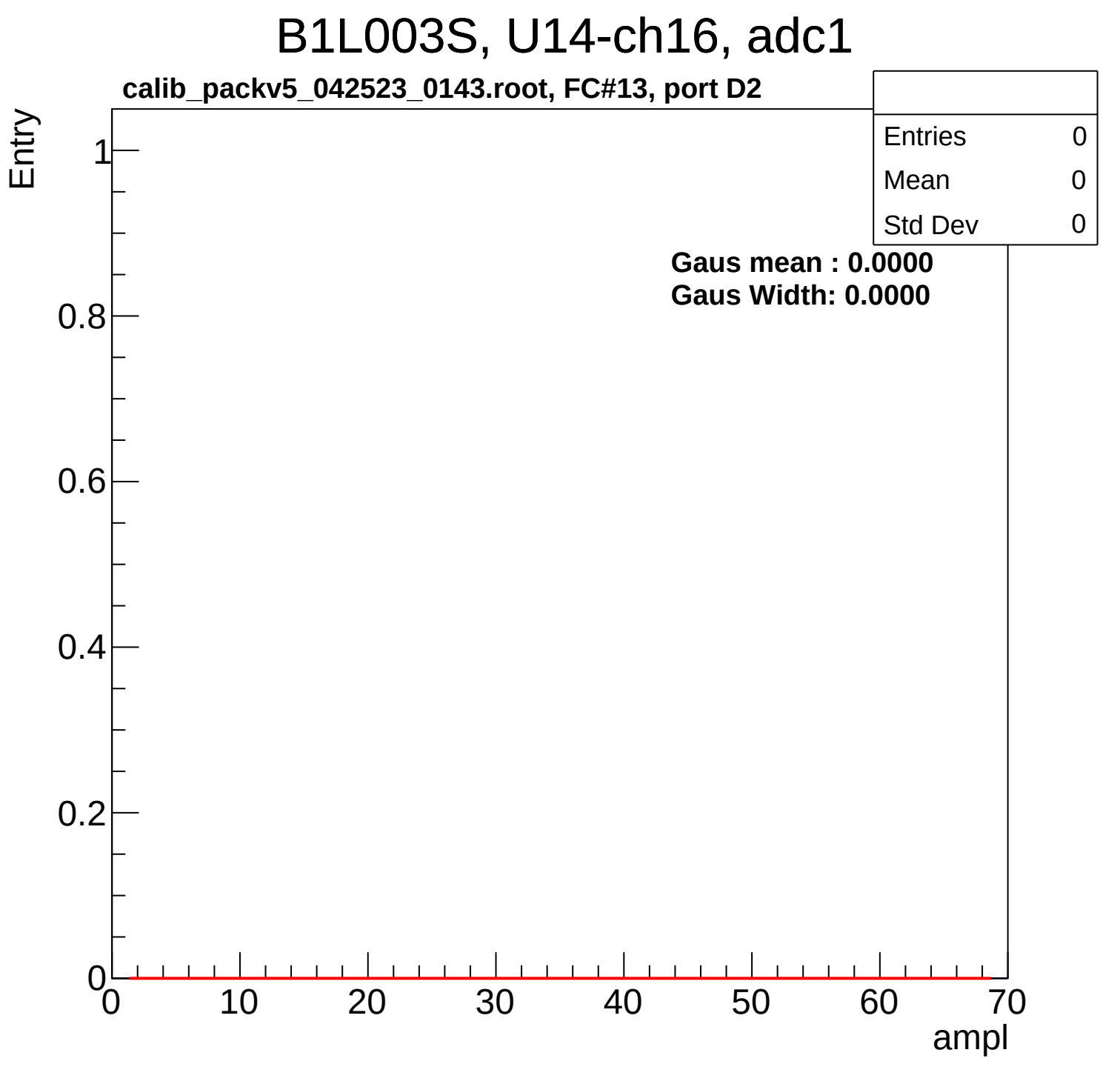

B1L003S, U14-ch15, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

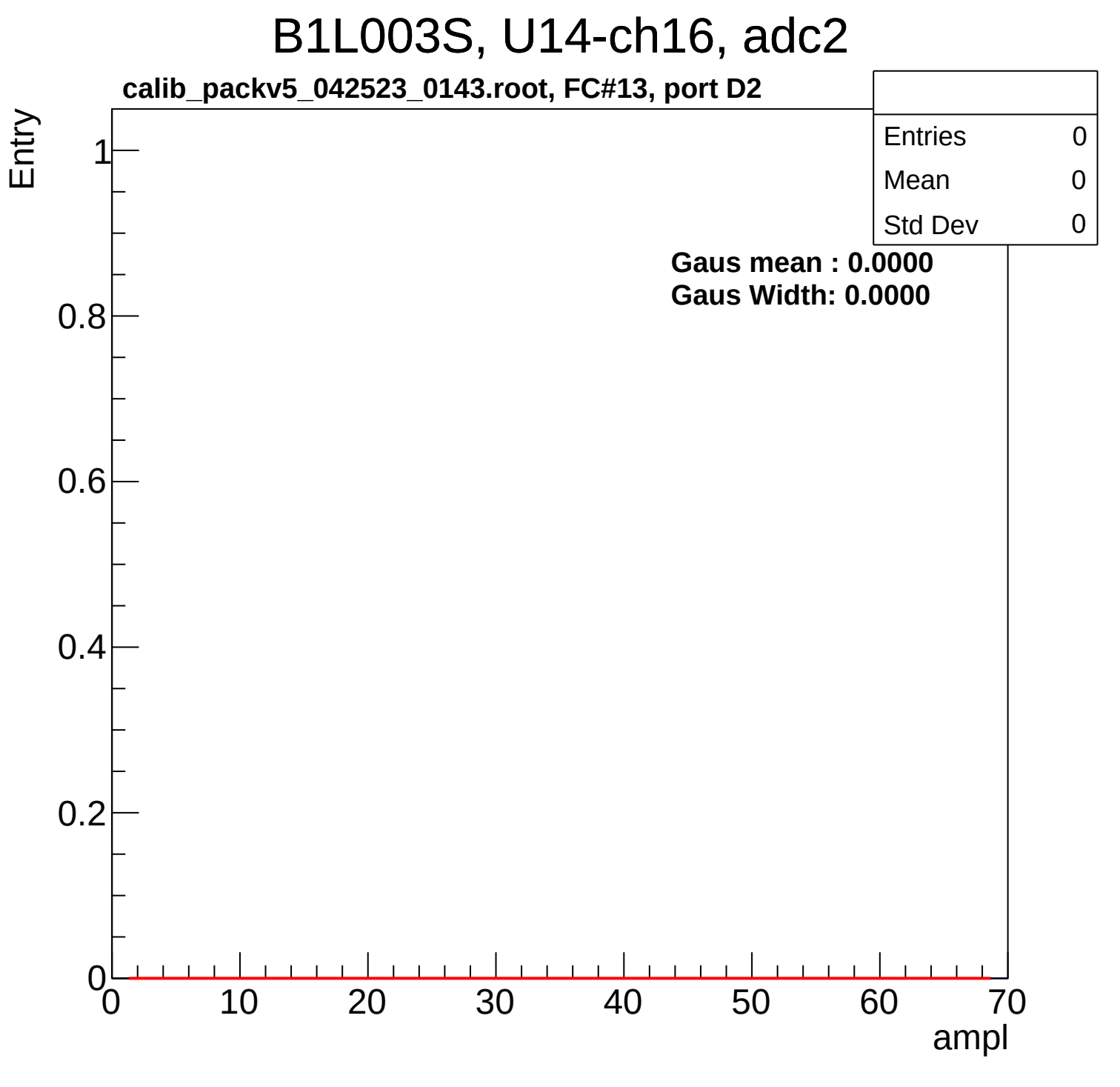

B1L003S, U14-ch15, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

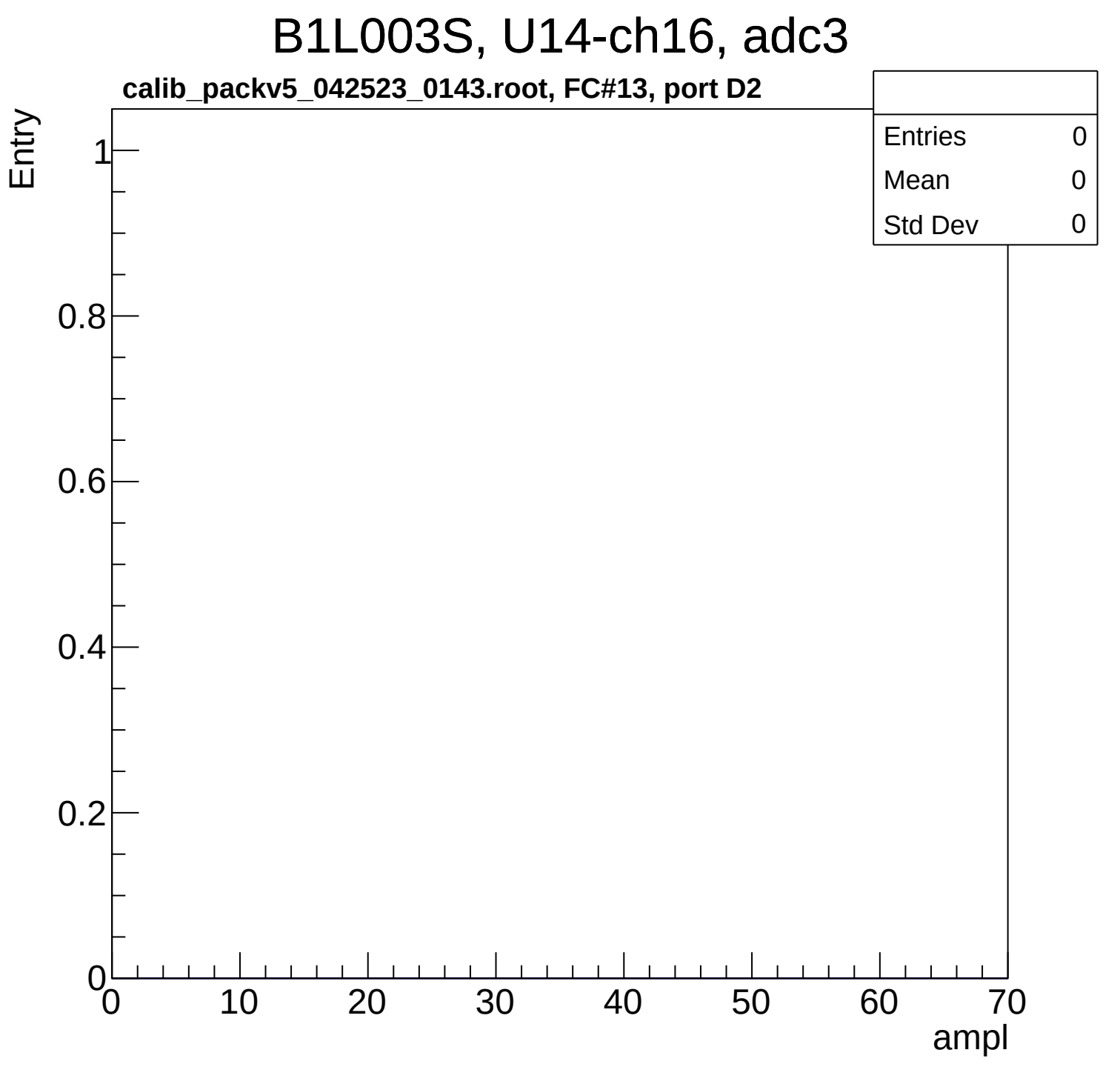

B1L003S, U14-ch15, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

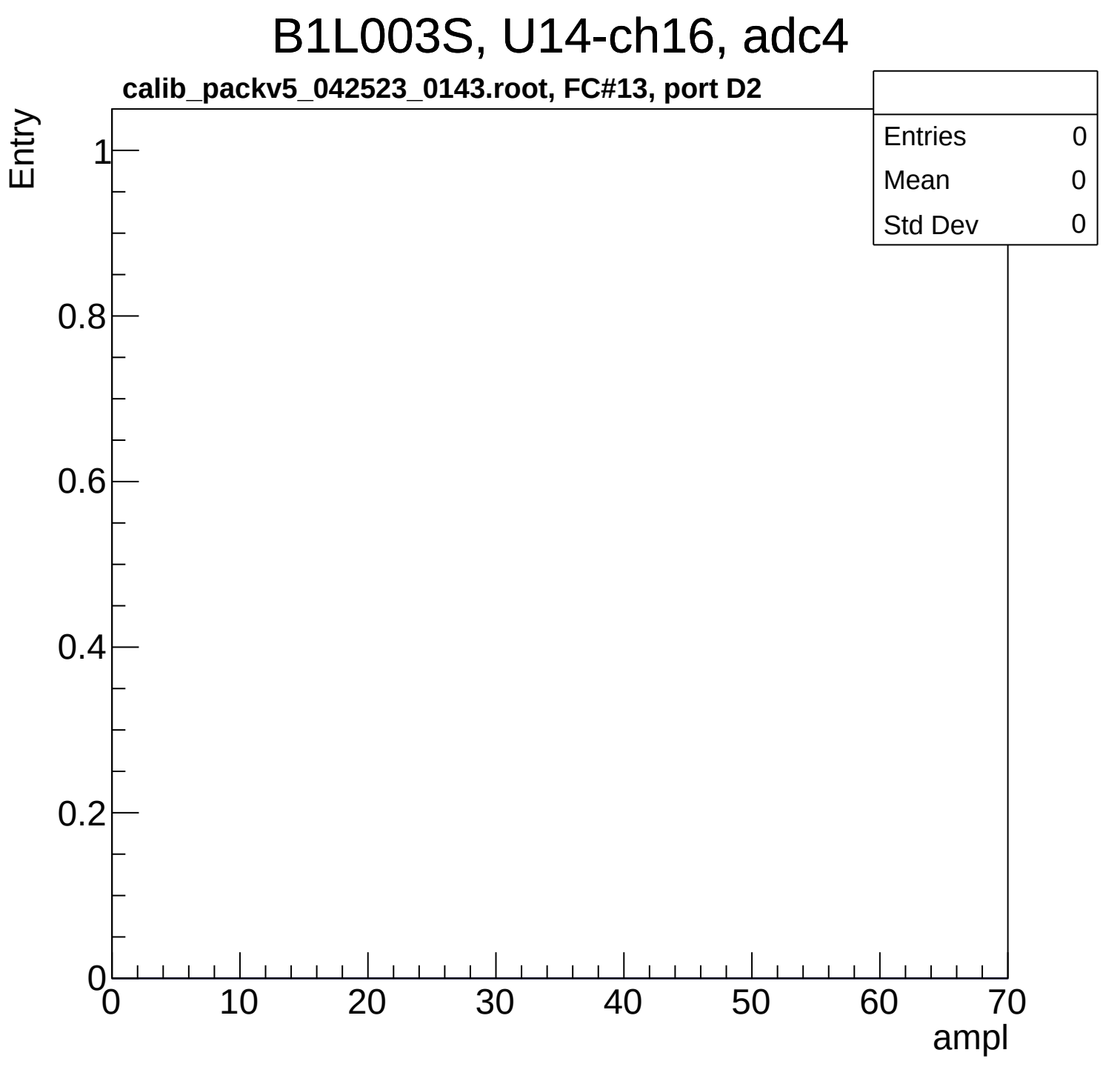

B1L003S, U14-ch15, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

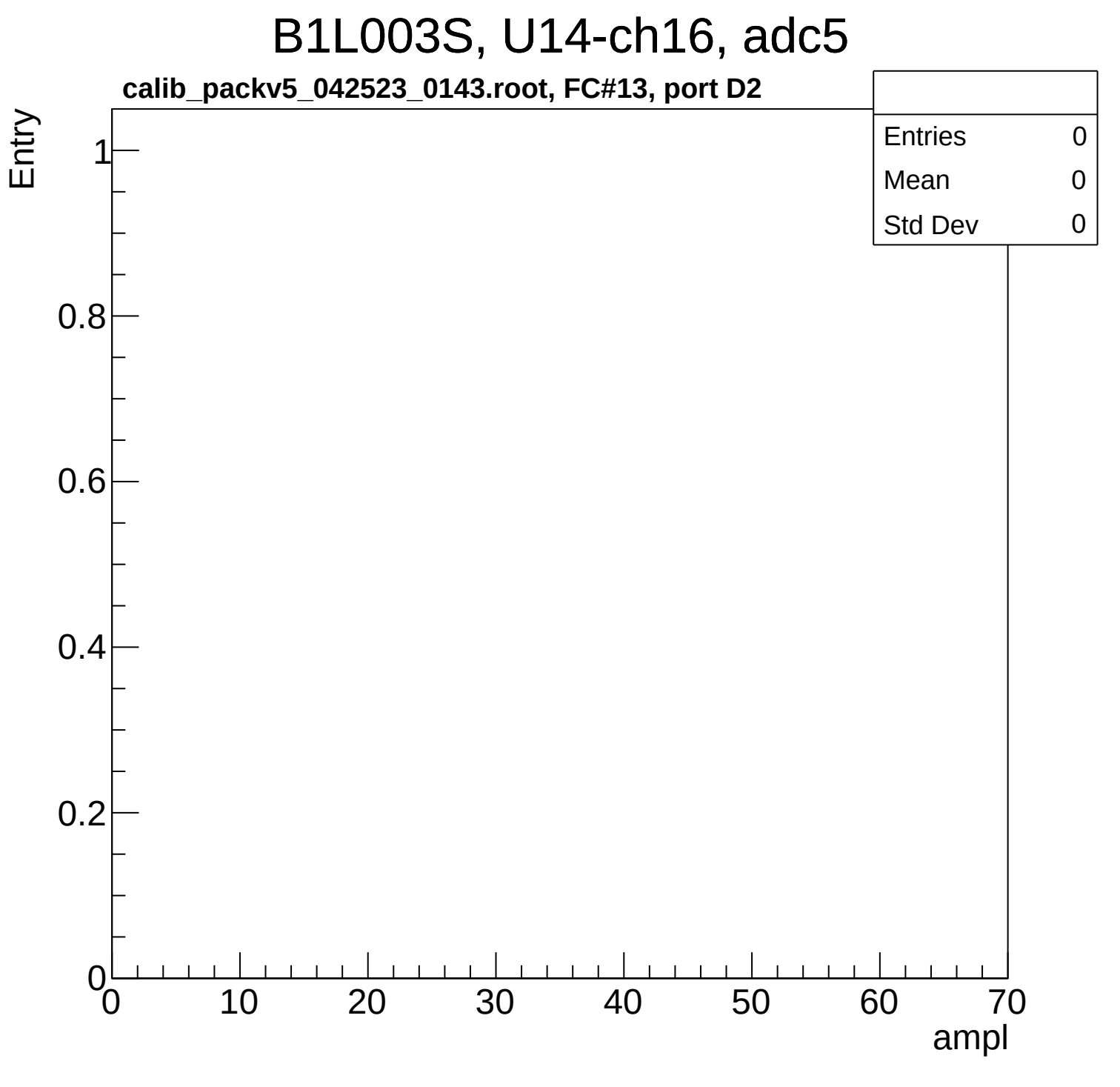

B1L003S, U14-ch16, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

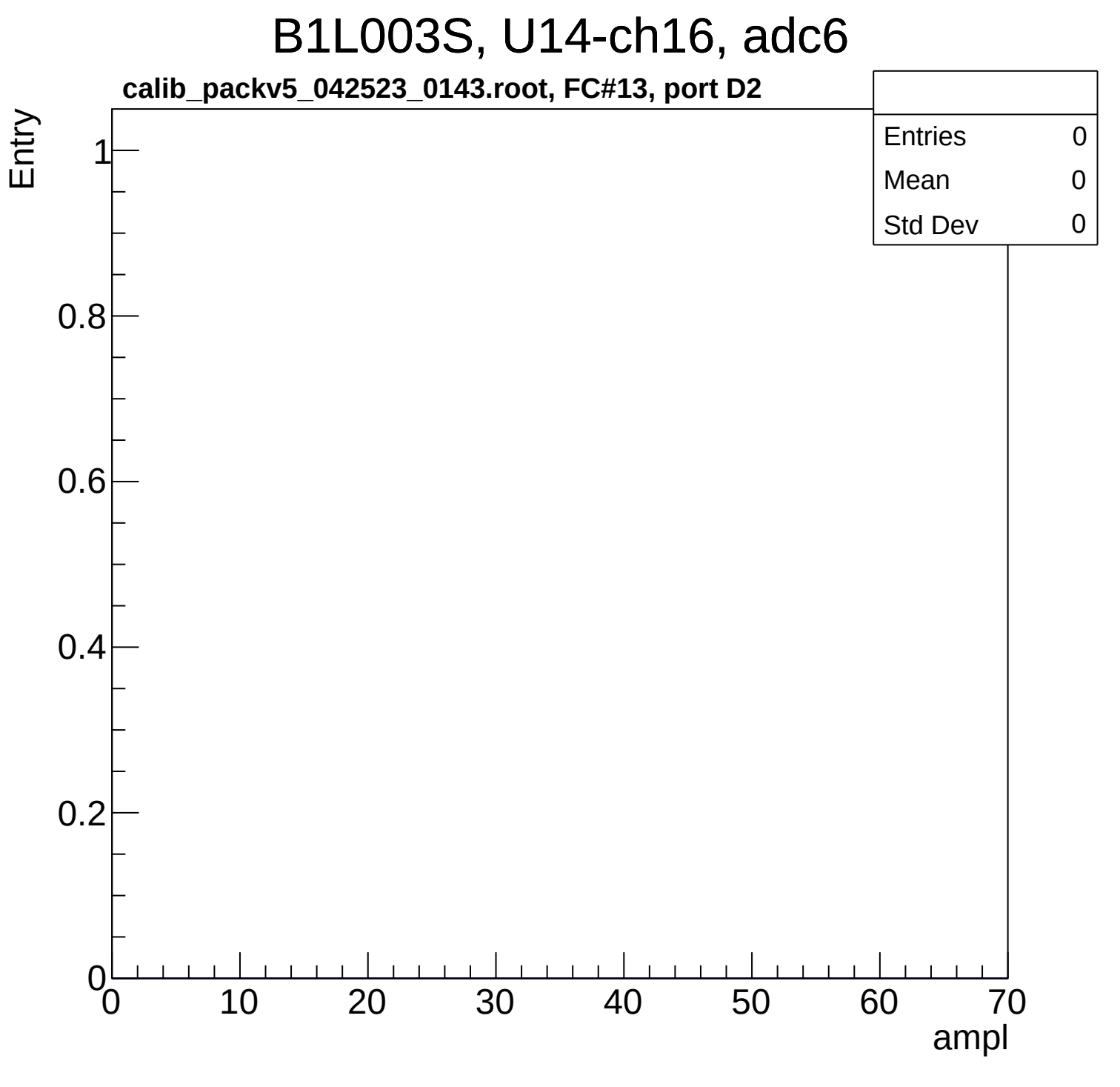

B1L003S, U14-ch16, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

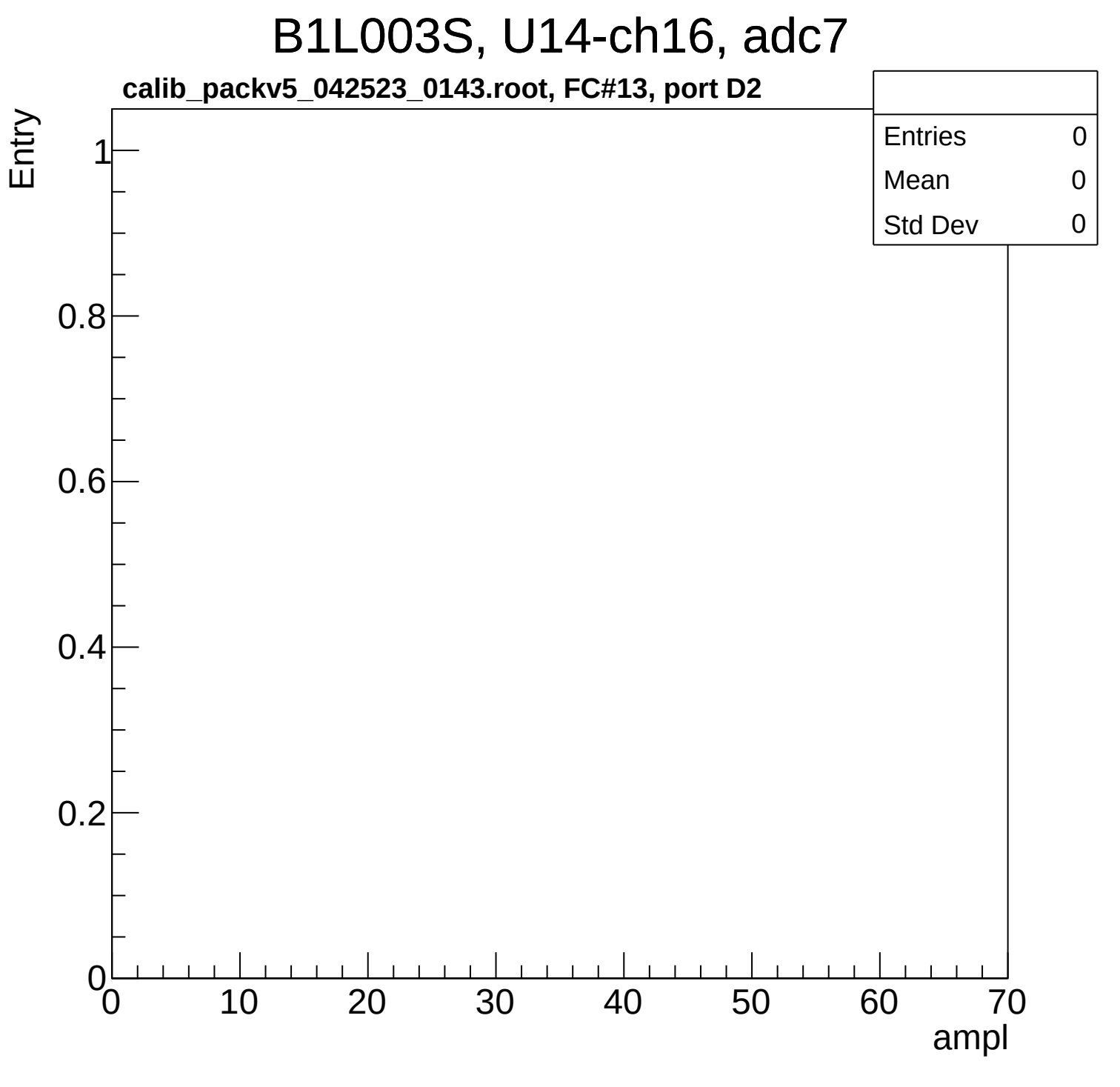

B1L003S, U14-ch16, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

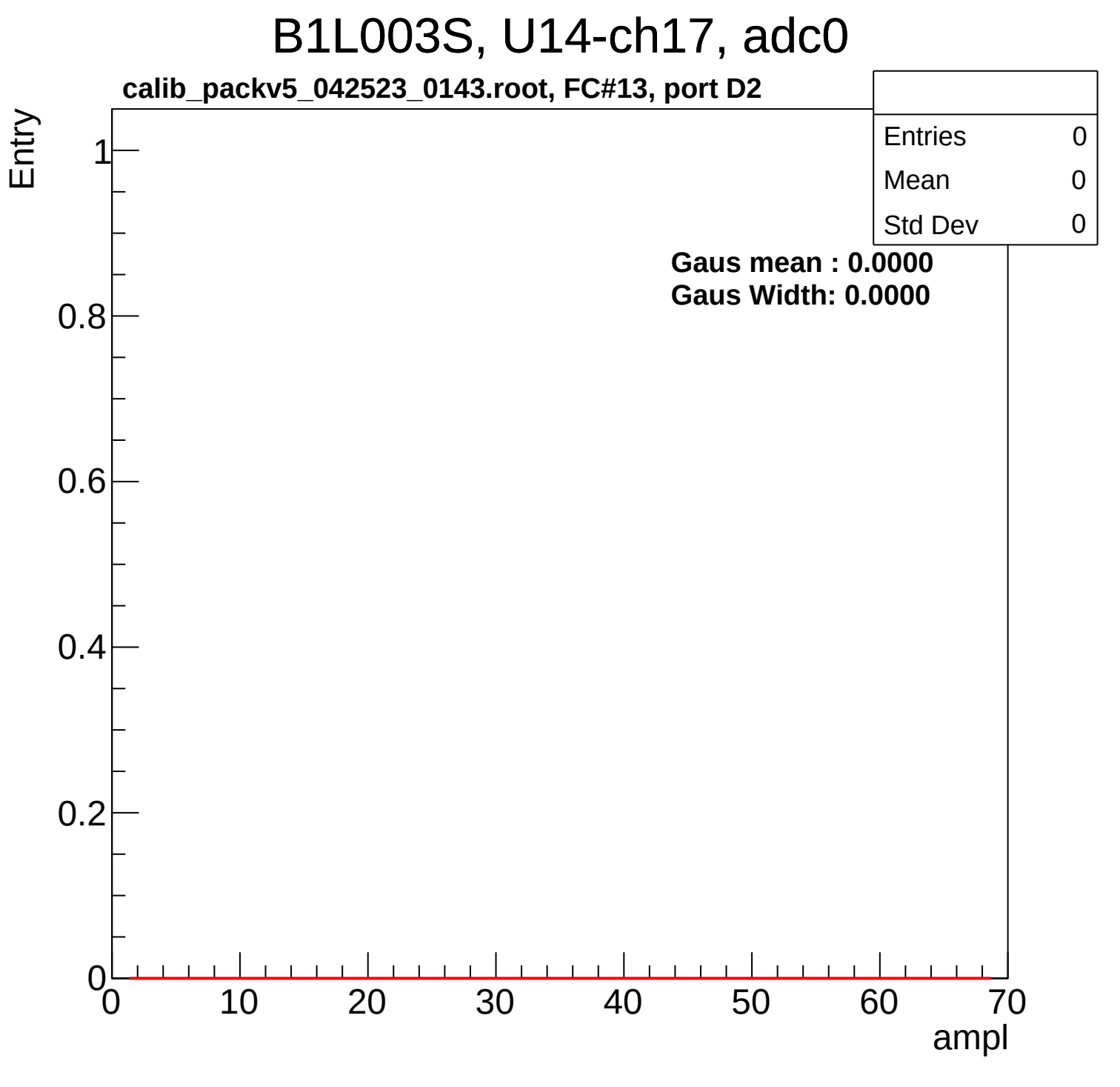

B1L003S, U14-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

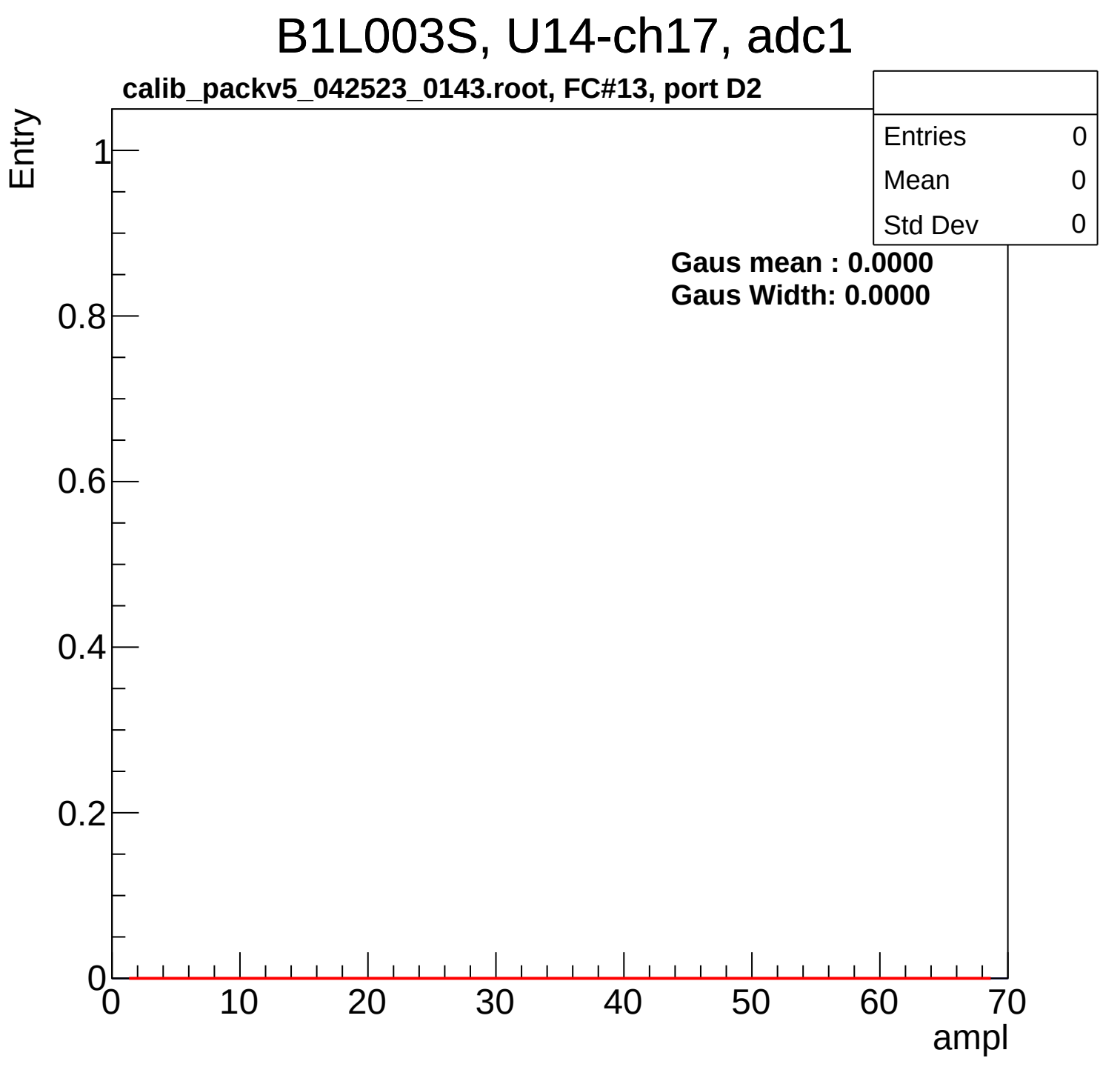

B1L003S, U14-ch16, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

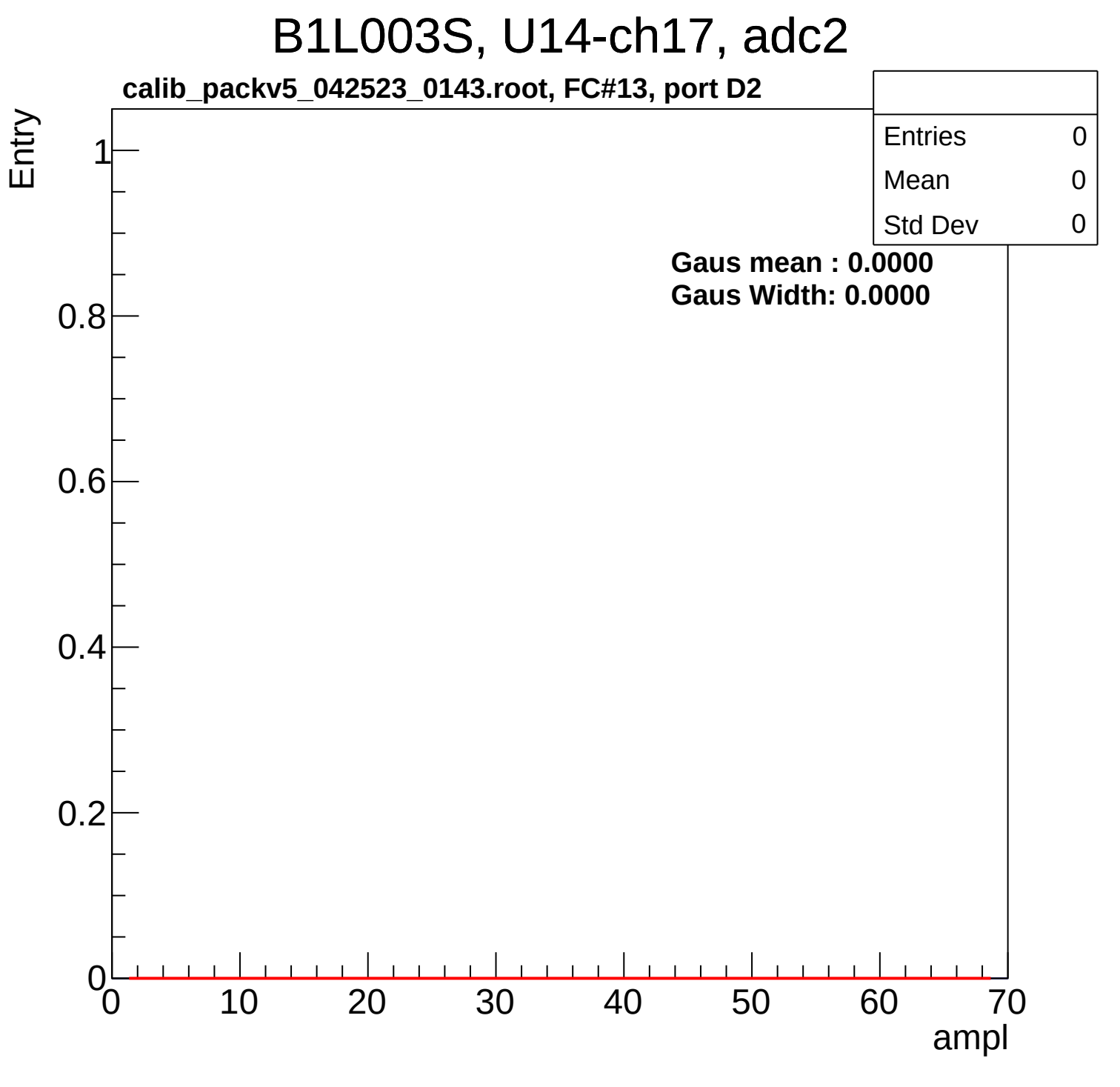

B1L003S, U14-ch16, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

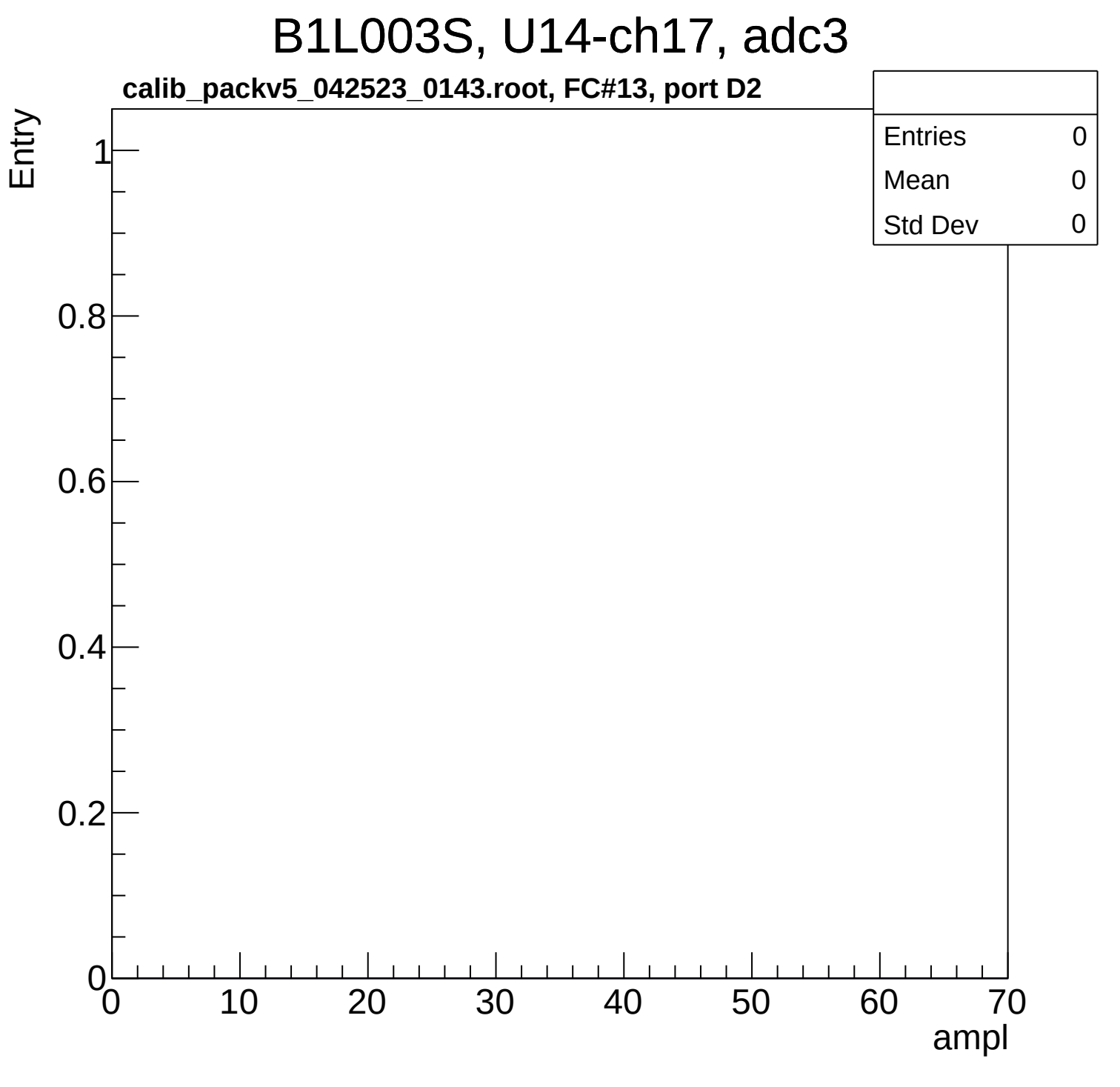

B1L003S, U14-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

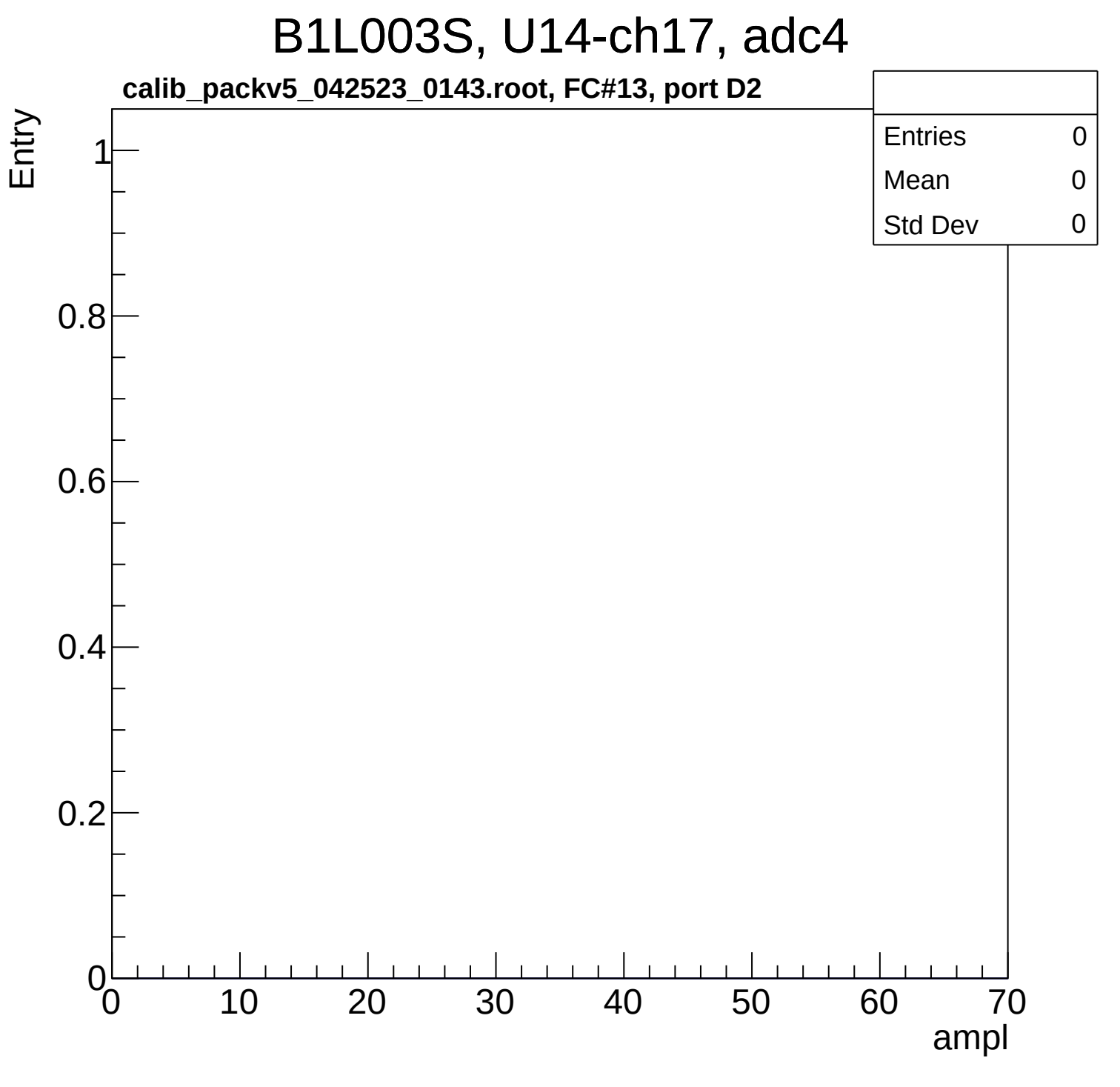

B1L003S, U14-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

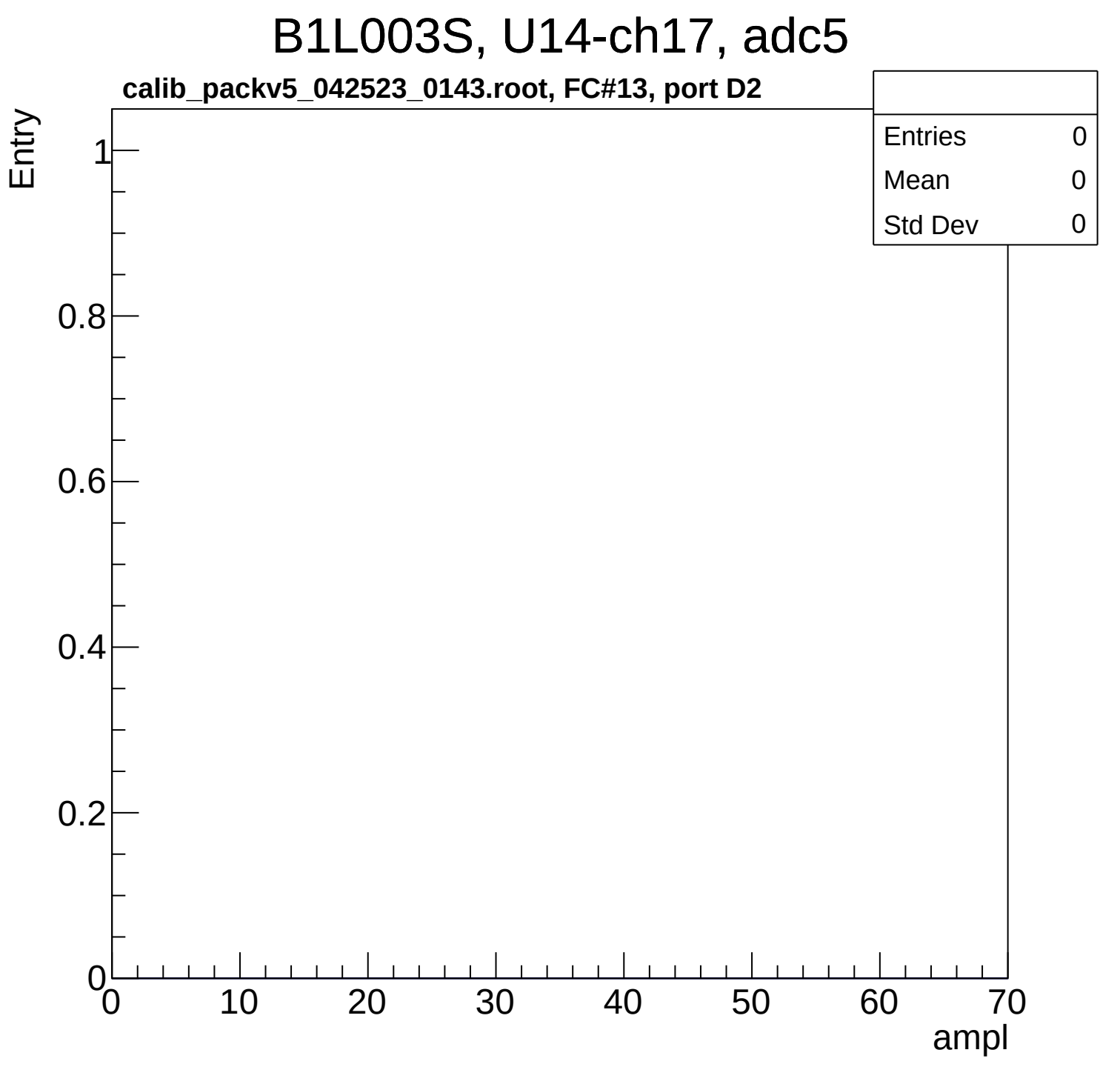

B1L003S, U14-ch17, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

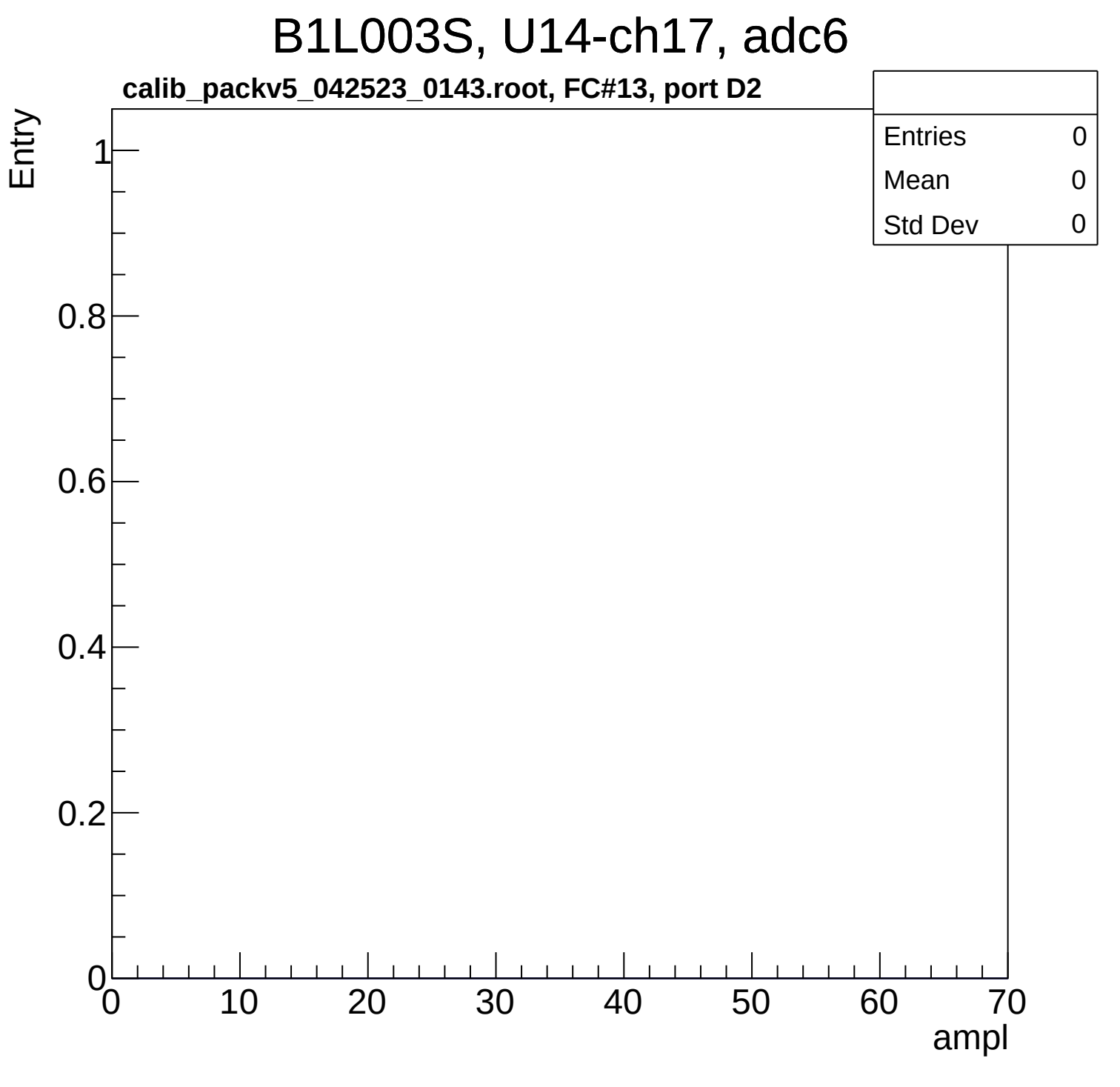

B1L003S, U14-ch17, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

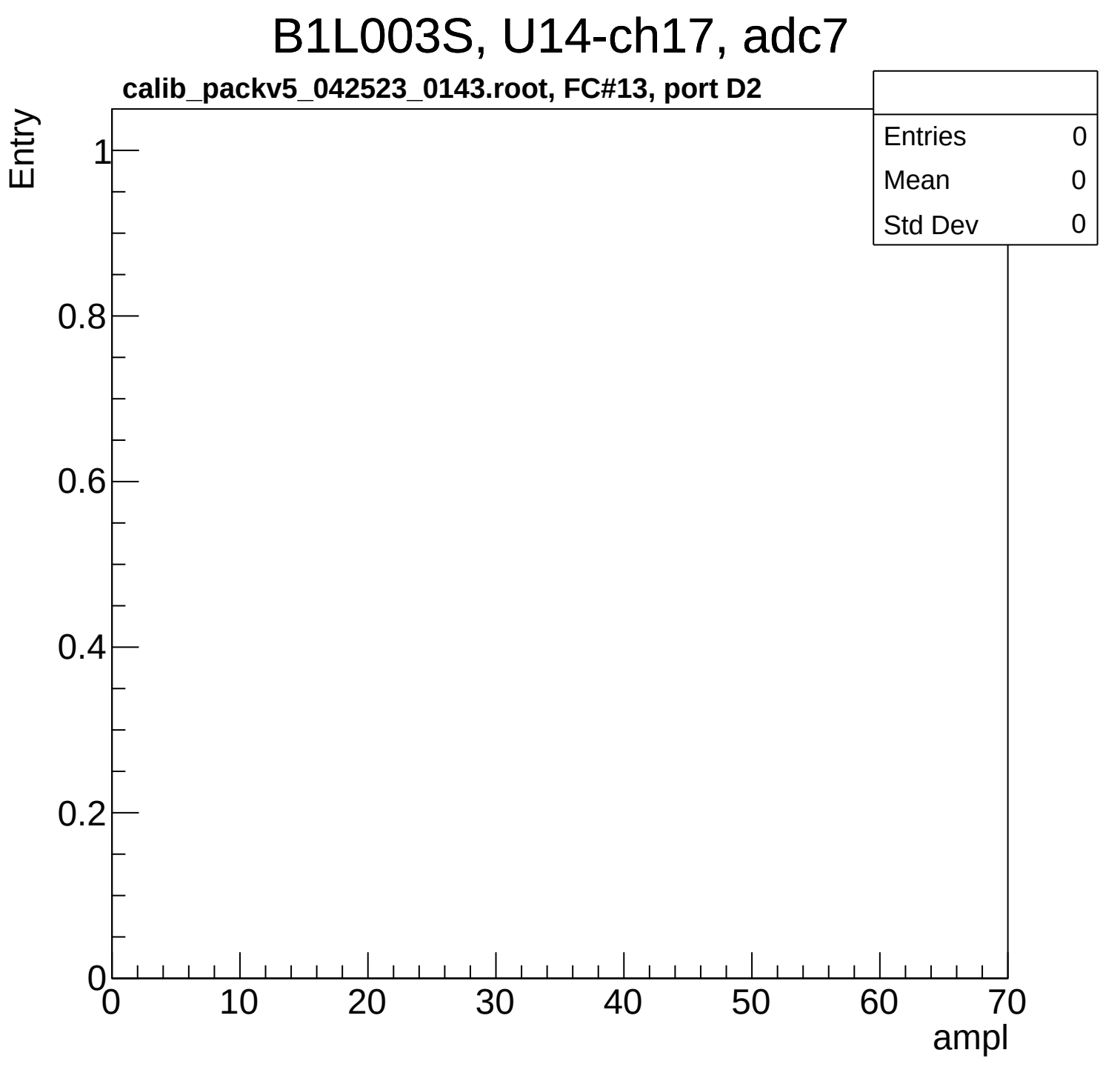

B1L003S, U14-ch17, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

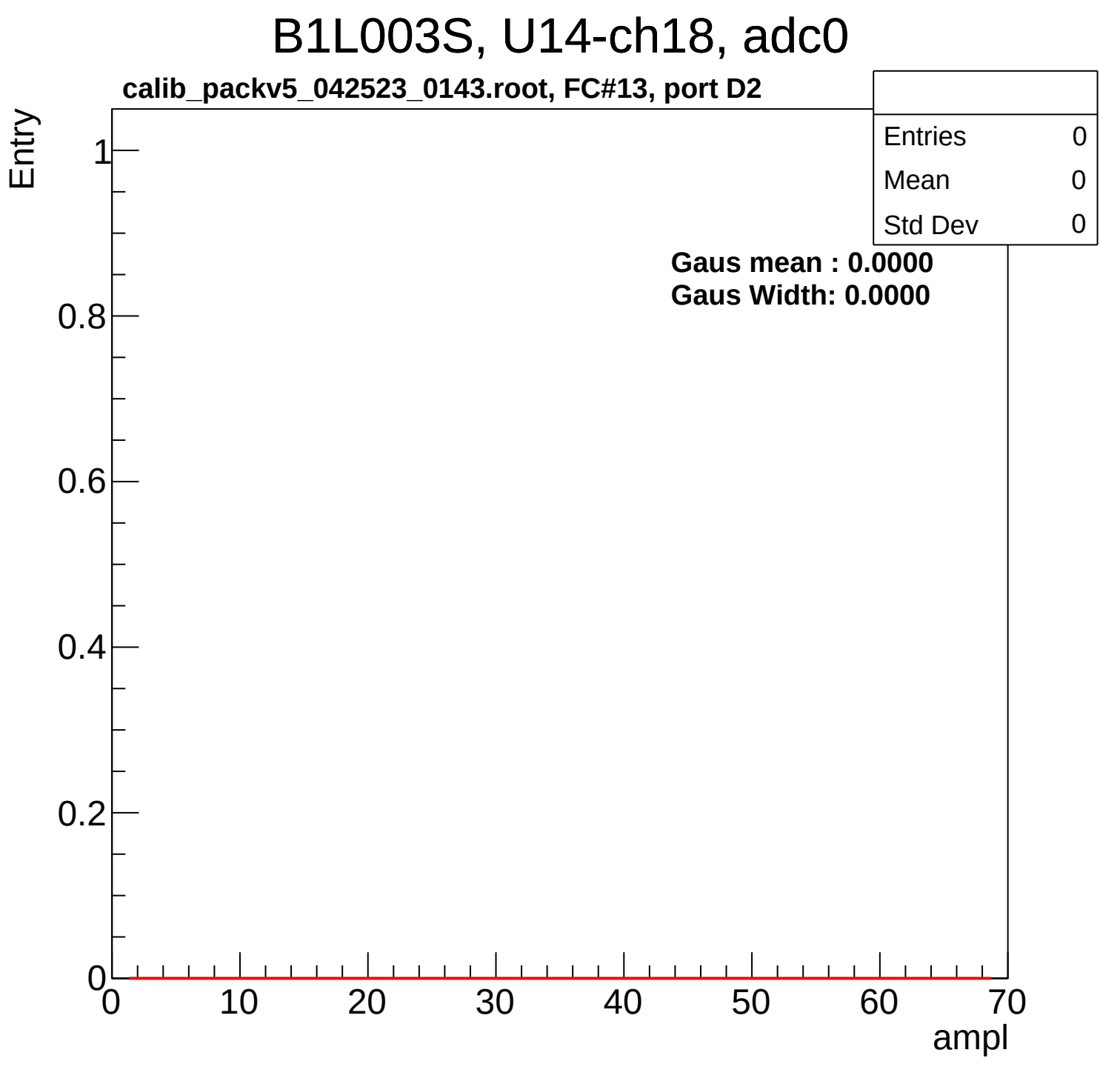

B1L003S, U14-ch17, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

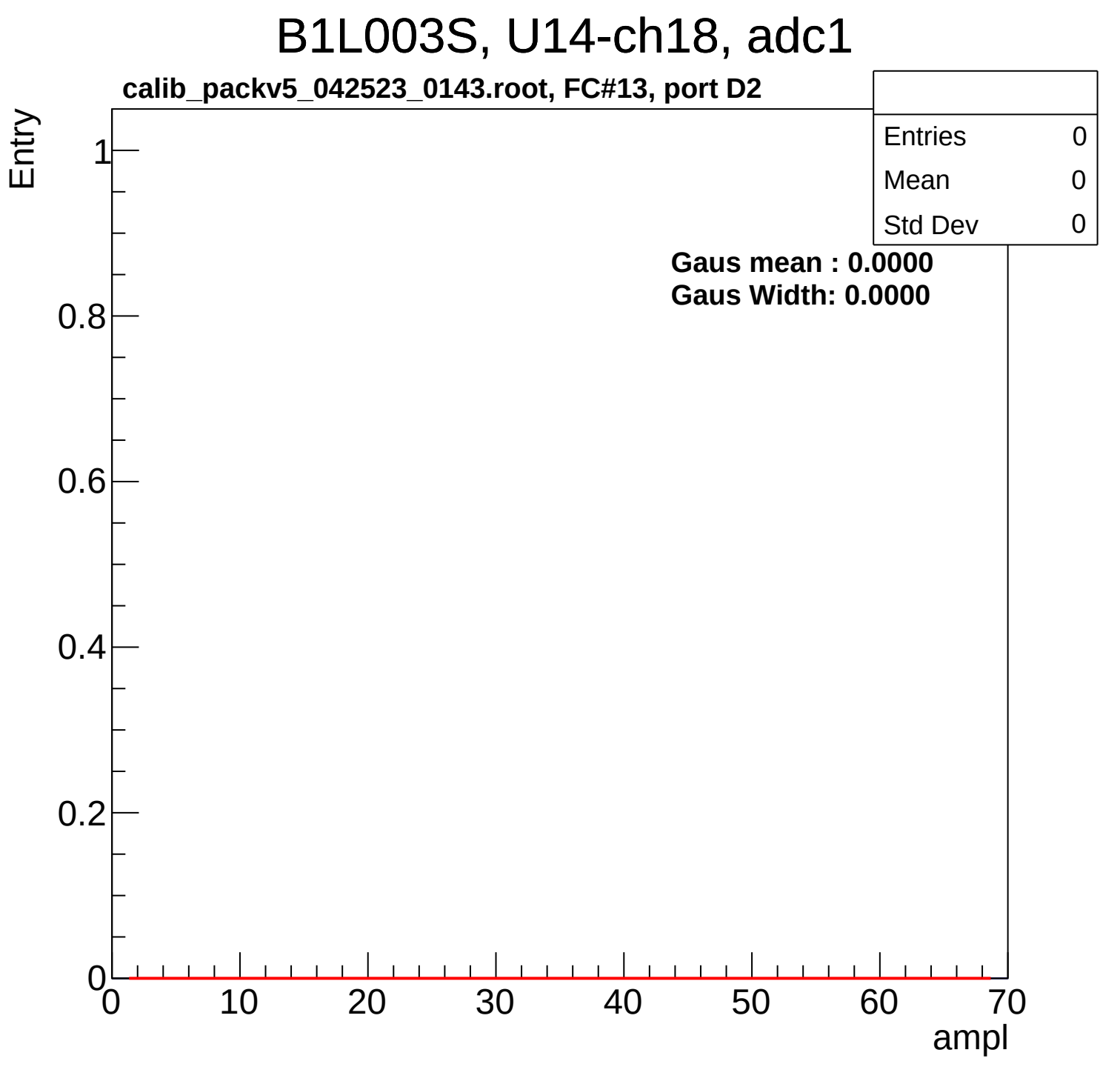

B1L003S, U14-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

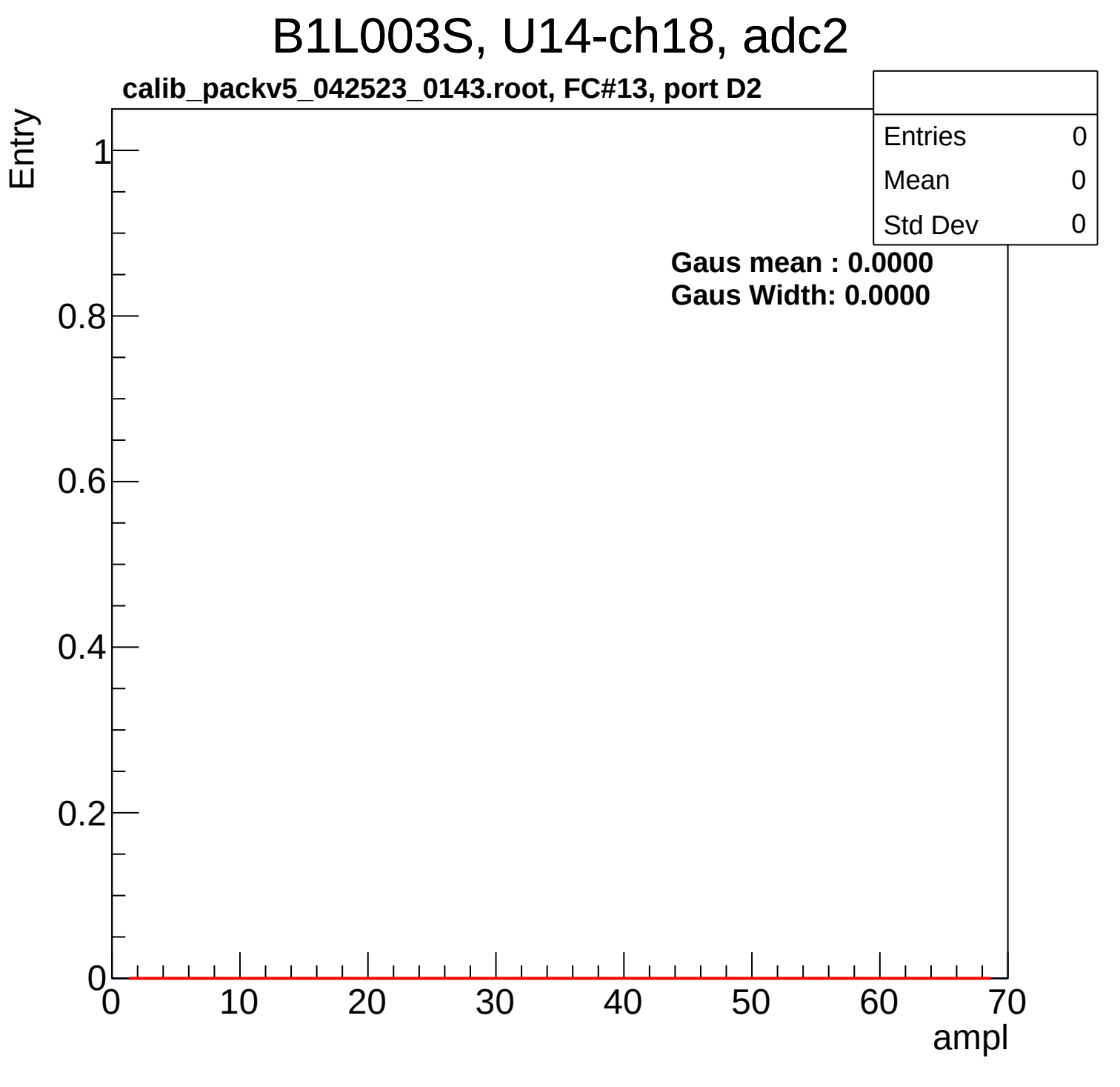

B1L003S, U14-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

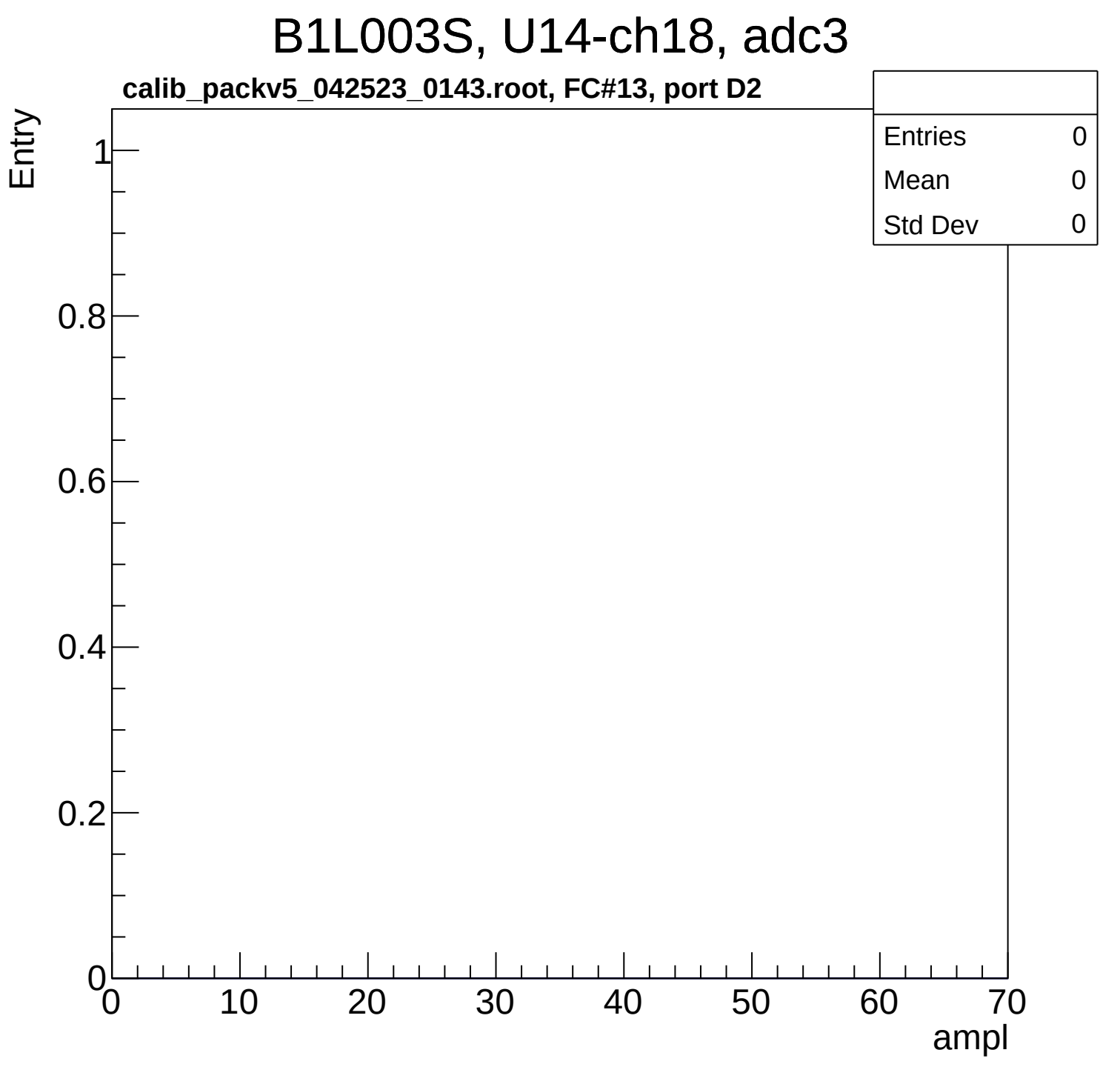

B1L003S, U14-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

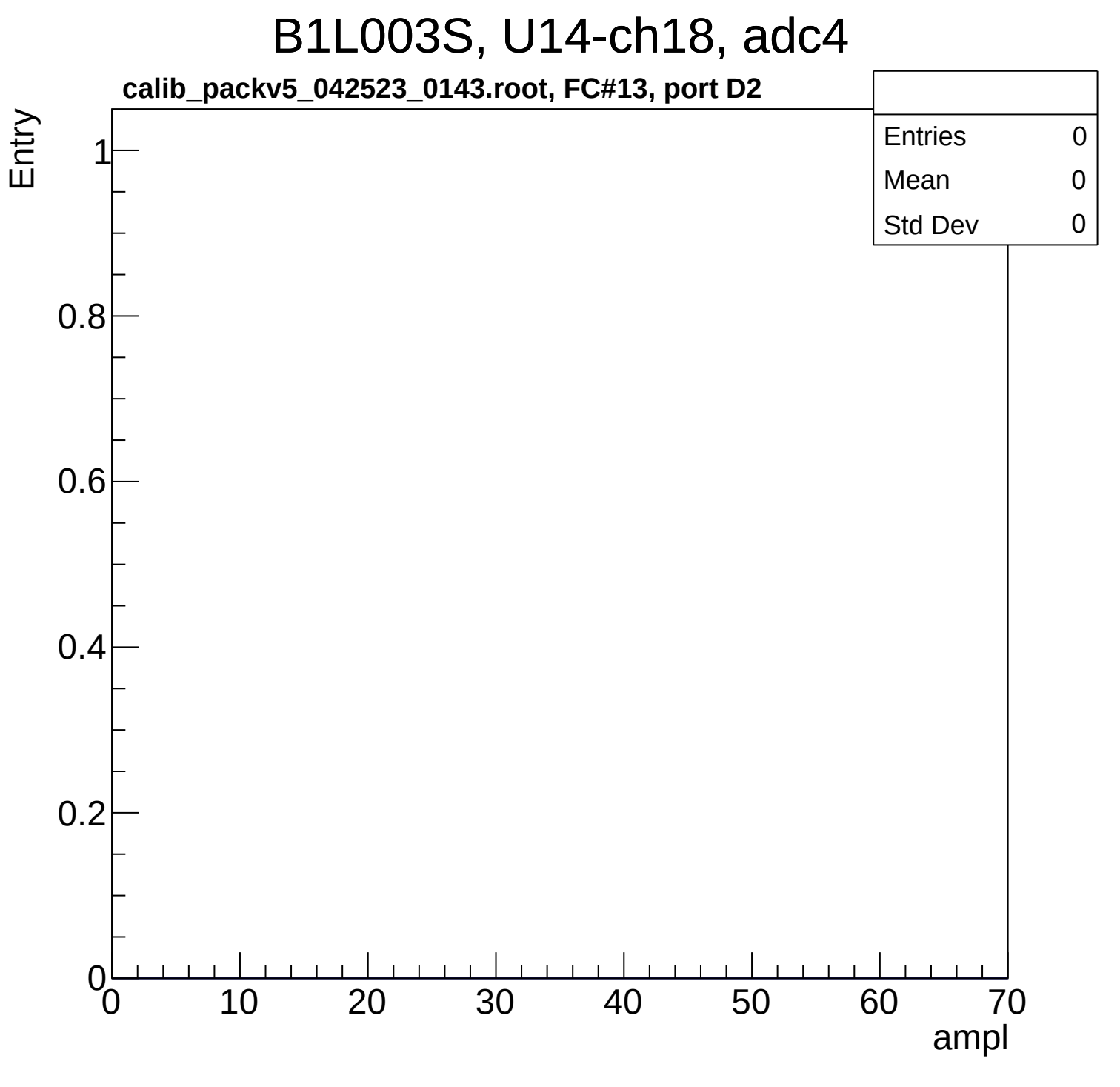

B1L003S, U14-ch17, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

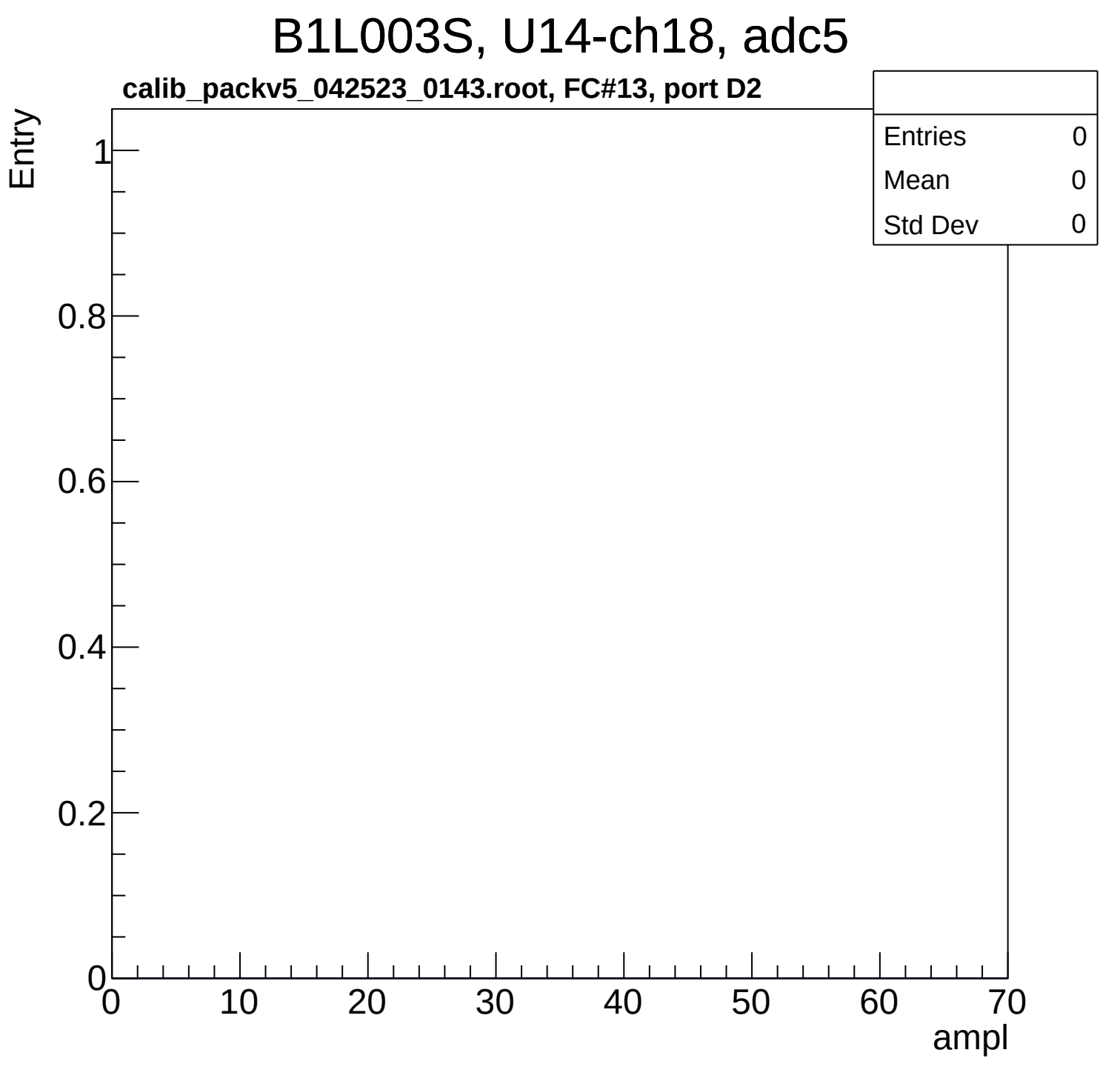

B1L003S, U14-ch18, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

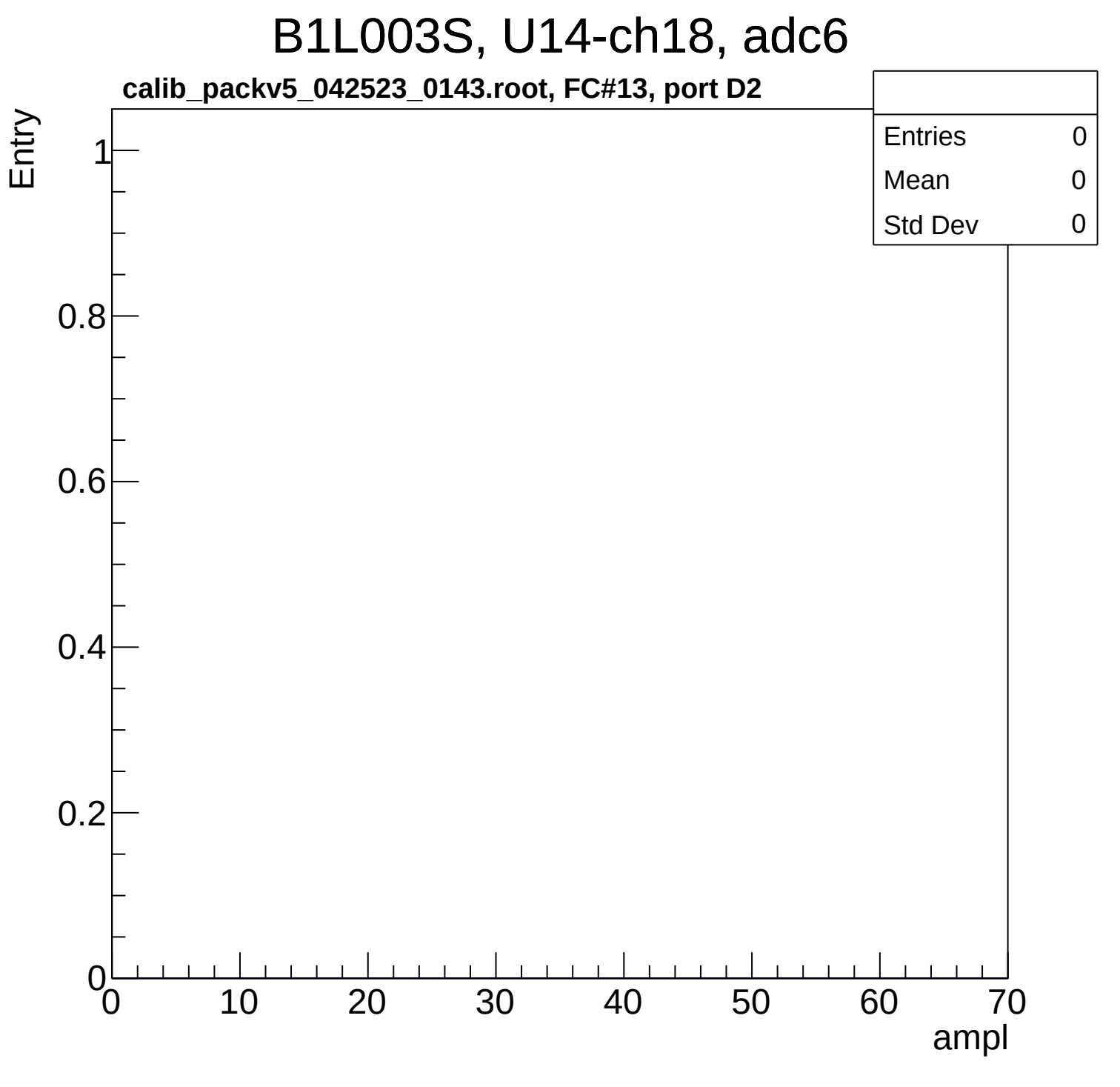

B1L003S, U14-ch18, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

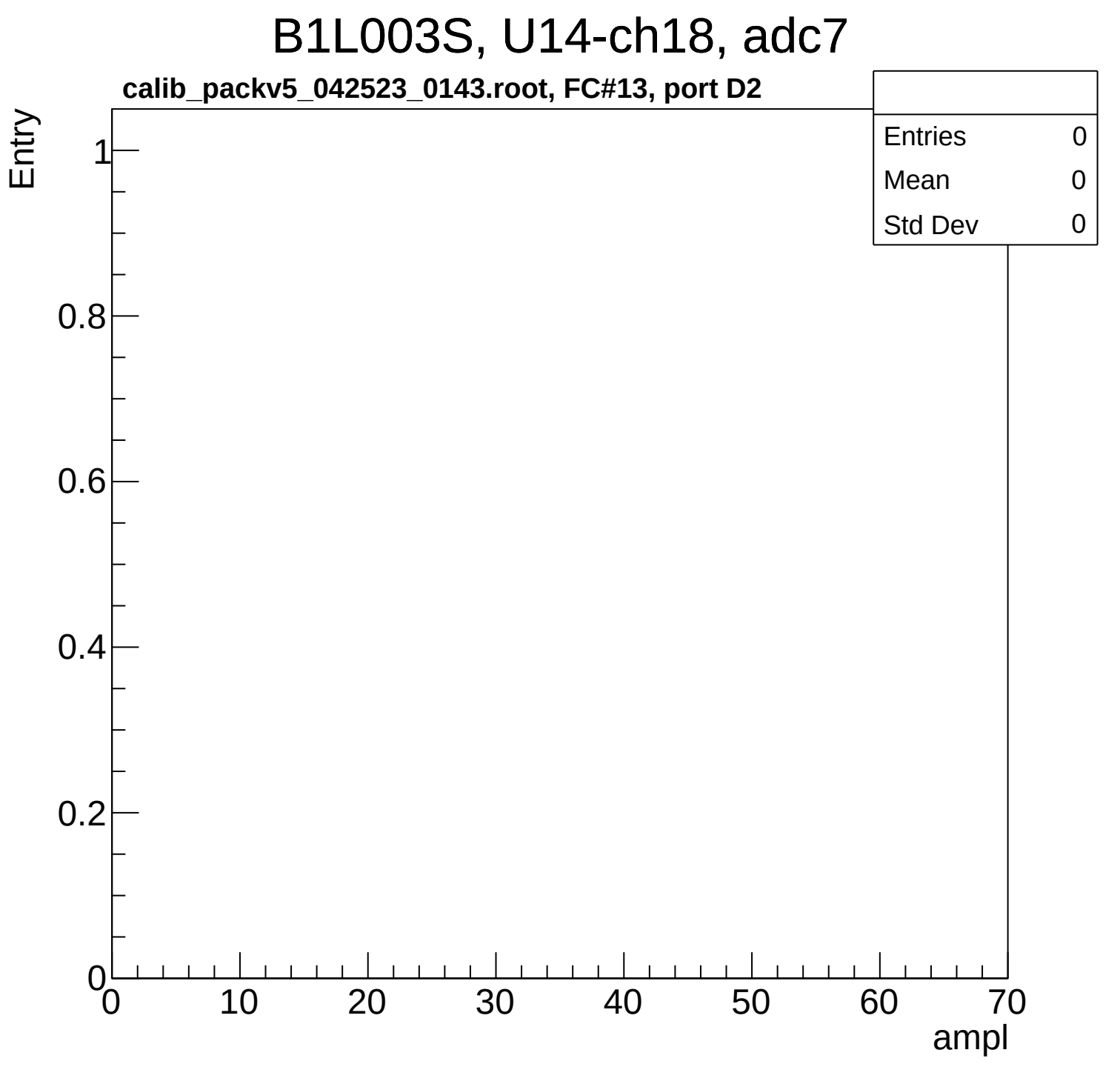

B1L003S, U14-ch18, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

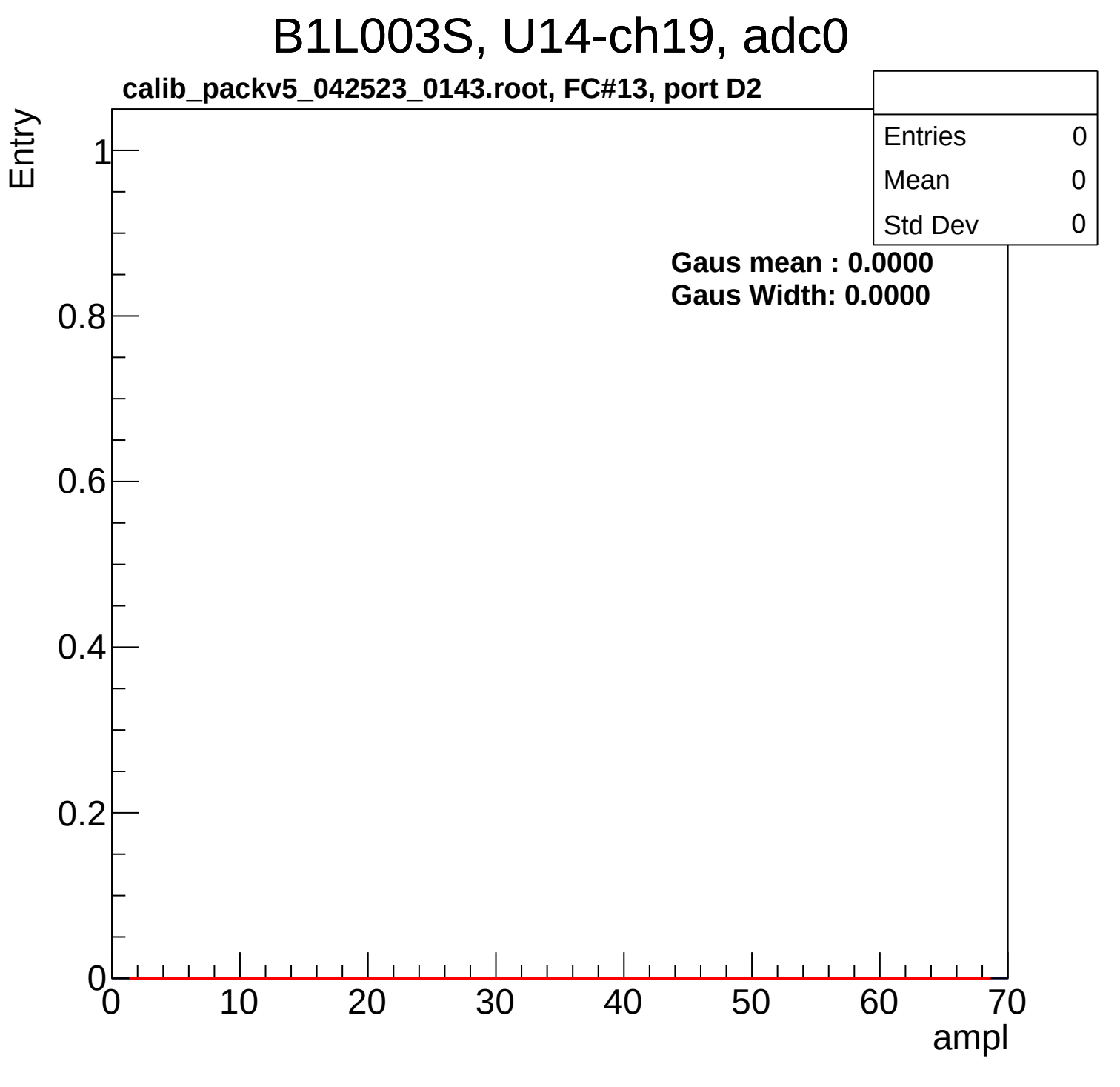

B1L003S, U14-ch18, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

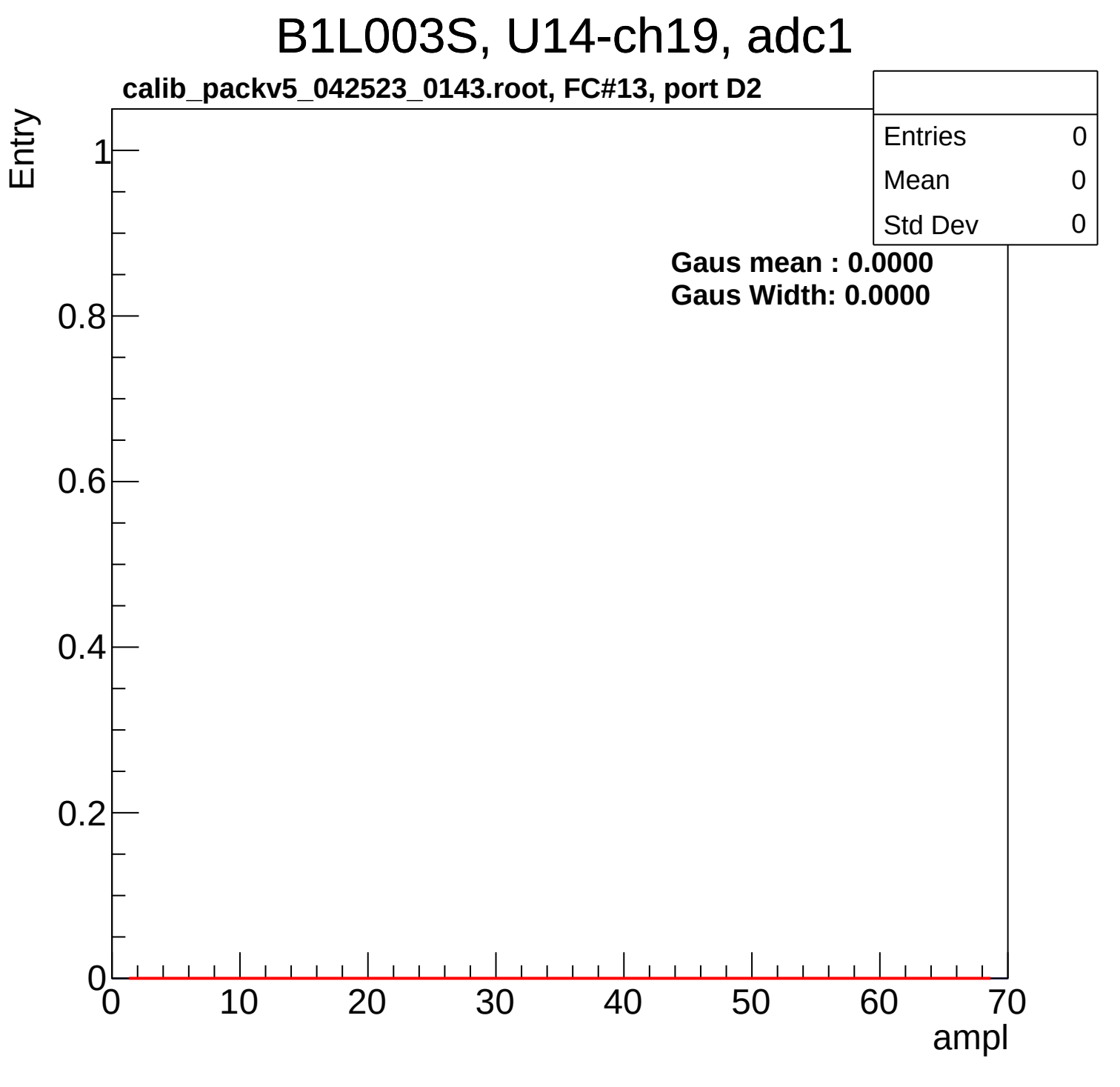

B1L003S, U14-ch18, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

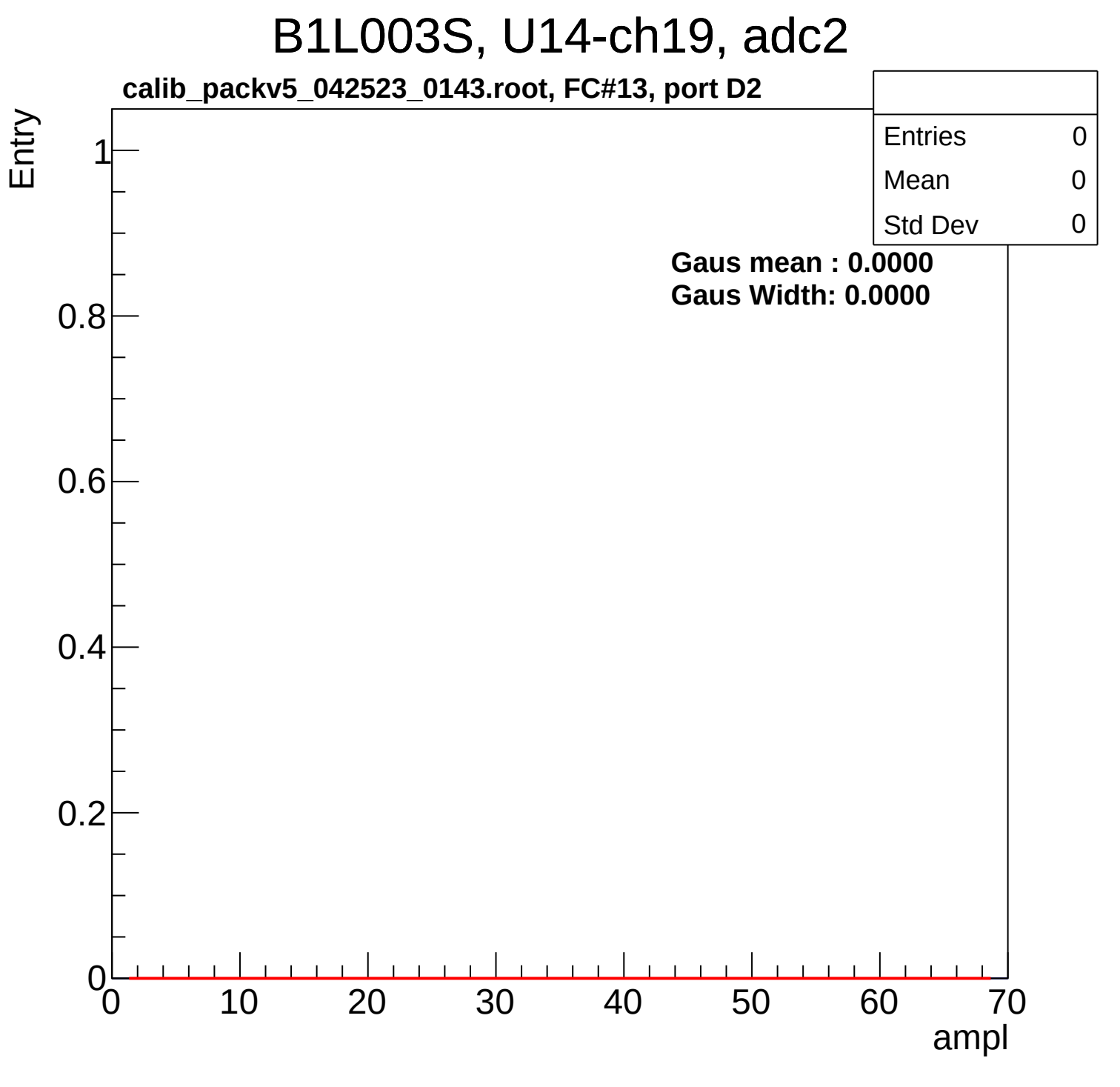

B1L003S, U14-ch18, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

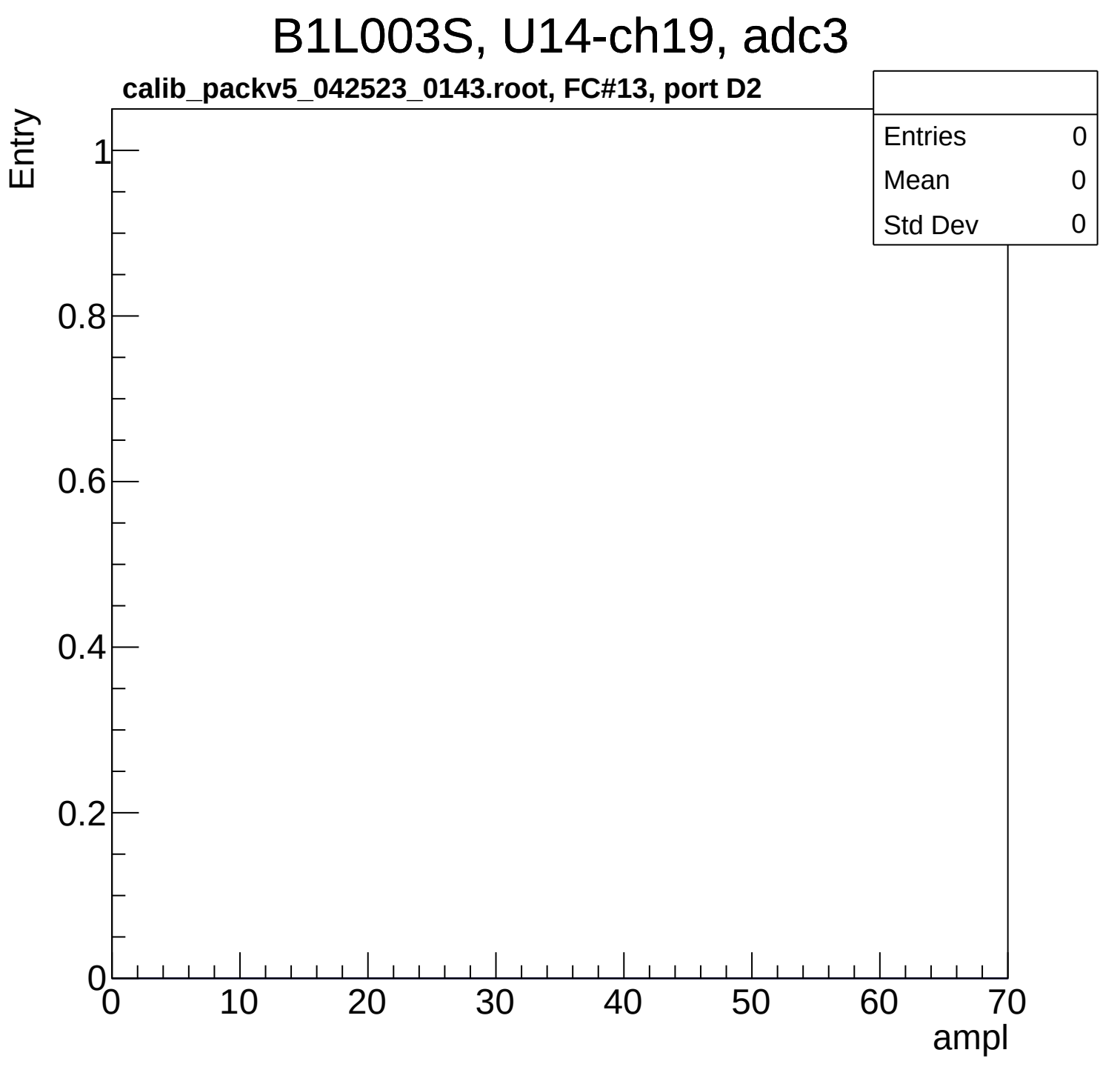

B1L003S, U14-ch18, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

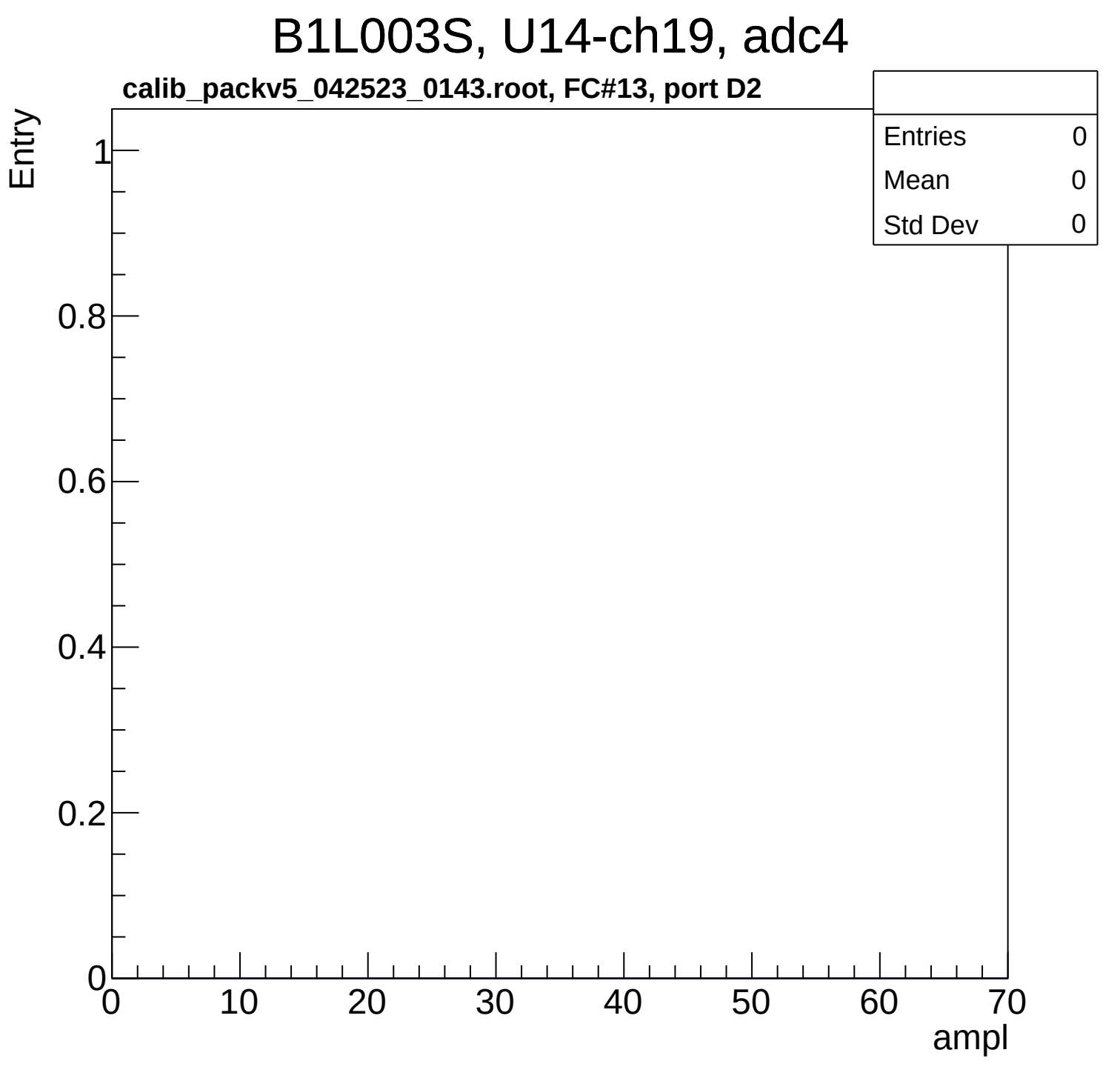

B1L003S, U14-ch18, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

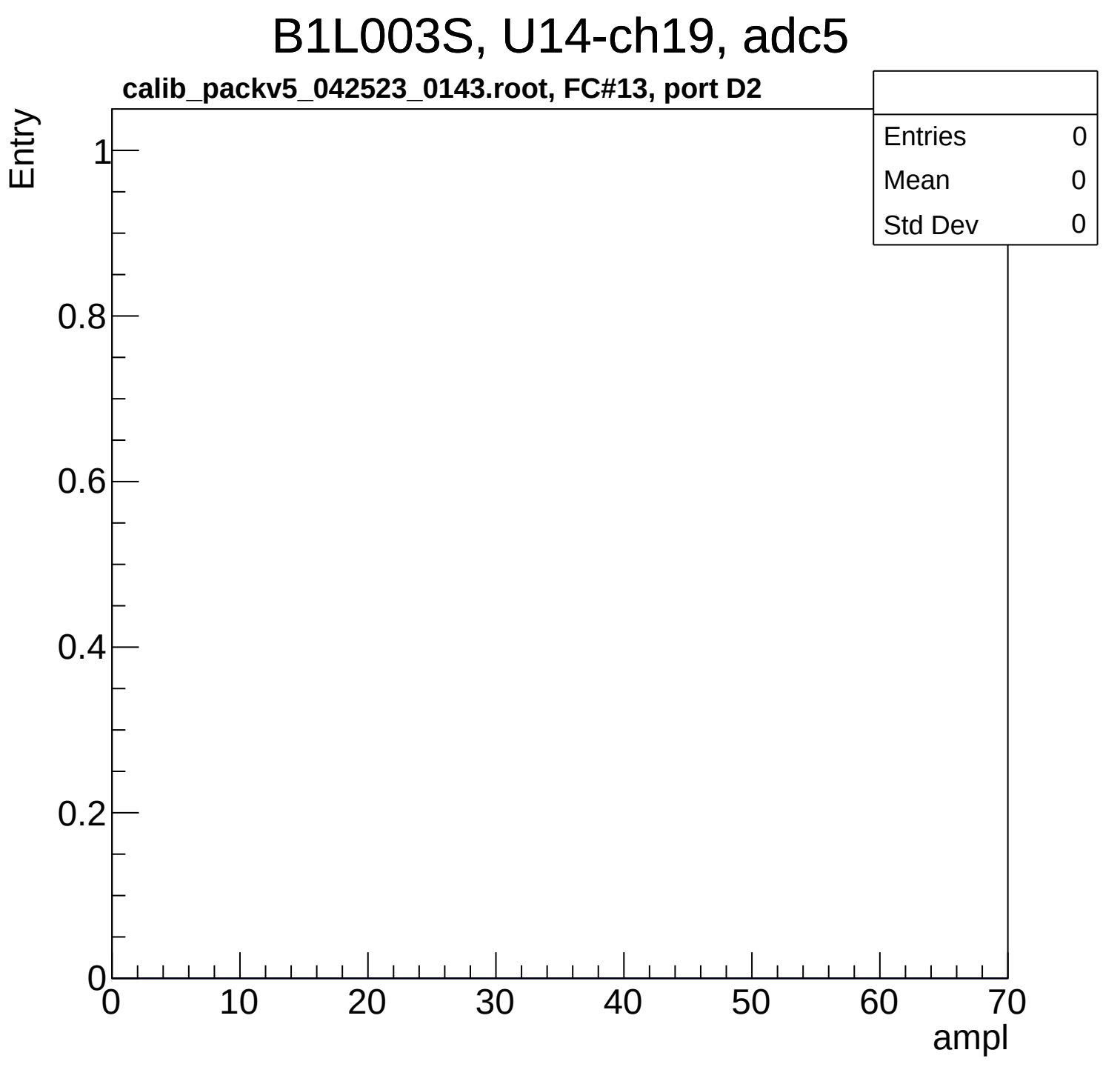

B1L003S, U14-ch19, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

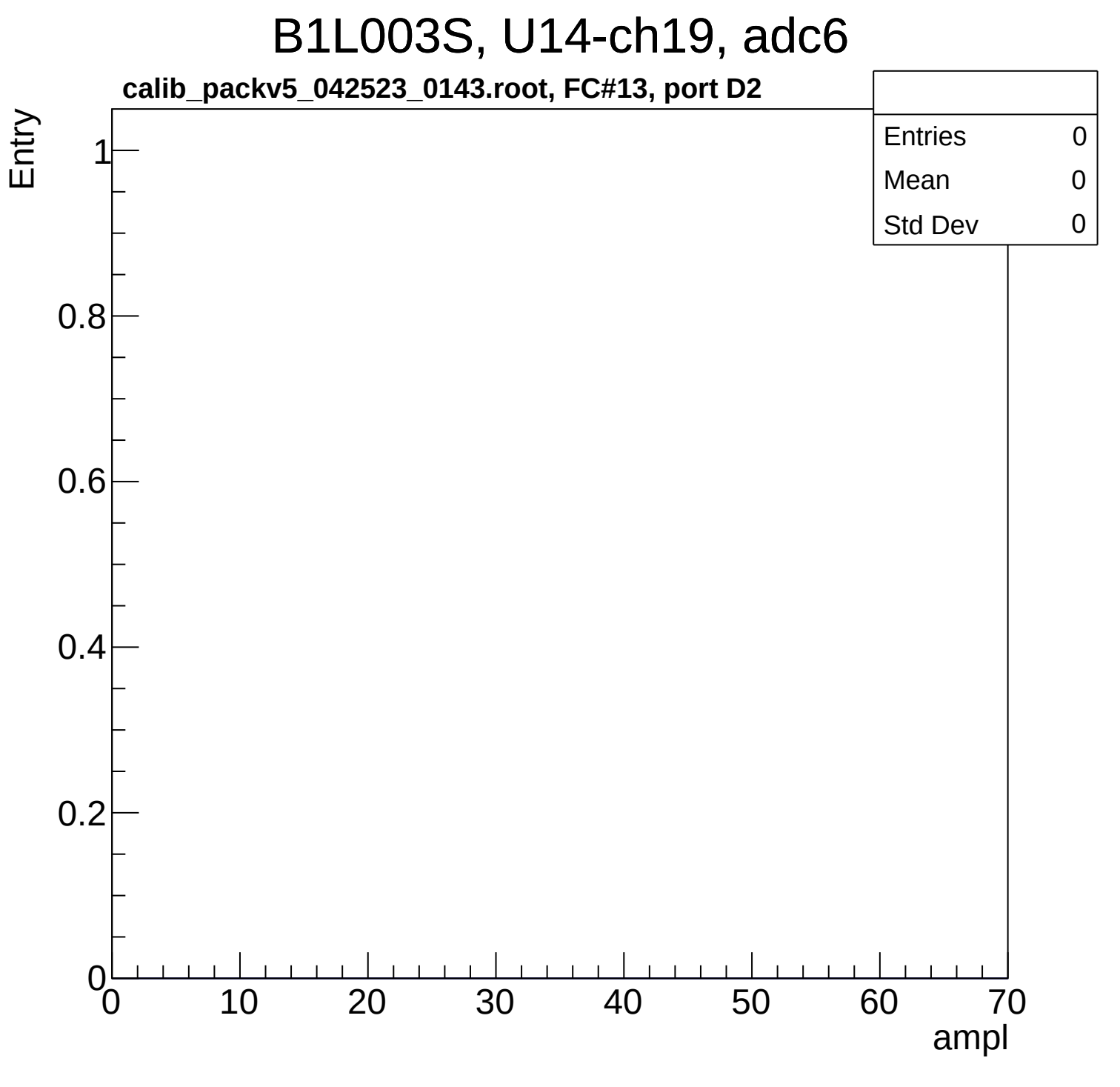

B1L003S, U14-ch19, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

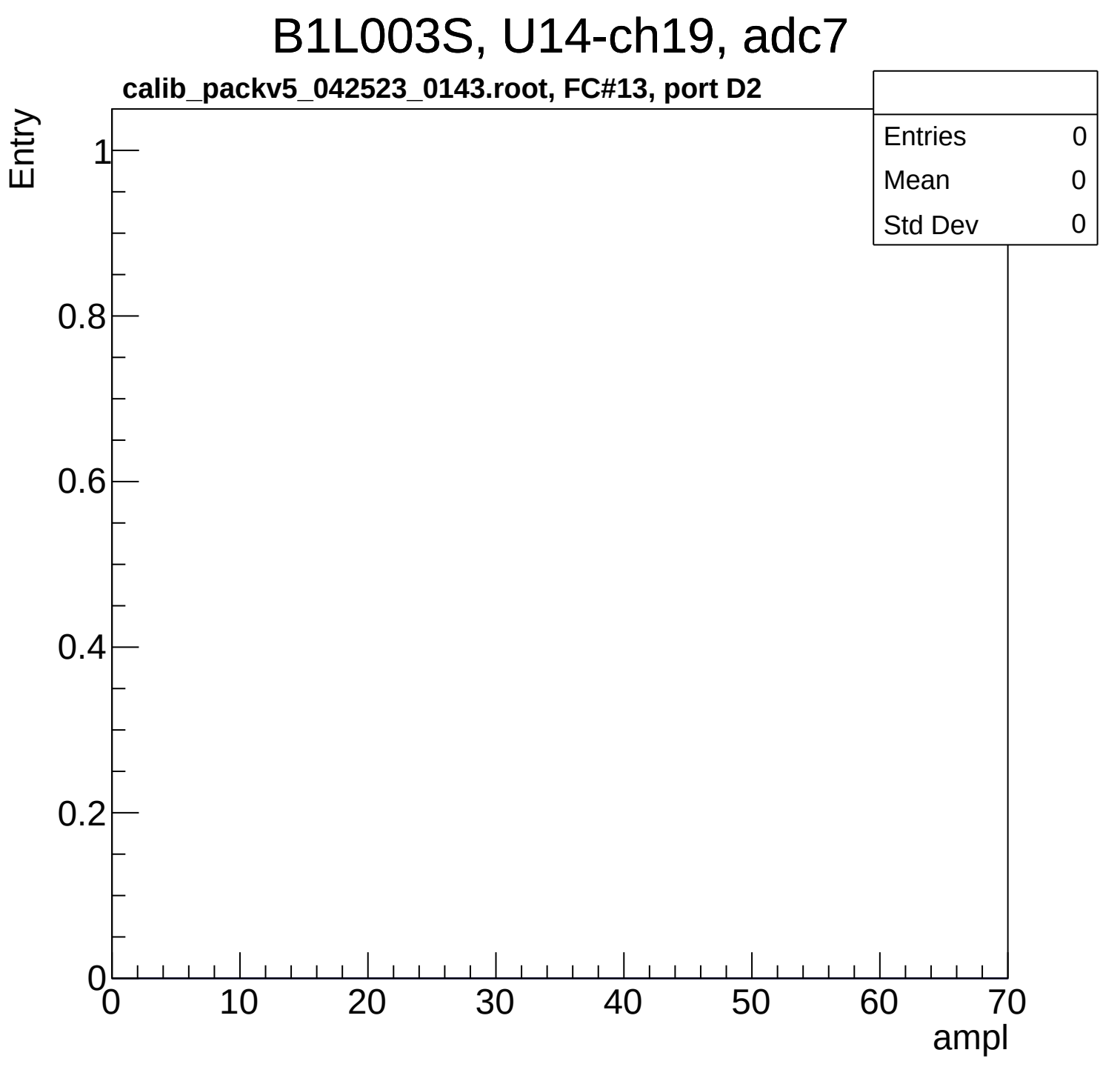

B1L003S, U14-ch19, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

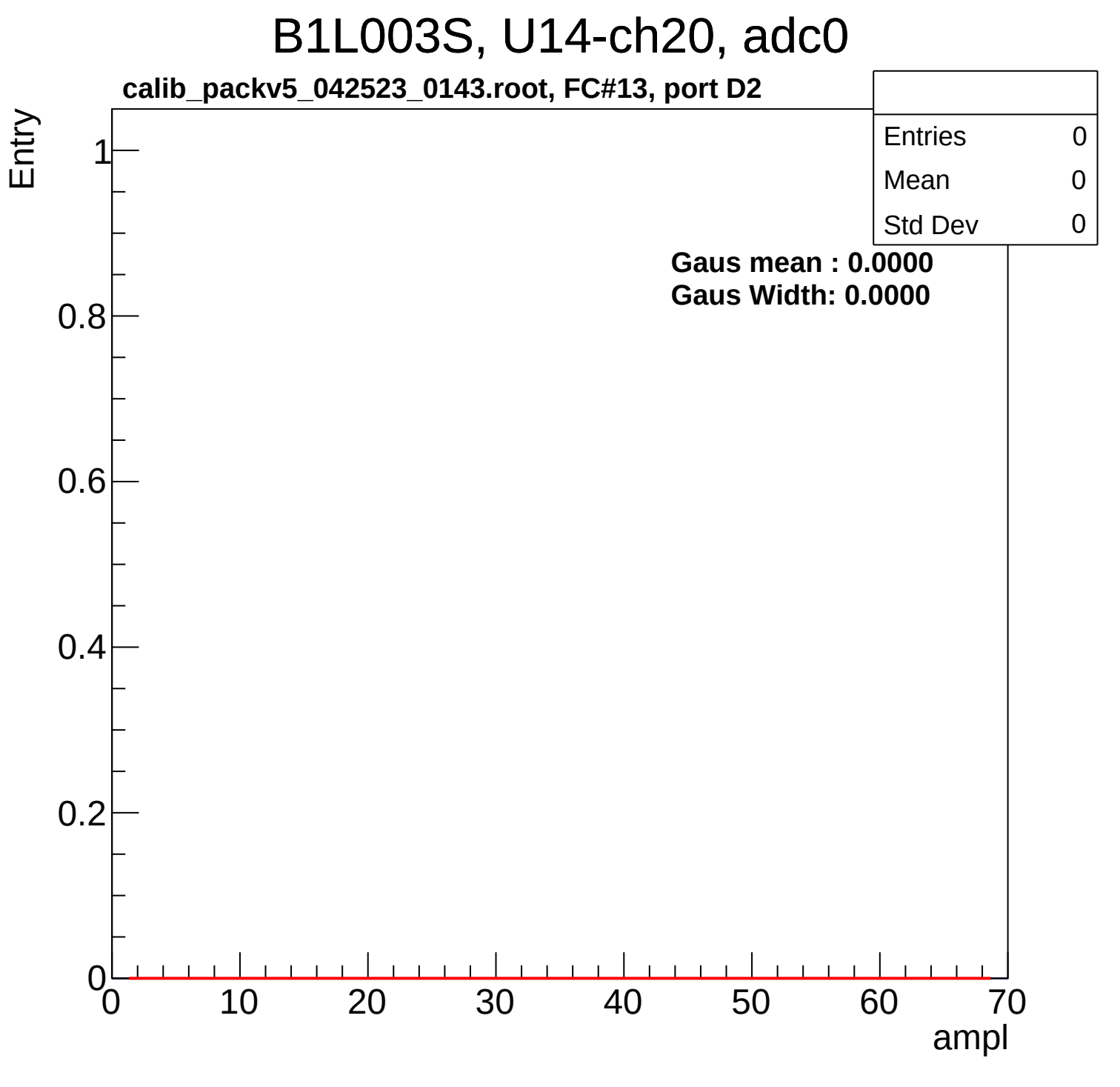

B1L003S, U14-ch19, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

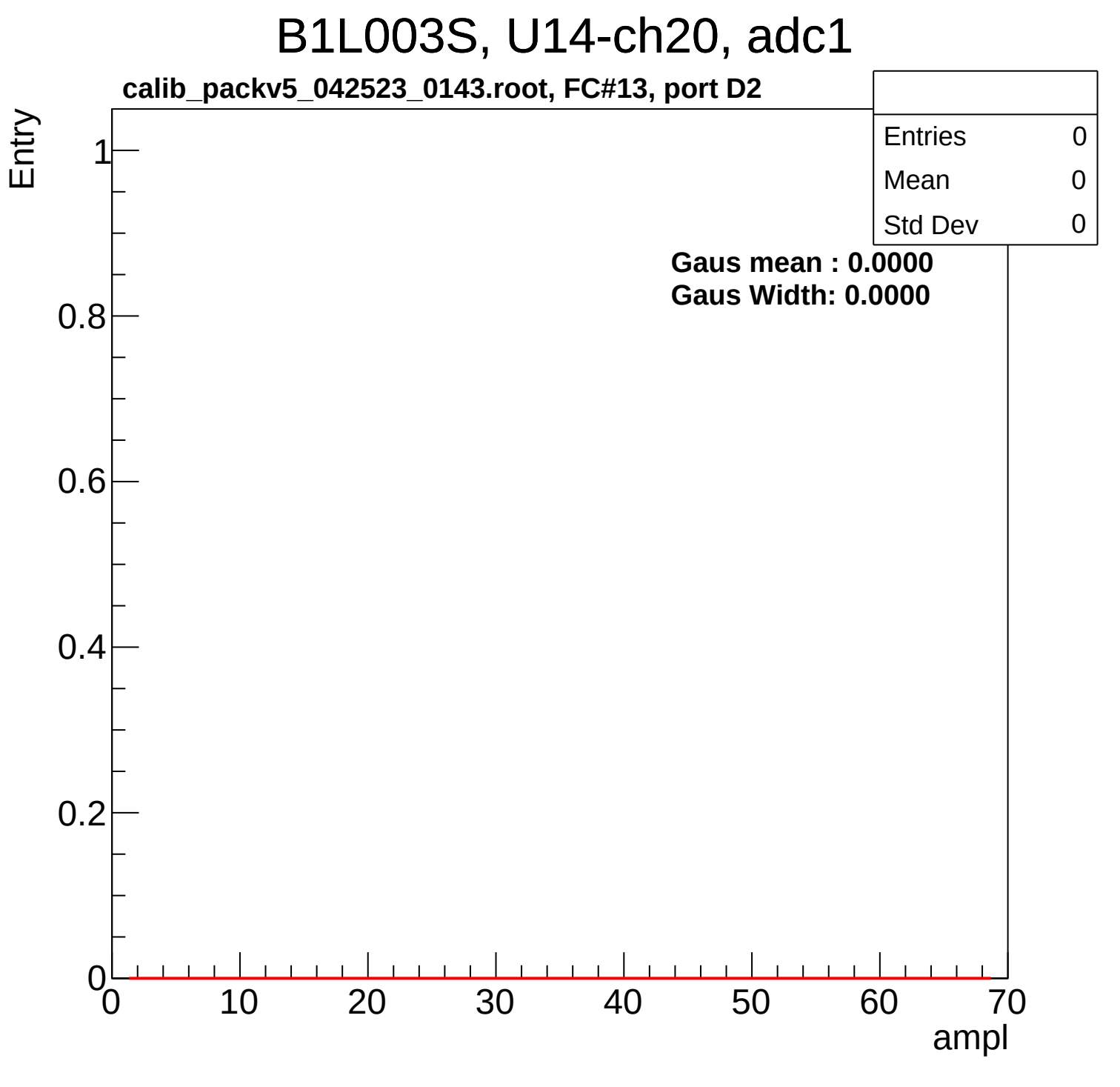

B1L003S, U14-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

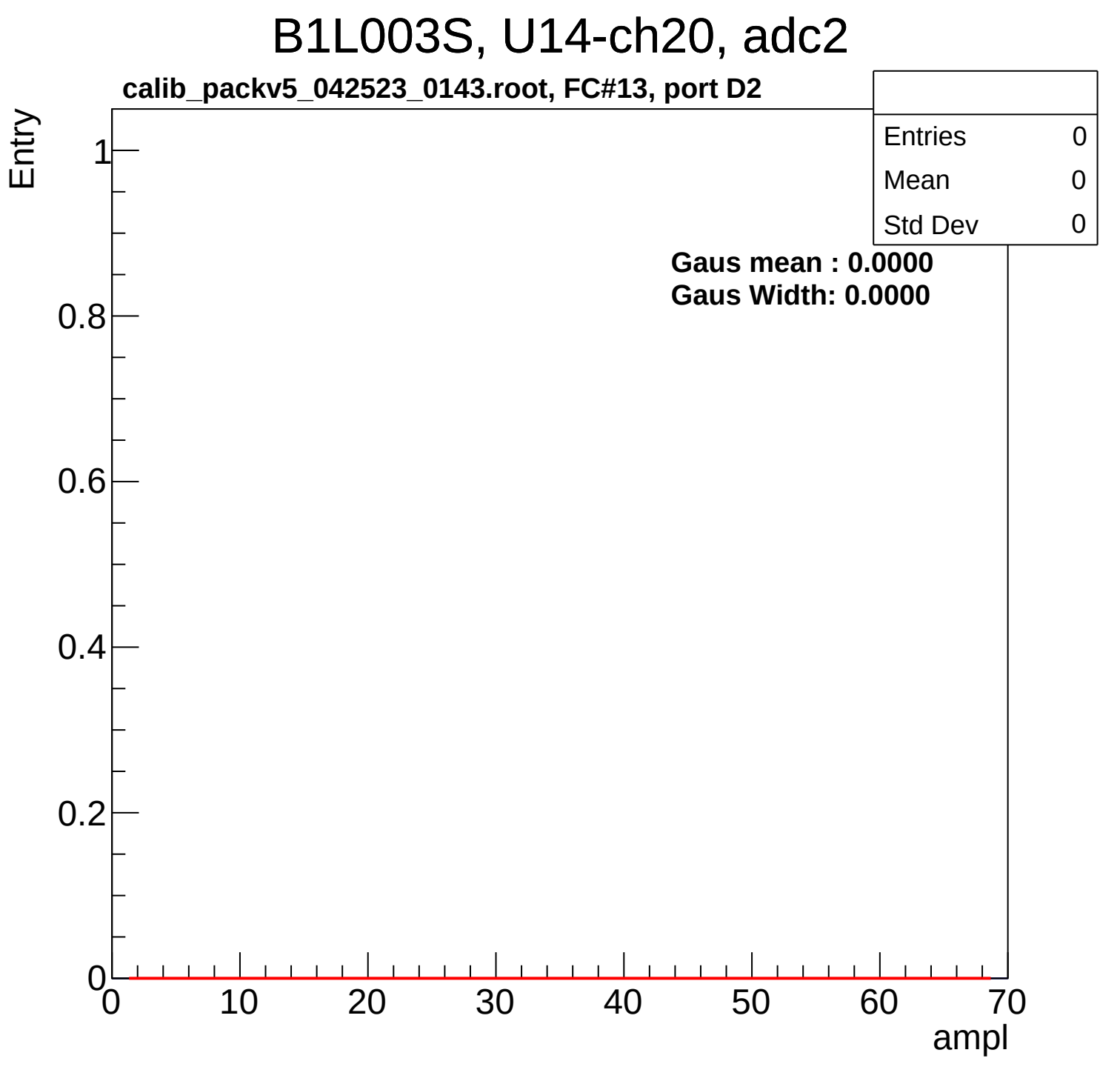

B1L003S, U14-ch19, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

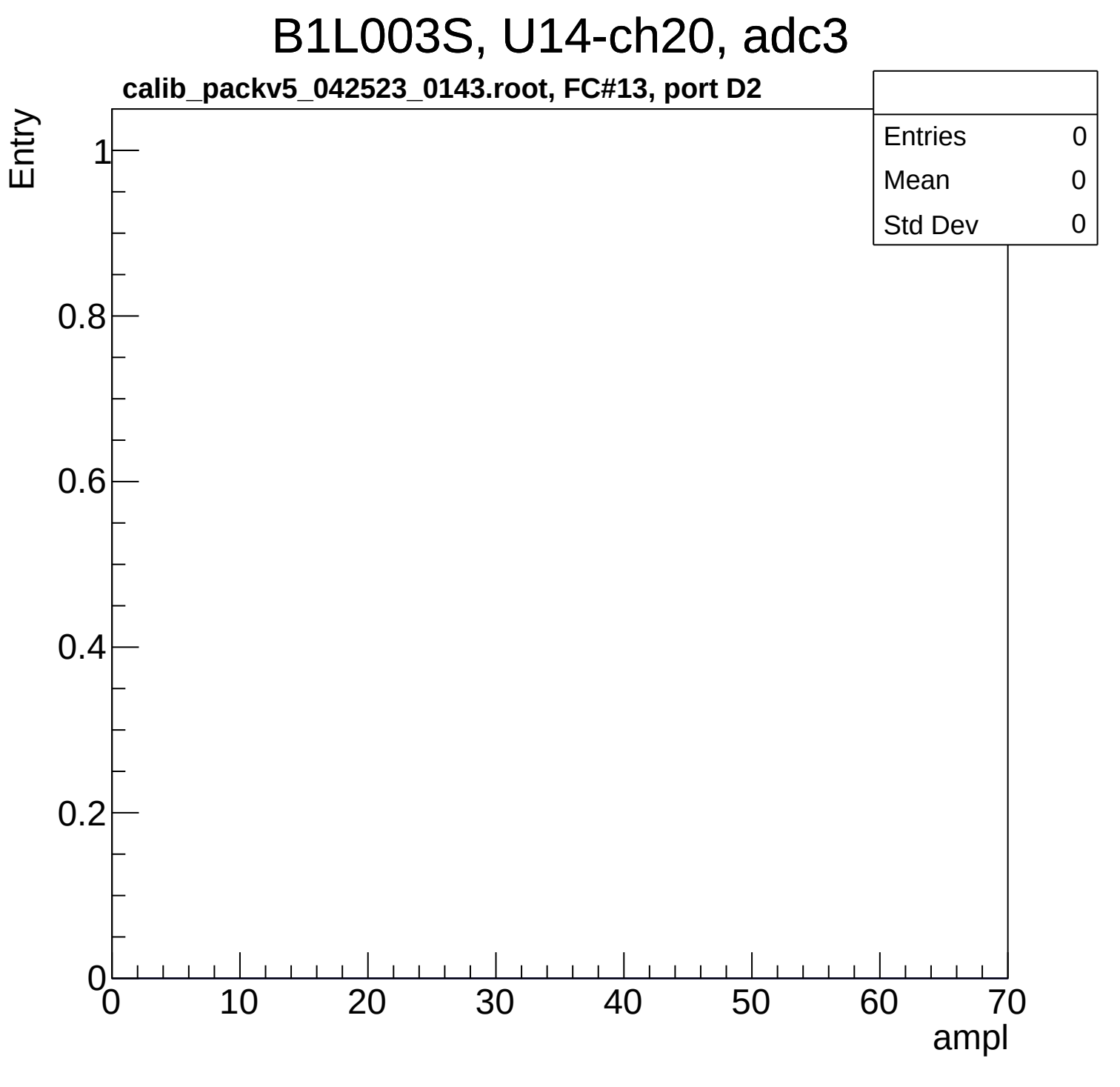

B1L003S, U14-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

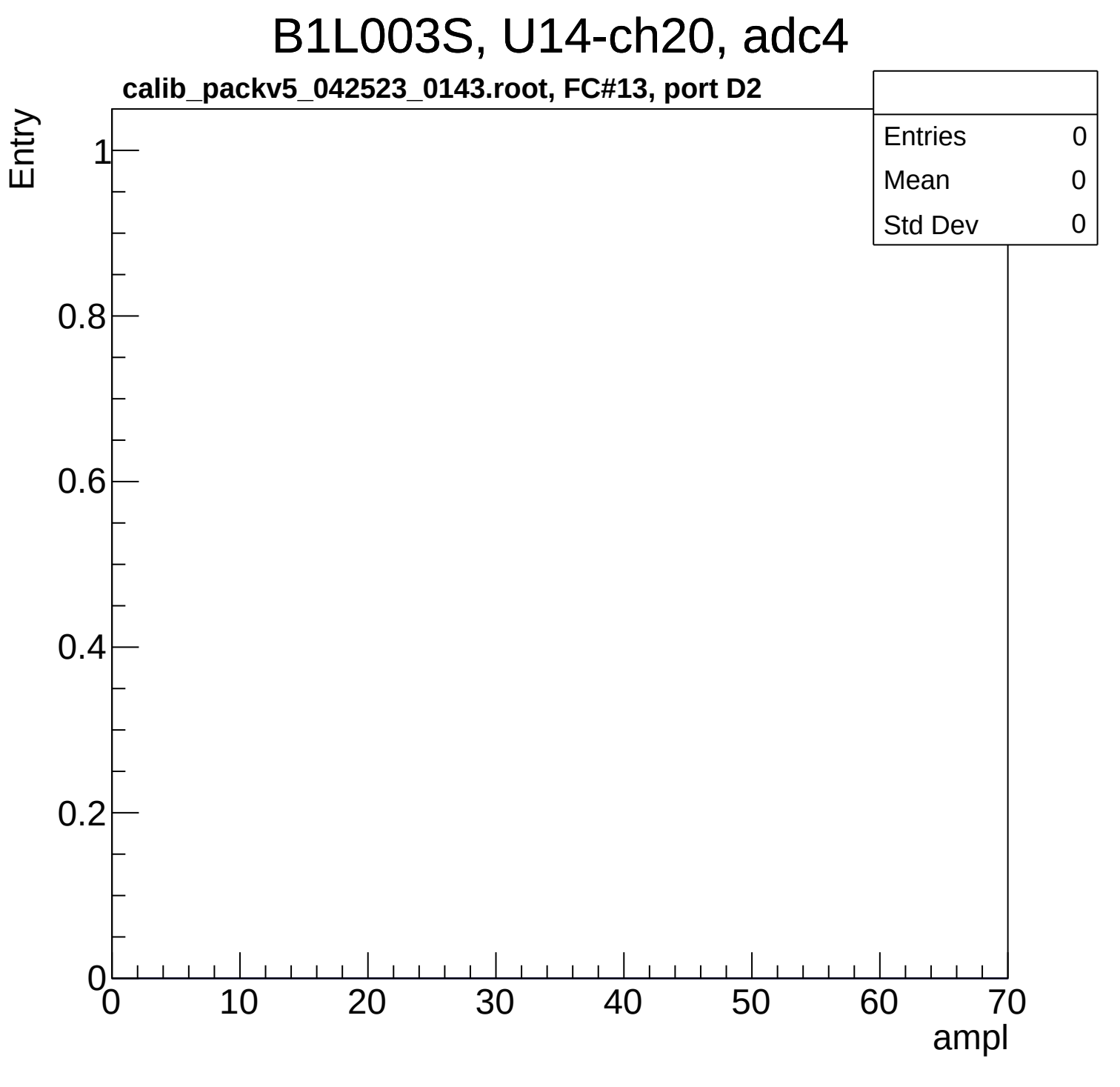

B1L003S, U14-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

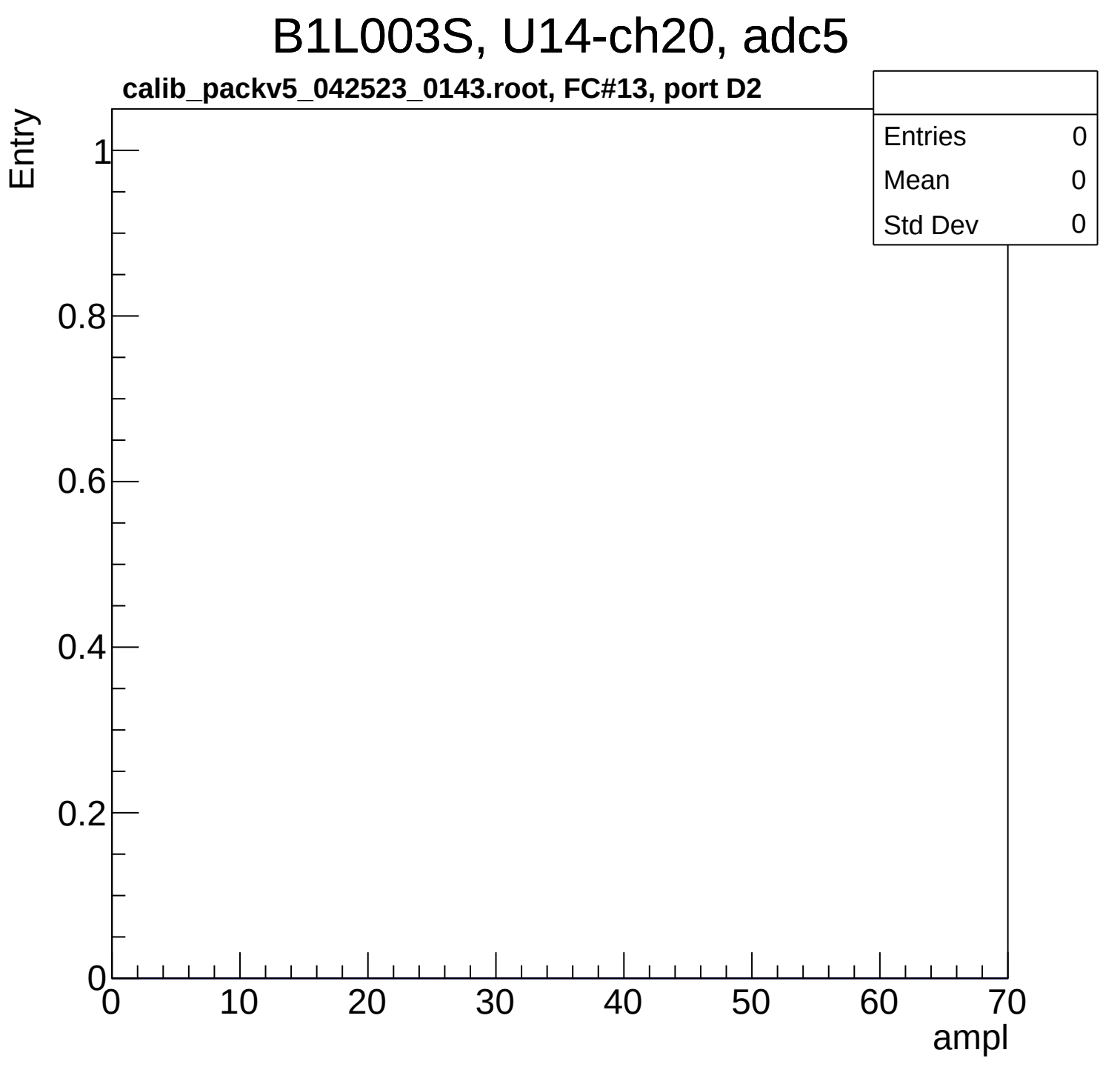

B1L003S, U14-ch20, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

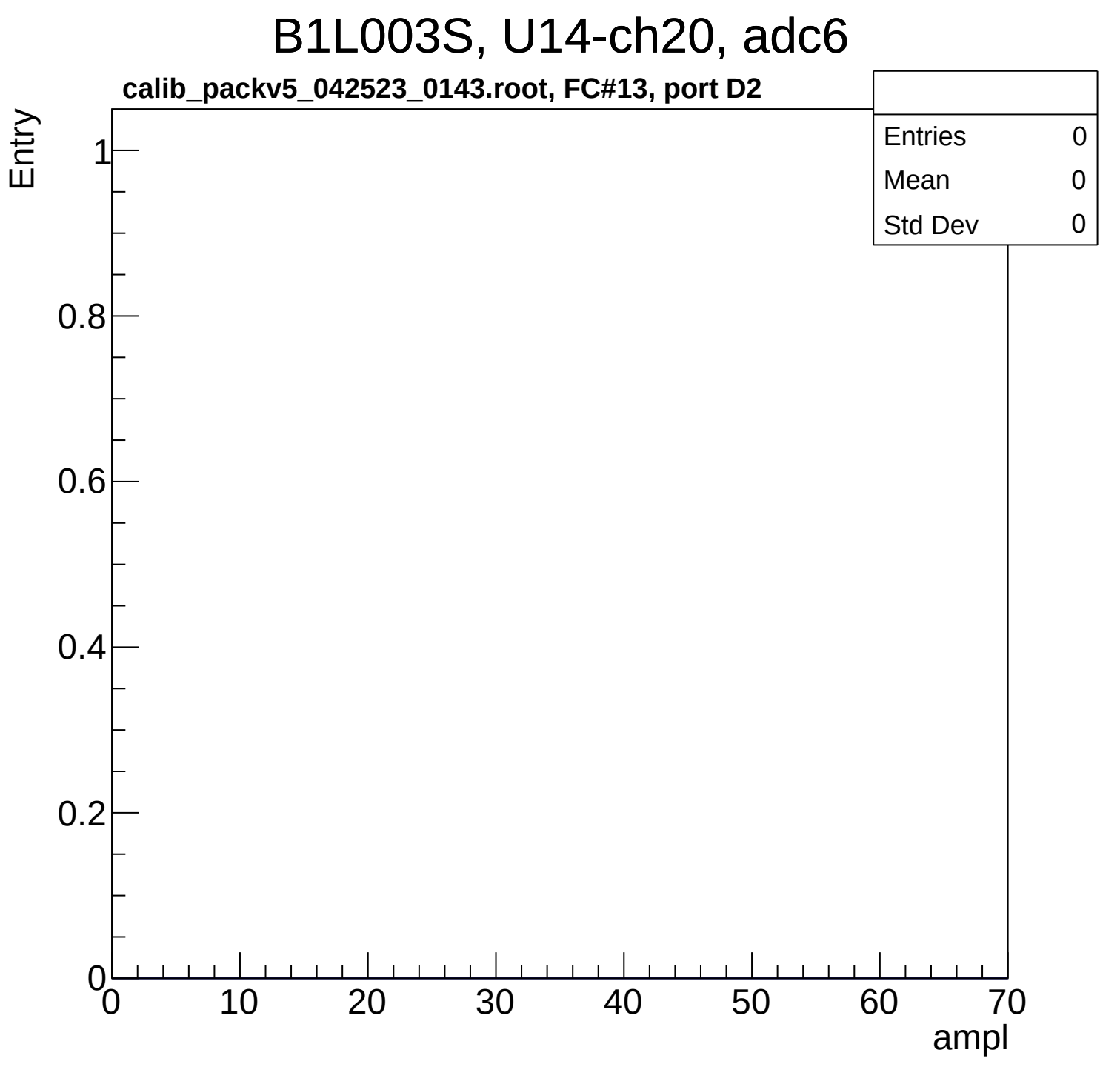

B1L003S, U14-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

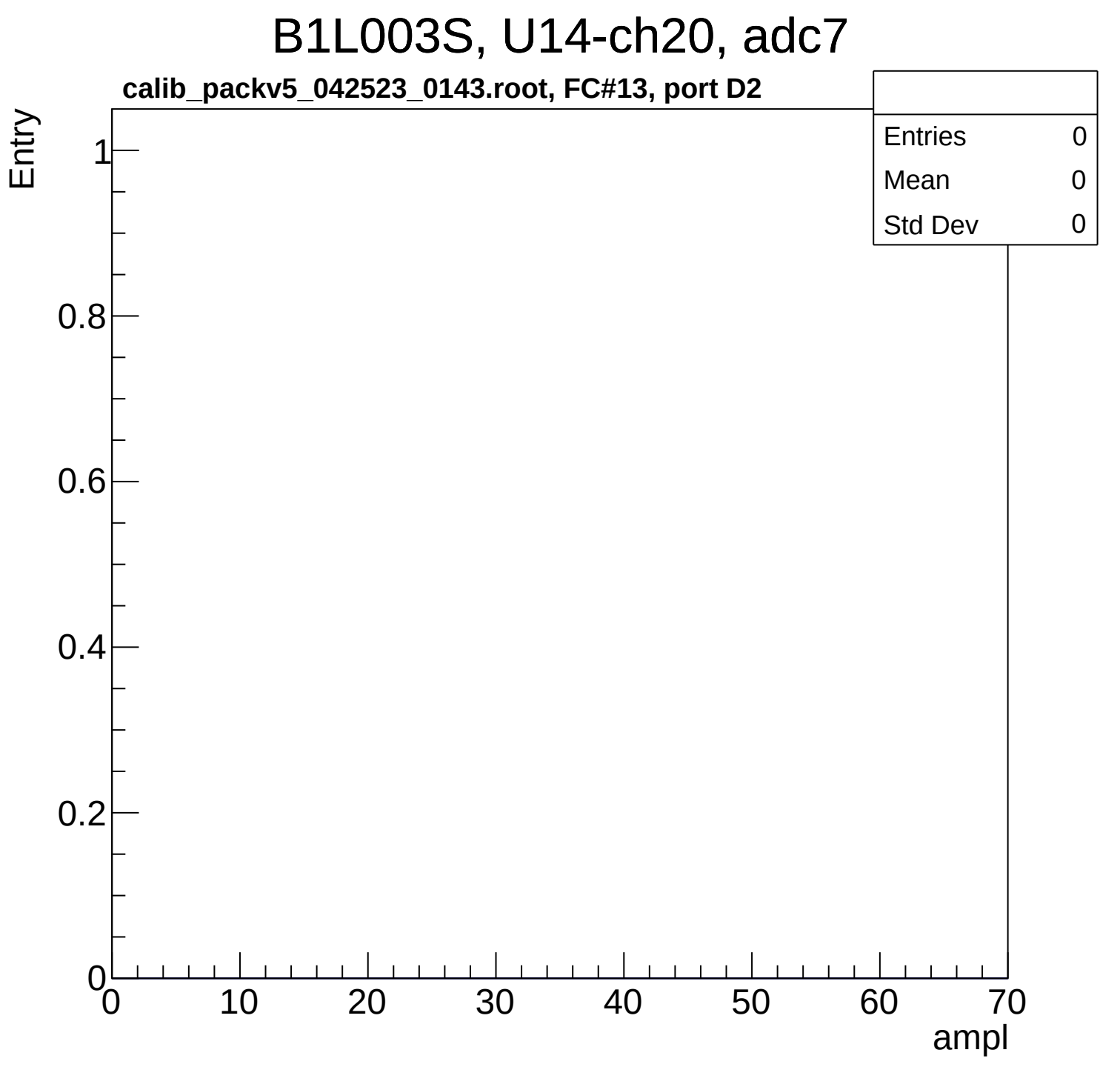

B1L003S, U14-ch20, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

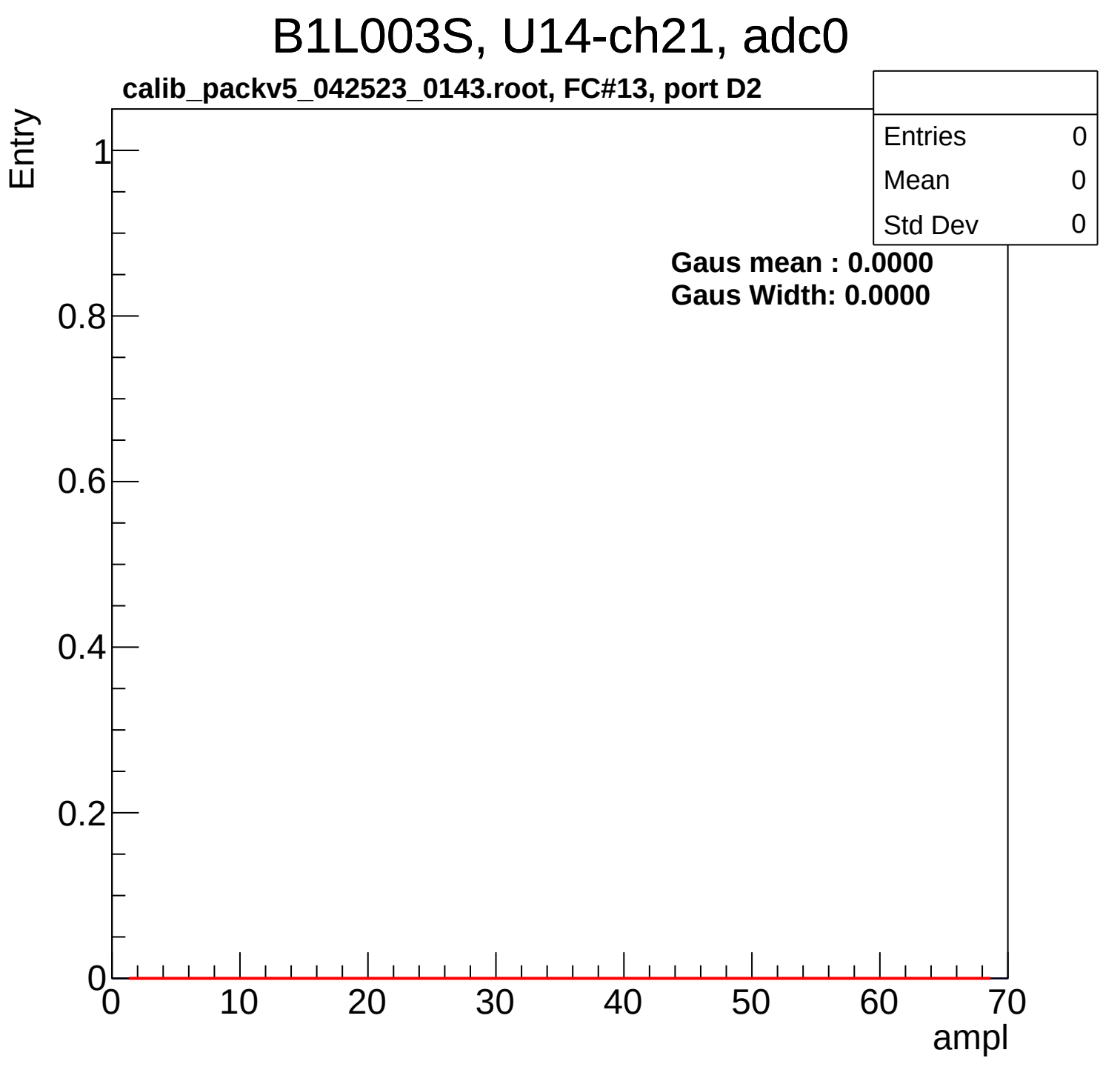

B1L003S, U14-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

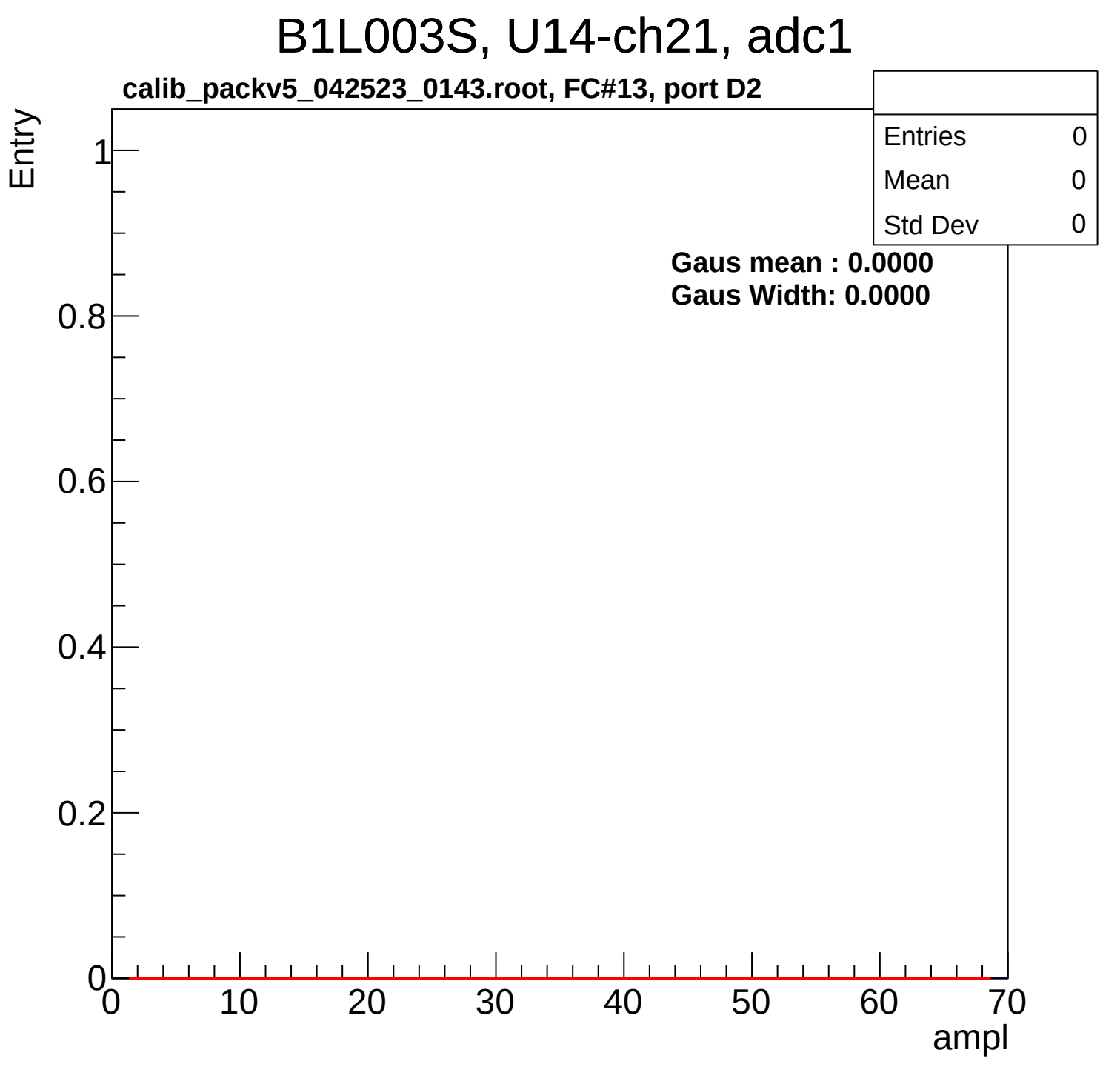

B1L003S, U14-ch20, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

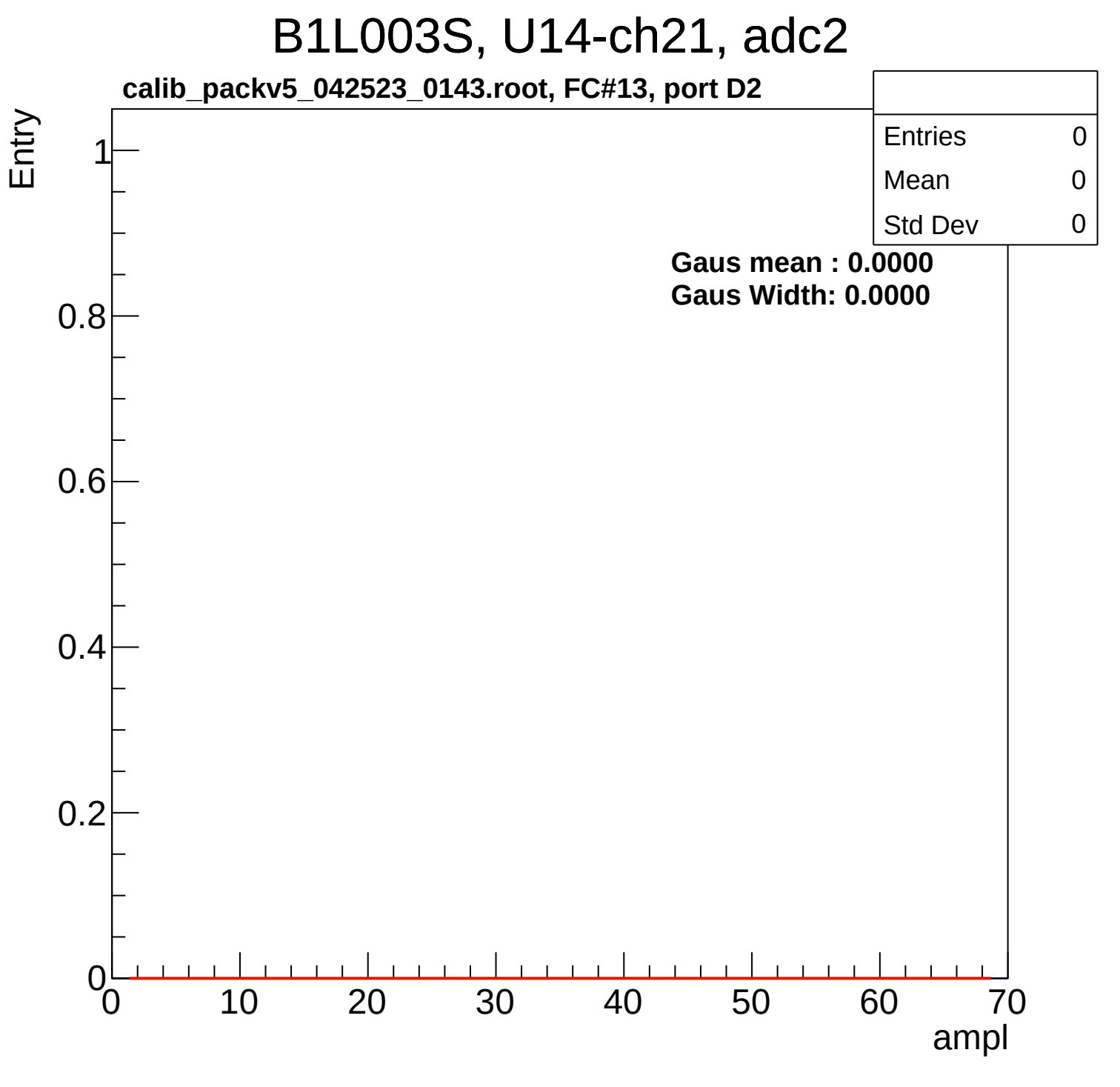

B1L003S, U14-ch20, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

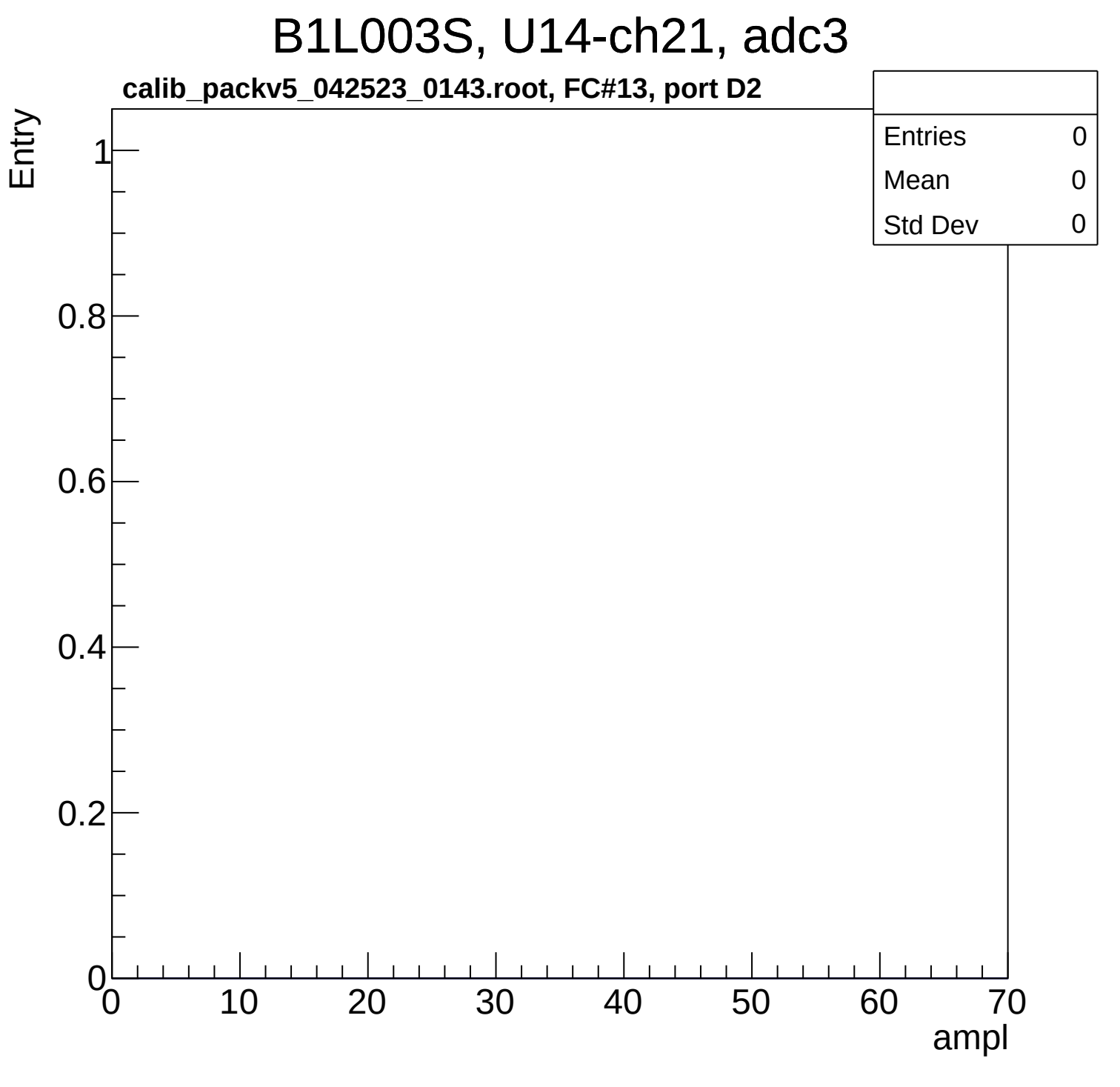

B1L003S, U14-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

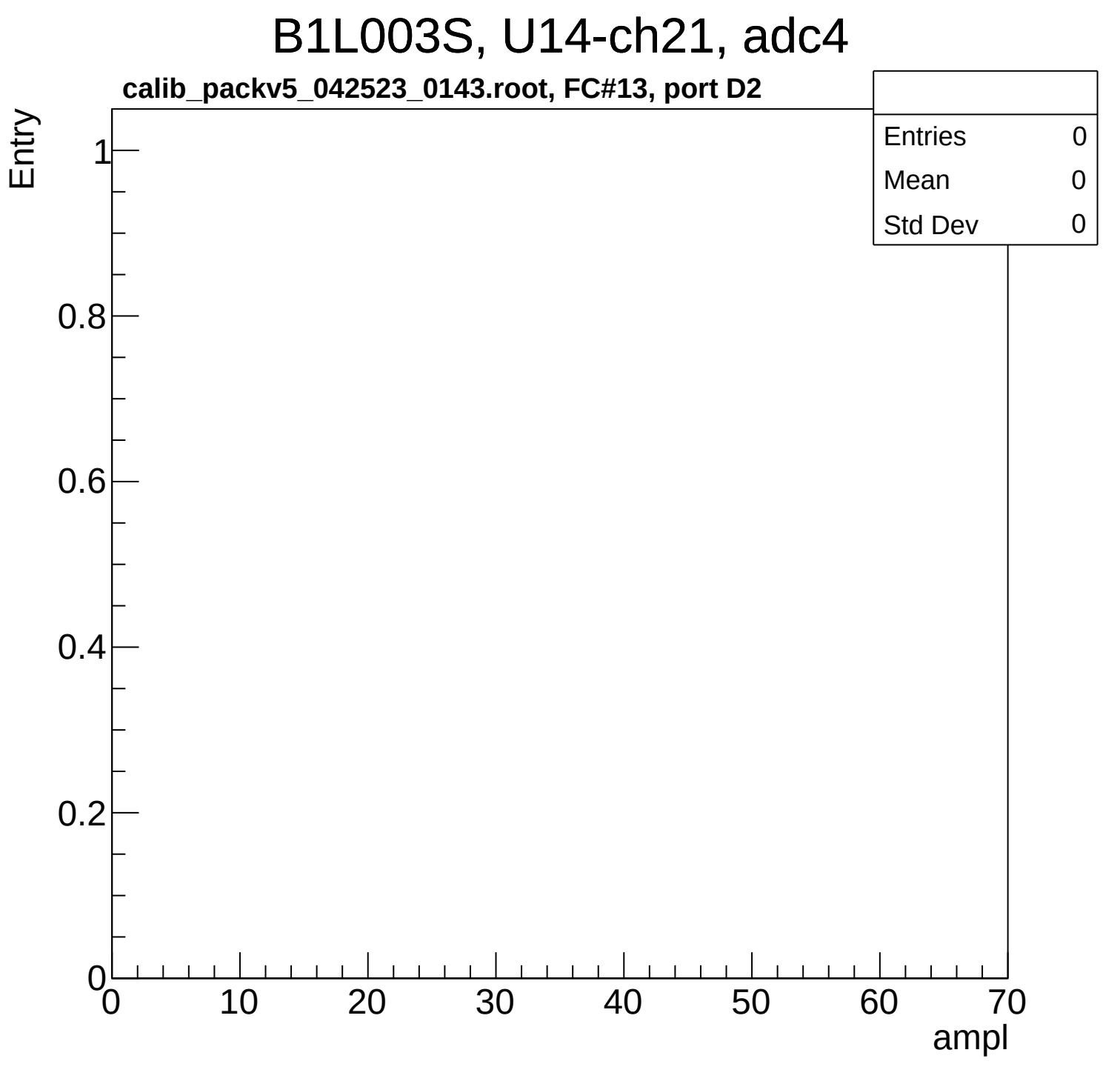

B1L003S, U14-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

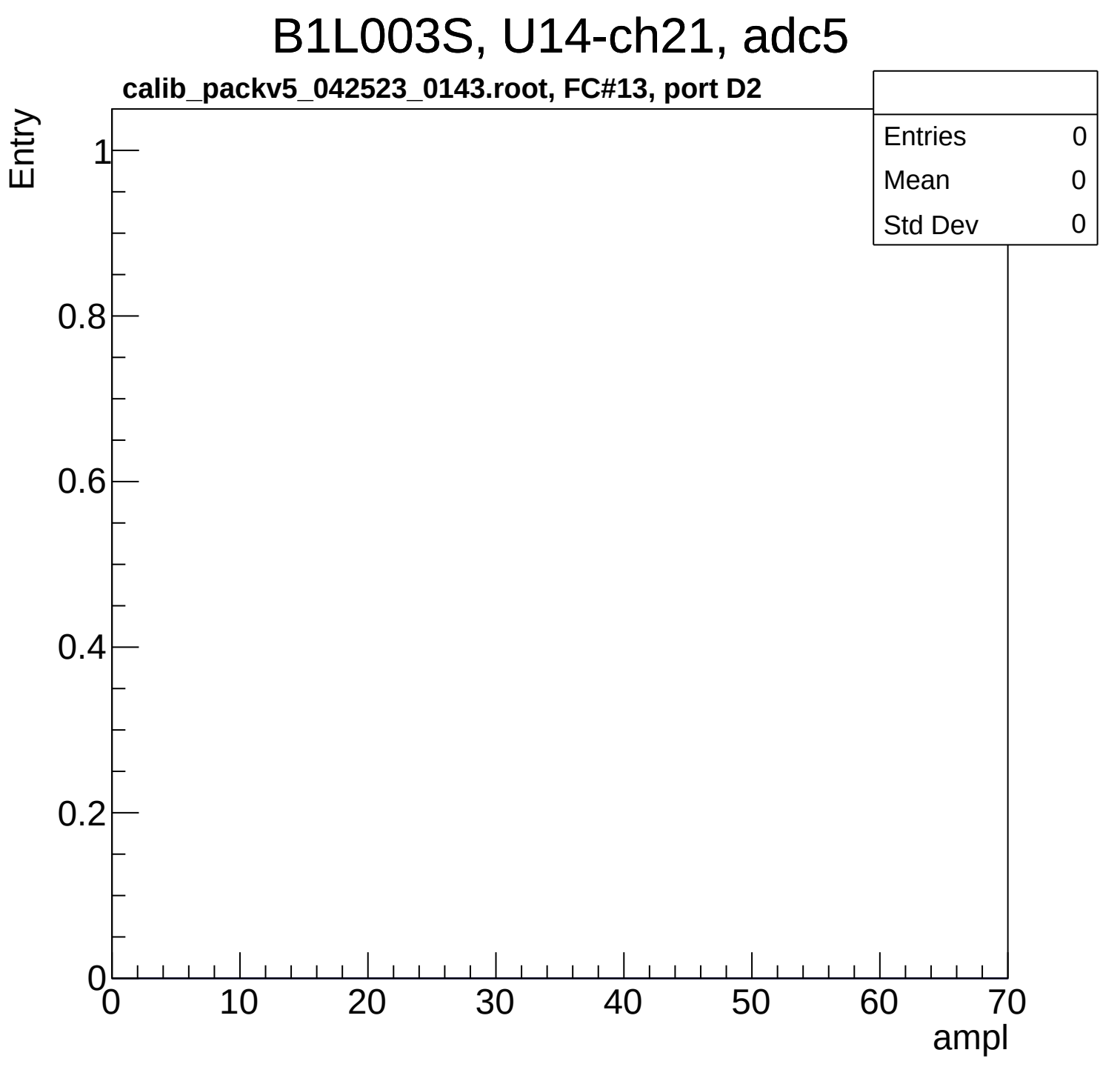

B1L003S, U14-ch21, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

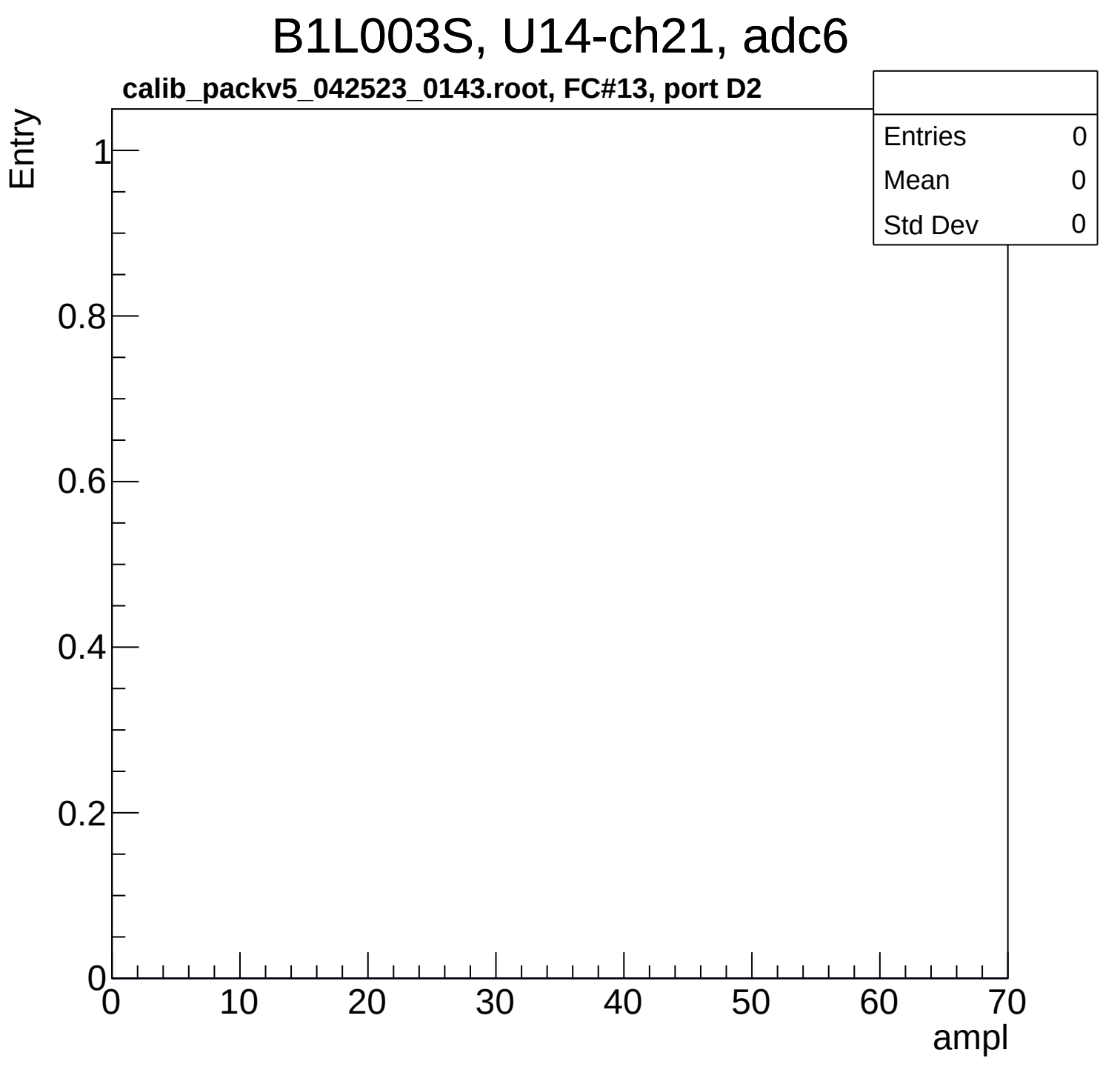

B1L003S, U14-ch21, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

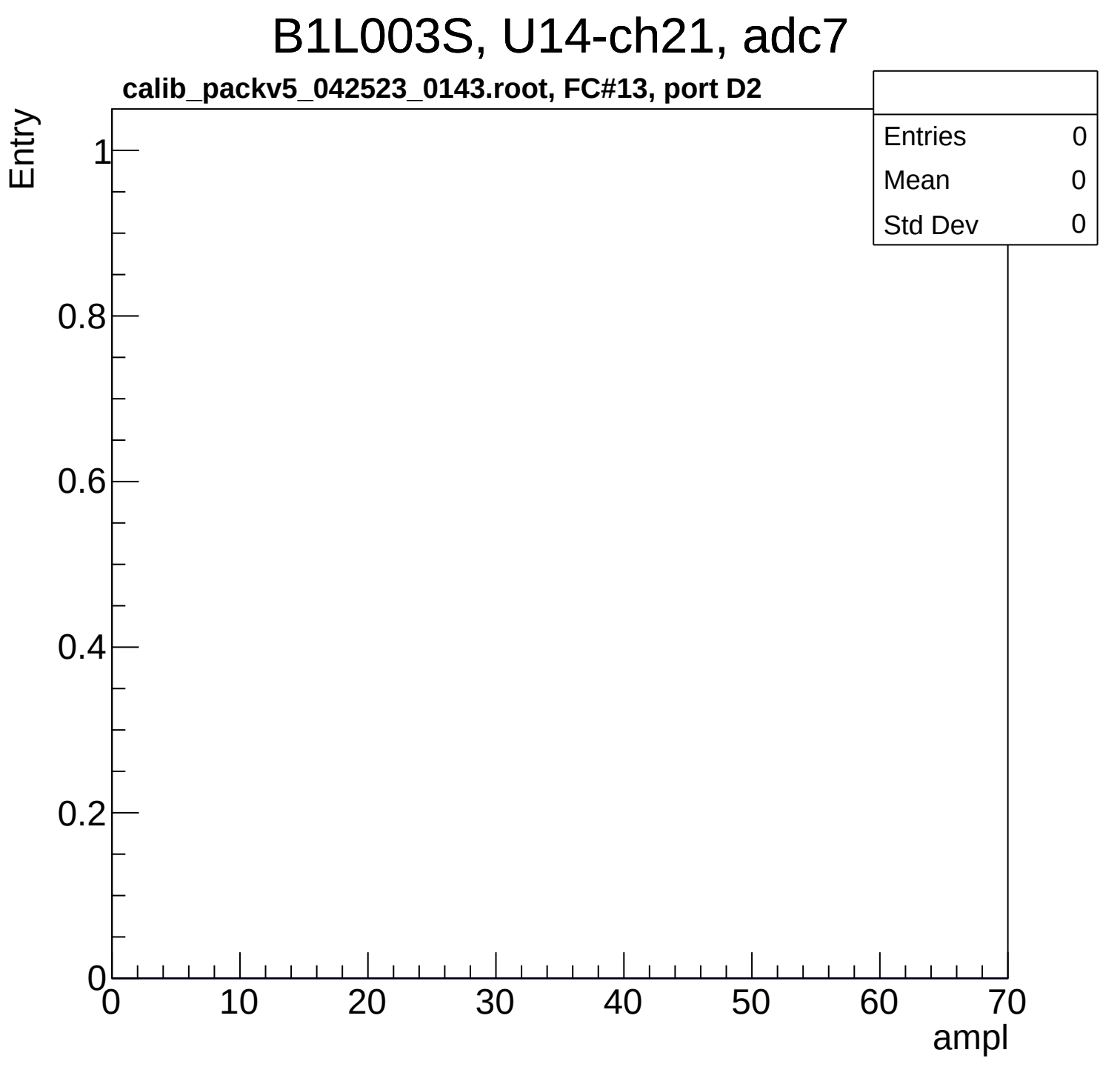

B1L003S, U14-ch21, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

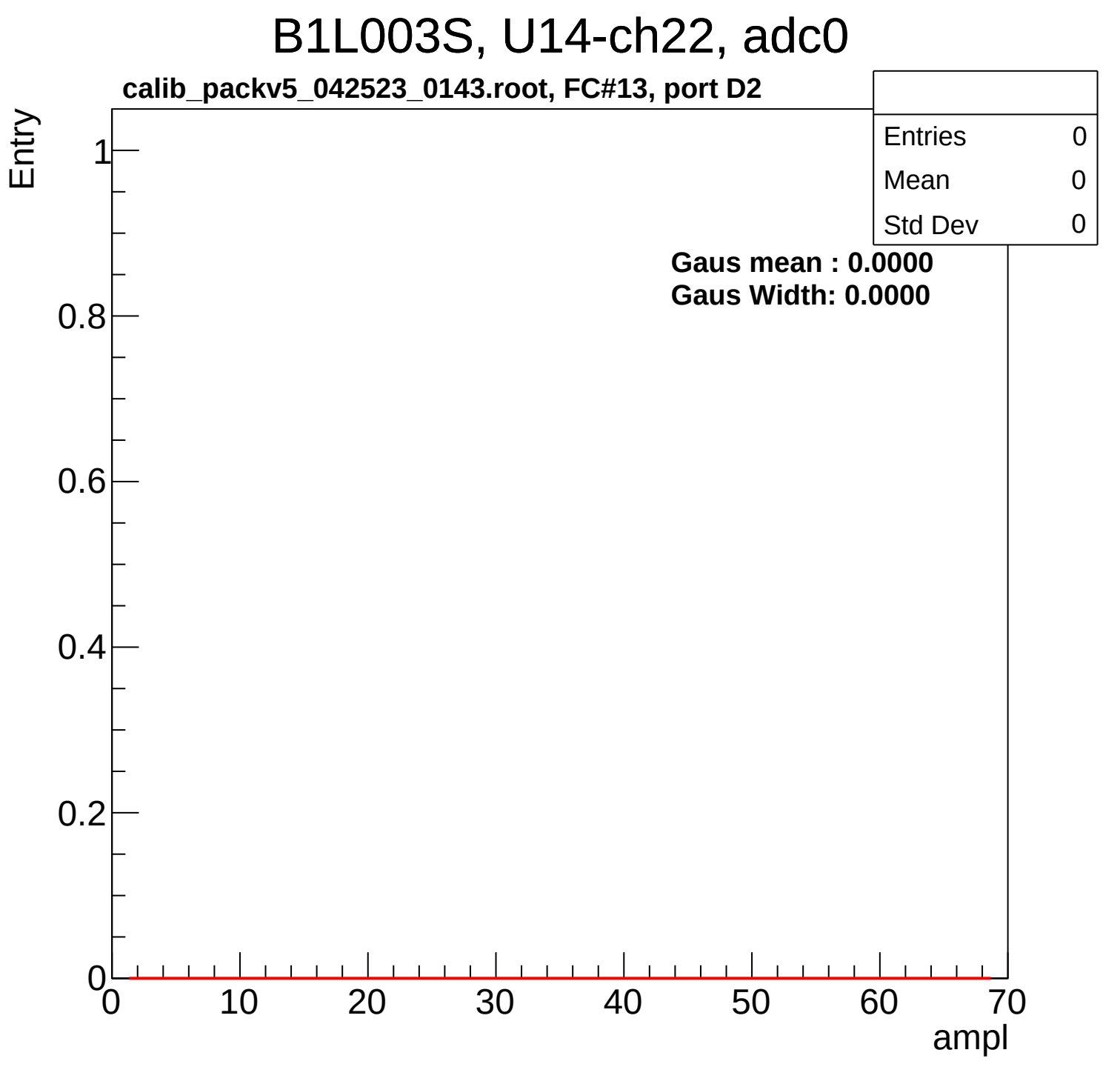

B1L003S, U14-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

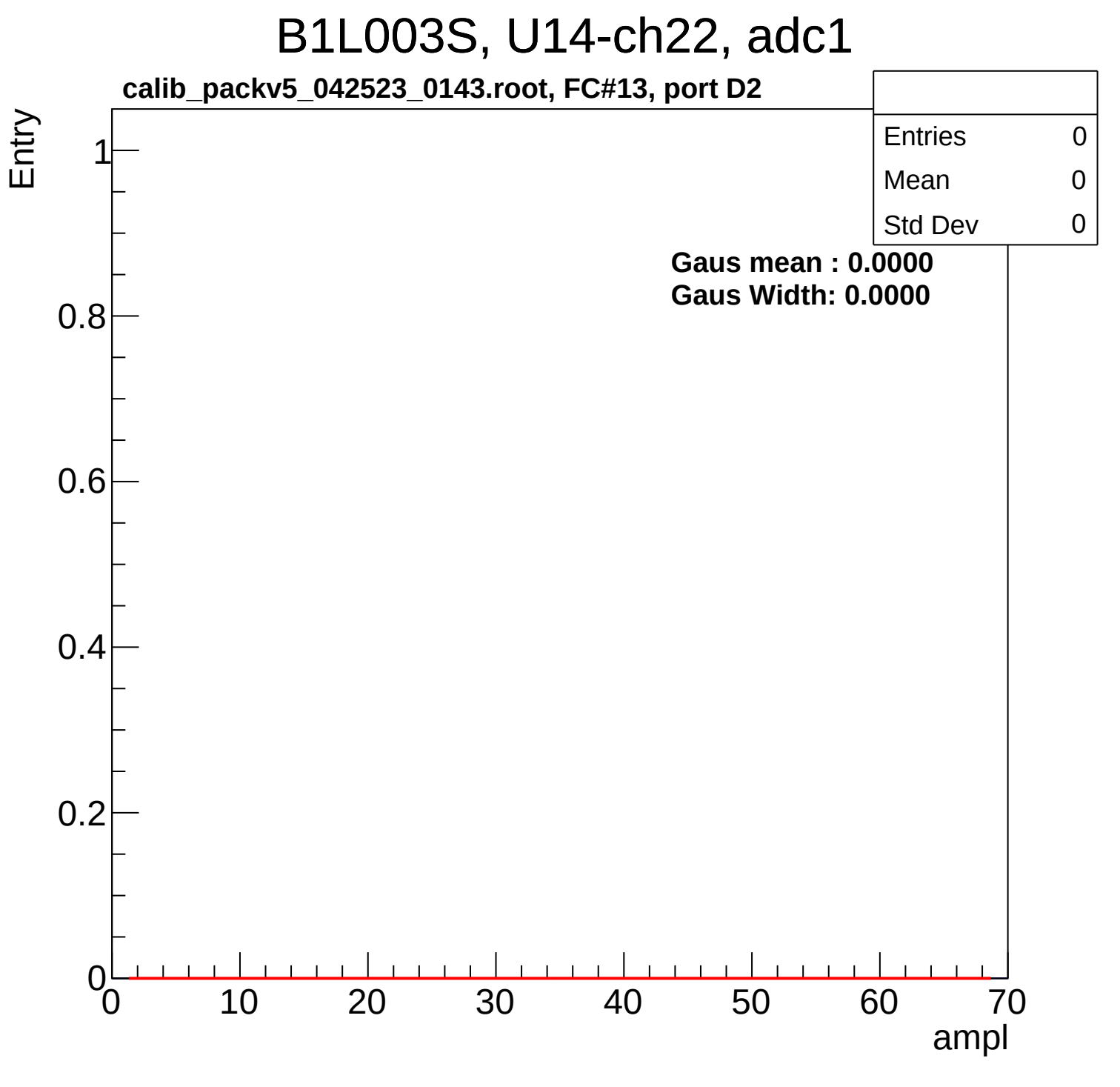

B1L003S, U14-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

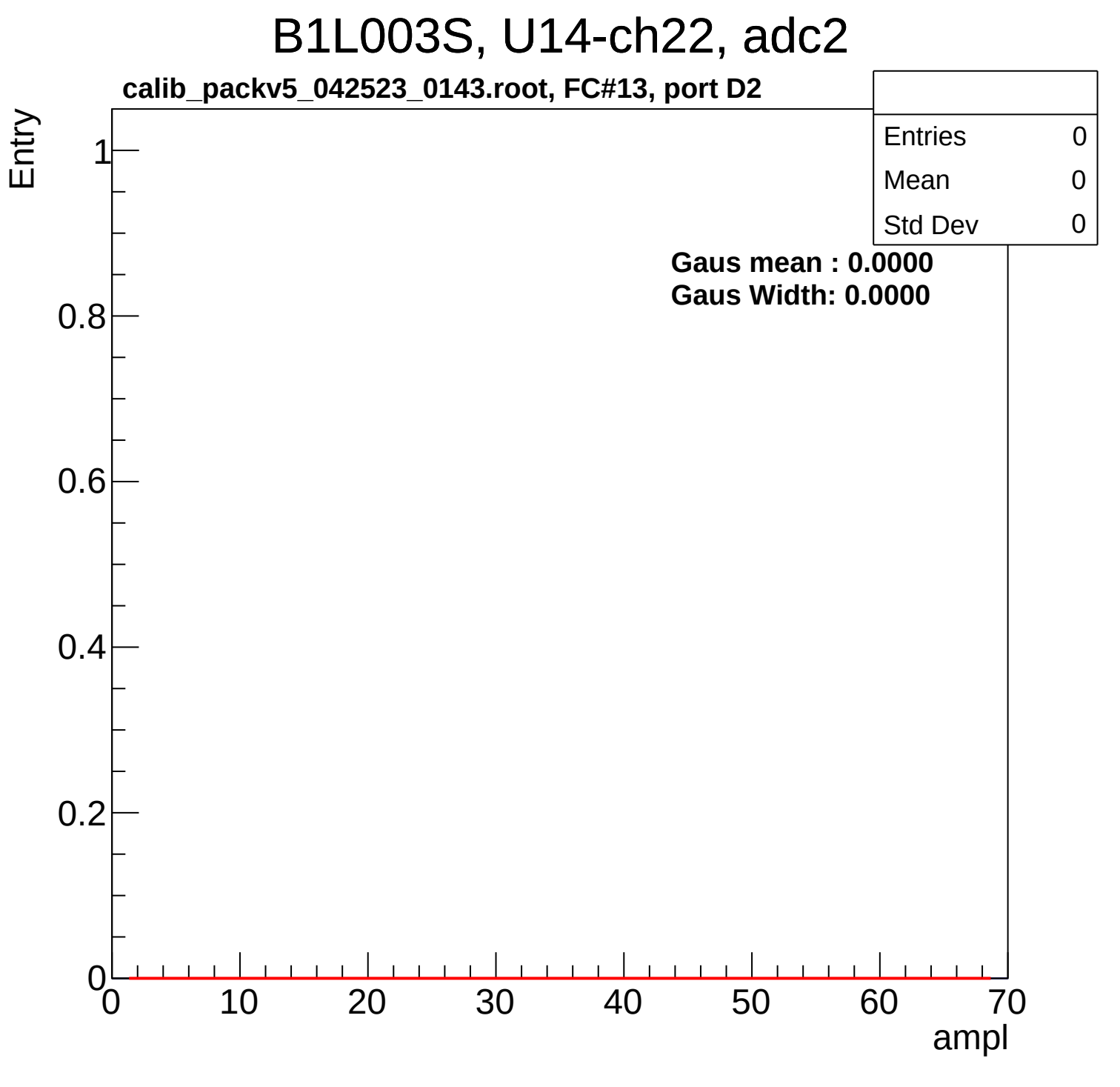

B1L003S, U14-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

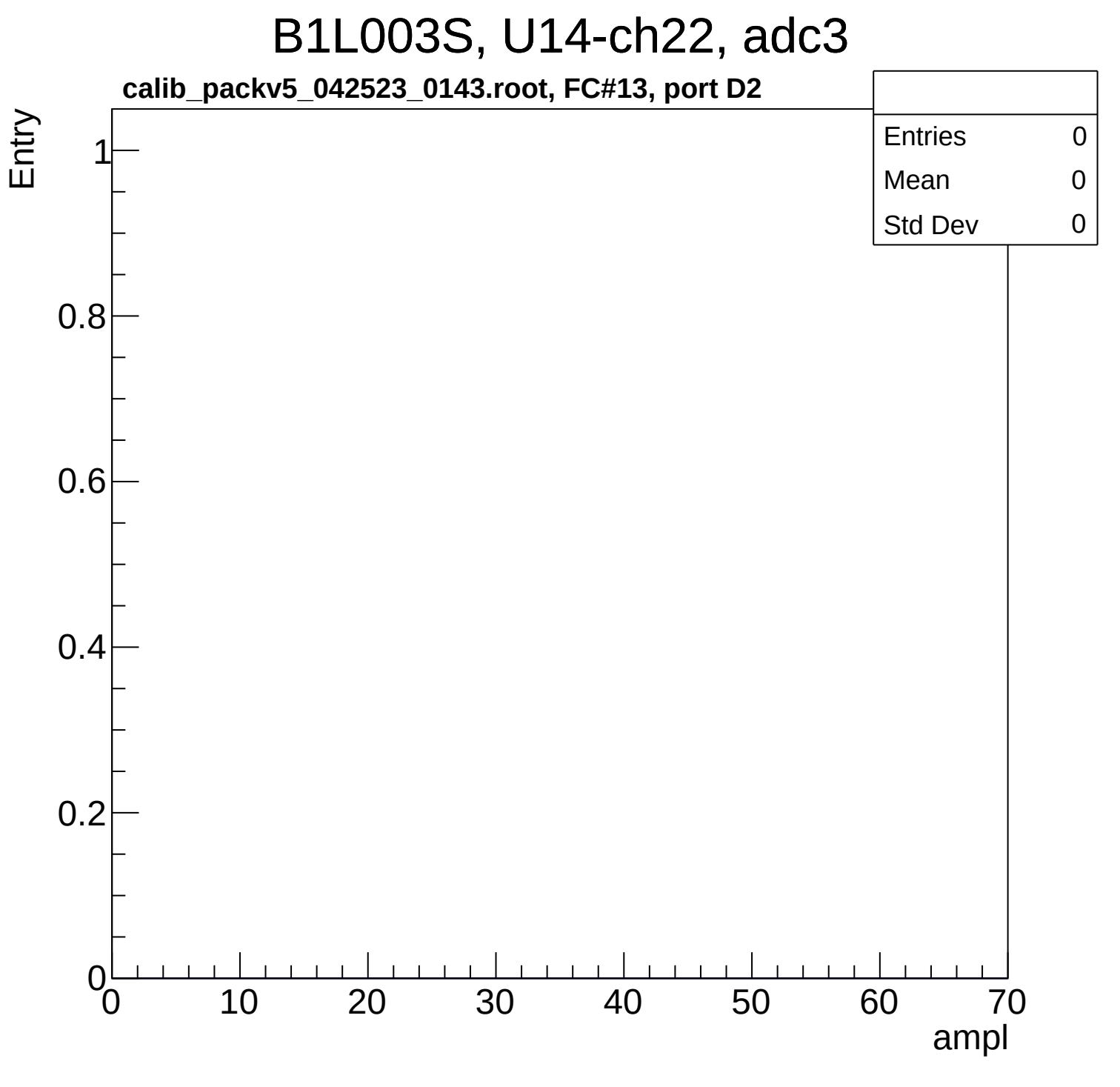

B1L003S, U14-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

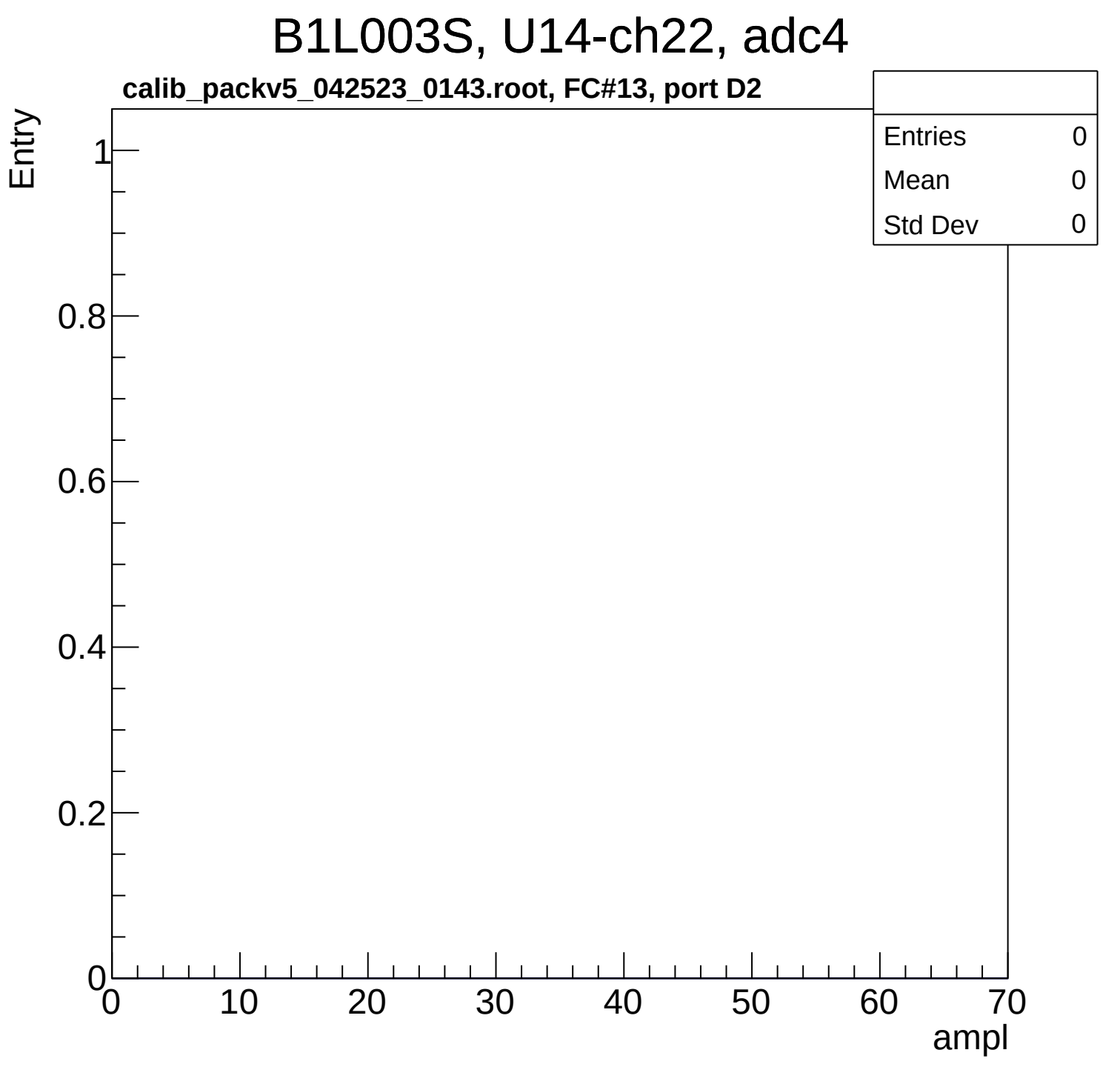

B1L003S, U14-ch21, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

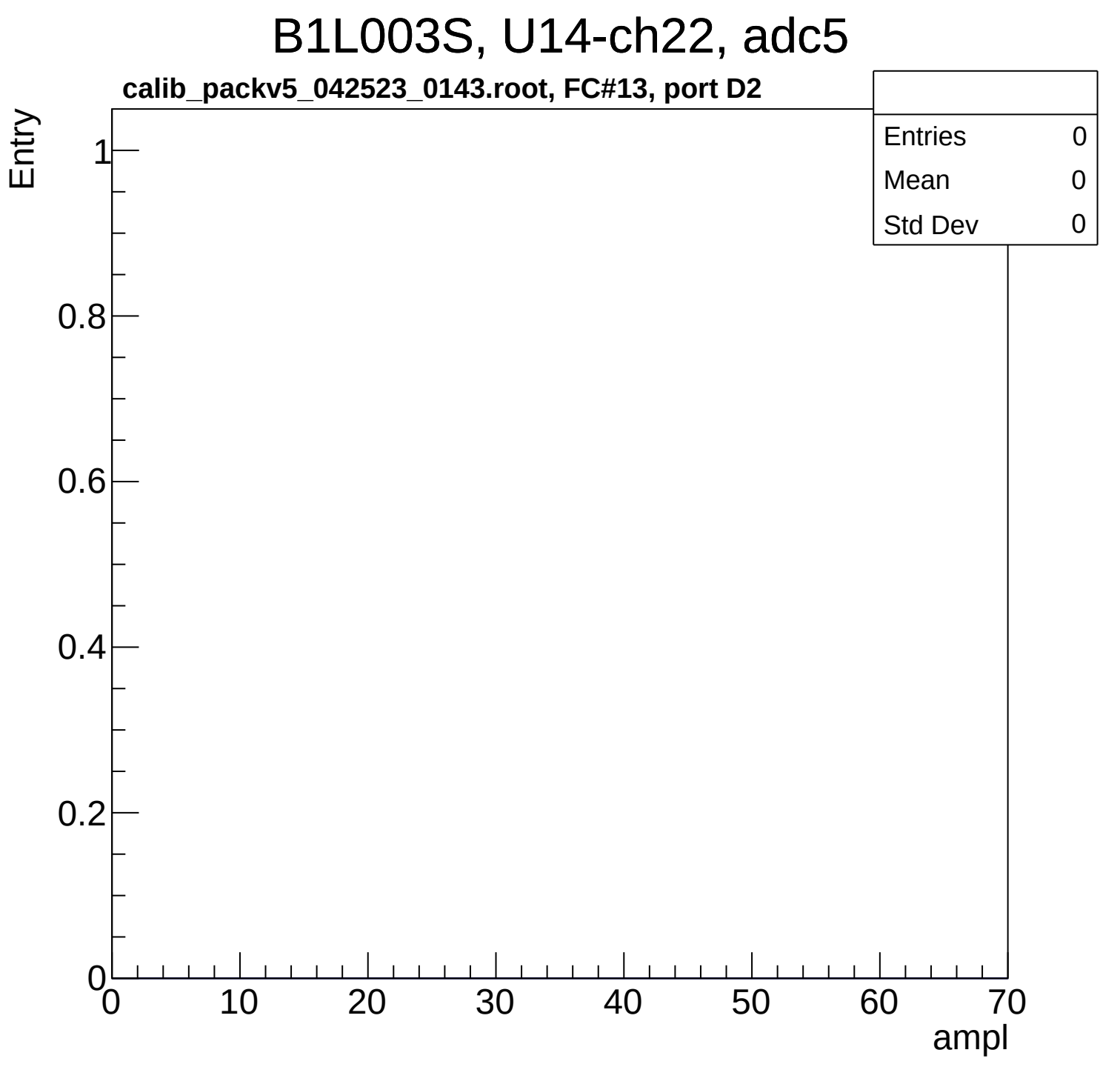

B1L003S, U14-ch22, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

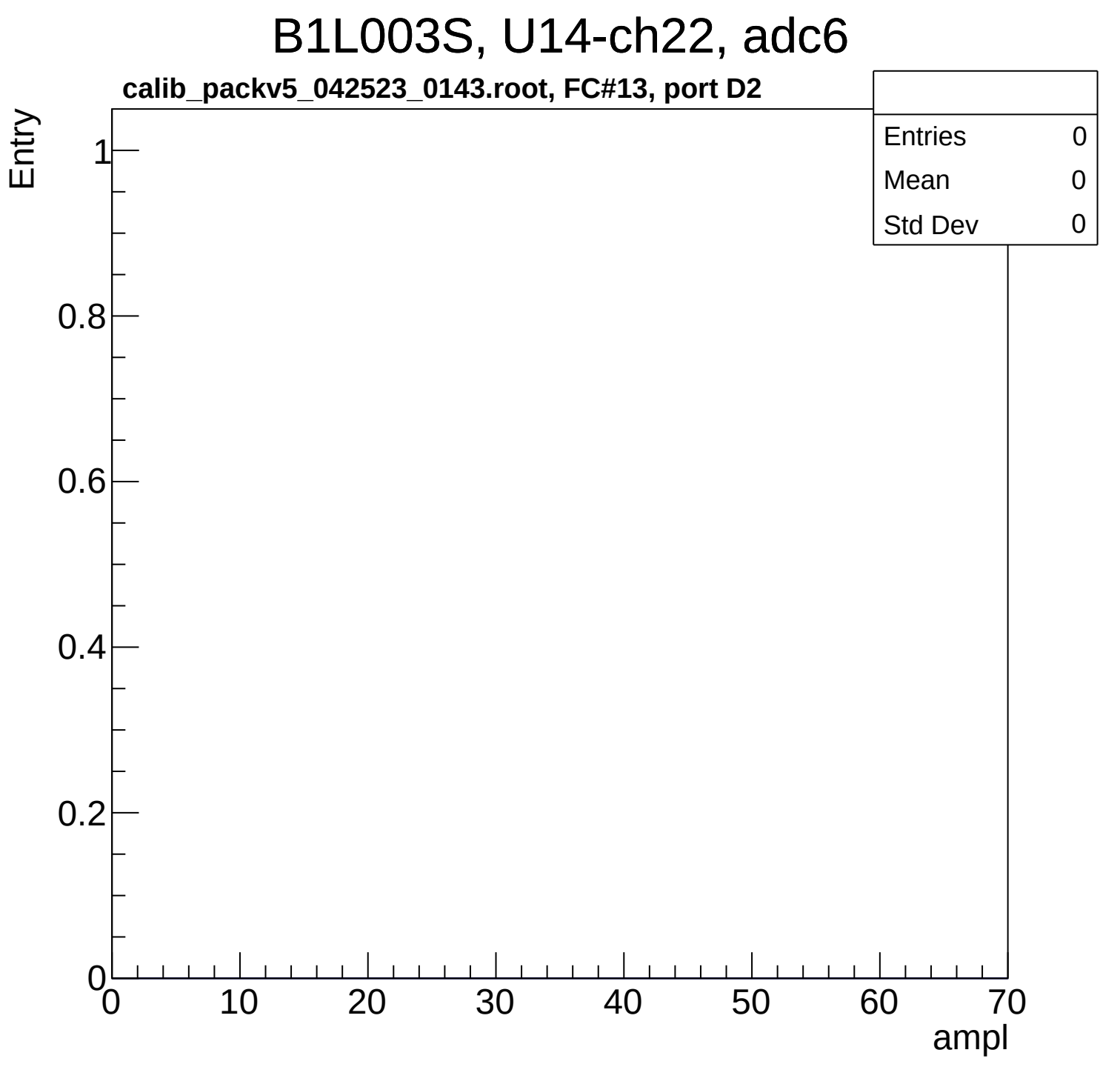

B1L003S, U14-ch22, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

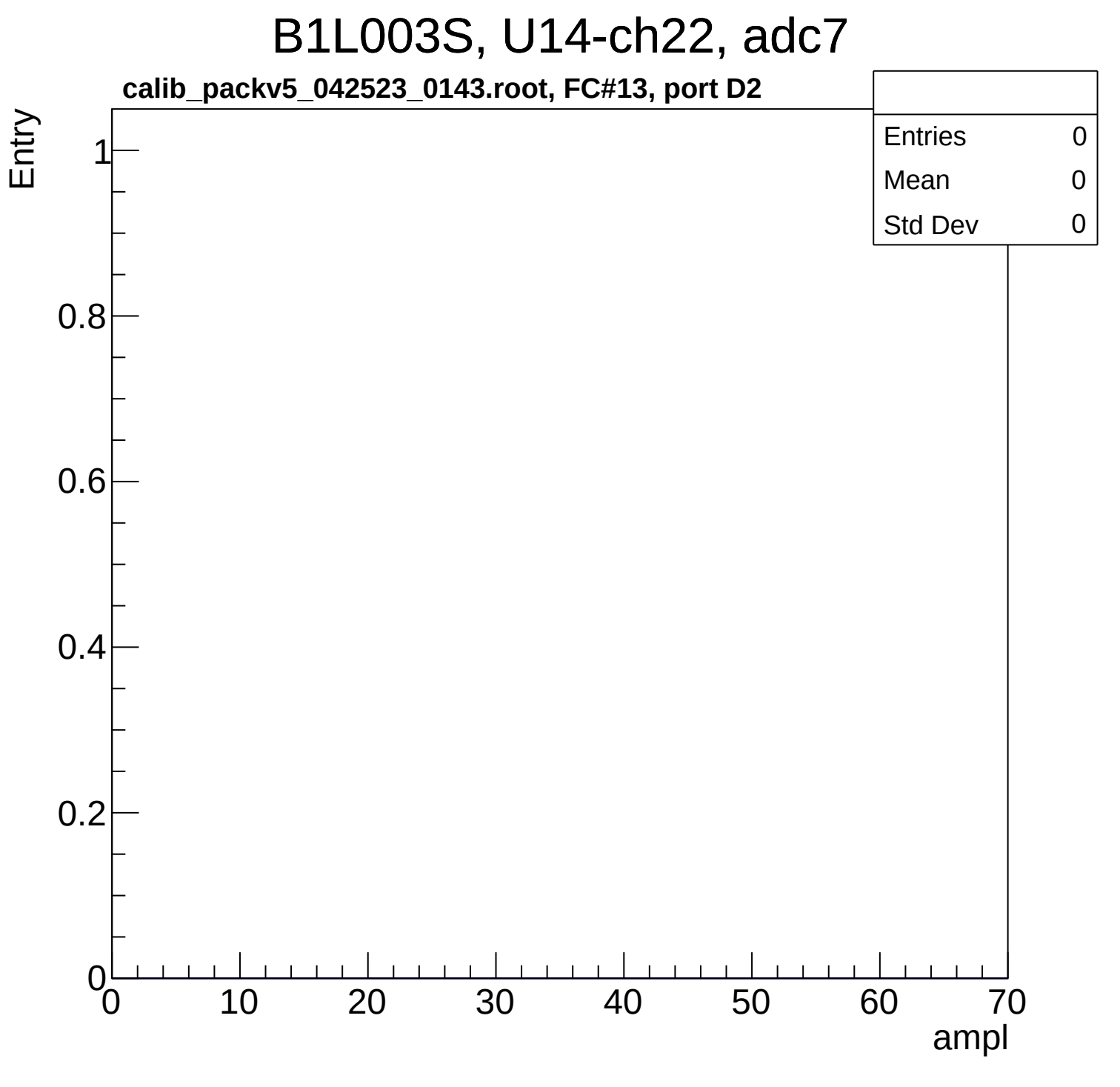

B1L003S, U14-ch22, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

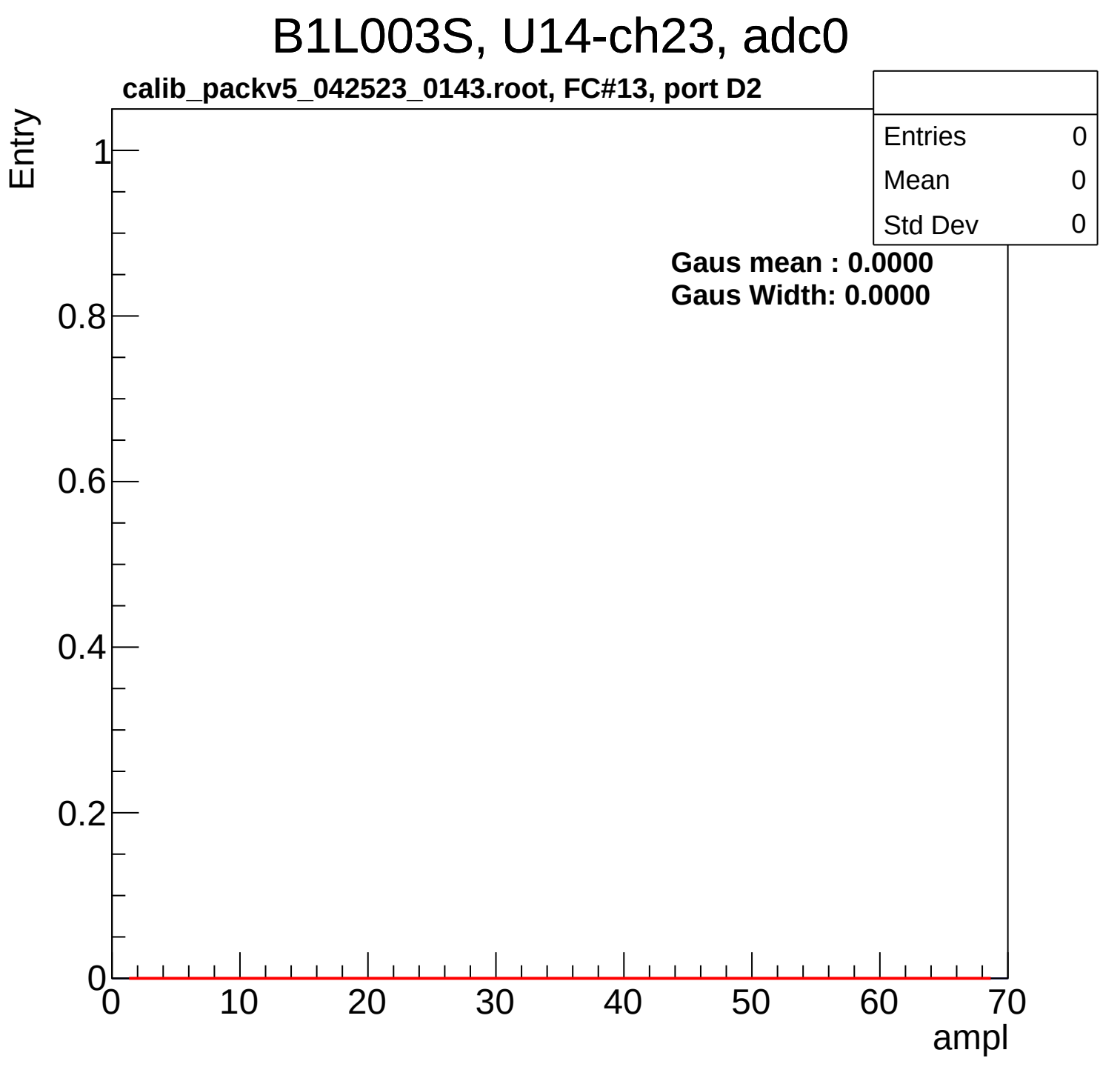

B1L003S, U14-ch22, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

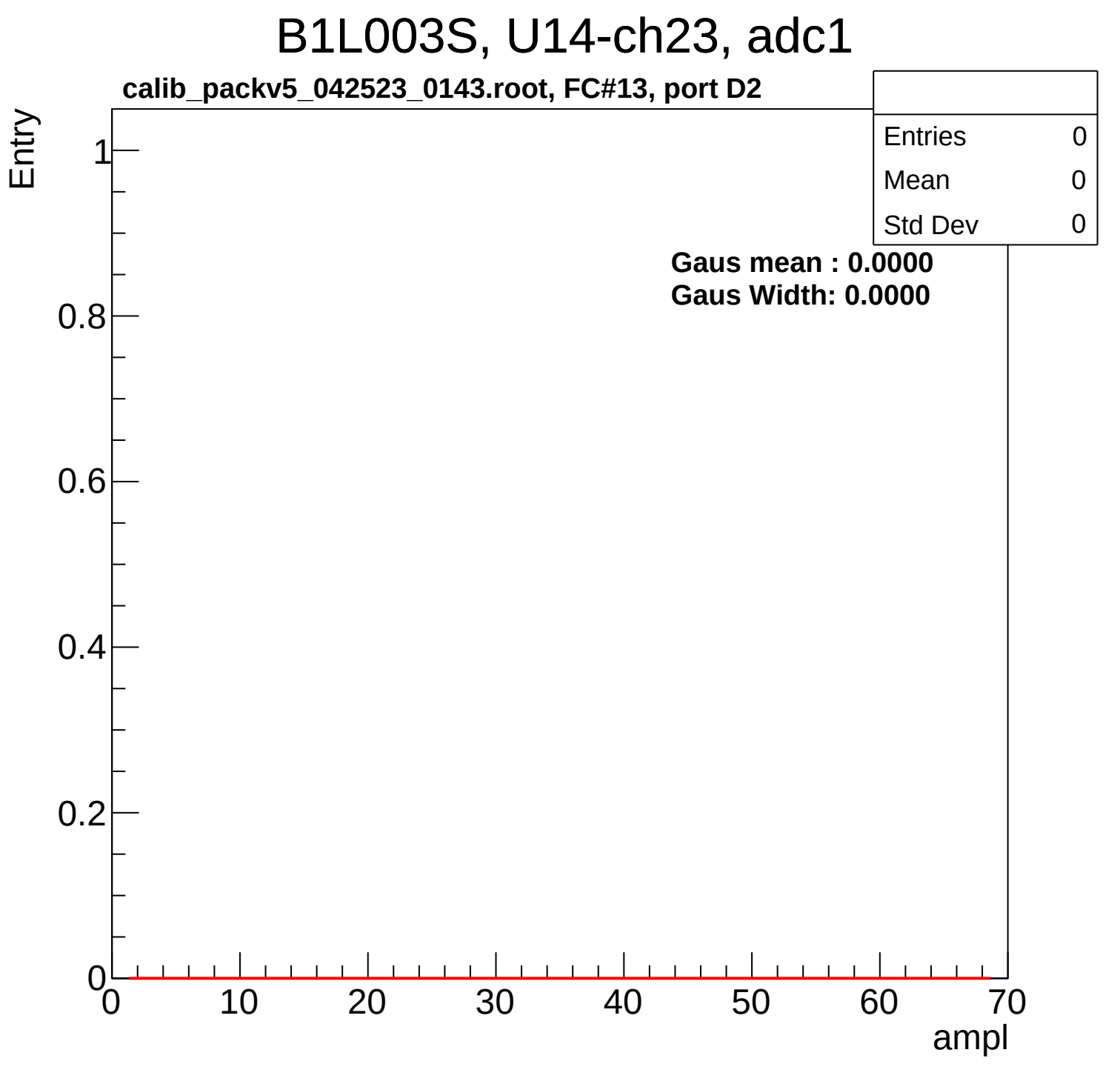

B1L003S, U14-ch22, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

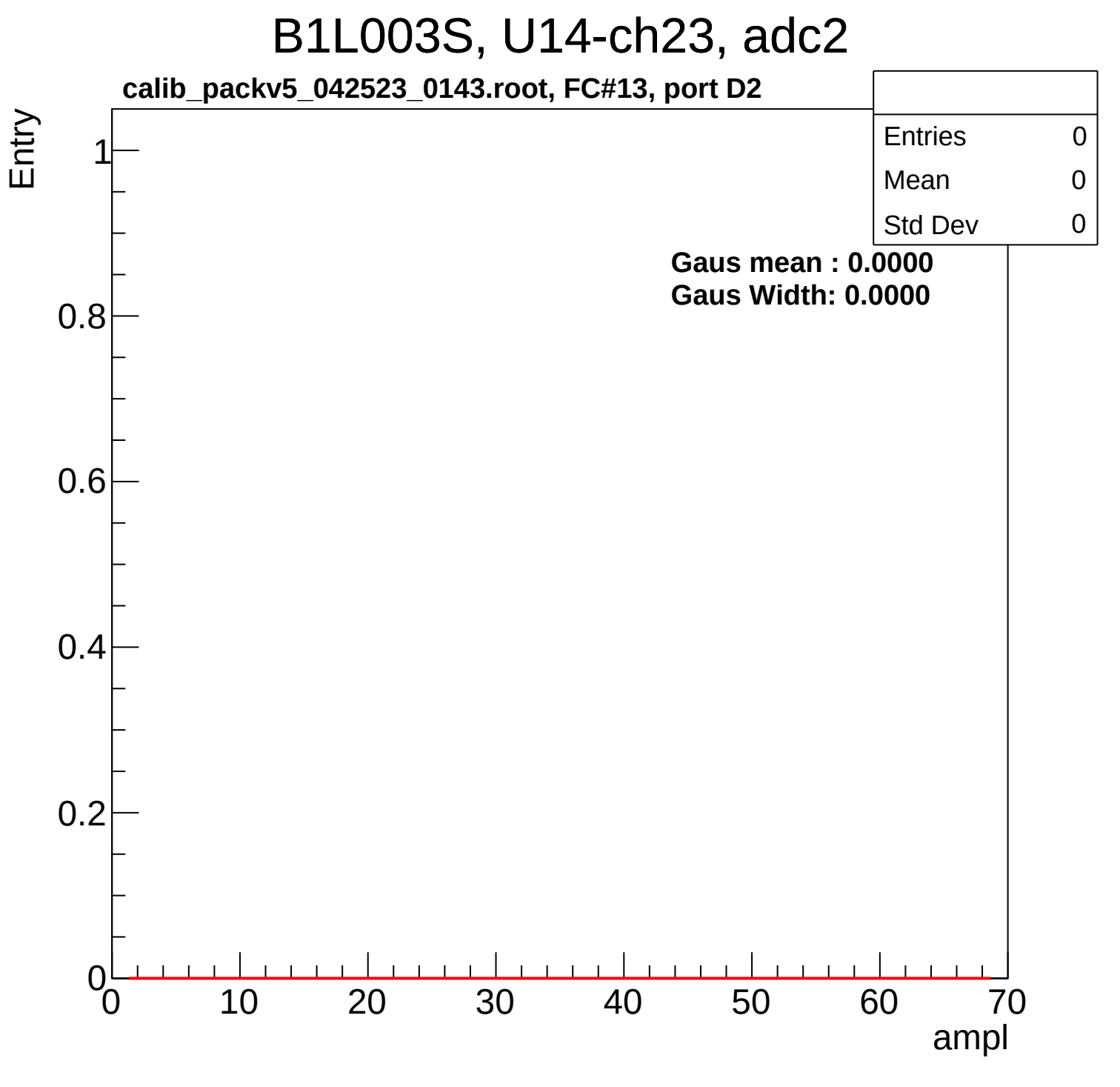

B1L003S, U14-ch22, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

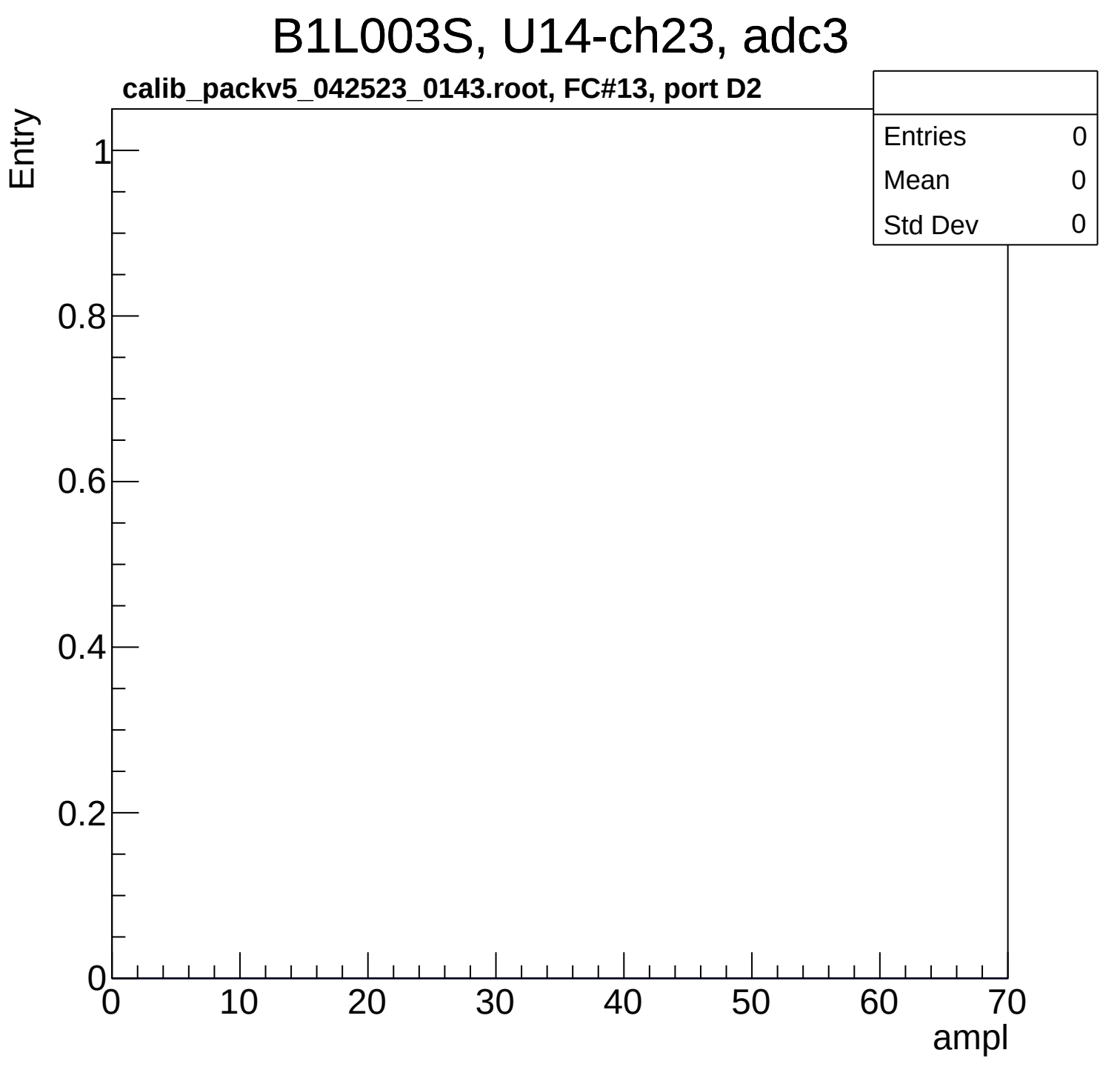

B1L003S, U14-ch22, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

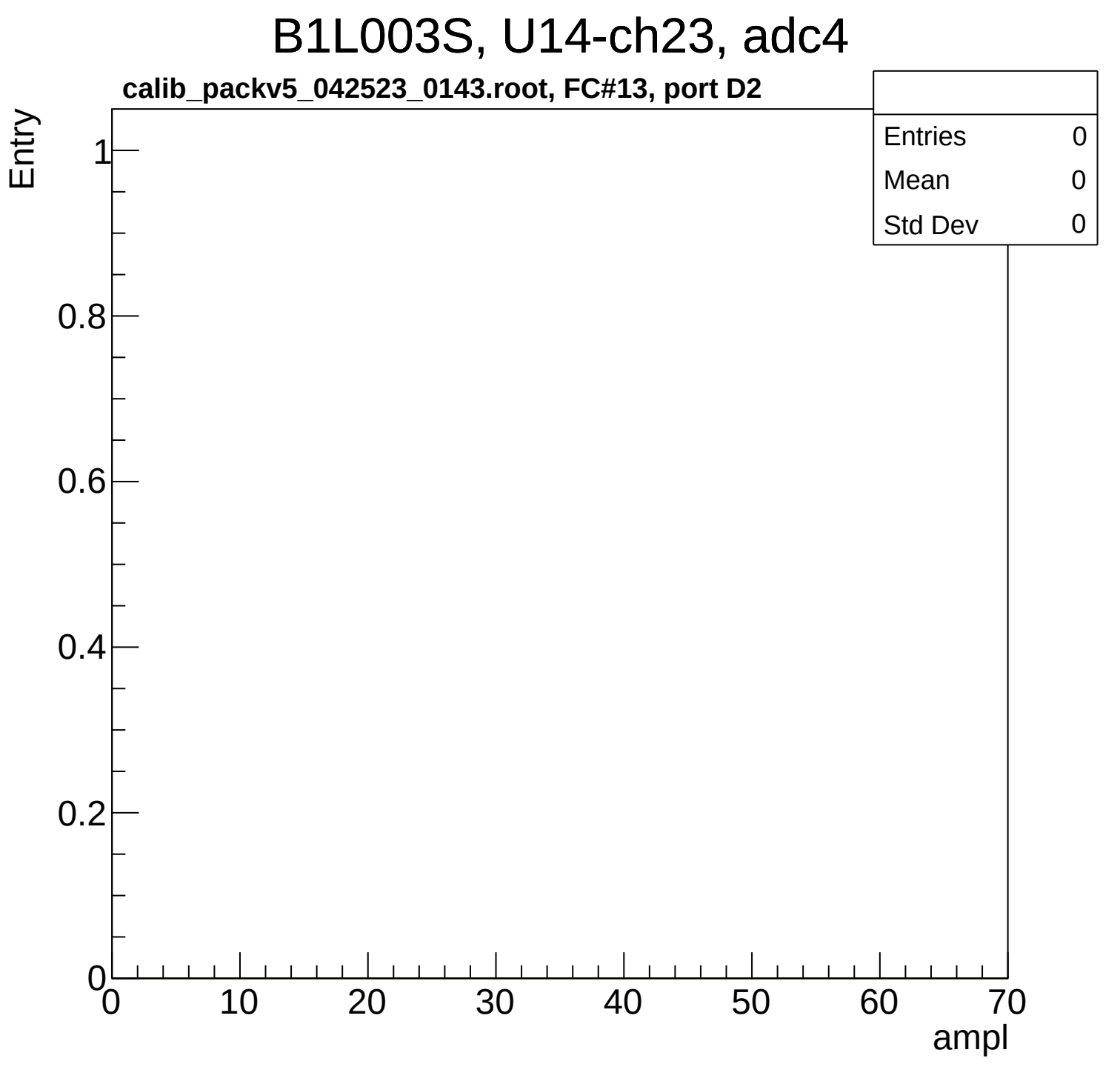

B1L003S, U14-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

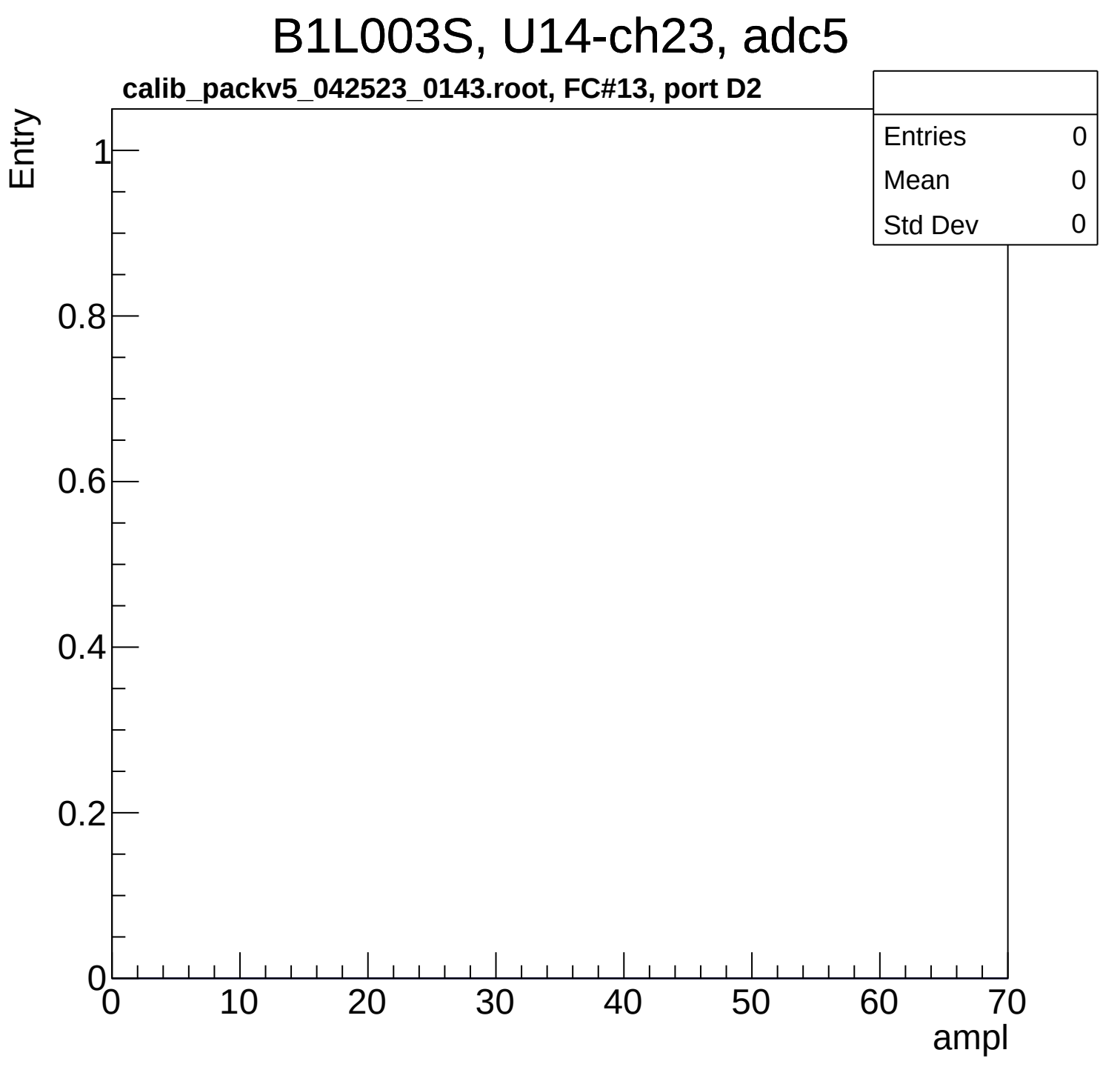

B1L003S, U14-ch23, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

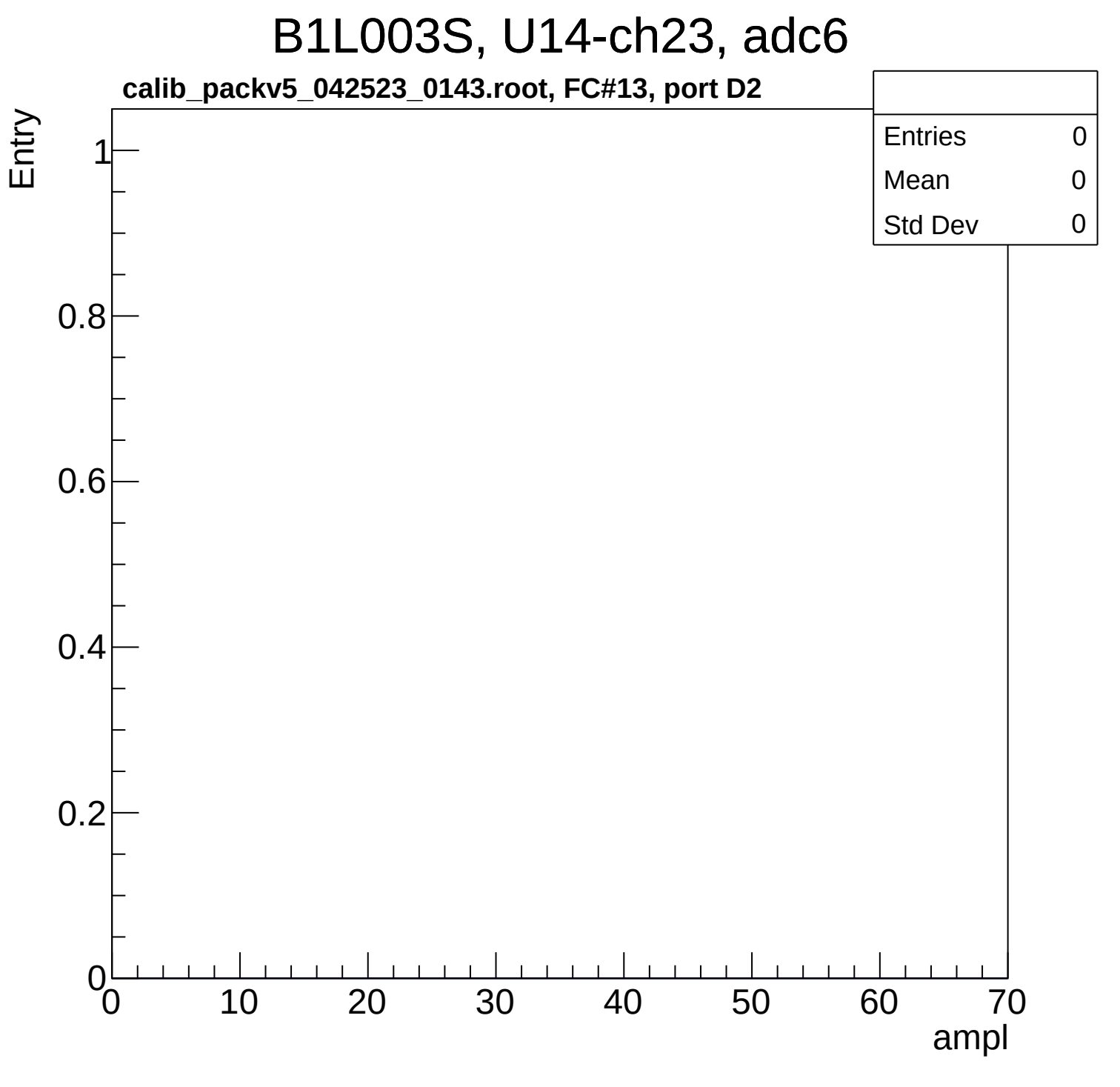

B1L003S, U14-ch23, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

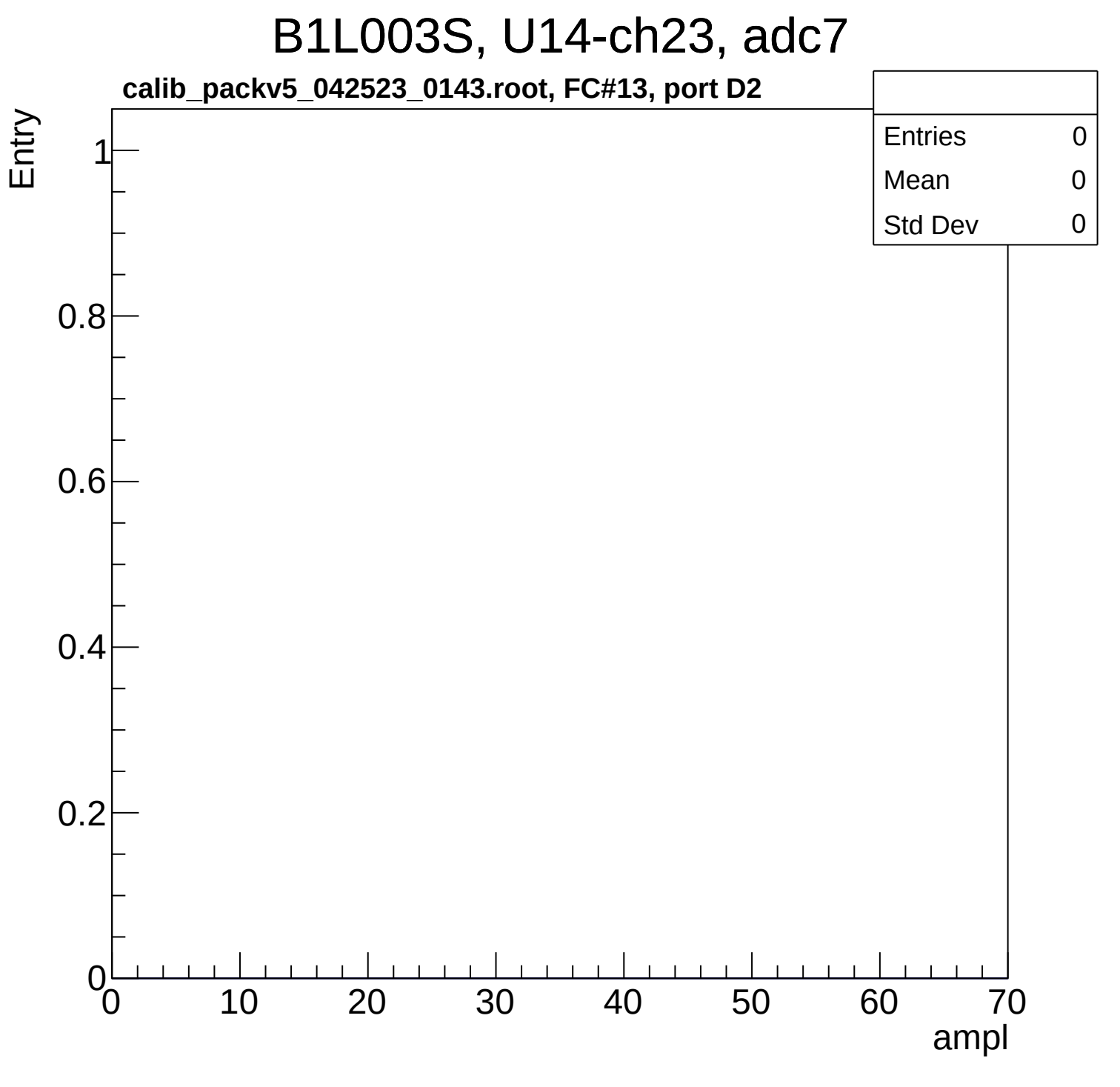

B1L003S, U14-ch23, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

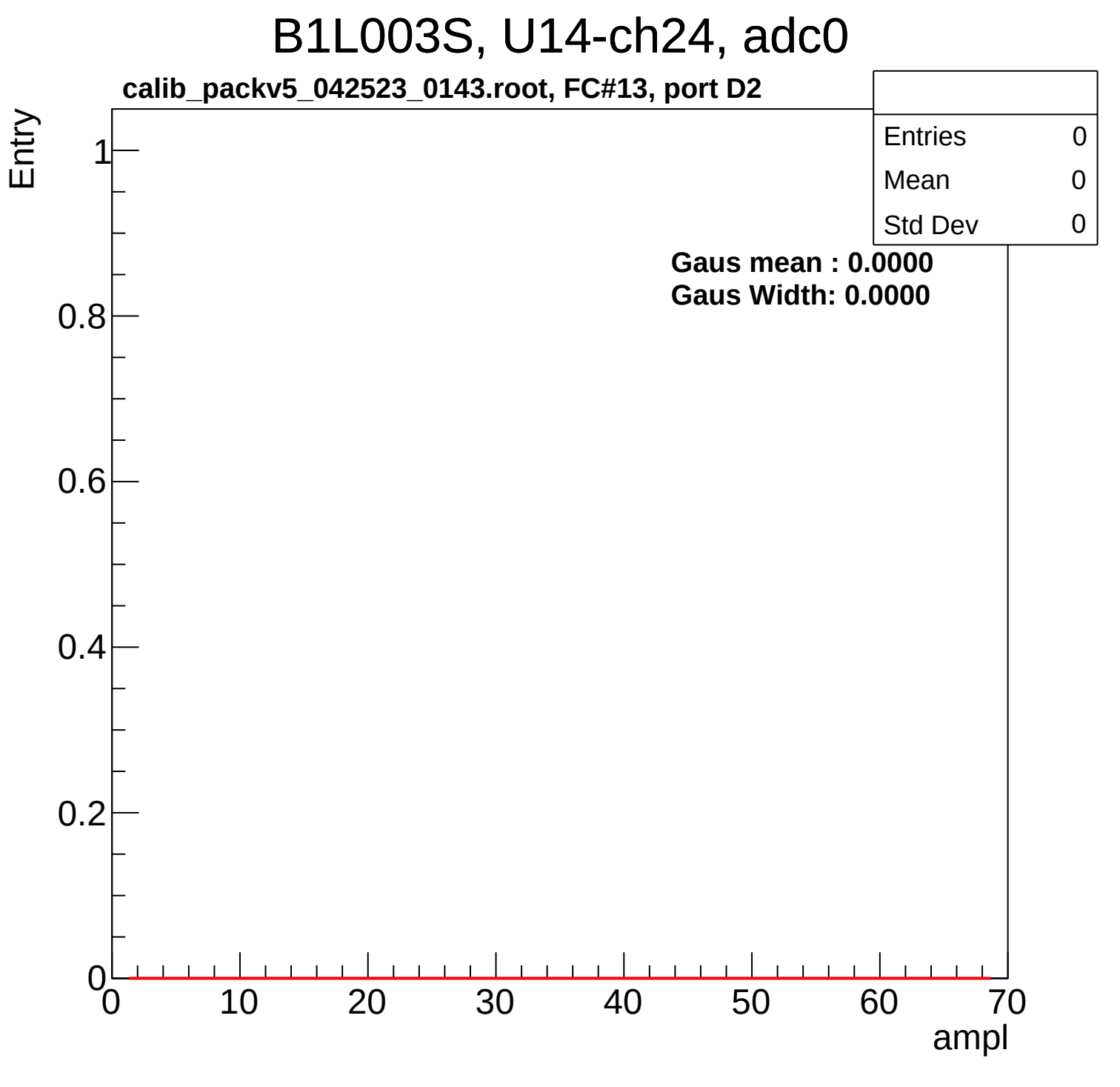

B1L003S, U14-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

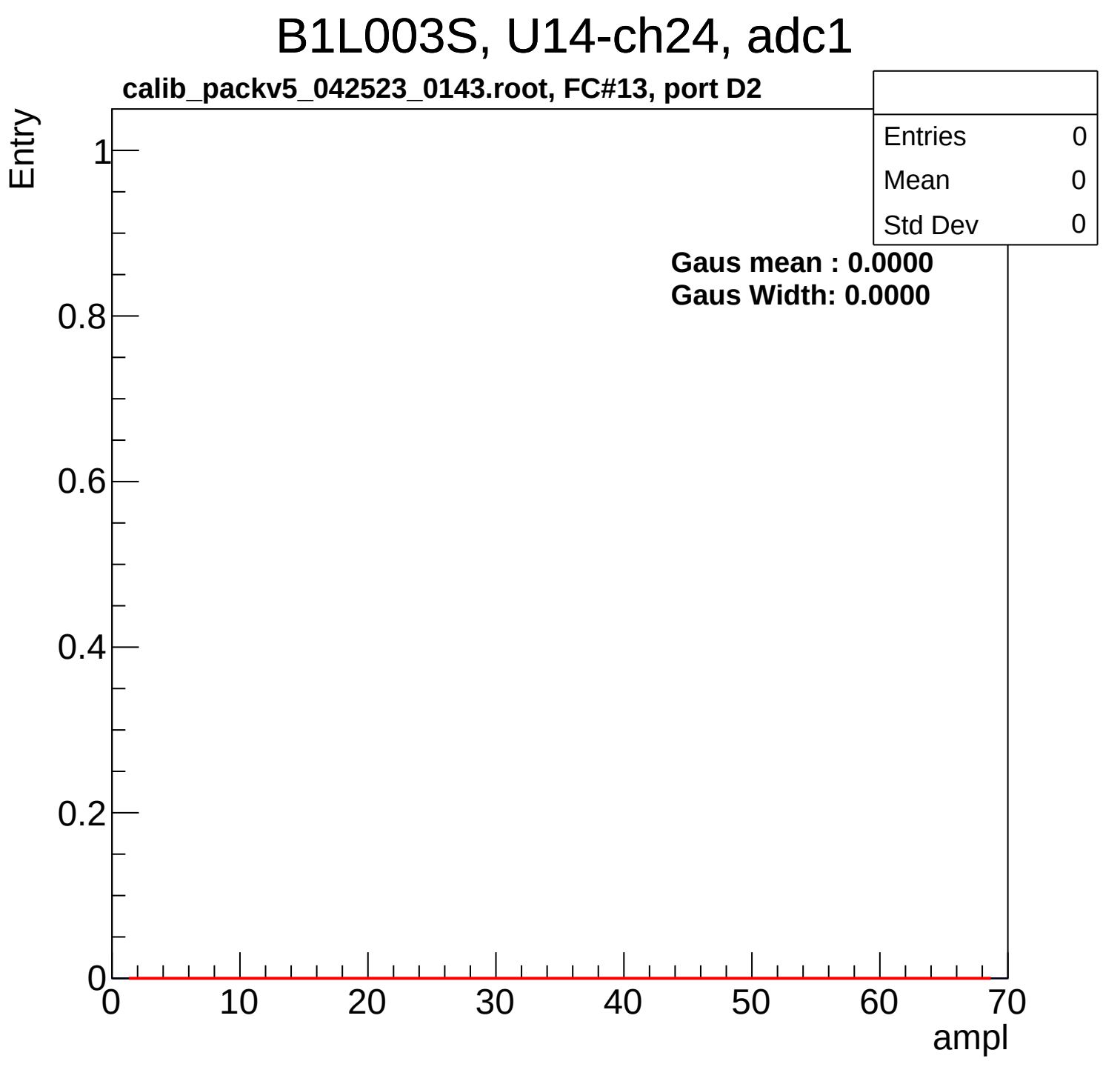

B1L003S, U14-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

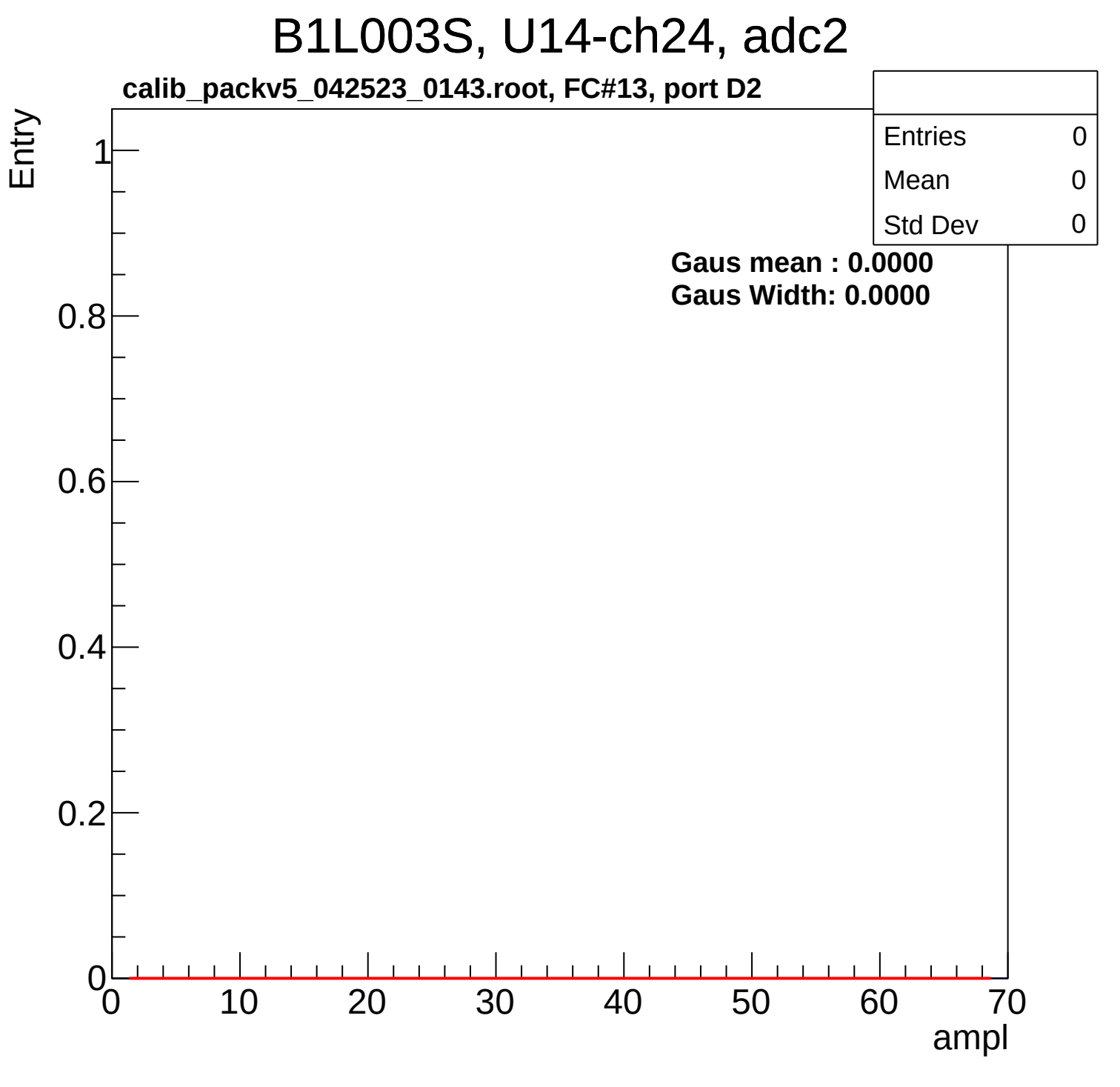

B1L003S, U14-ch23, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

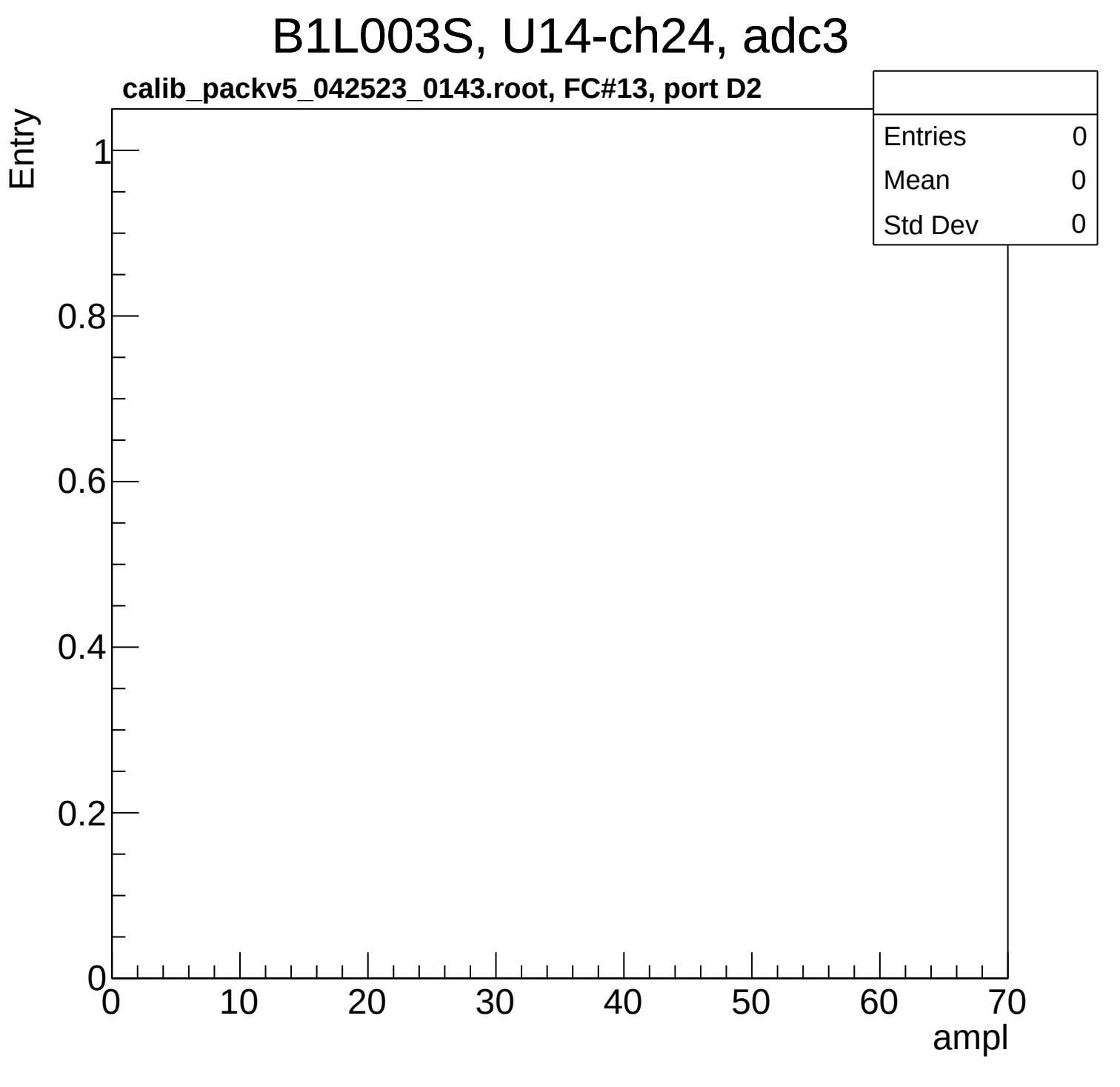

B1L003S, U14-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

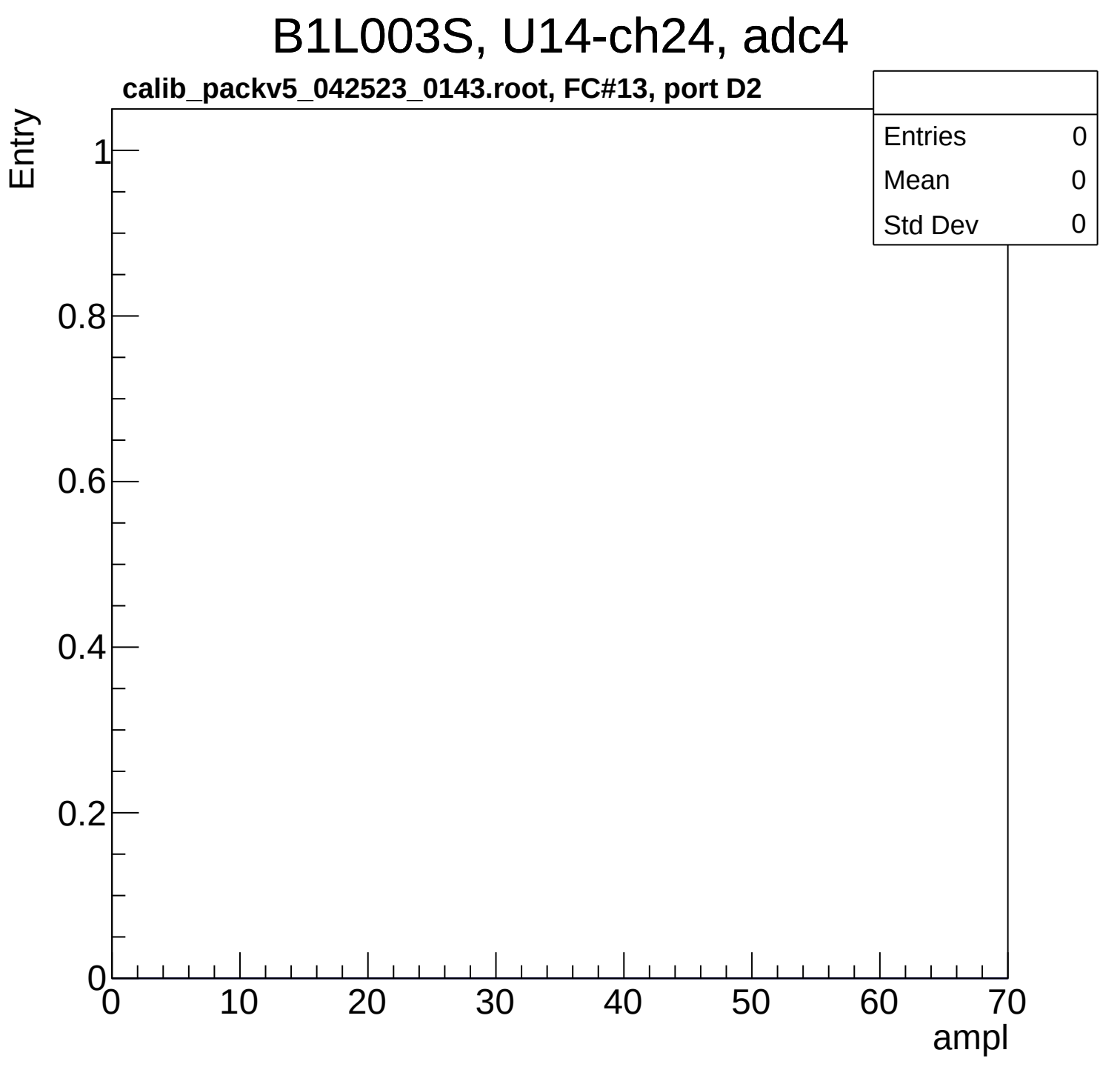

B1L003S, U14-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

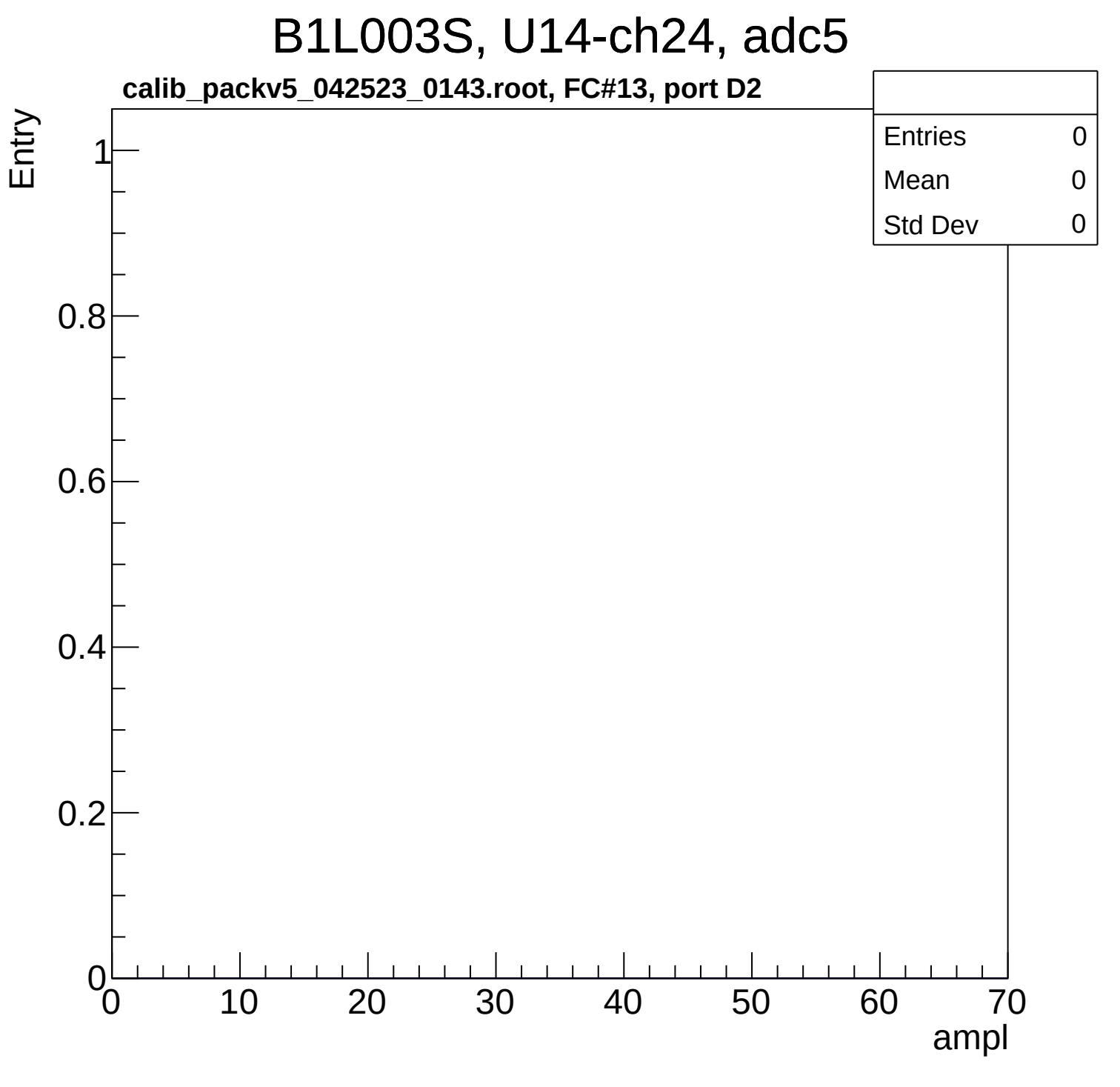

B1L003S, U14-ch24, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

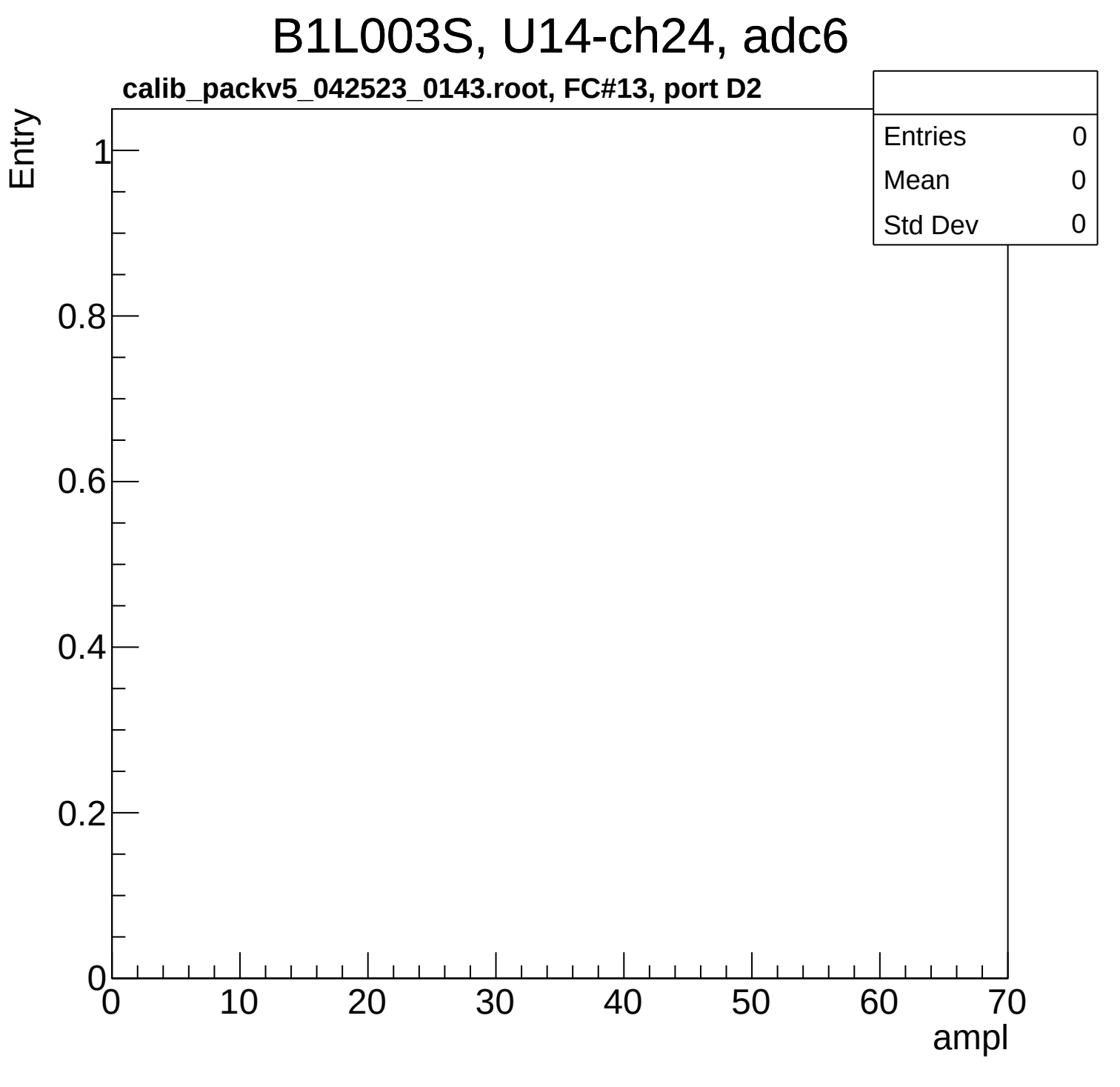

B1L003S, U14-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

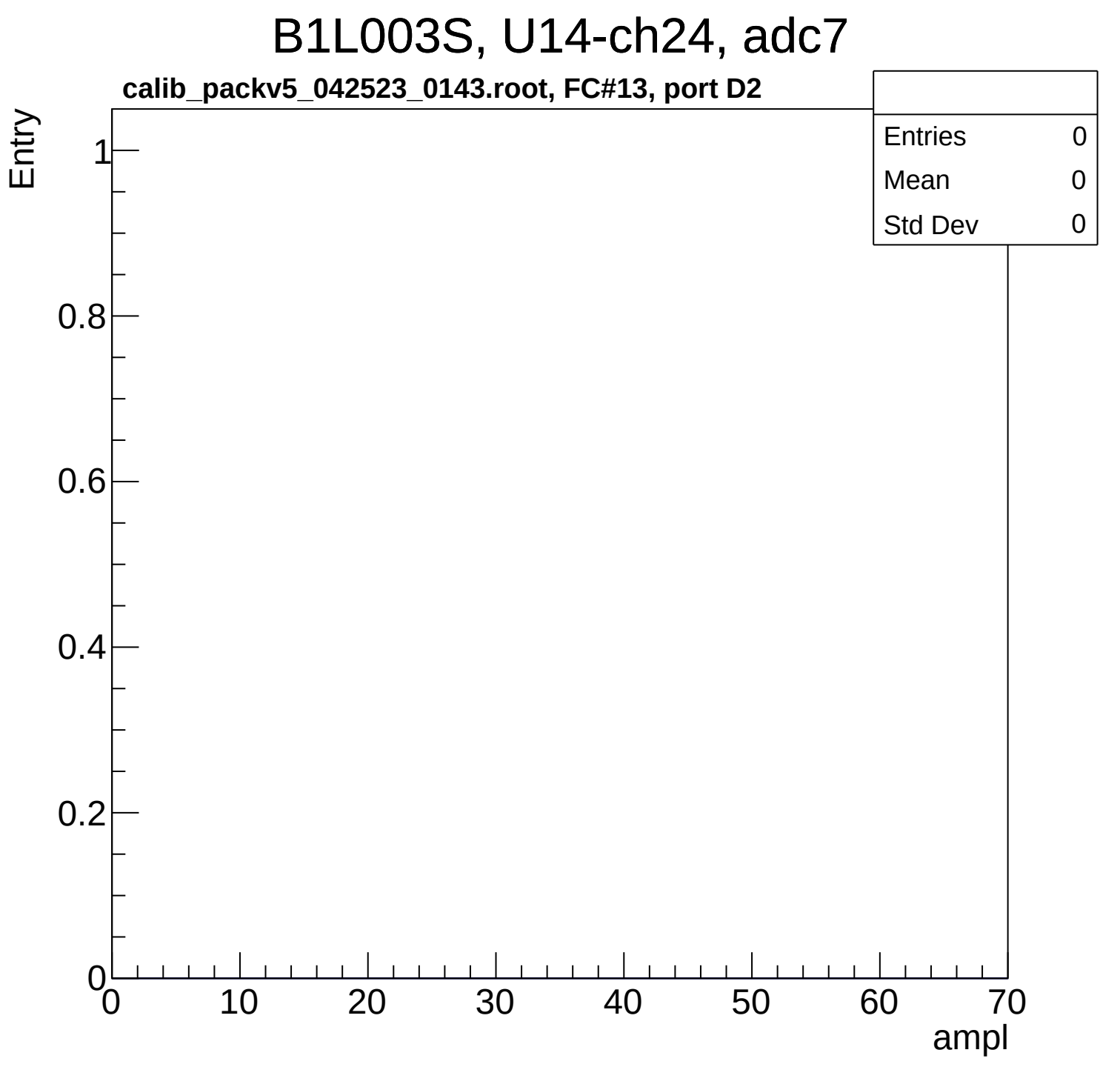

B1L003S, U14-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

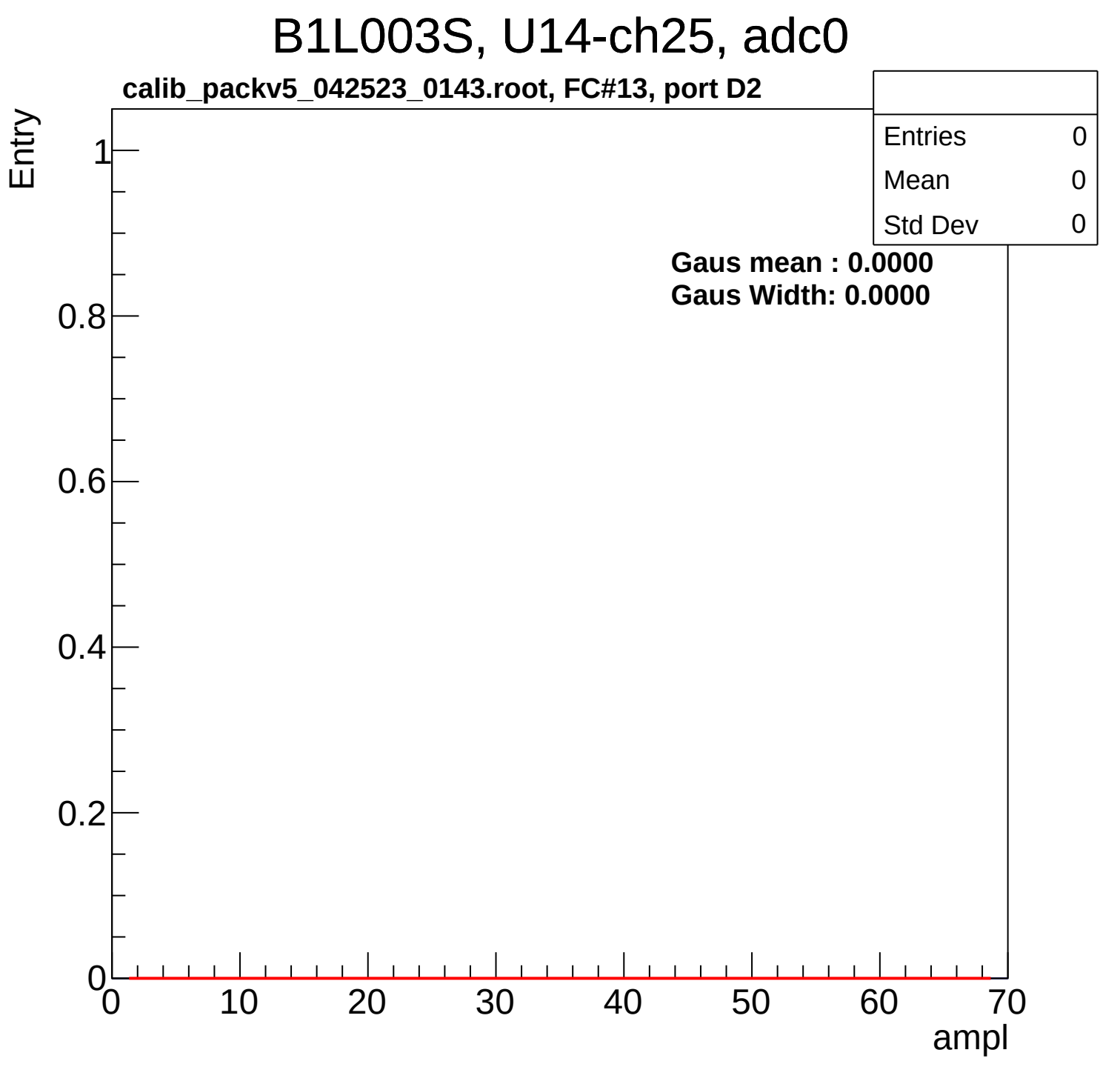

B1L003S, U14-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

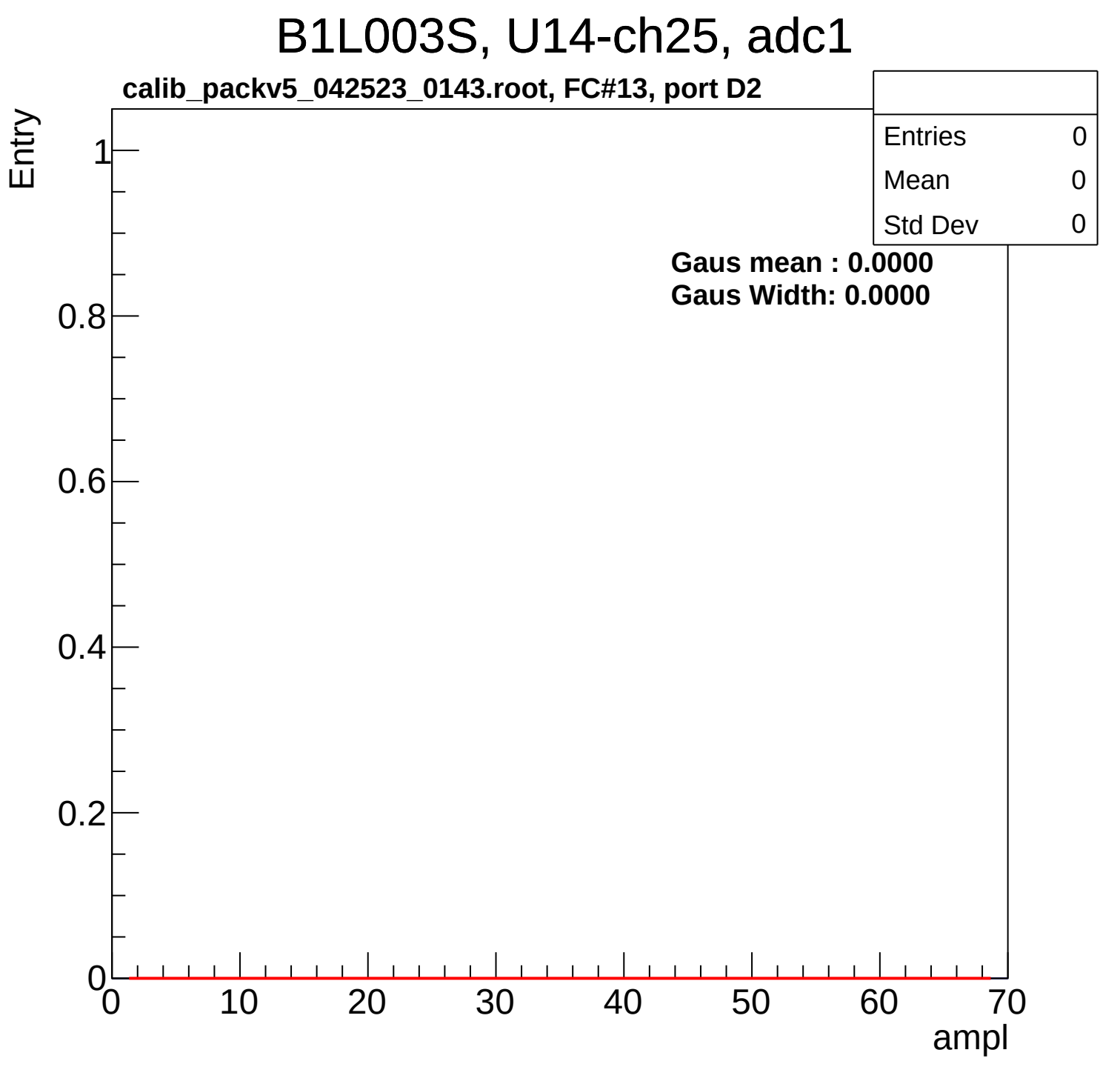

B1L003S, U14-ch24, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

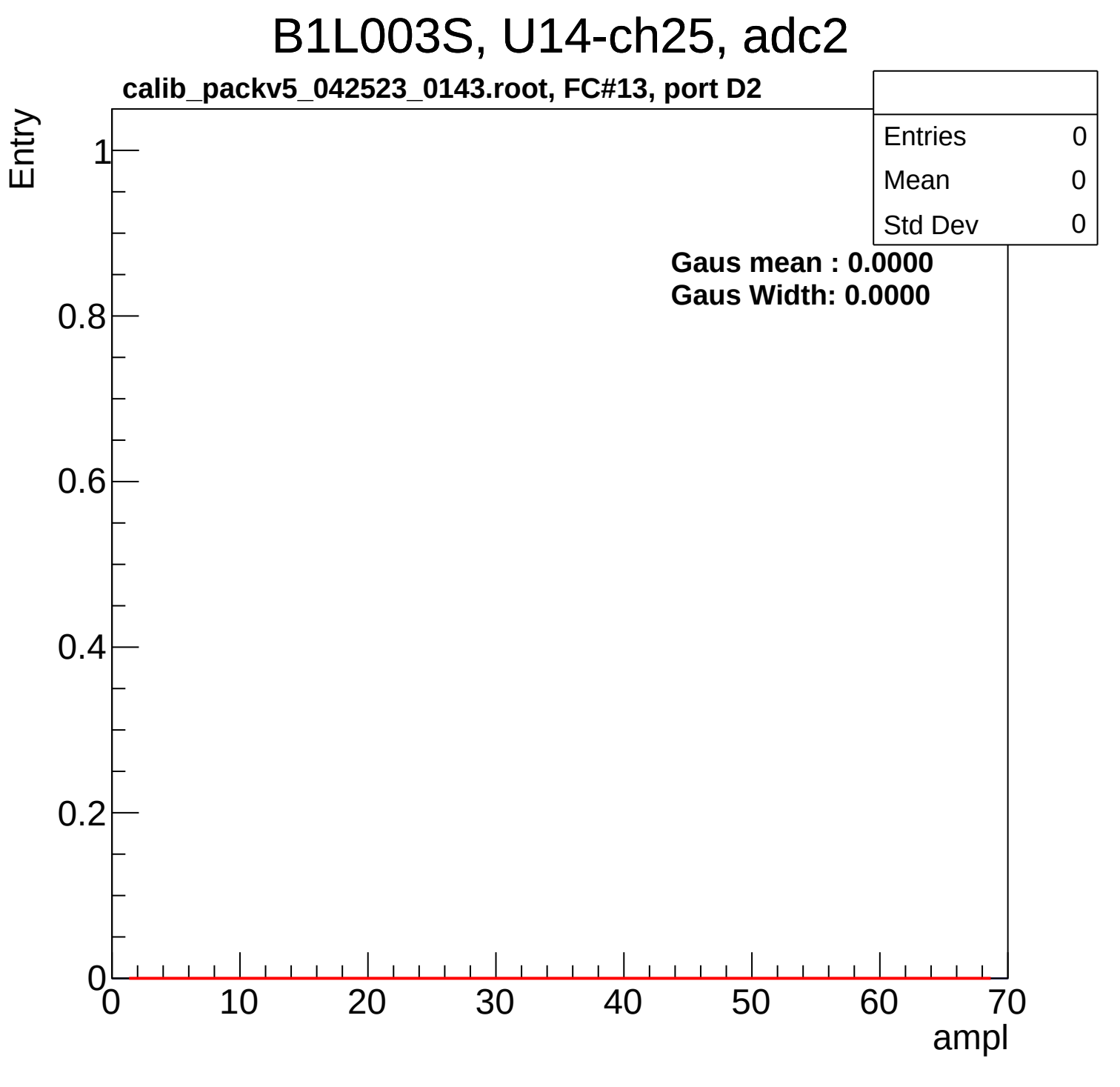

B1L003S, U14-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

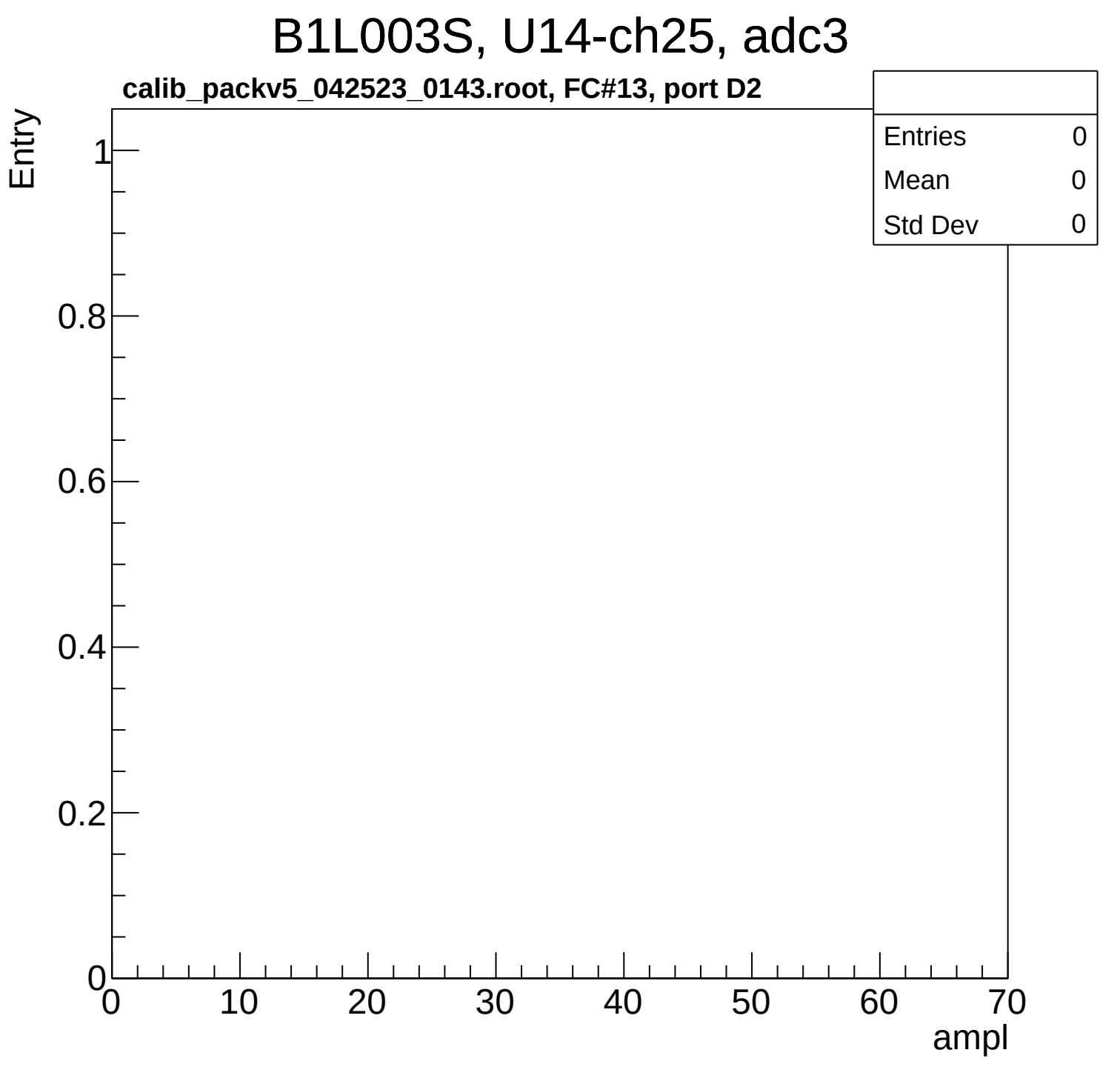

B1L003S, U14-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

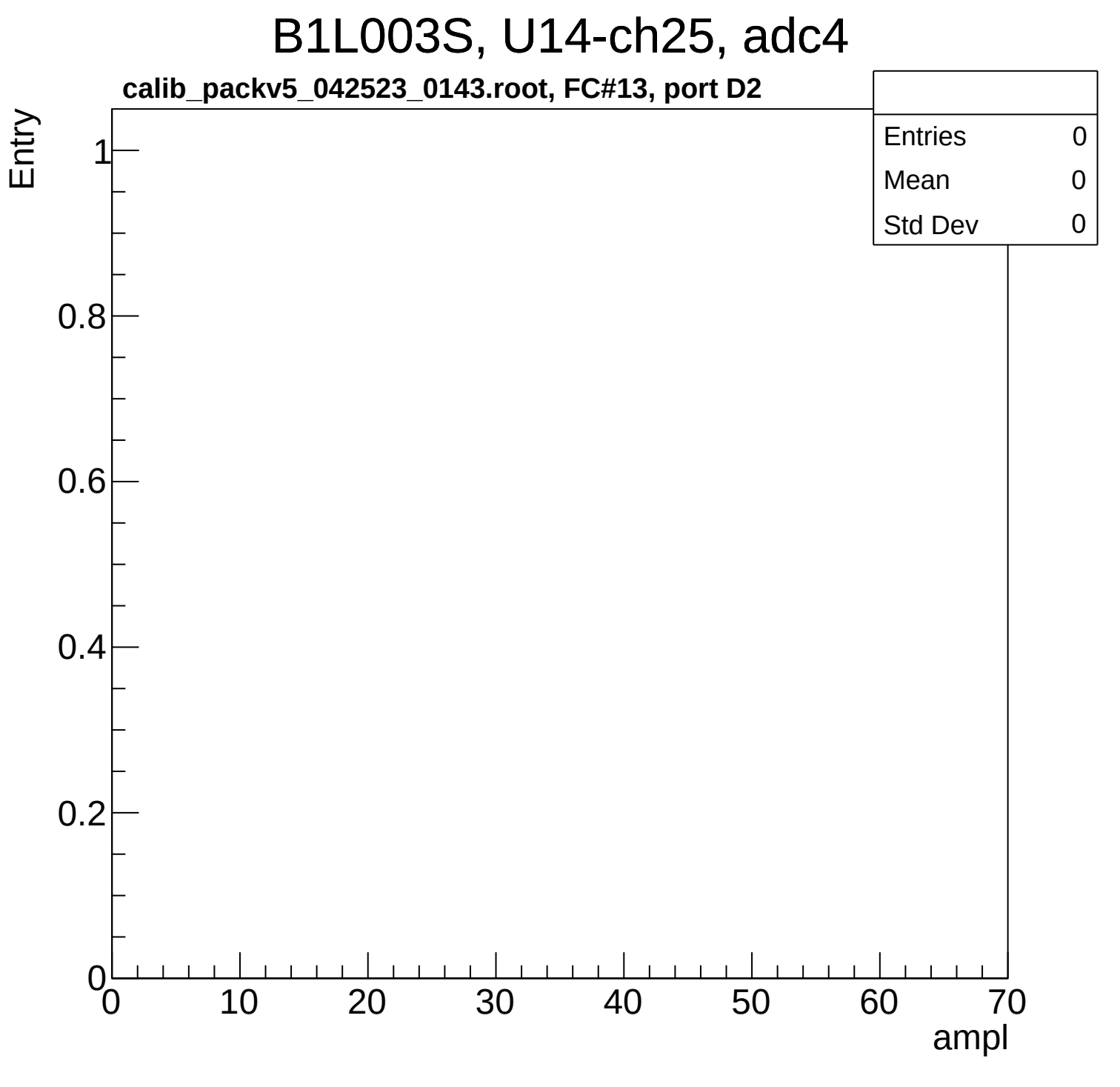

B1L003S, U14-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

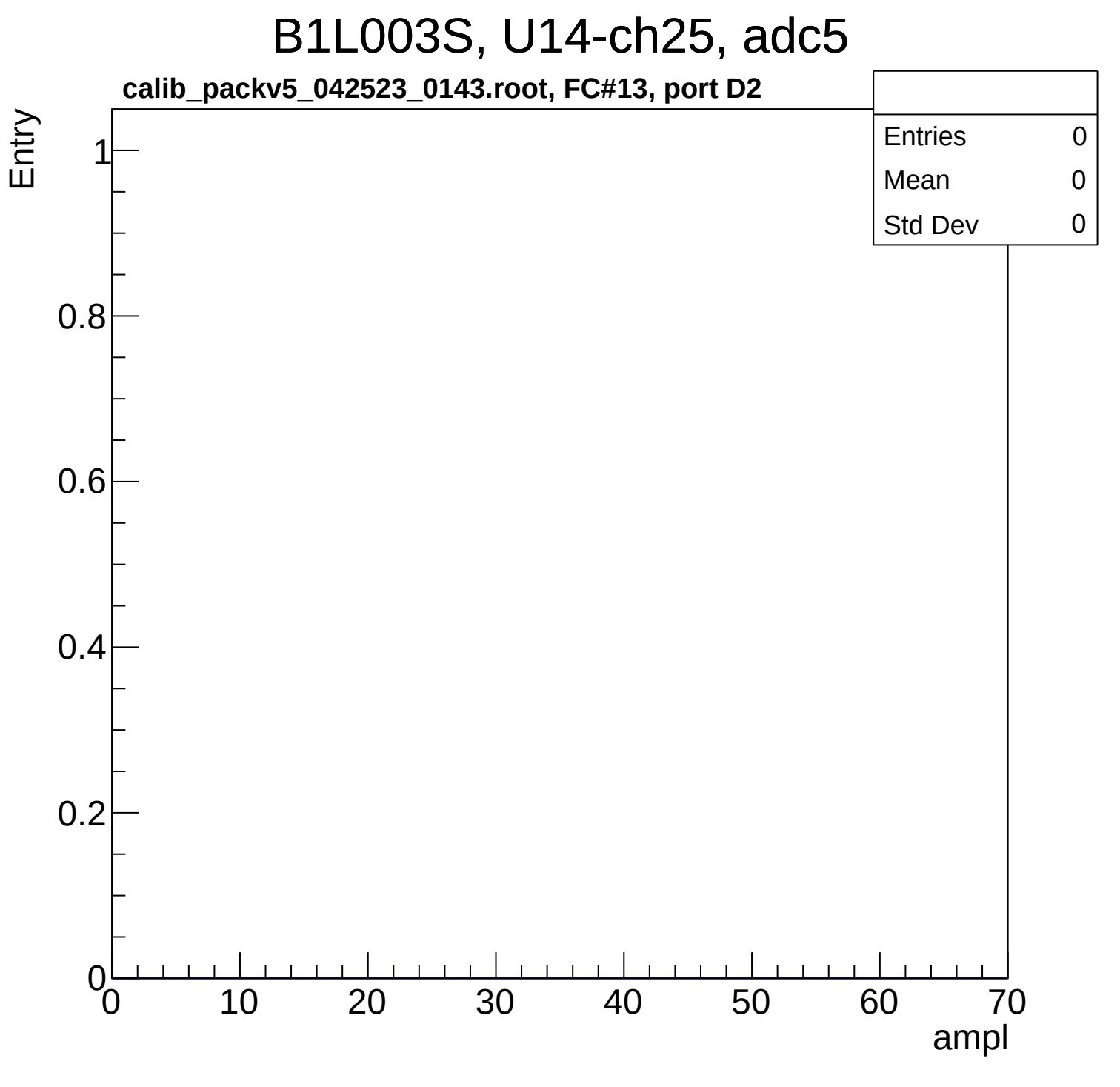

B1L003S, U14-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

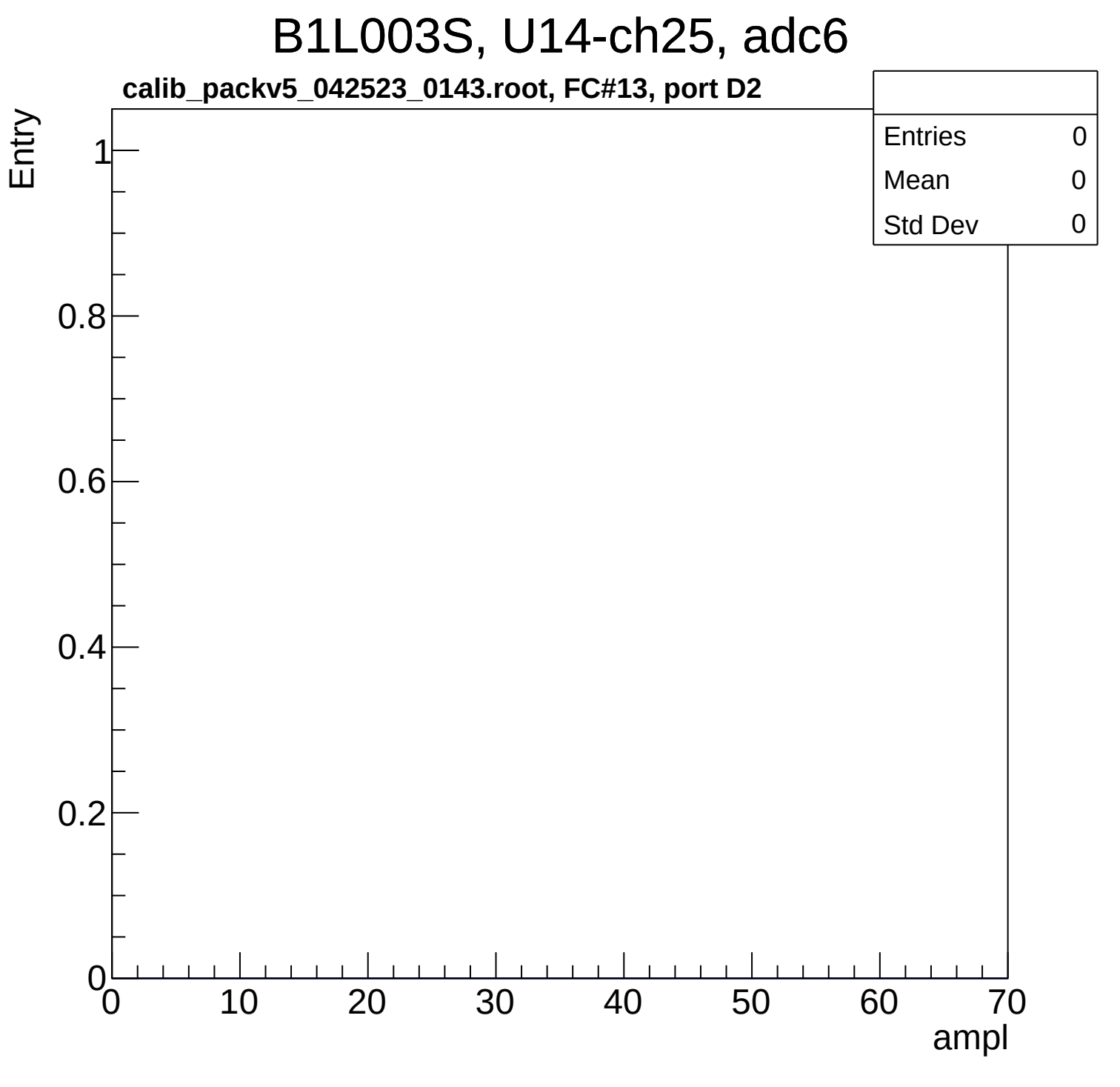

B1L003S, U14-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

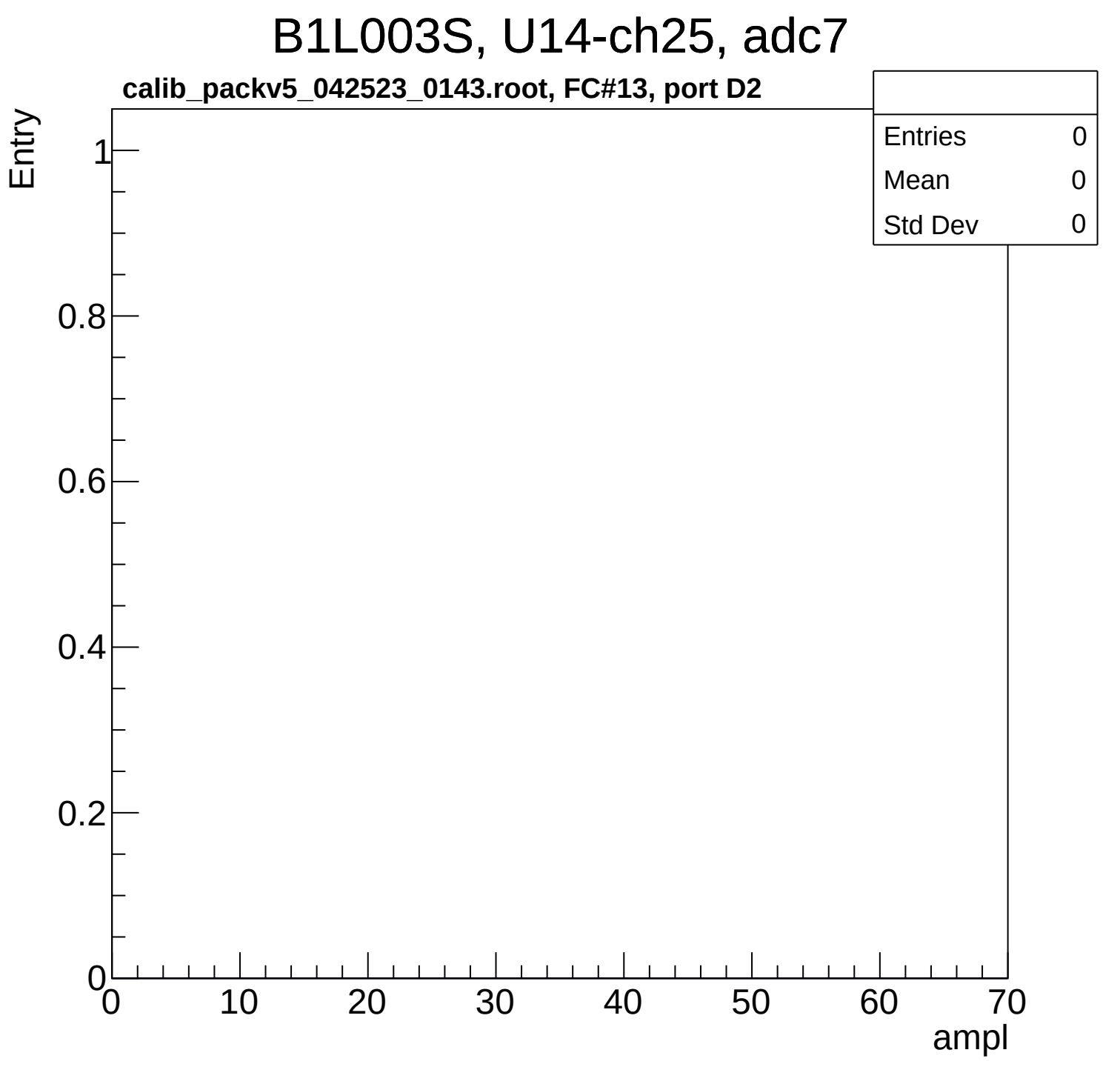

B1L003S, U14-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

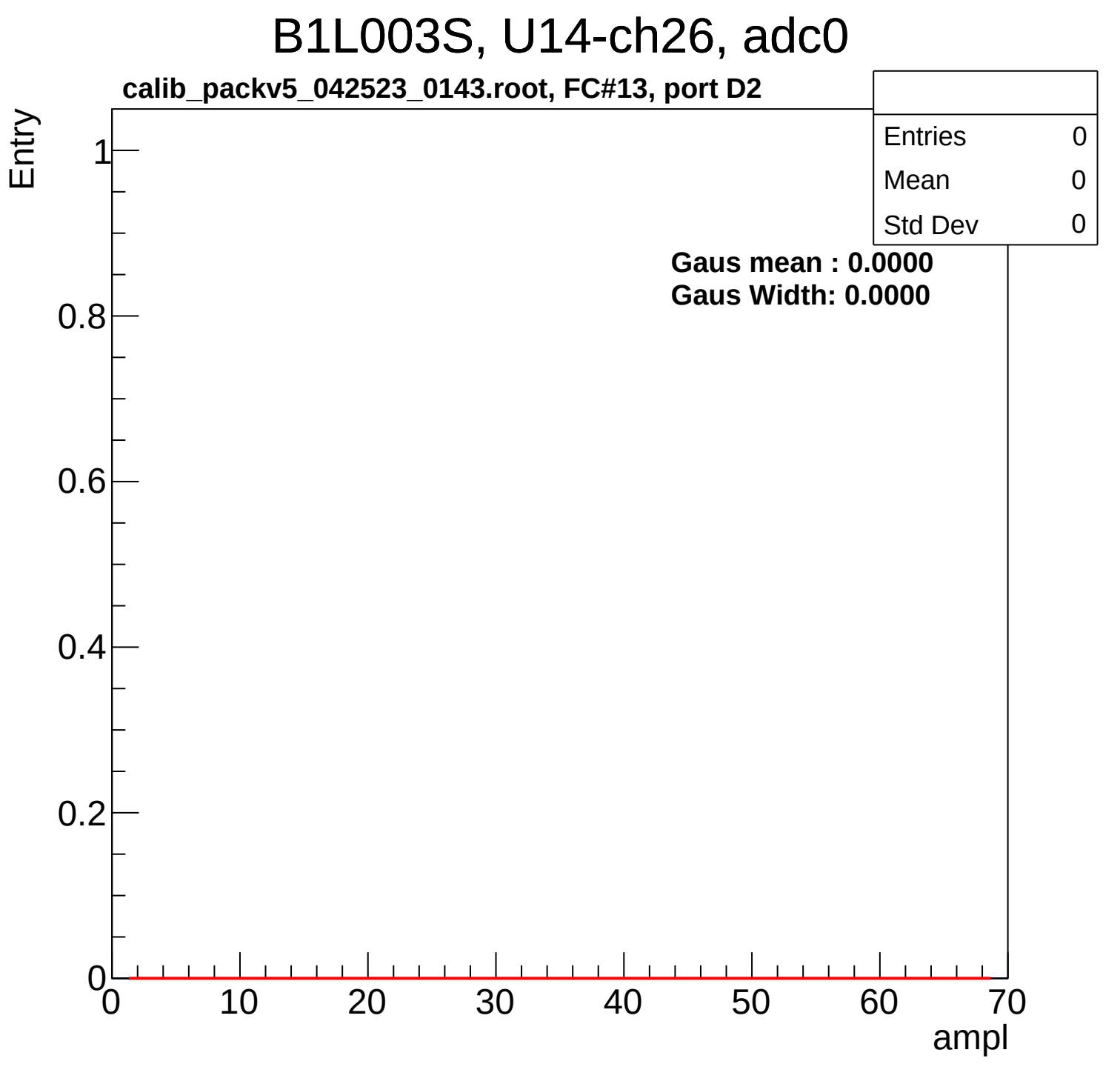

B1L003S, U14-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

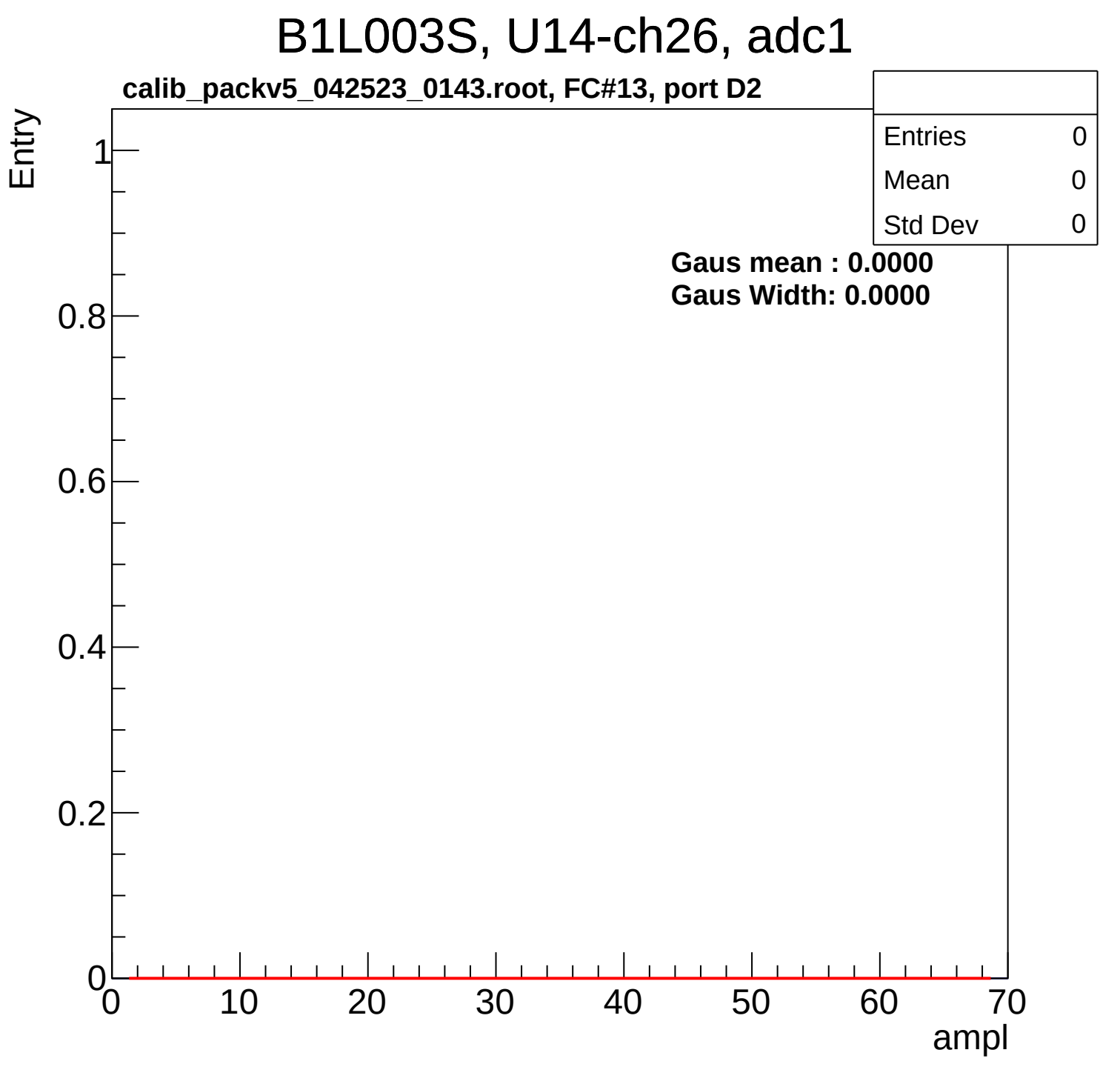

B1L003S, U14-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

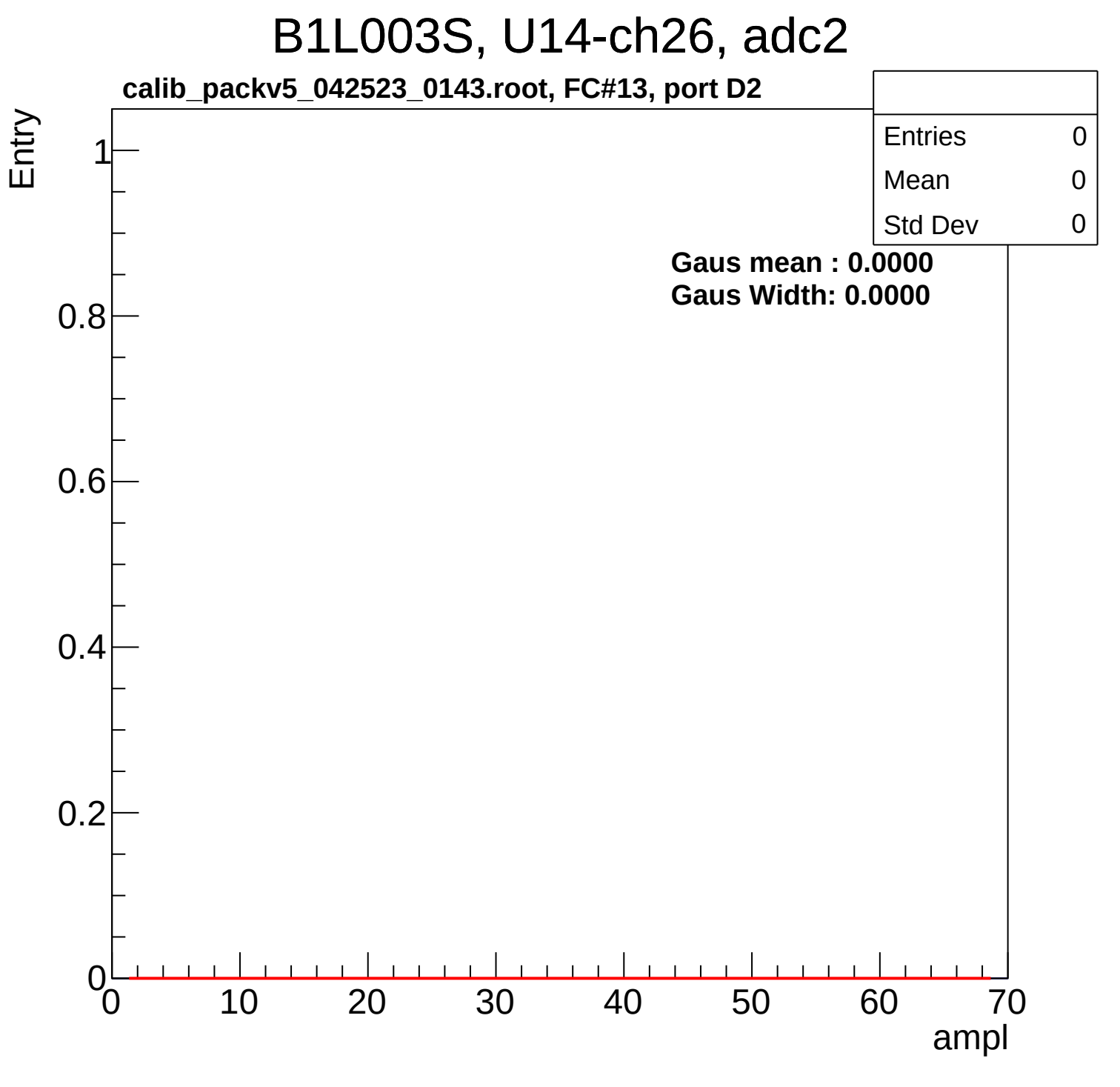

B1L003S, U14-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

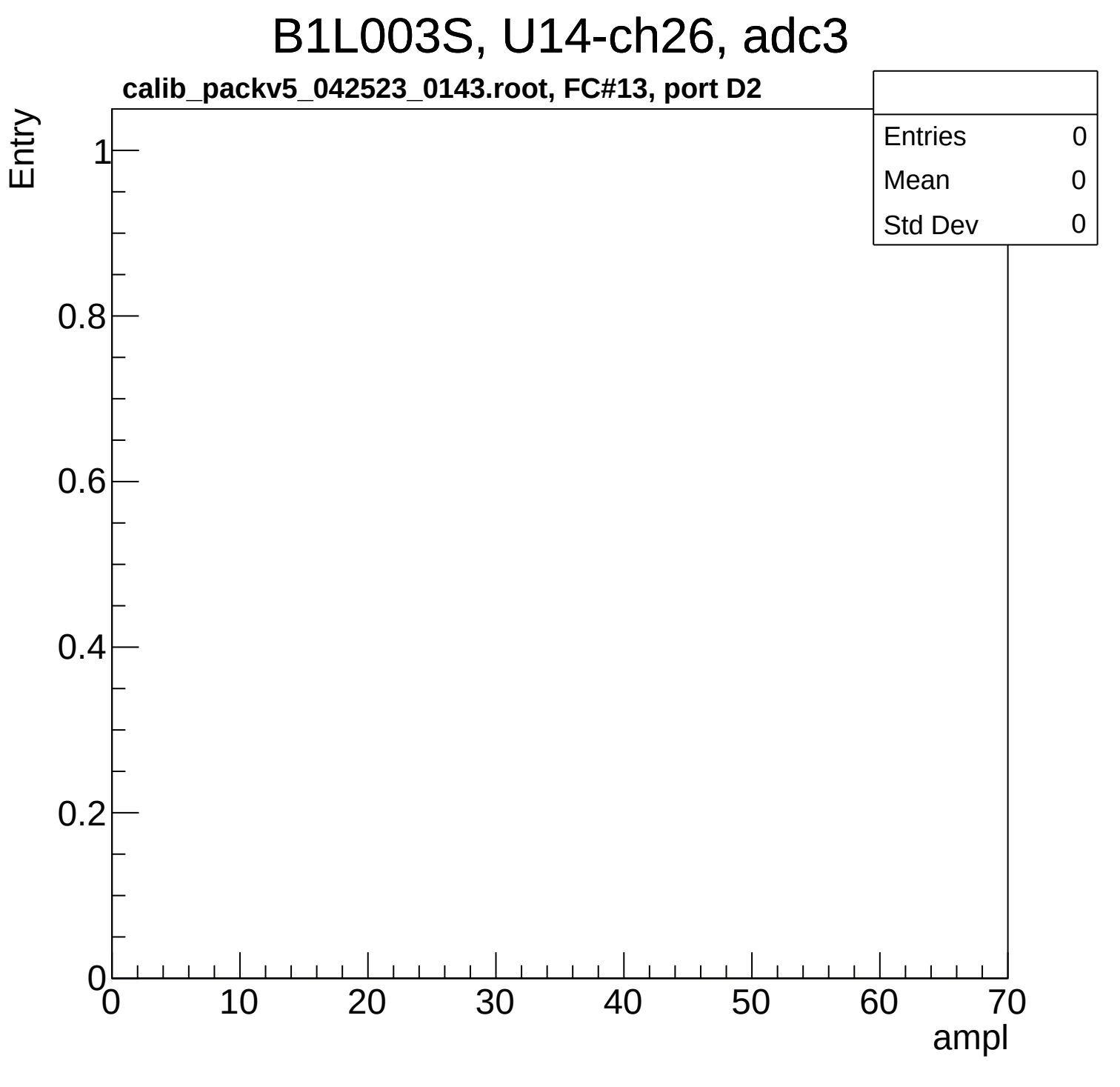

B1L003S, U14-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

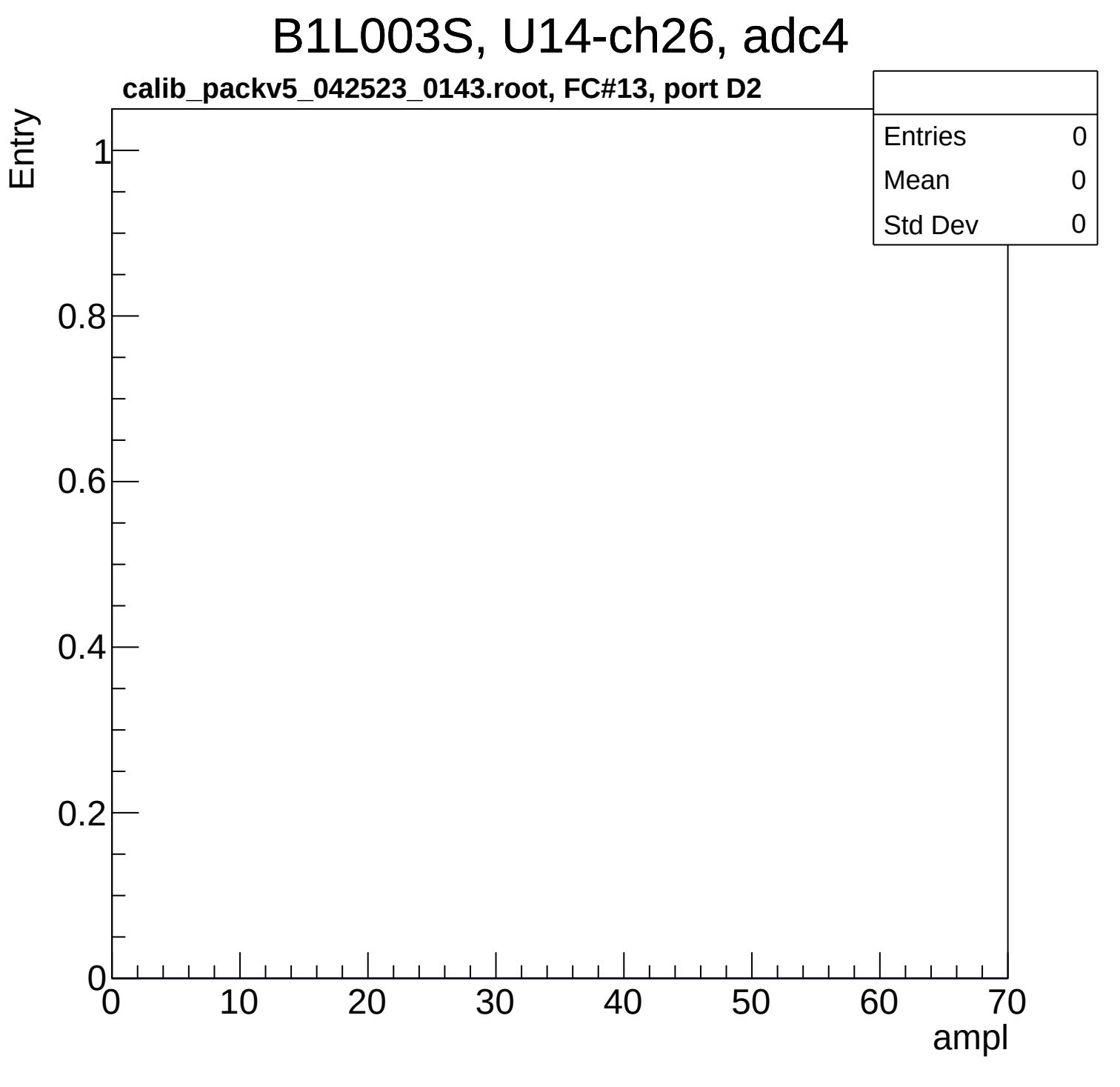

B1L003S, U14-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

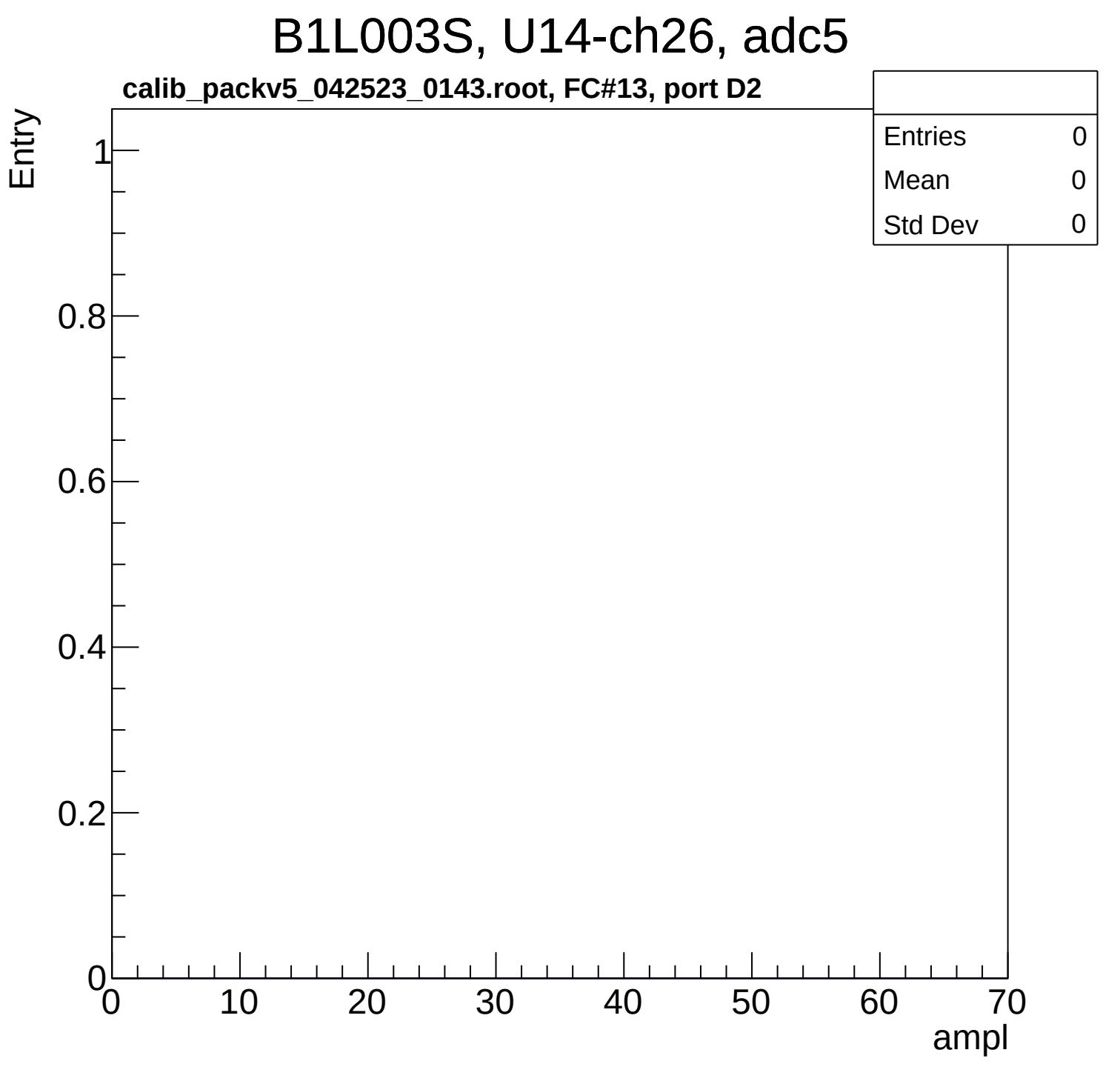

B1L003S, U14-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

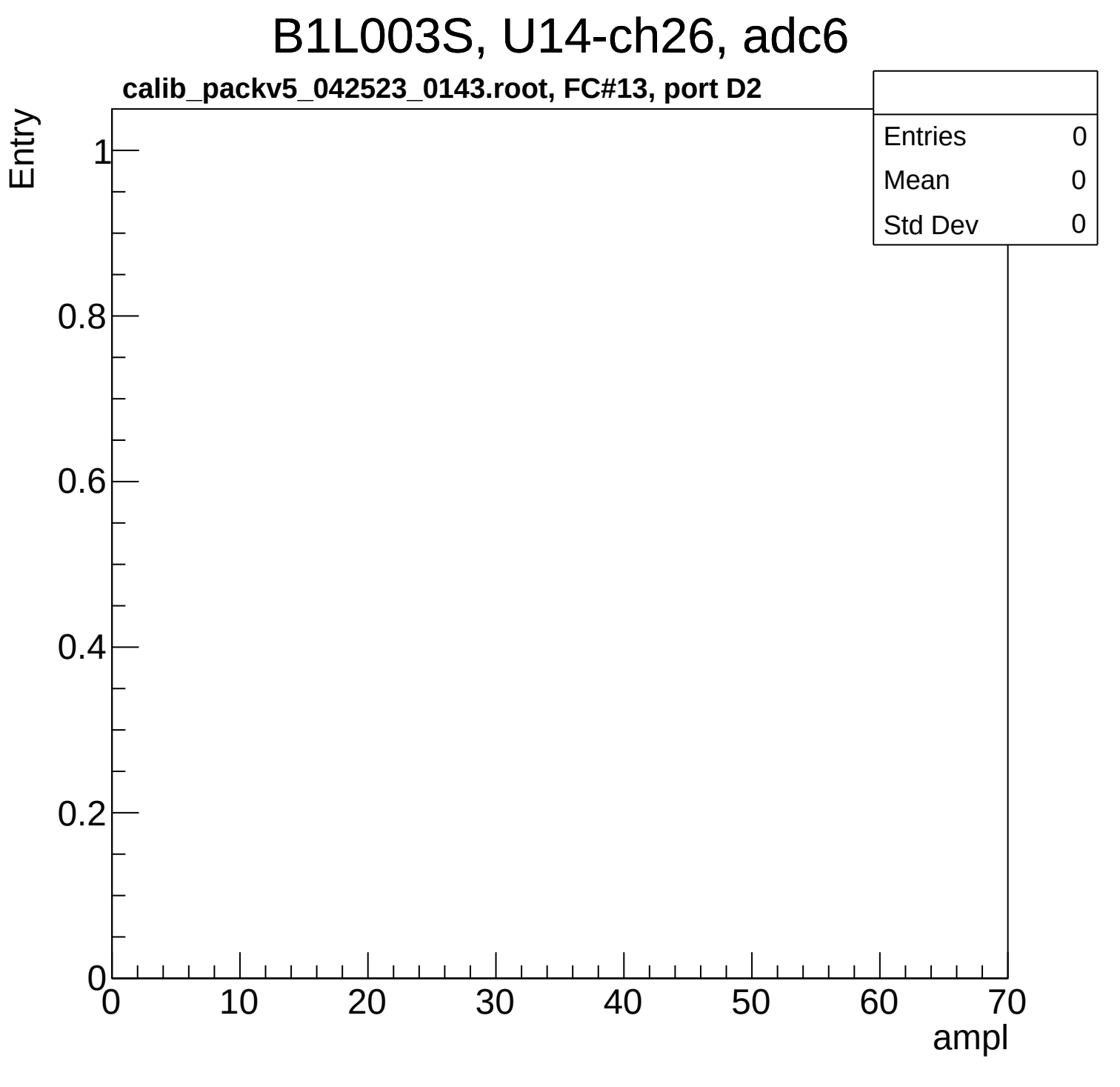

B1L003S, U14-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

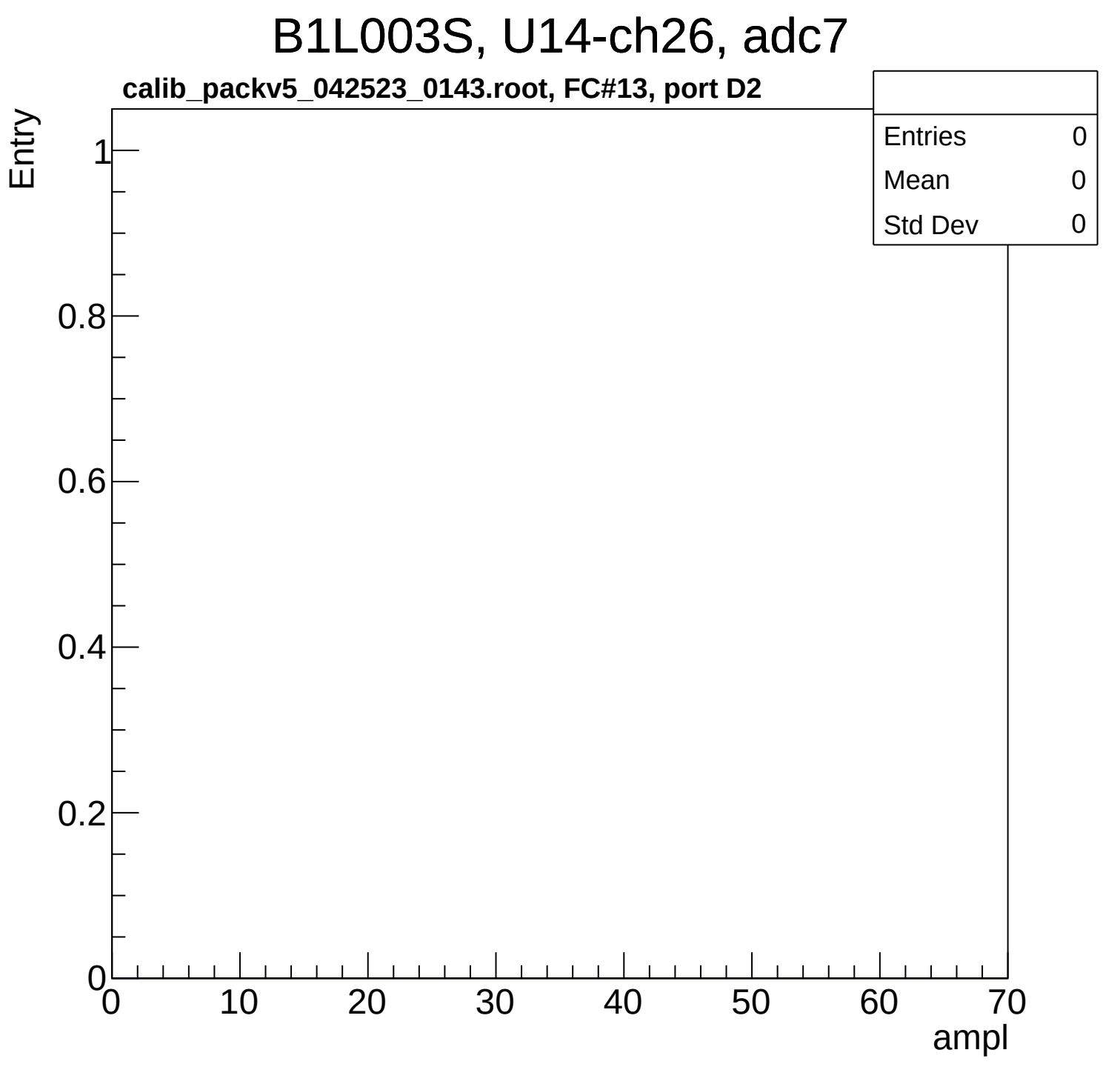

B1L003S, U14-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

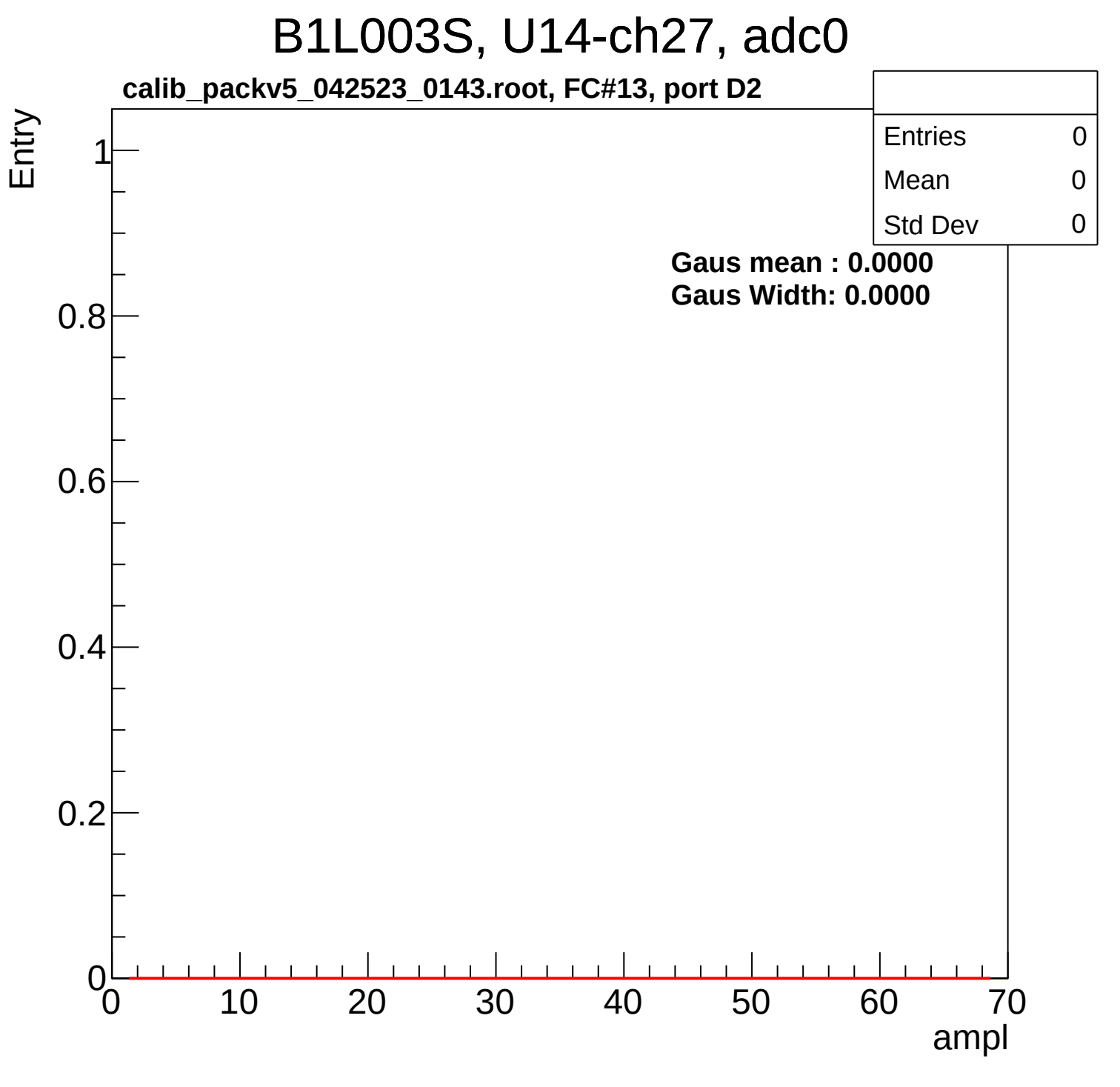

B1L003S, U14-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

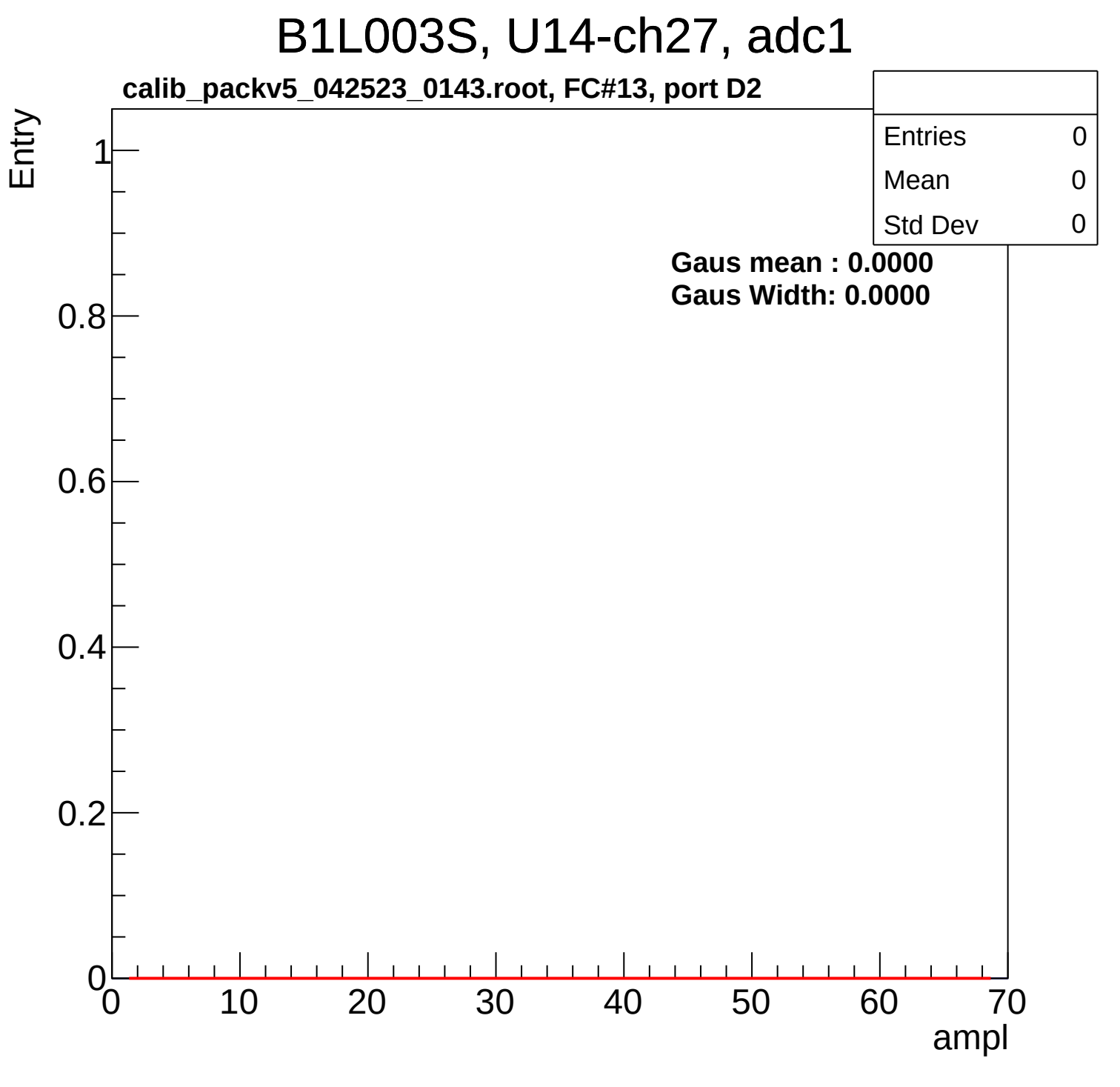

B1L003S, U14-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

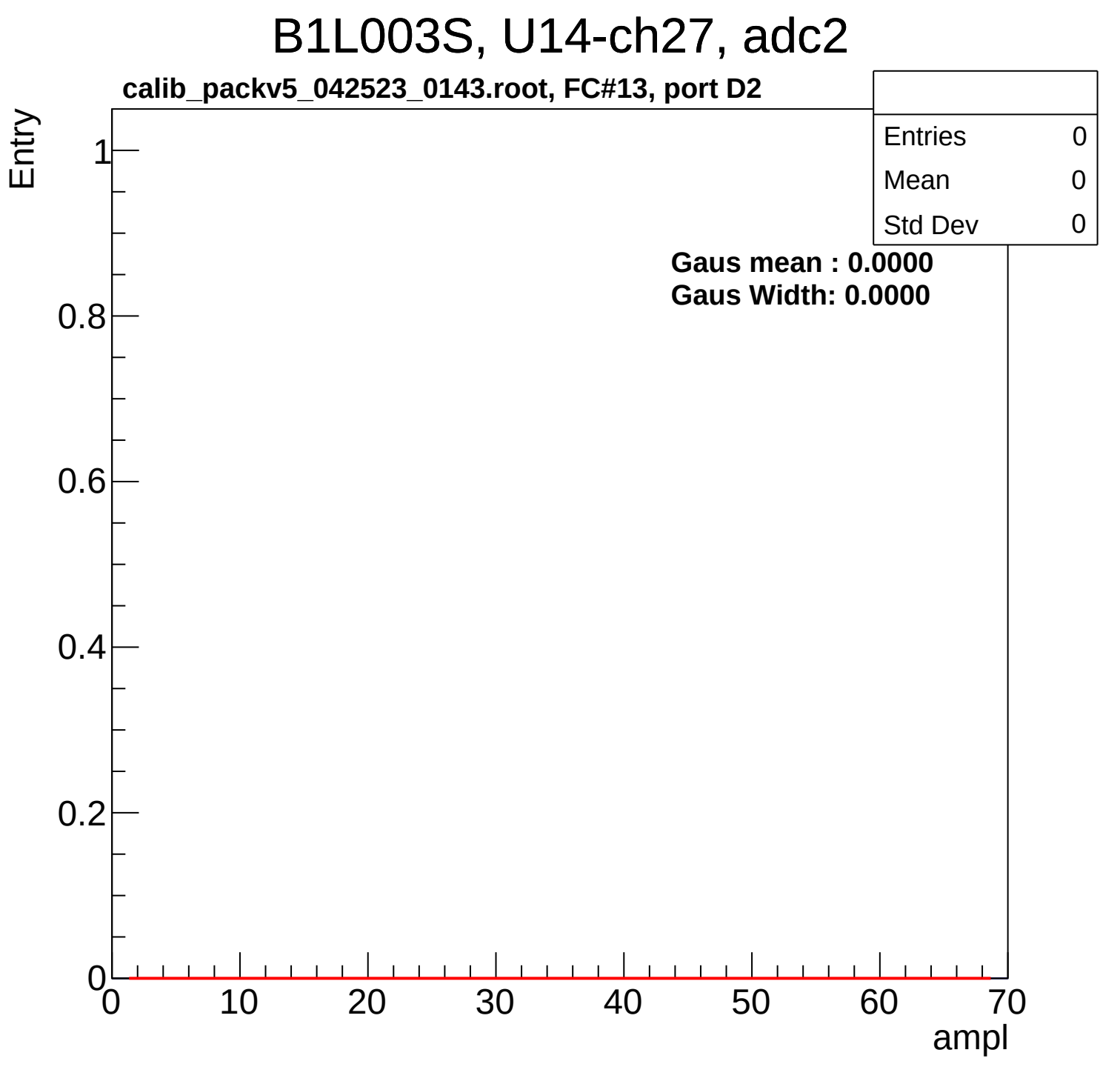

B1L003S, U14-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

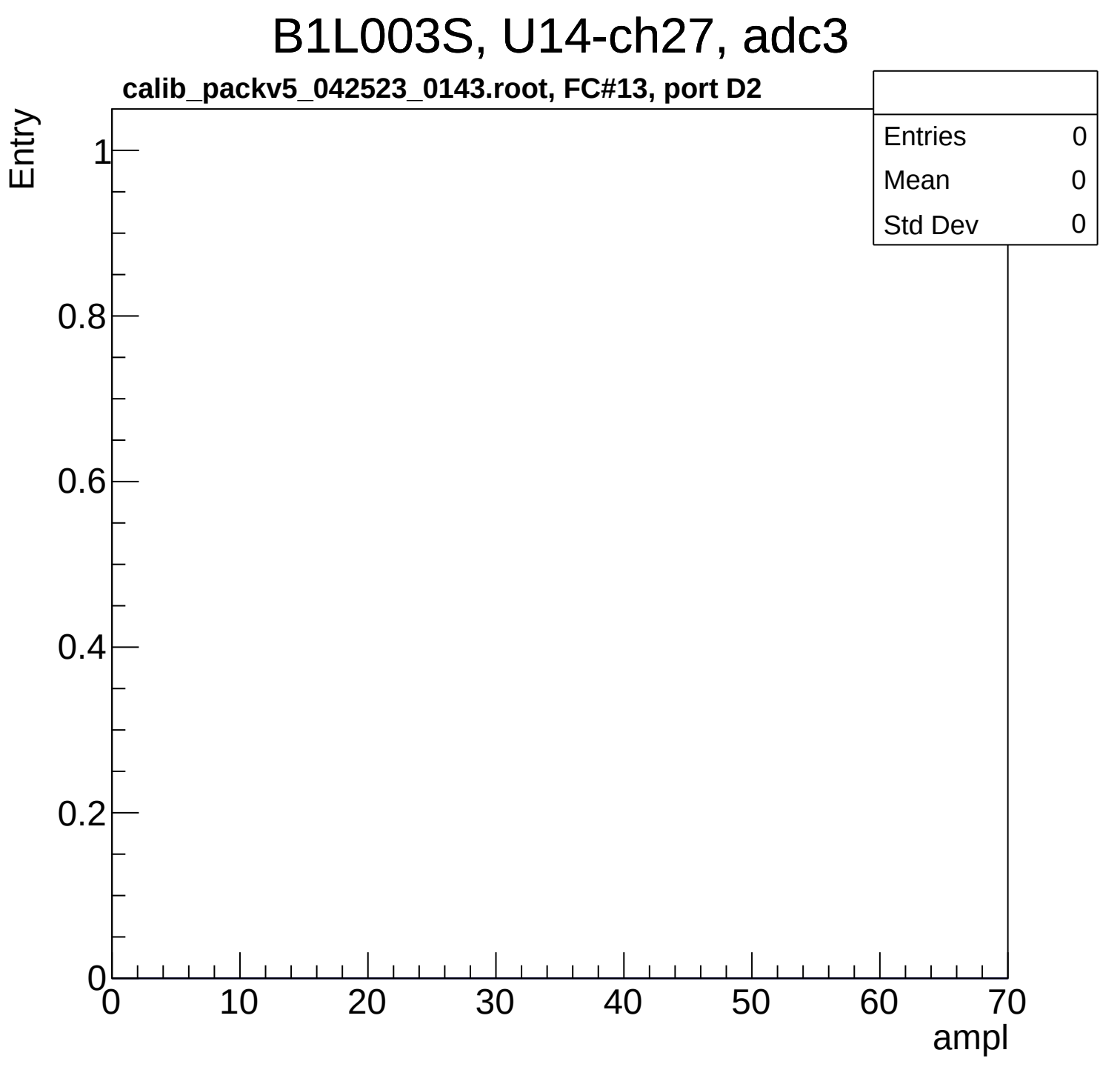

B1L003S, U14-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

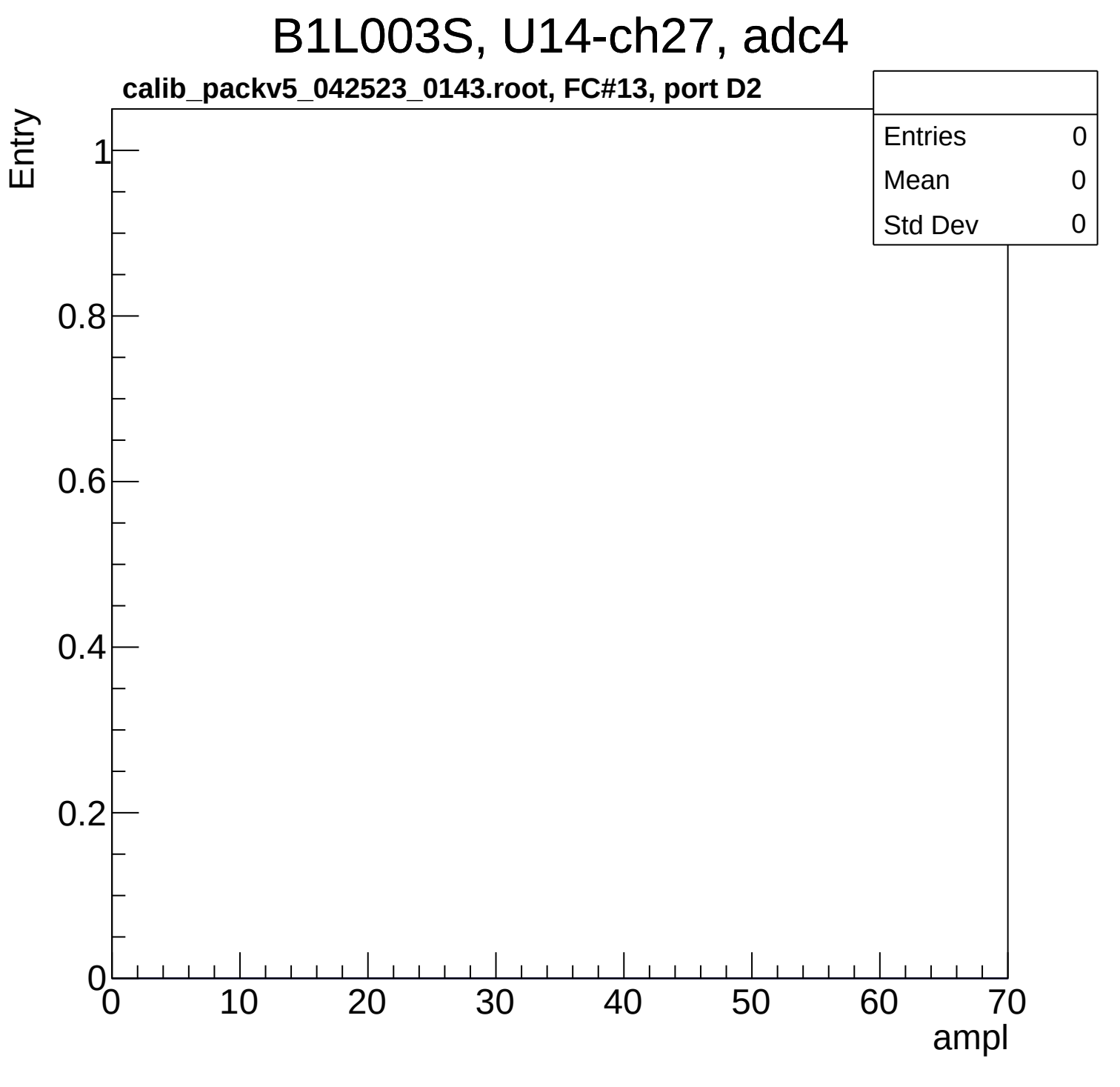

B1L003S, U14-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

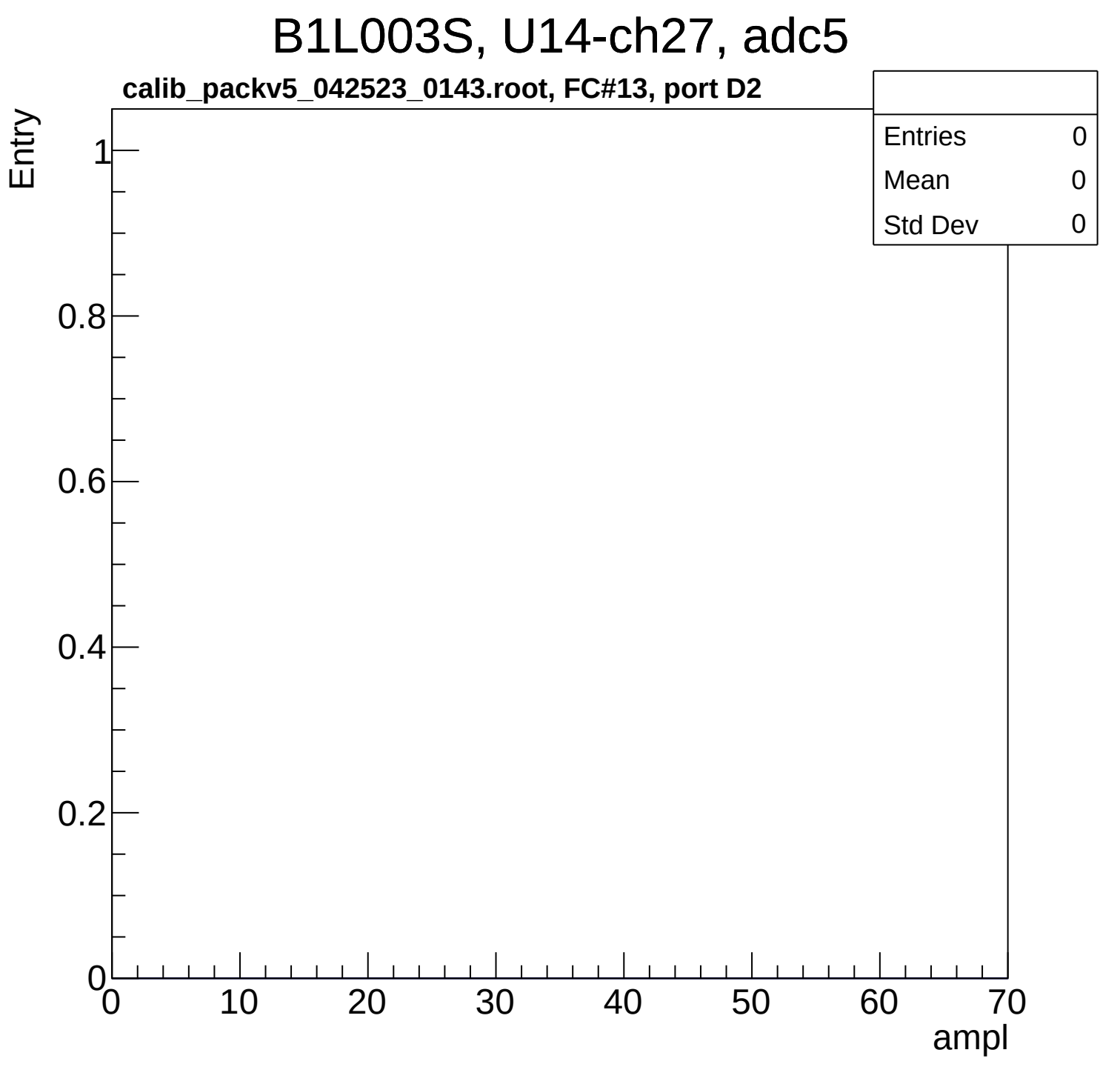

B1L003S, U14-ch27, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

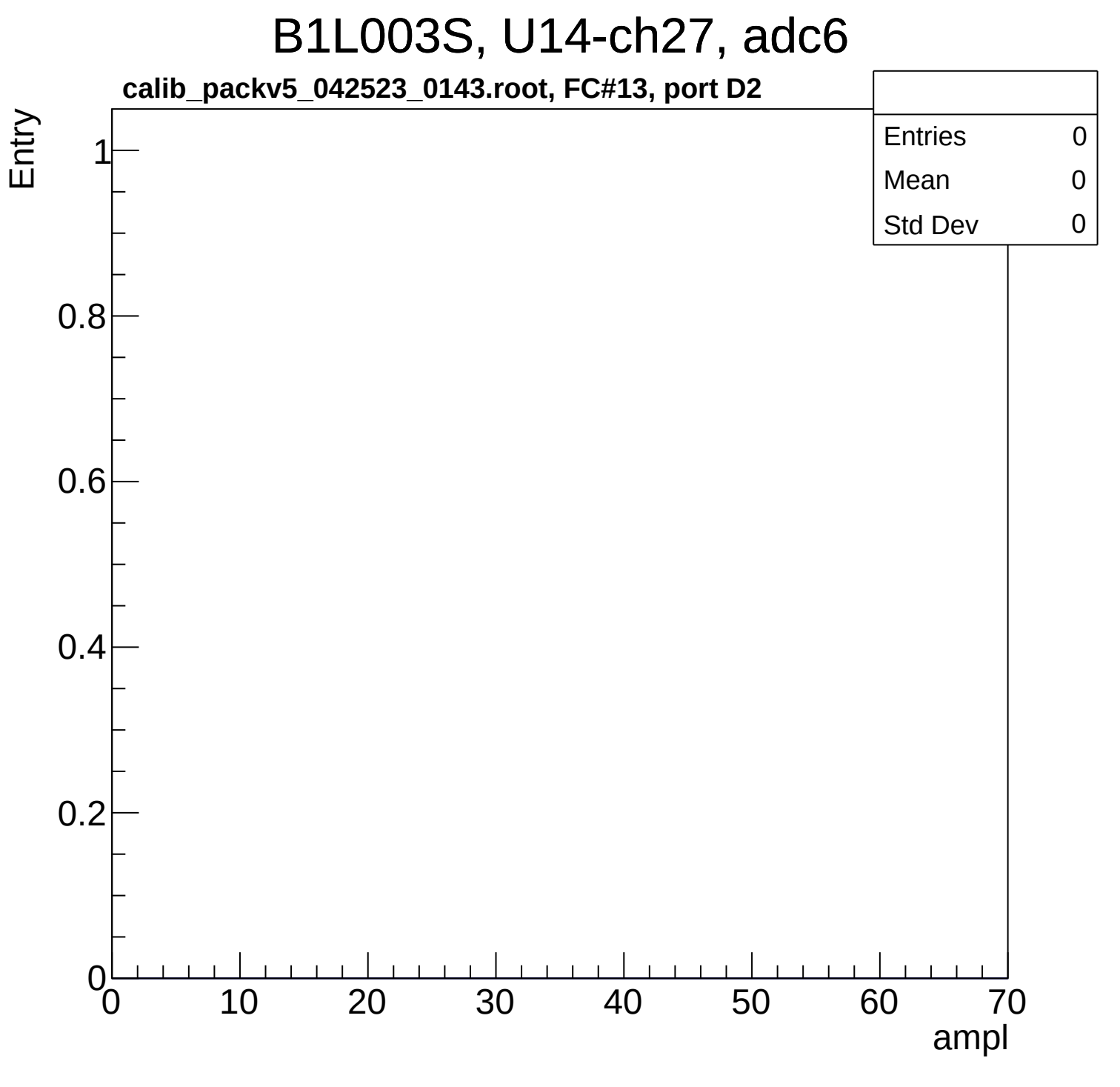

B1L003S, U14-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

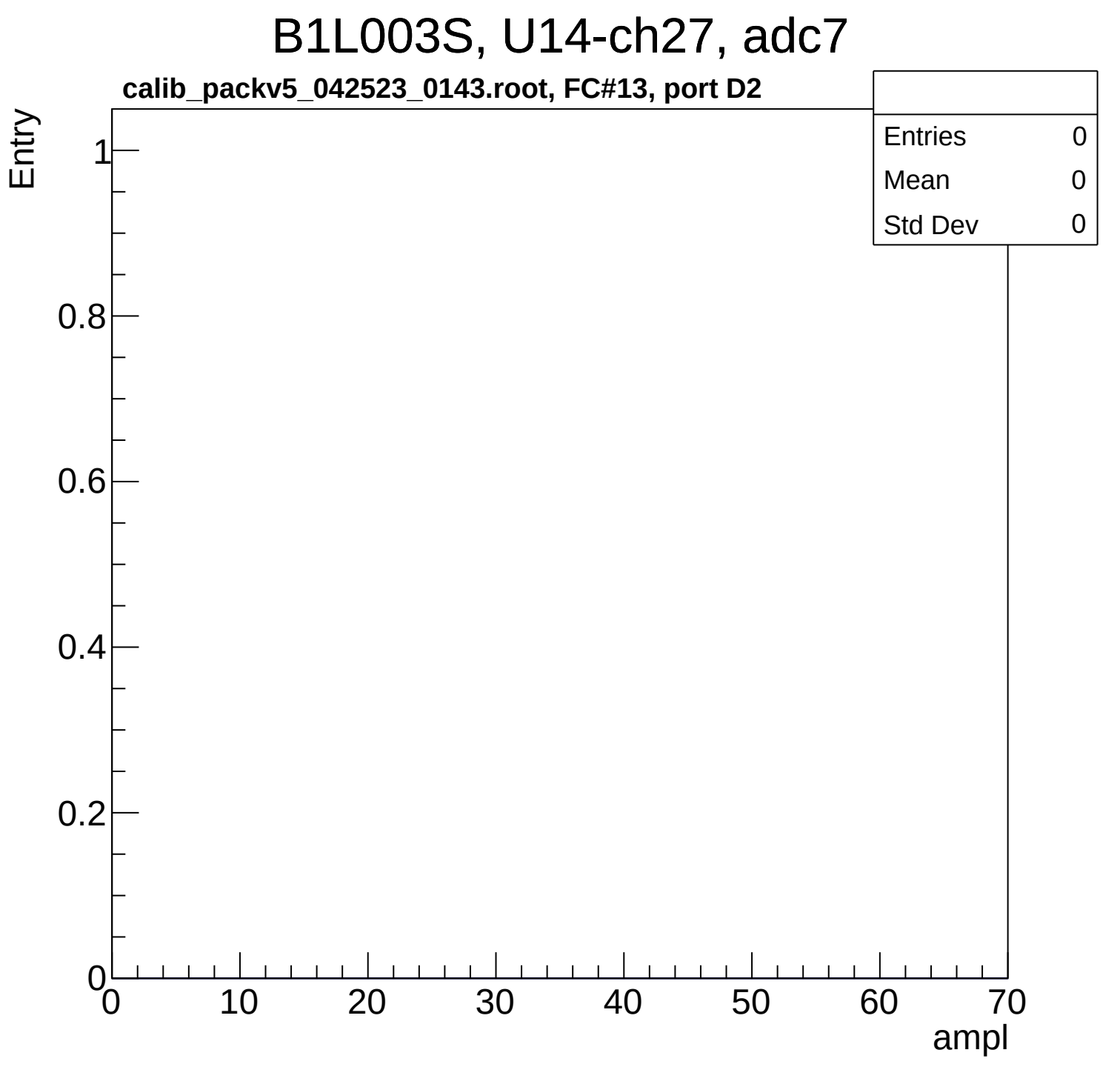

B1L003S, U14-ch27, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

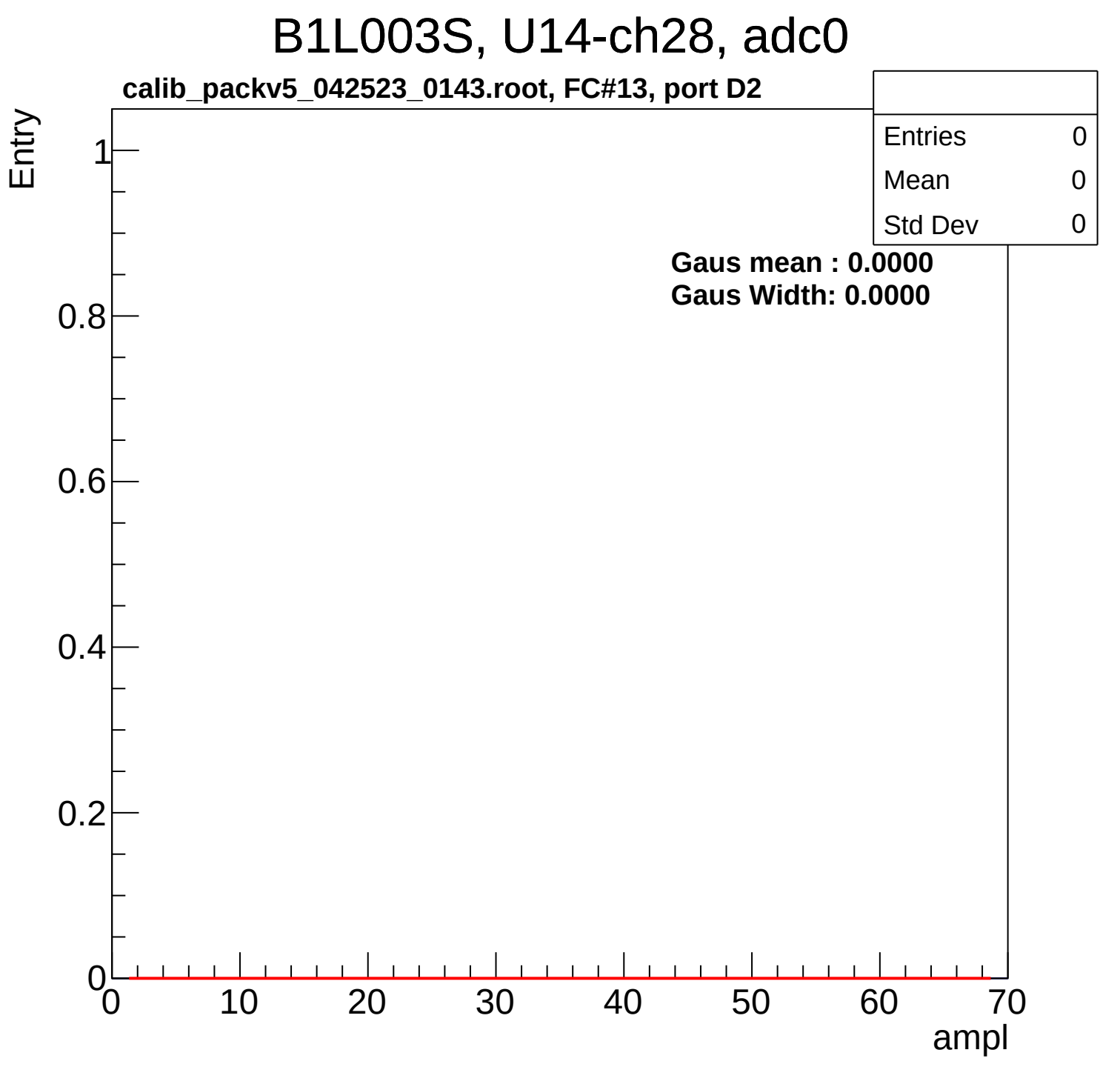

B1L003S, U14-ch27, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

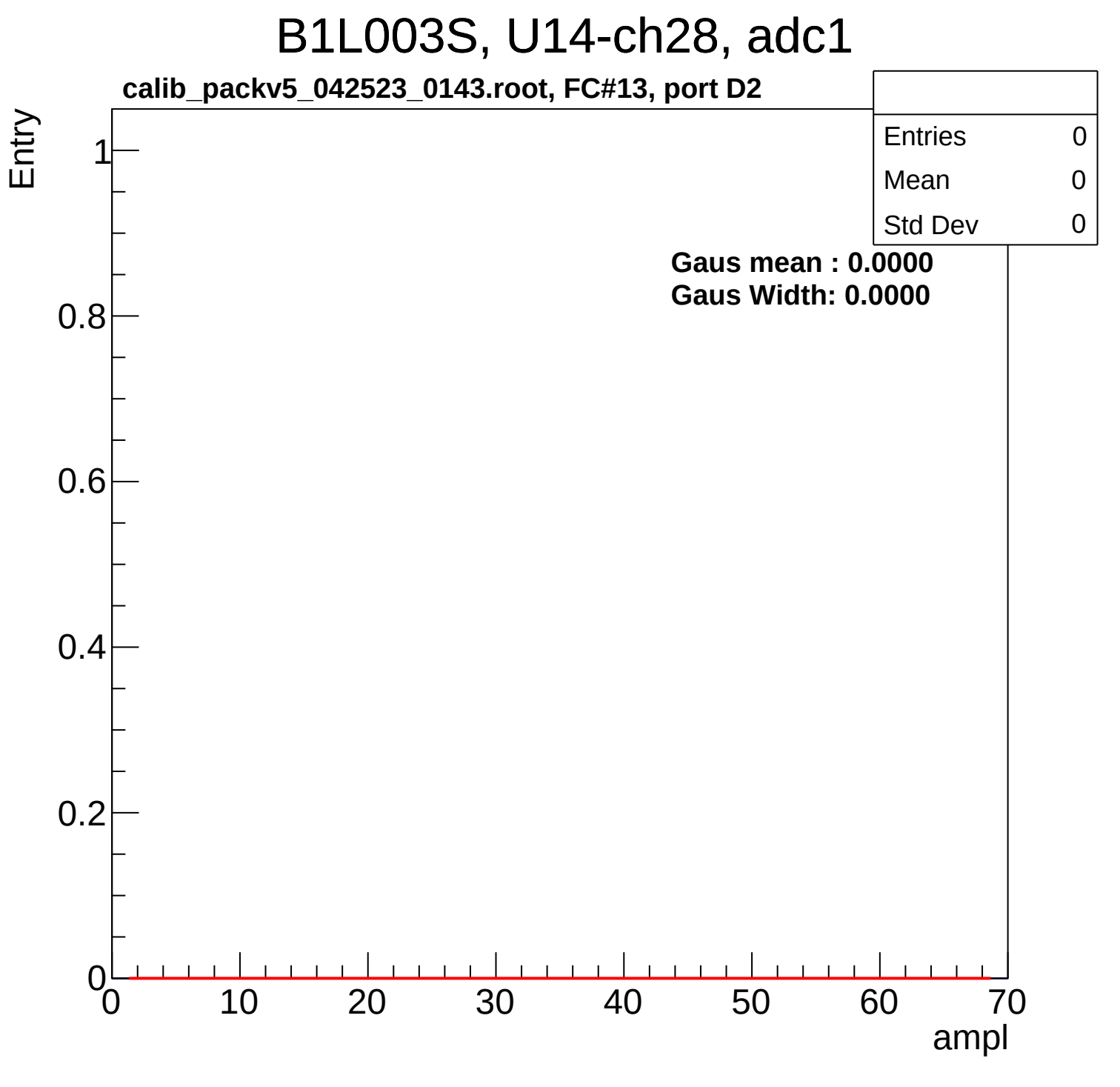

B1L003S, U14-ch27, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

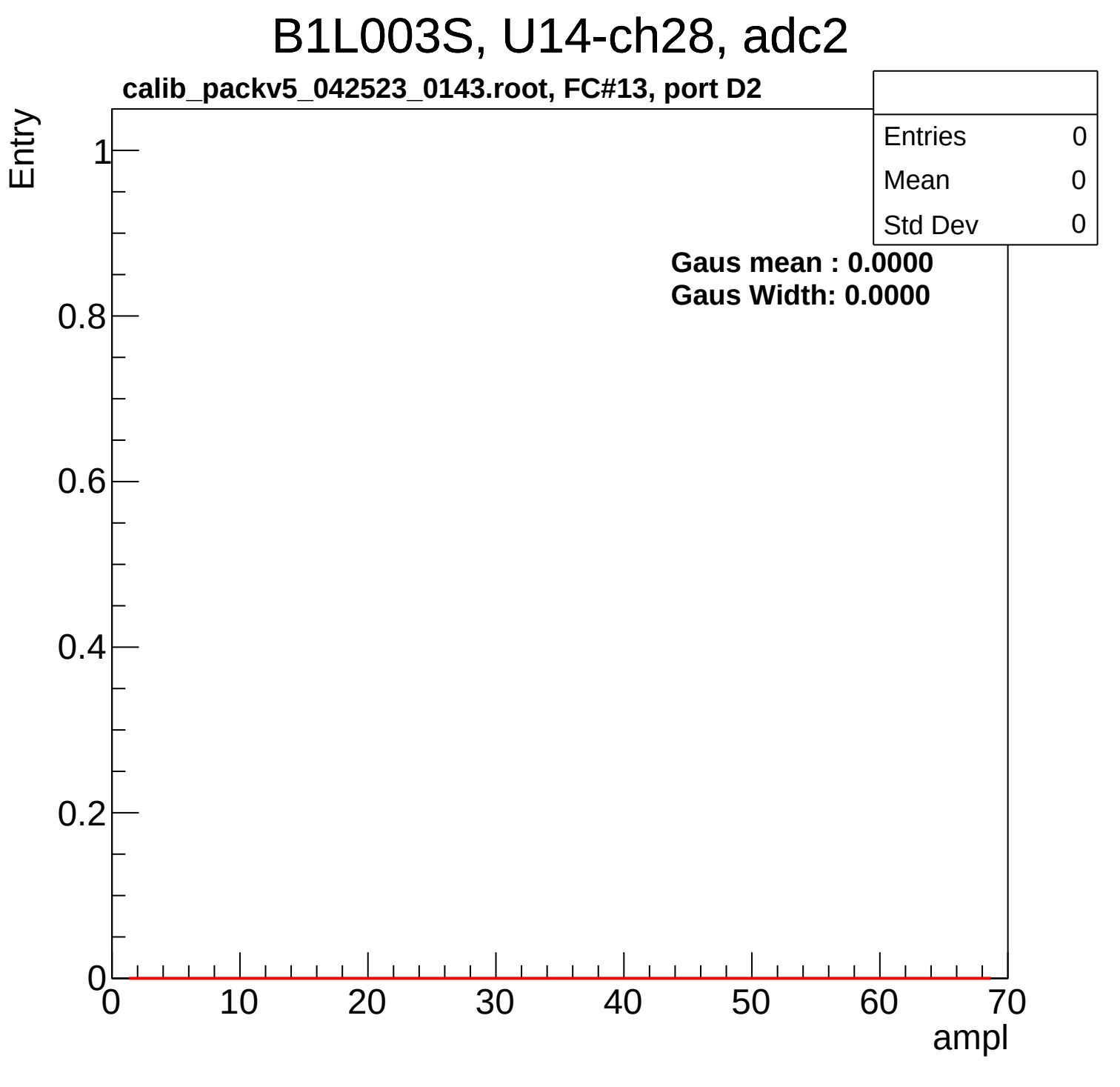

B1L003S, U14-ch27, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

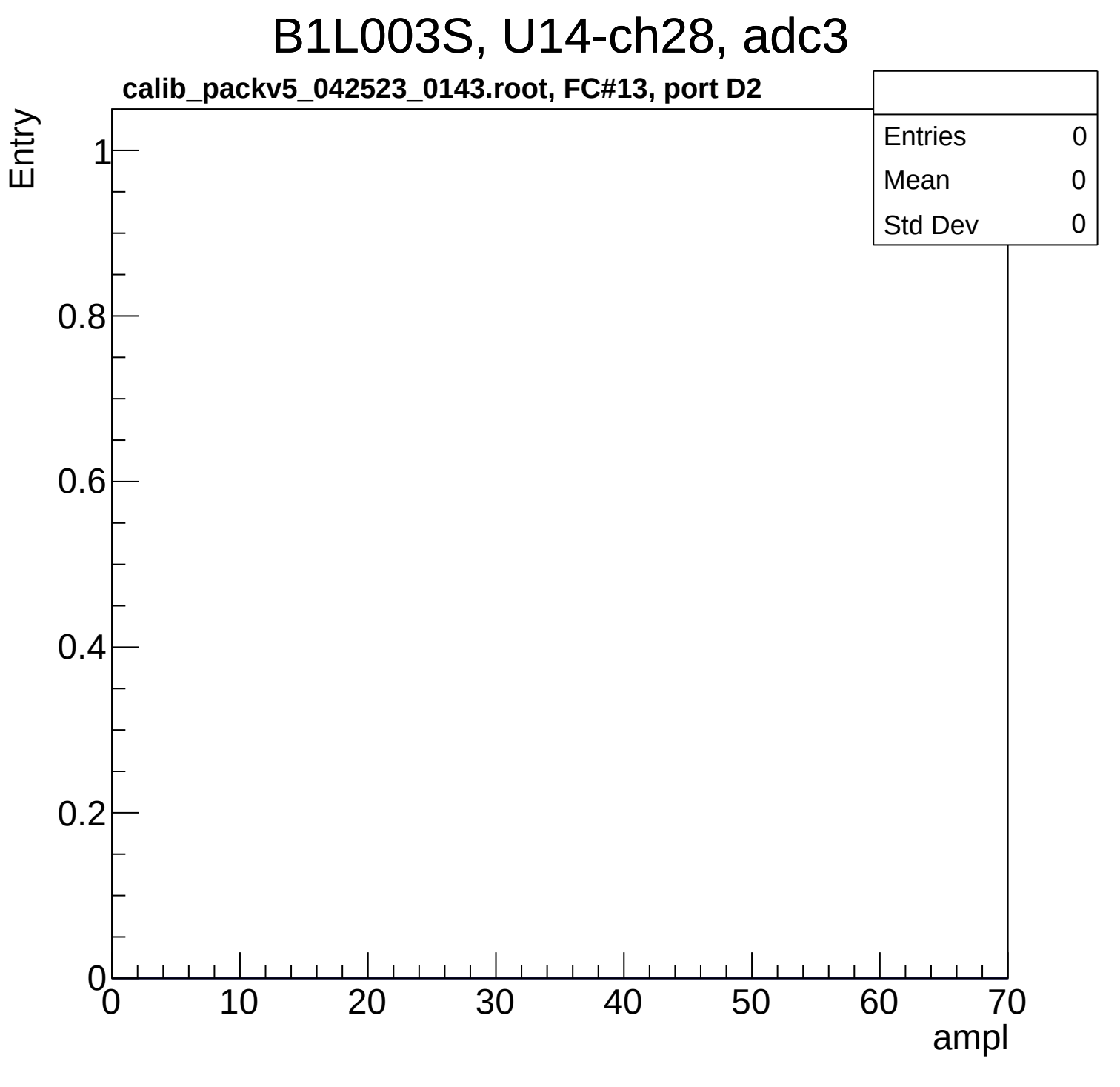

B1L003S, U14-ch27, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

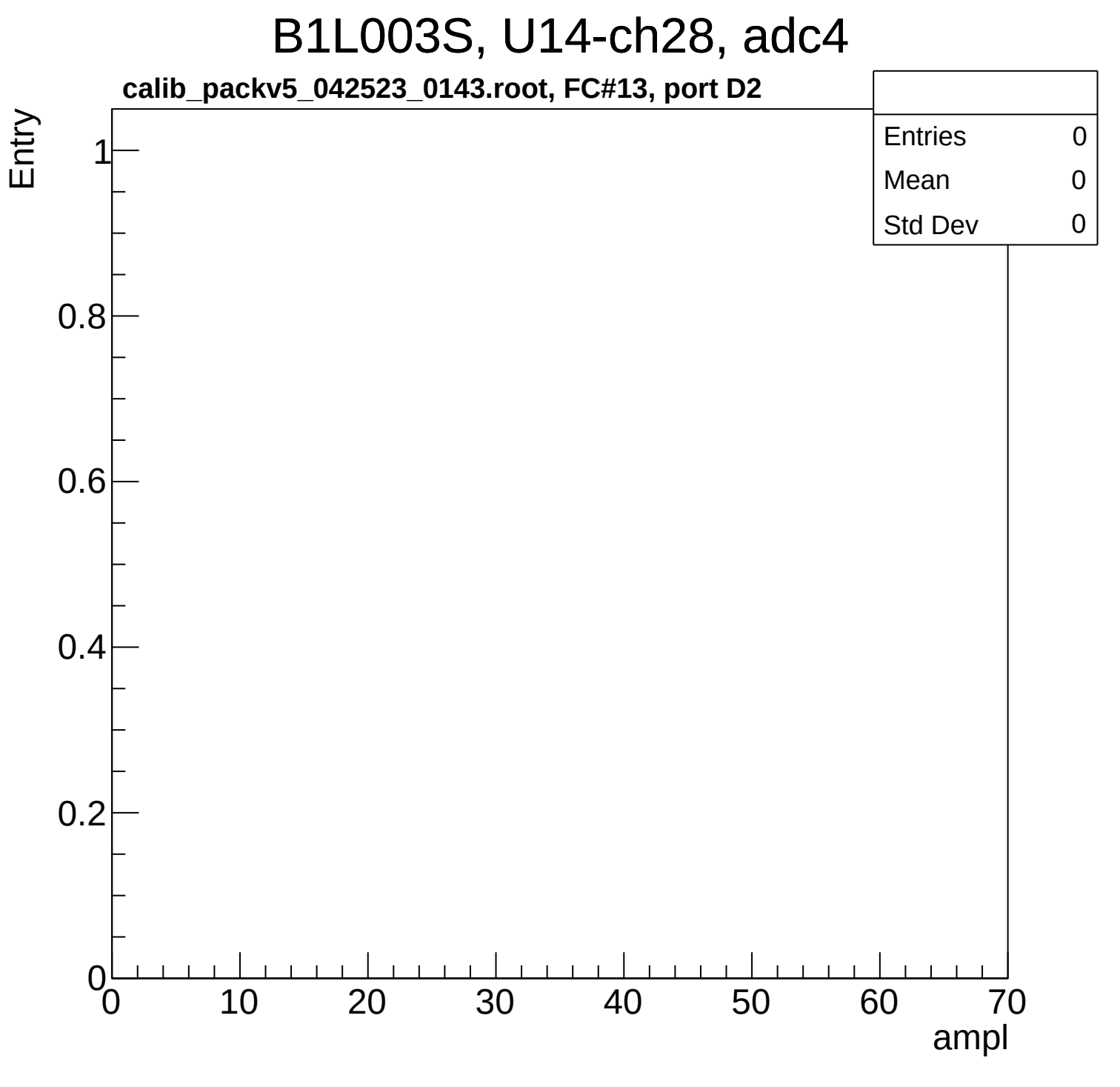

B1L003S, U14-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

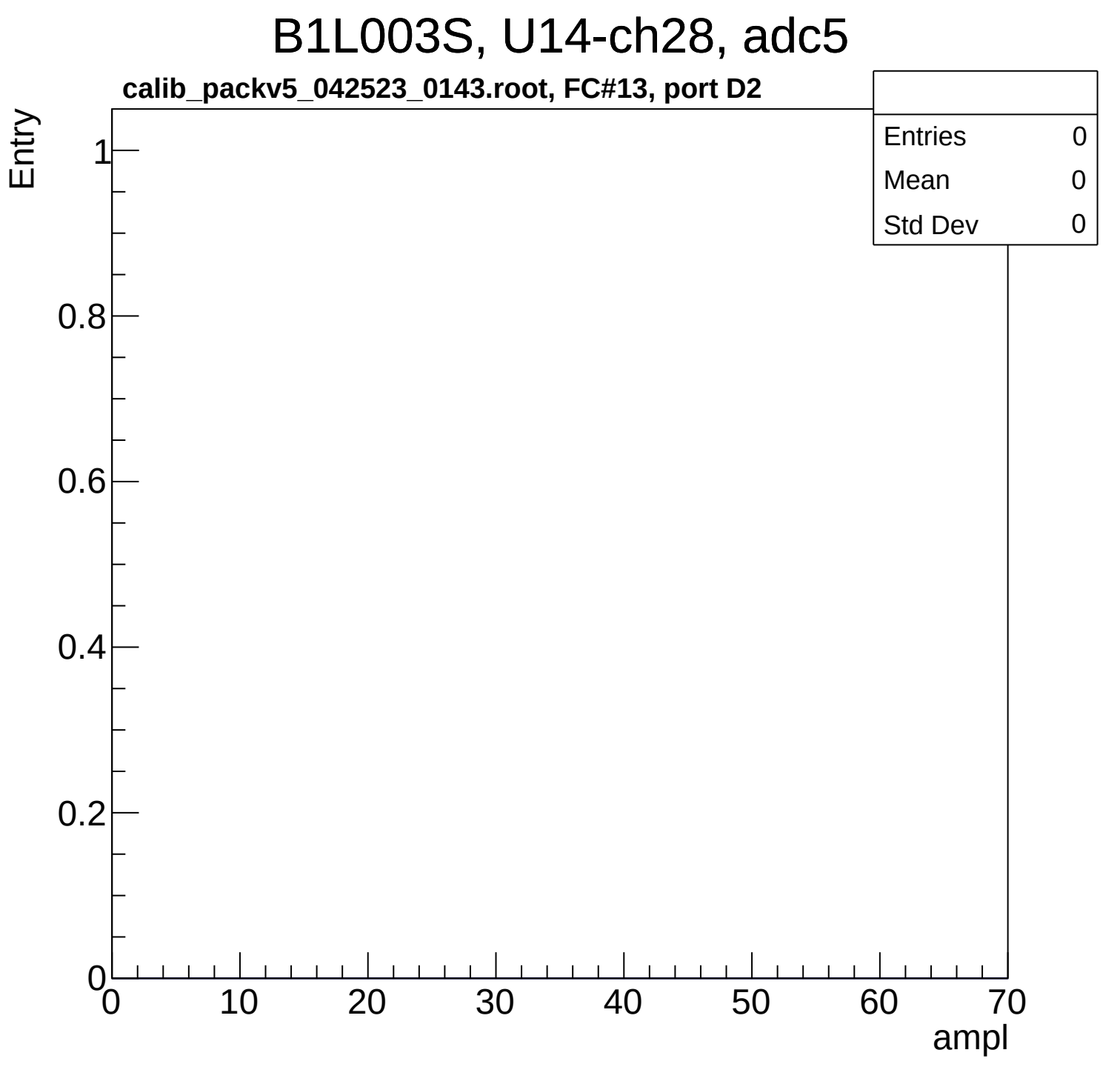

B1L003S, U14-ch28, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

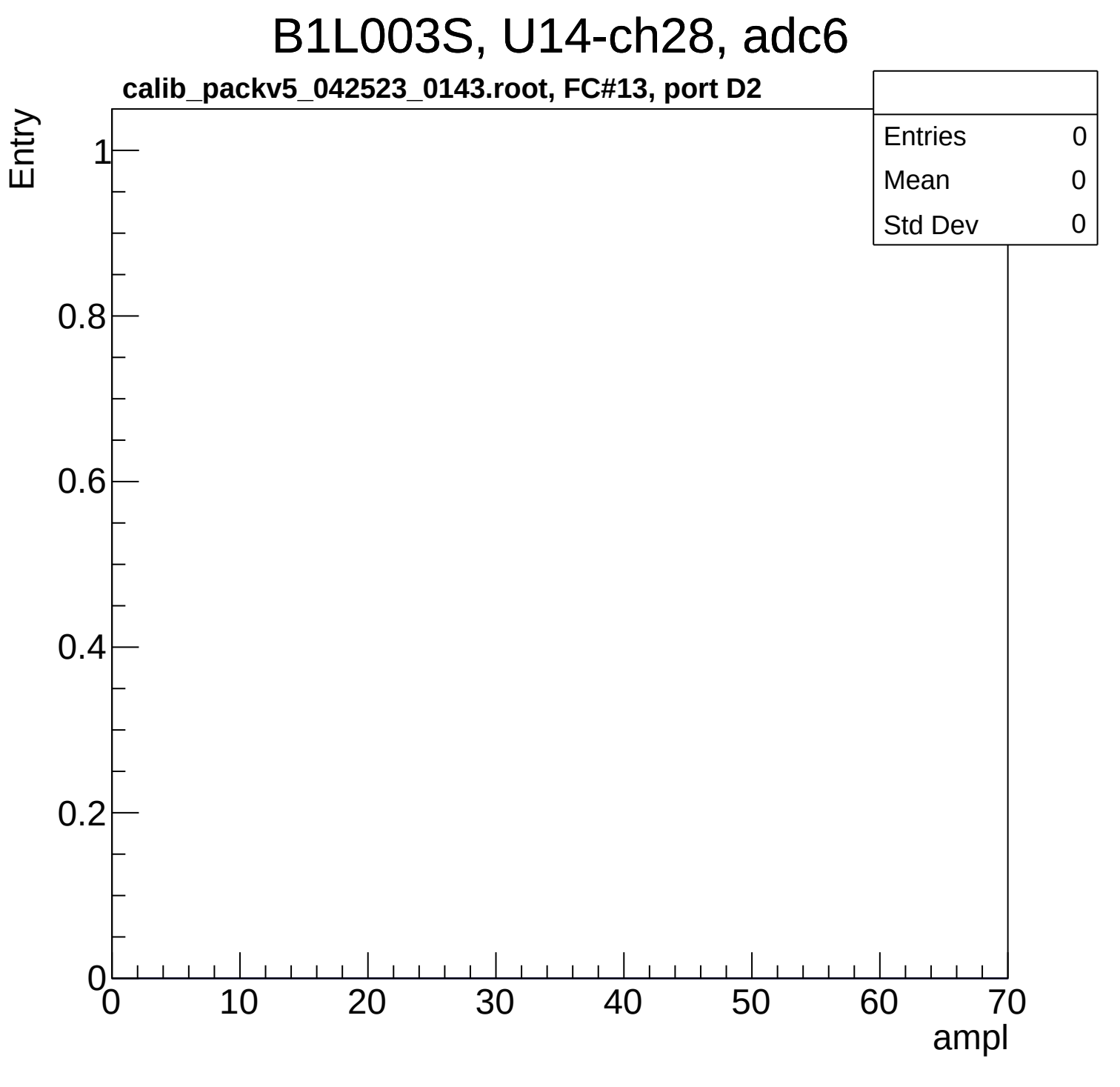

B1L003S, U14-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

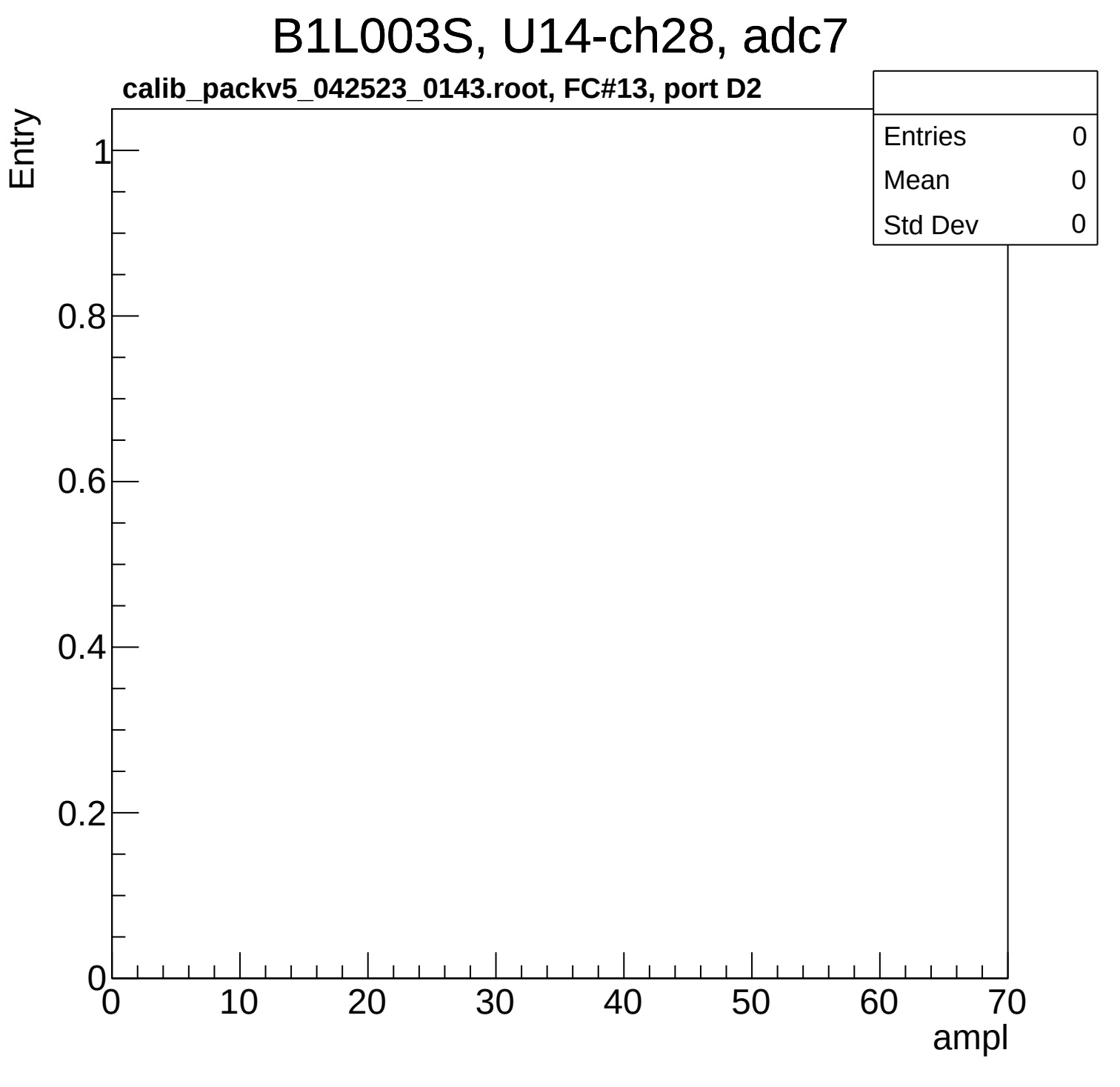

B1L003S, U14-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

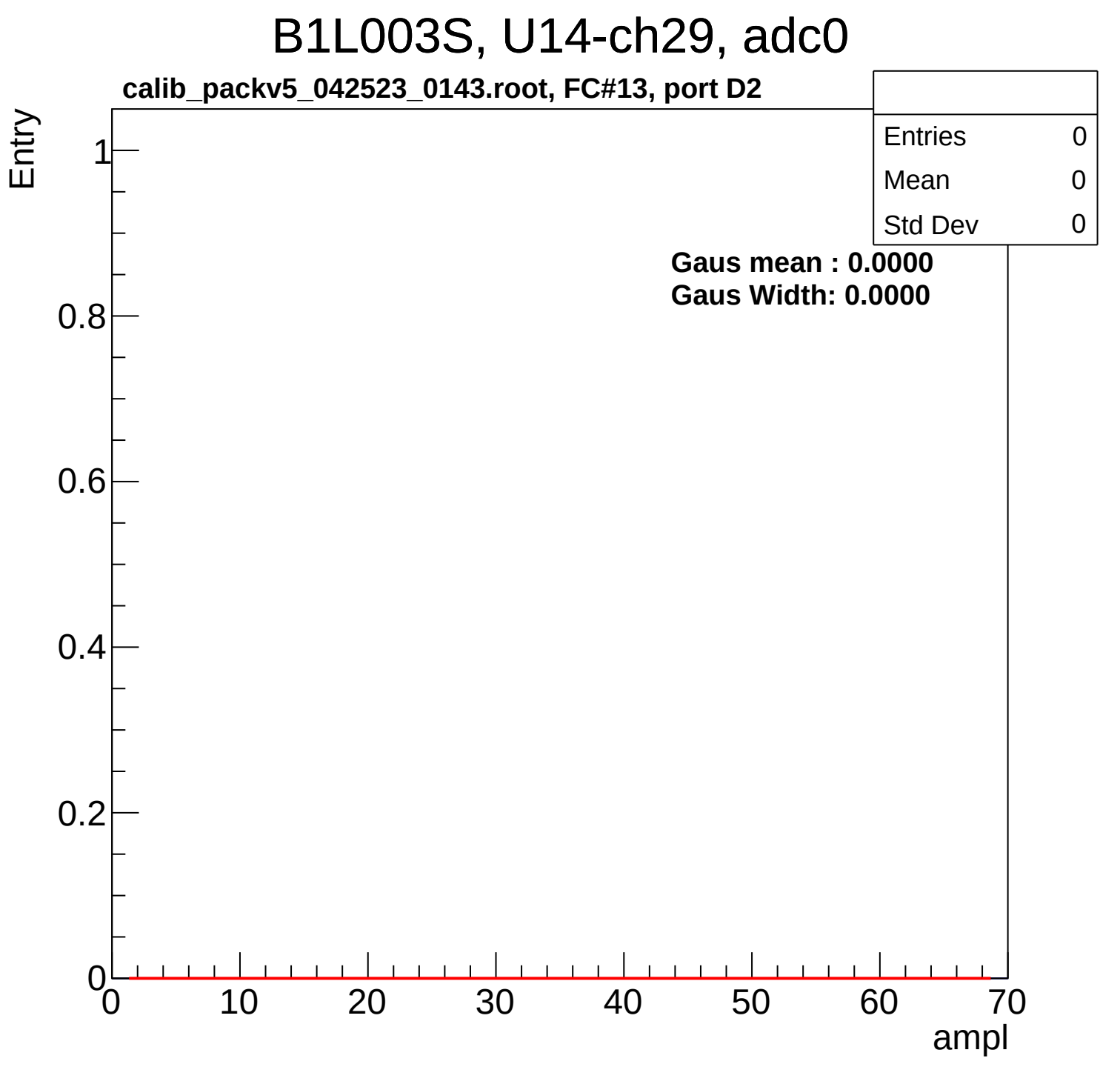

B1L003S, U14-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

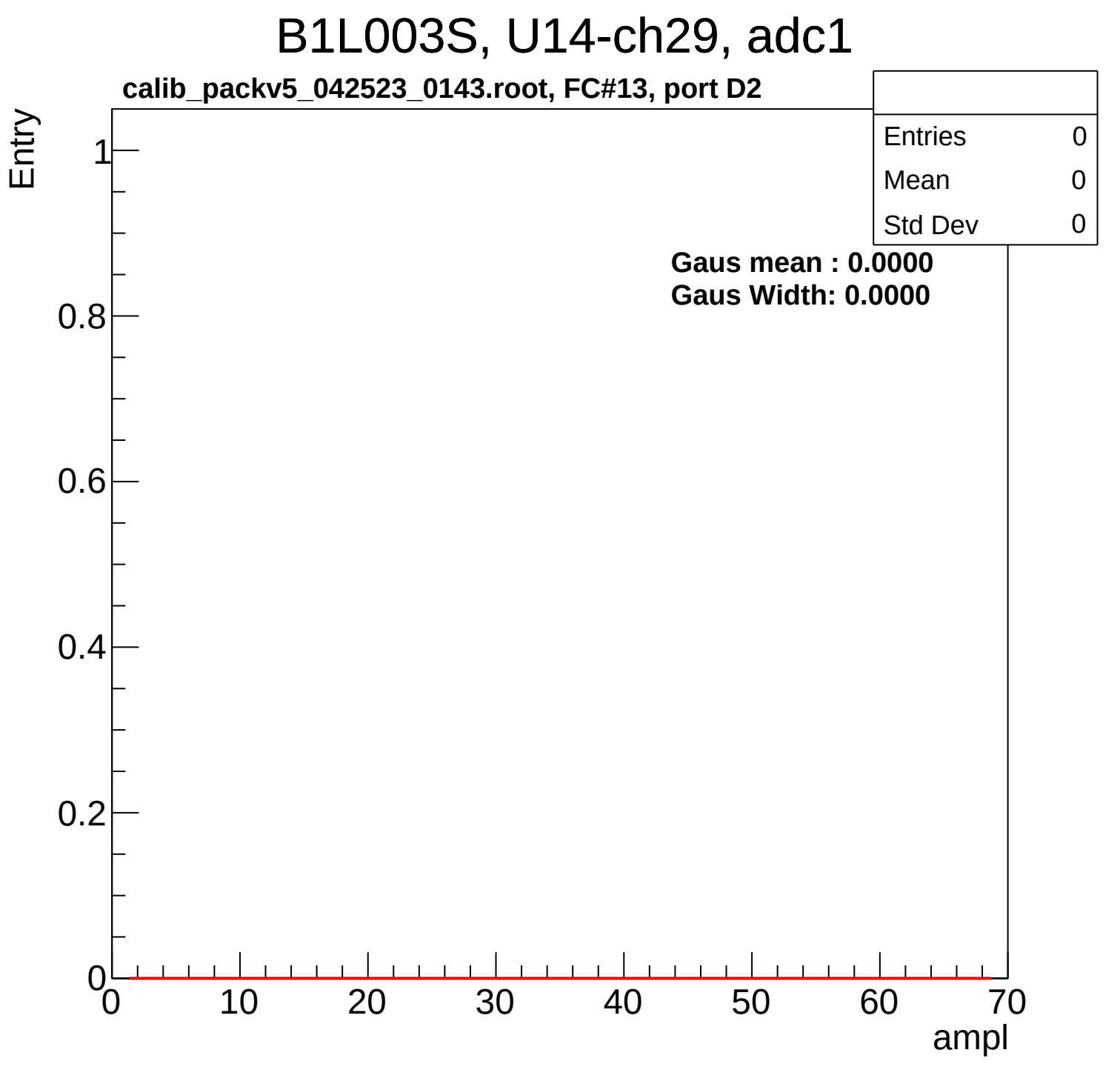

B1L003S, U14-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

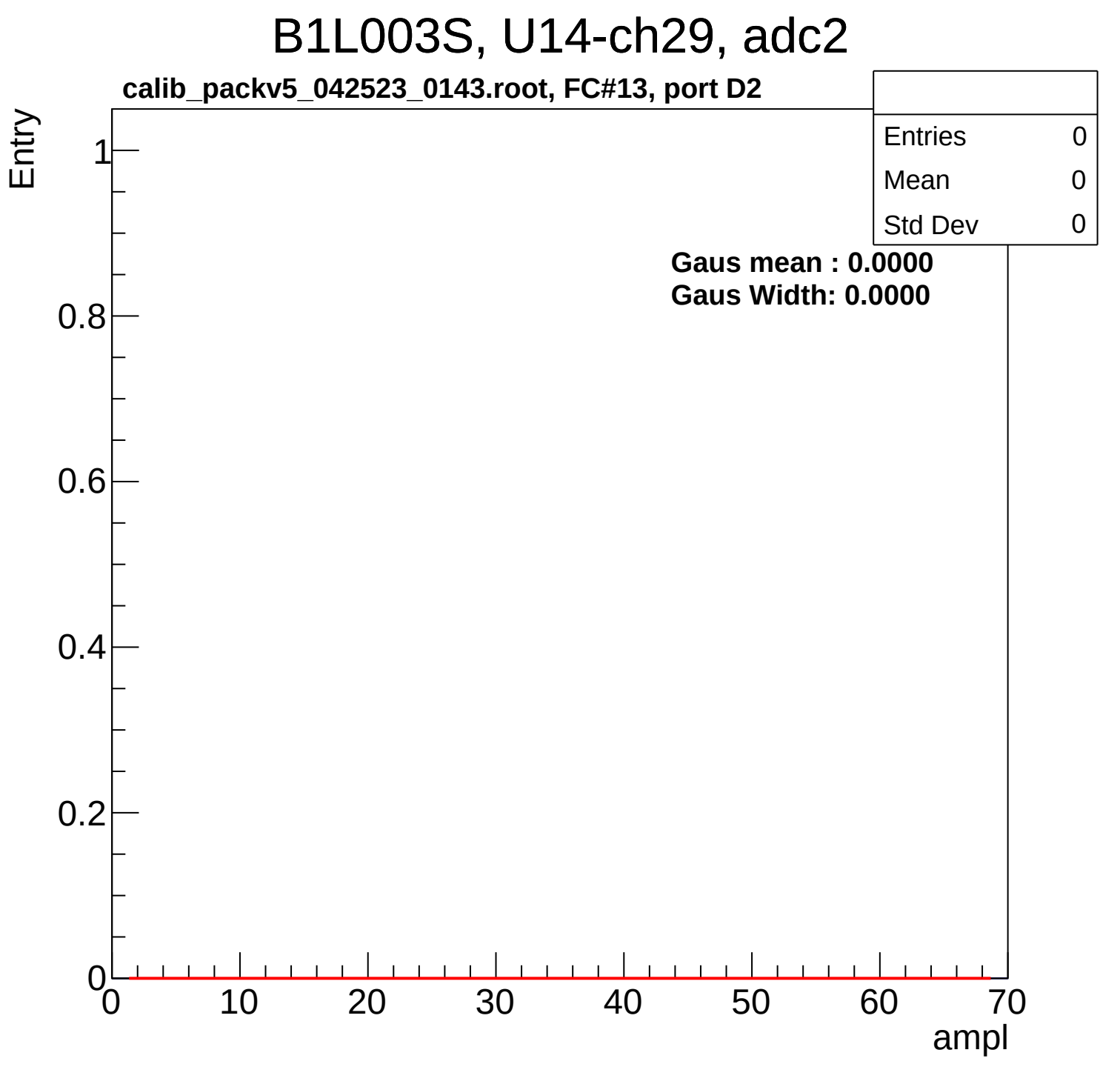

B1L003S, U14-ch28, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

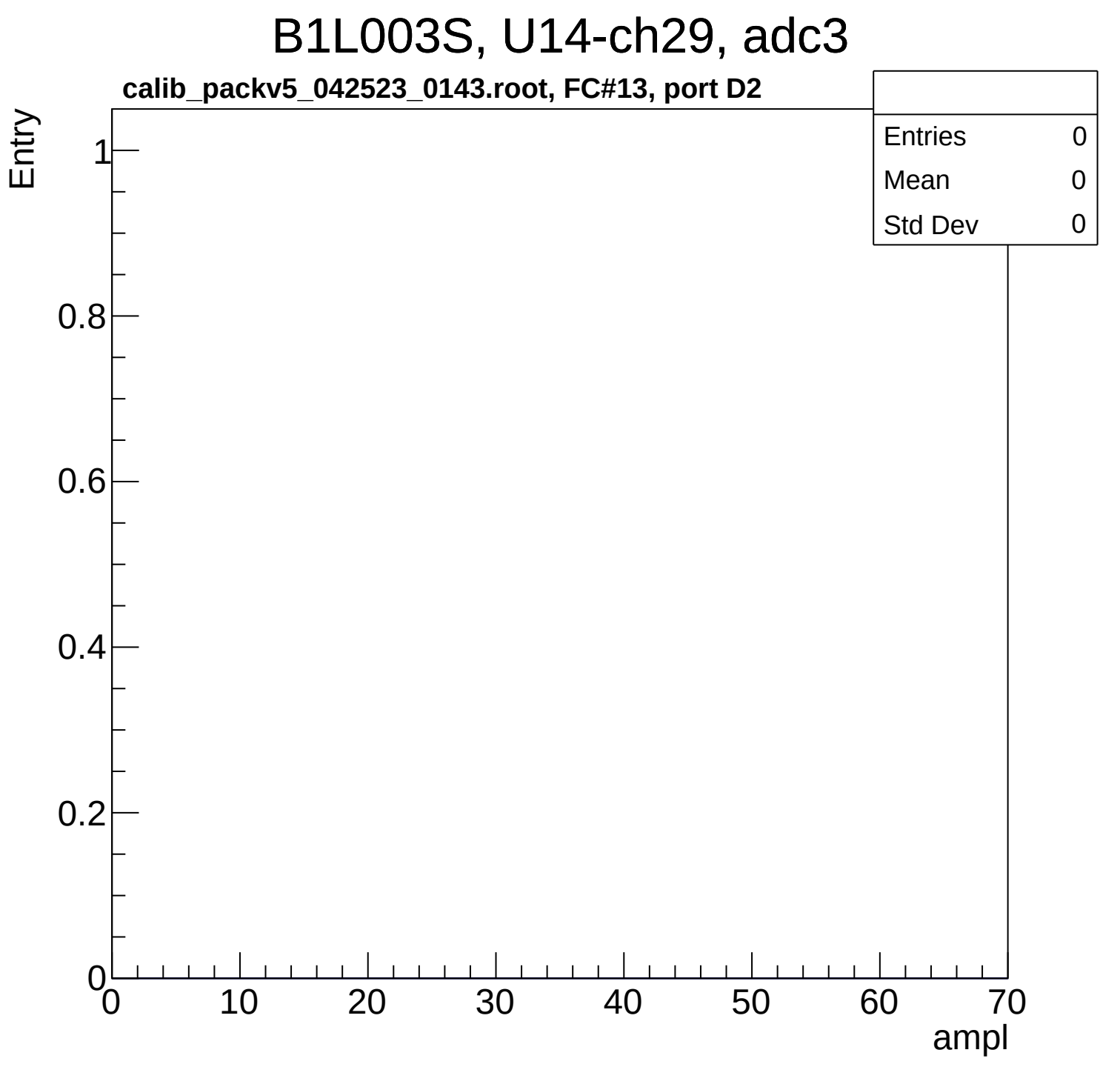

B1L003S, U14-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

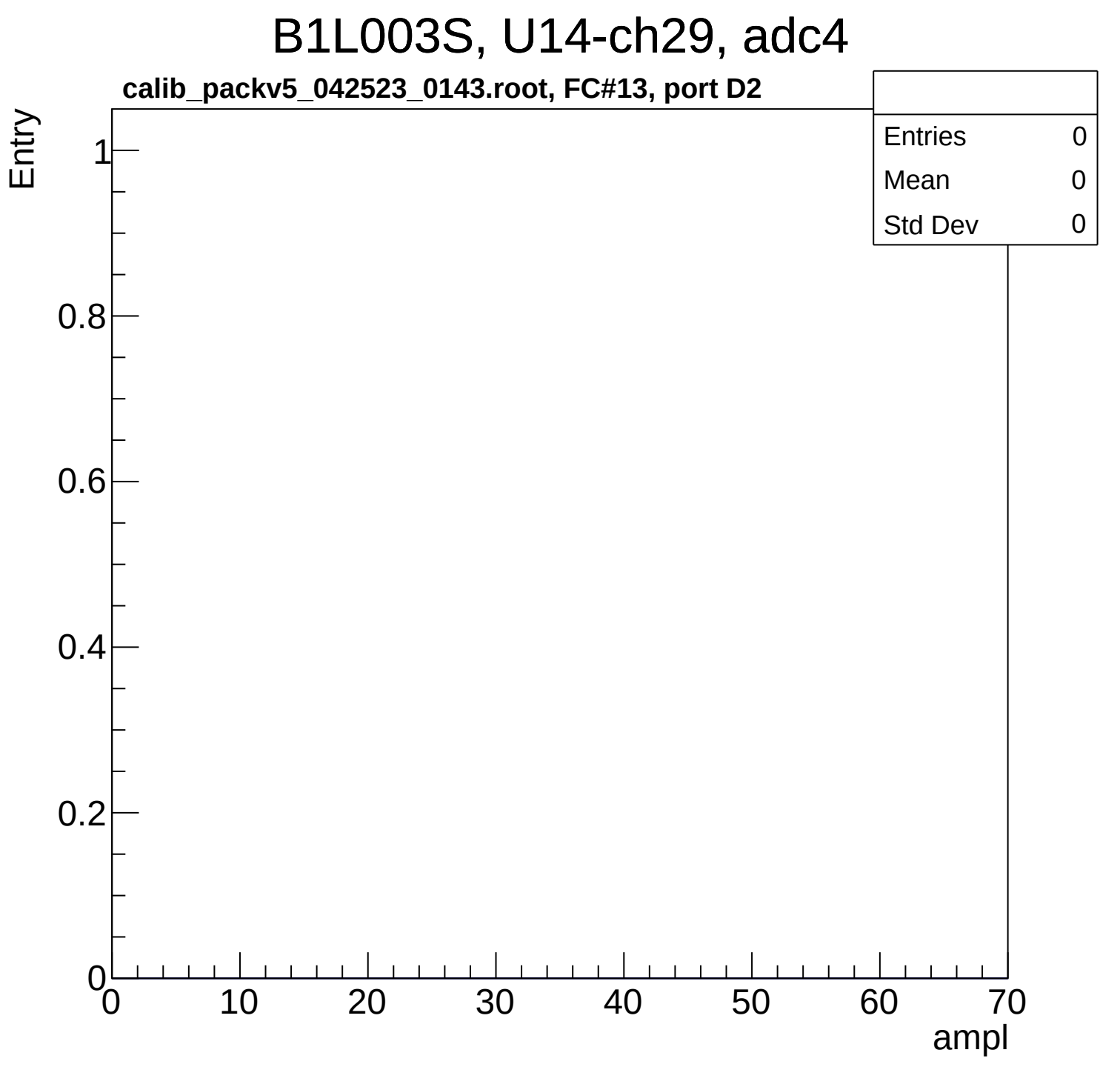

B1L003S, U14-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

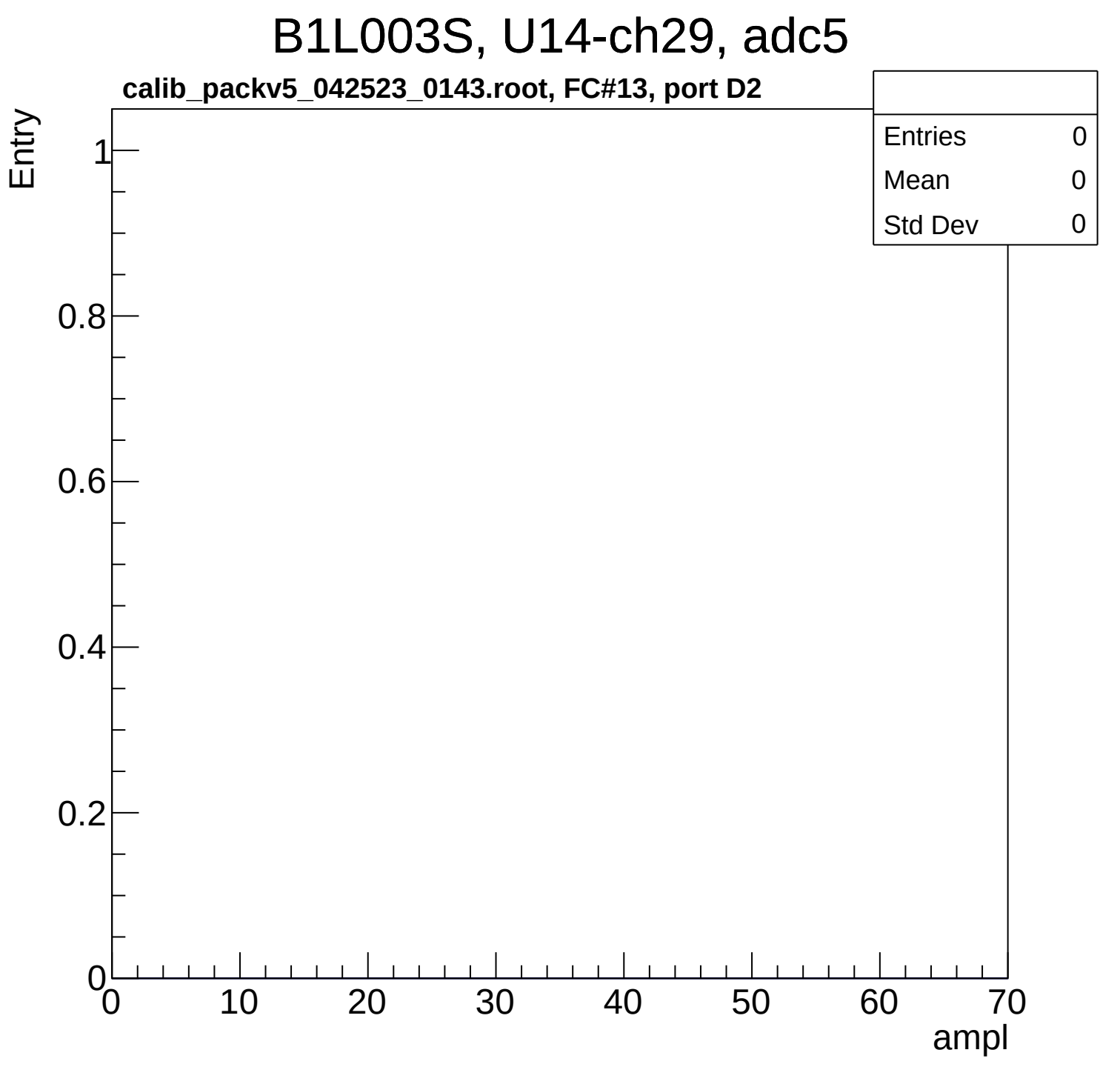

B1L003S, U14-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

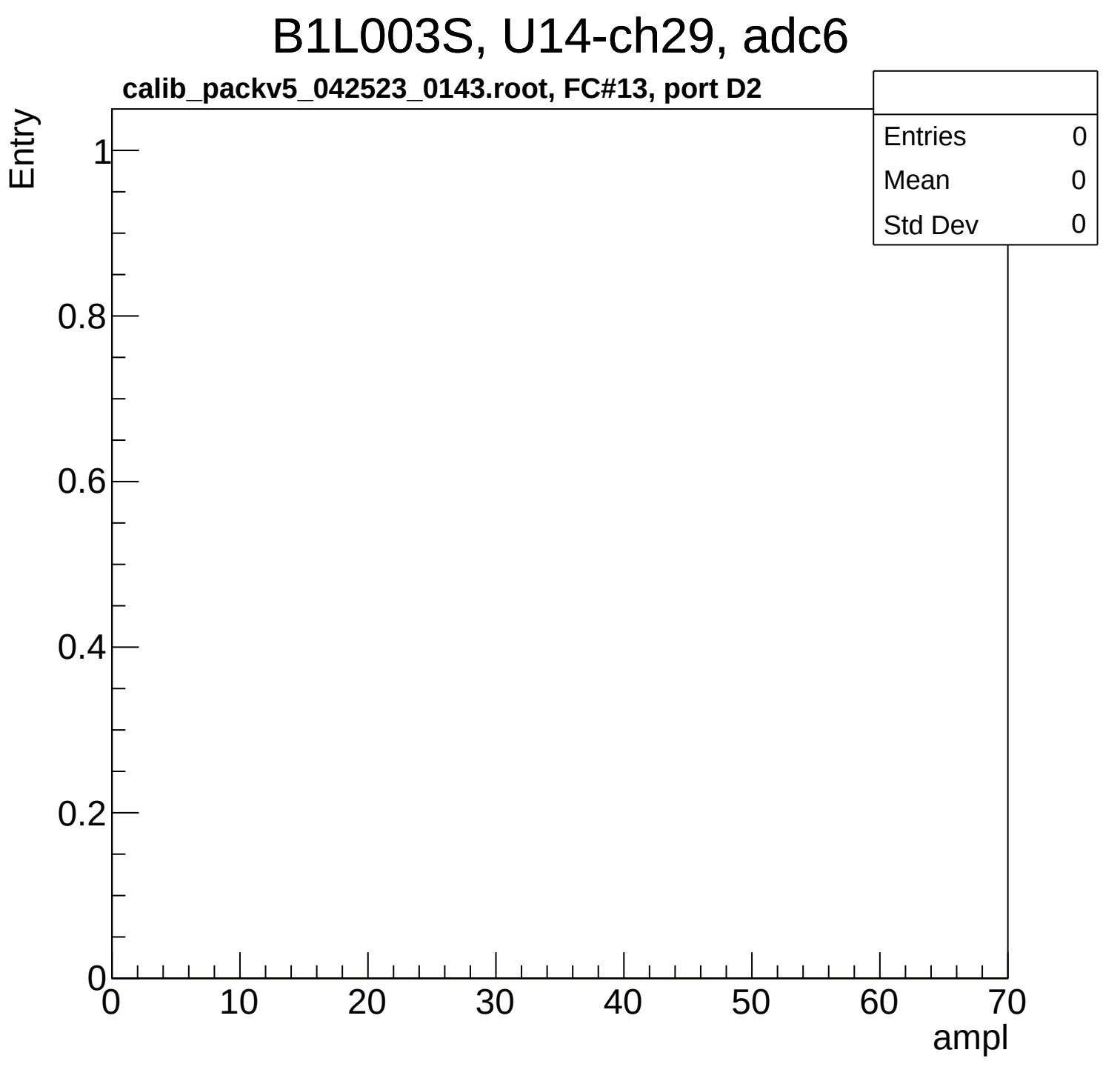

B1L003S, U14-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

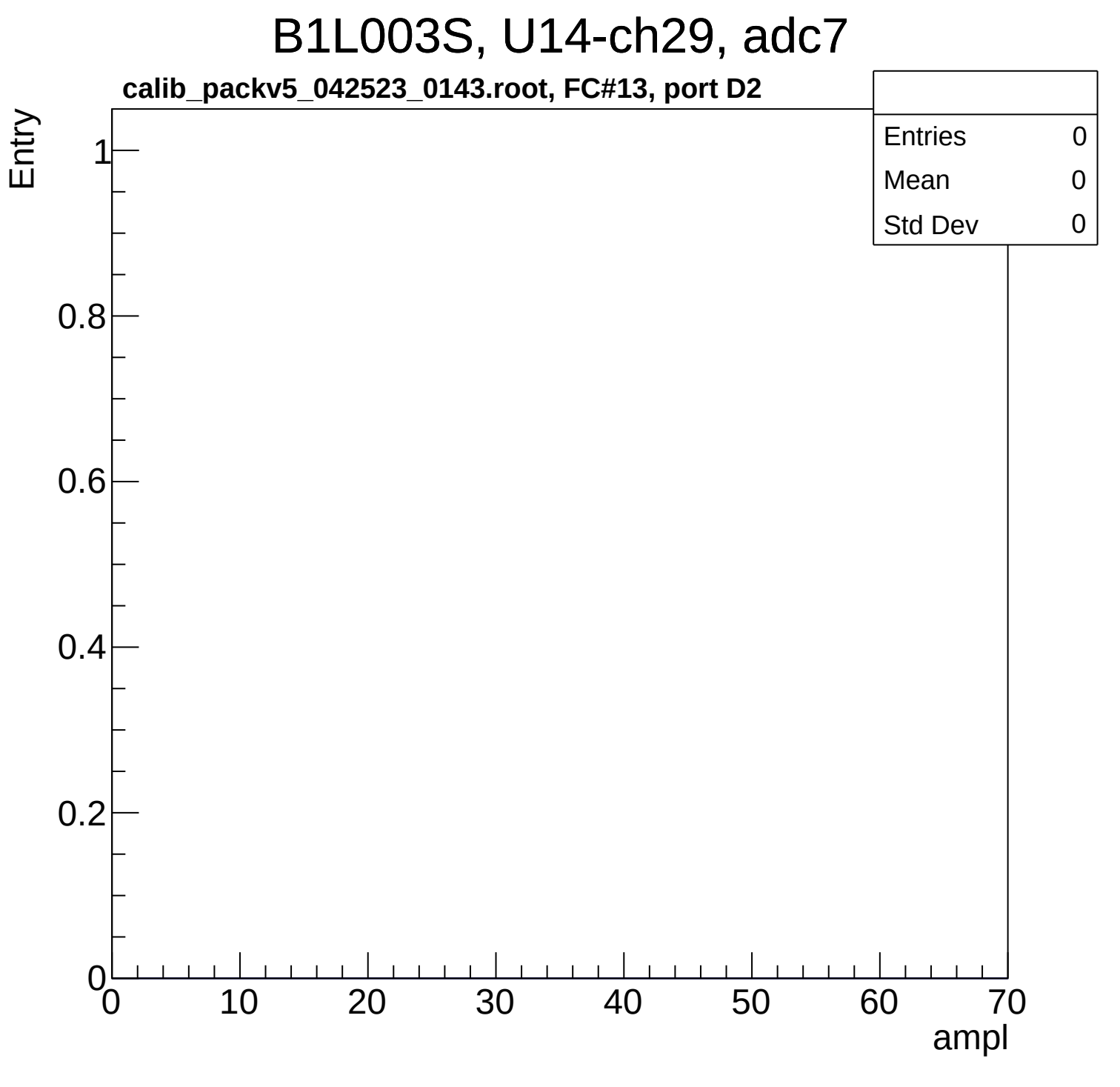

B1L003S, U14-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

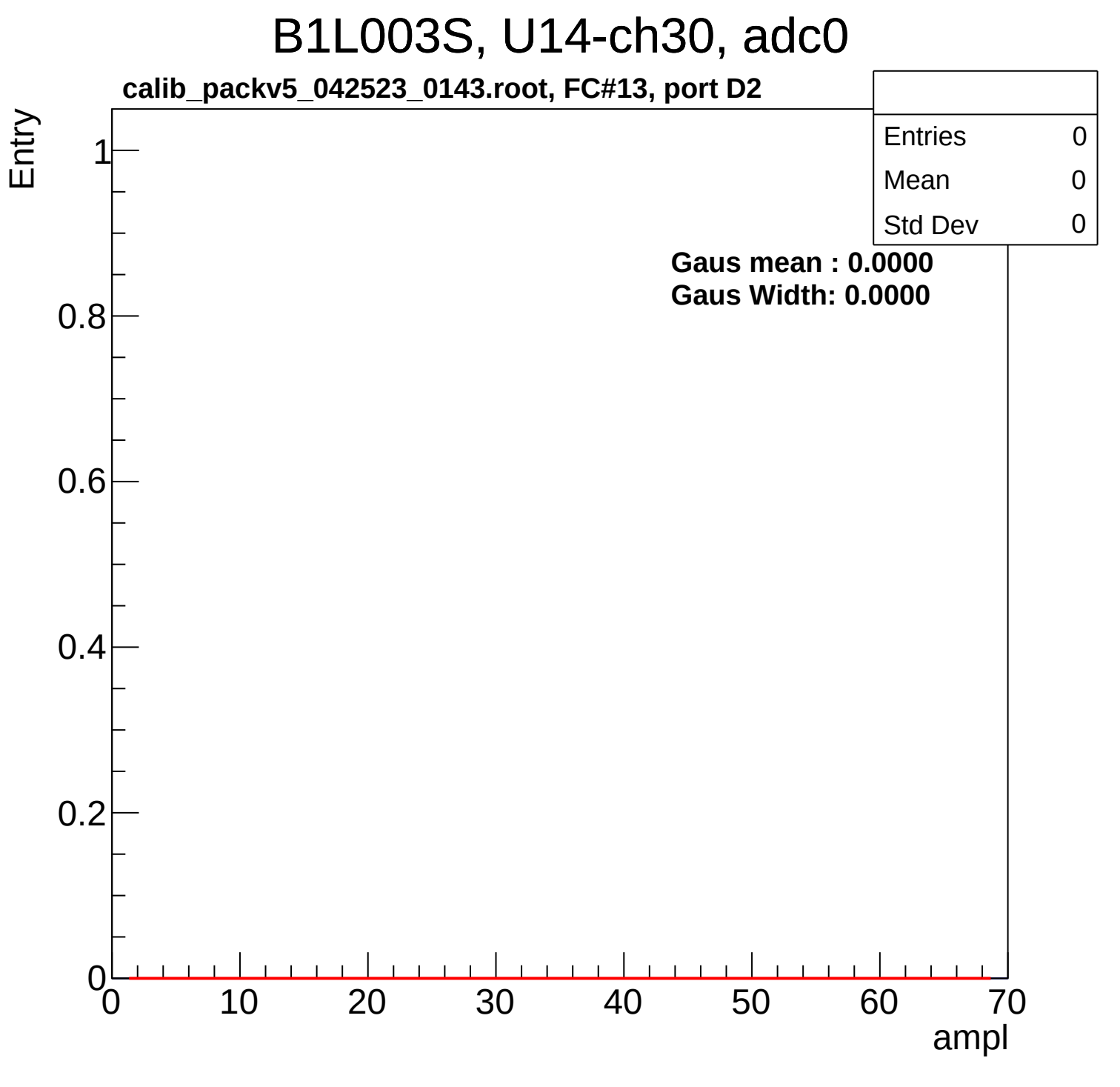

B1L003S, U14-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

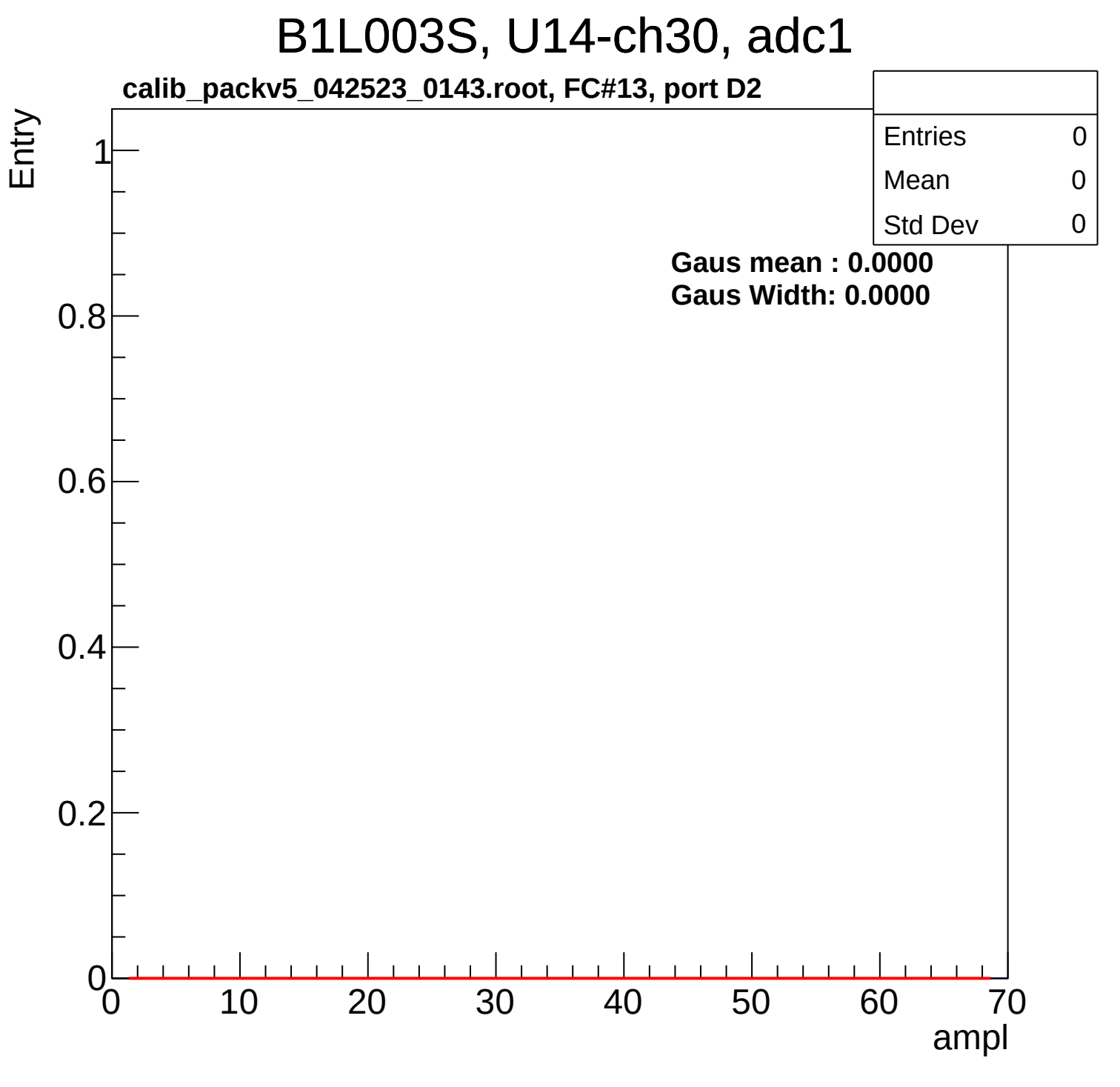

B1L003S, U14-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

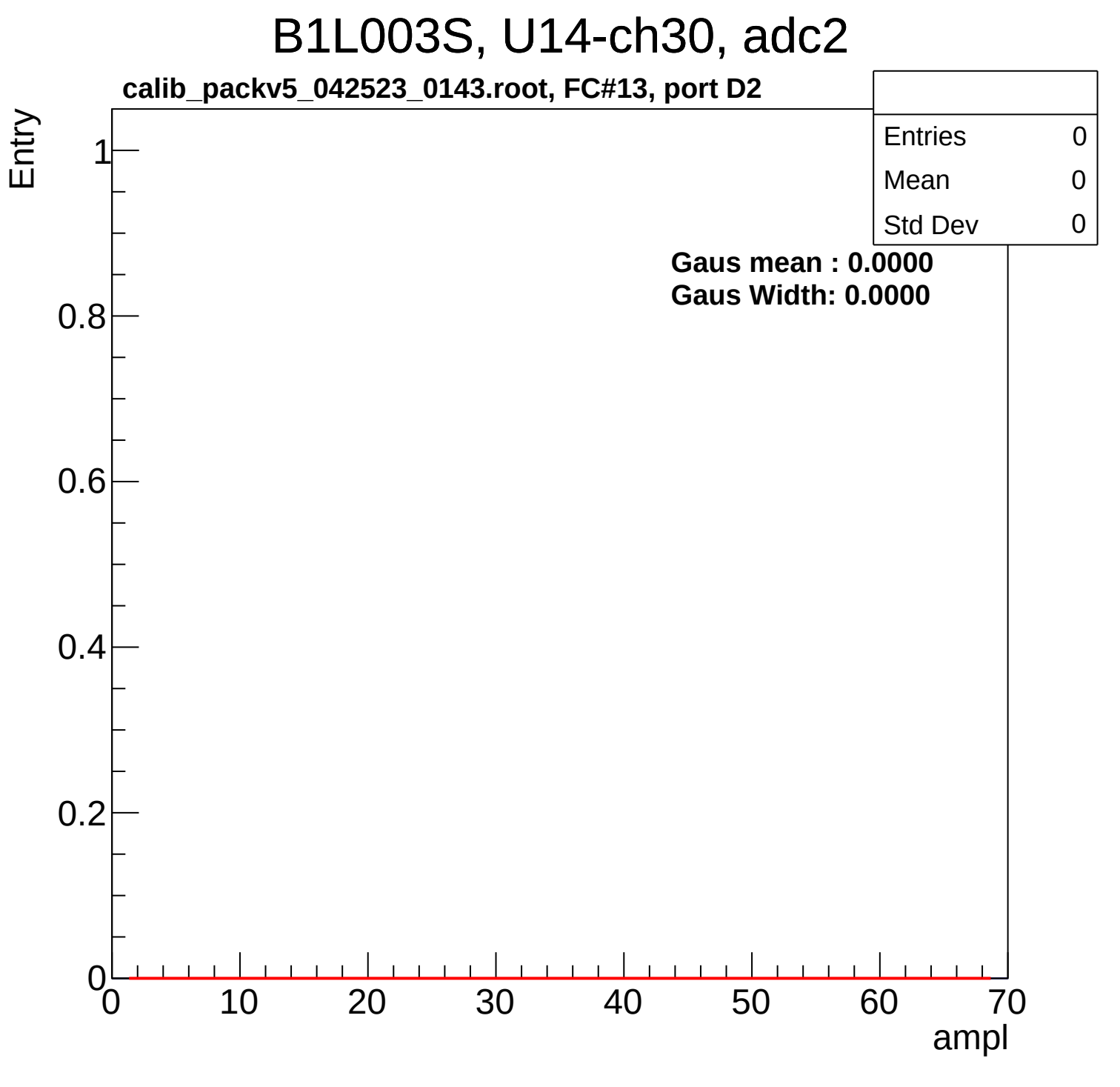

B1L003S, U14-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

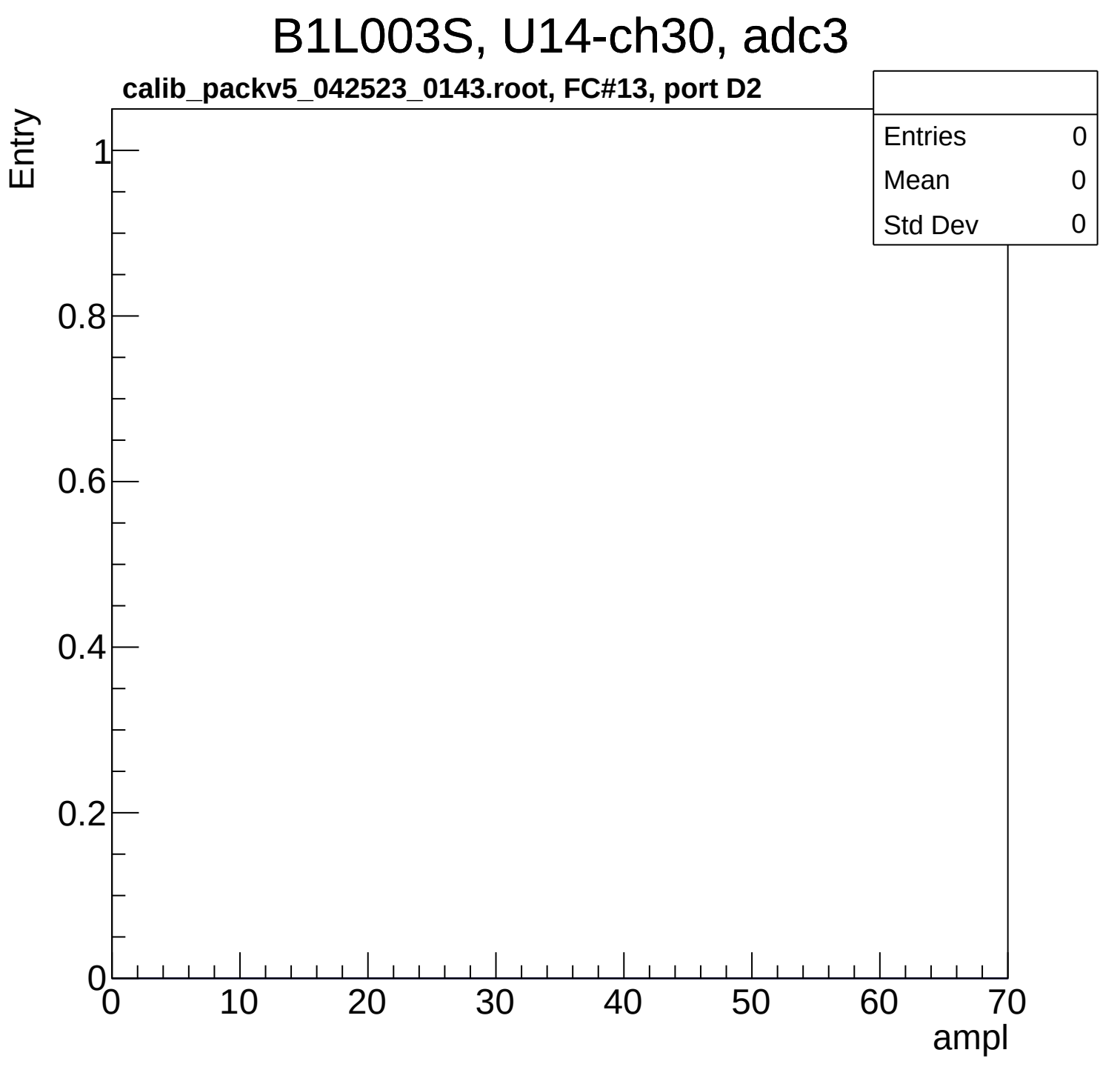

B1L003S, U14-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

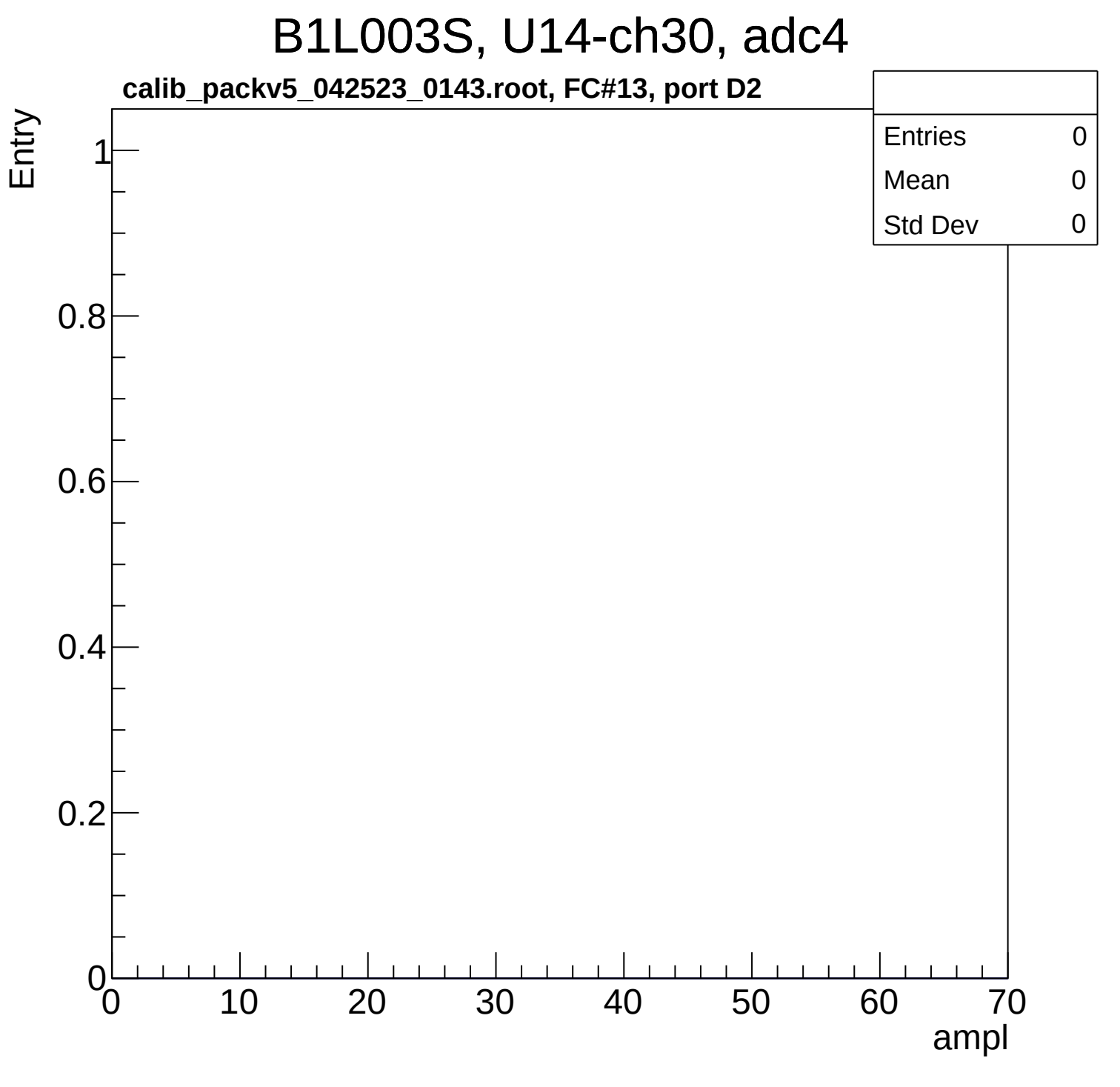

B1L003S, U14-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

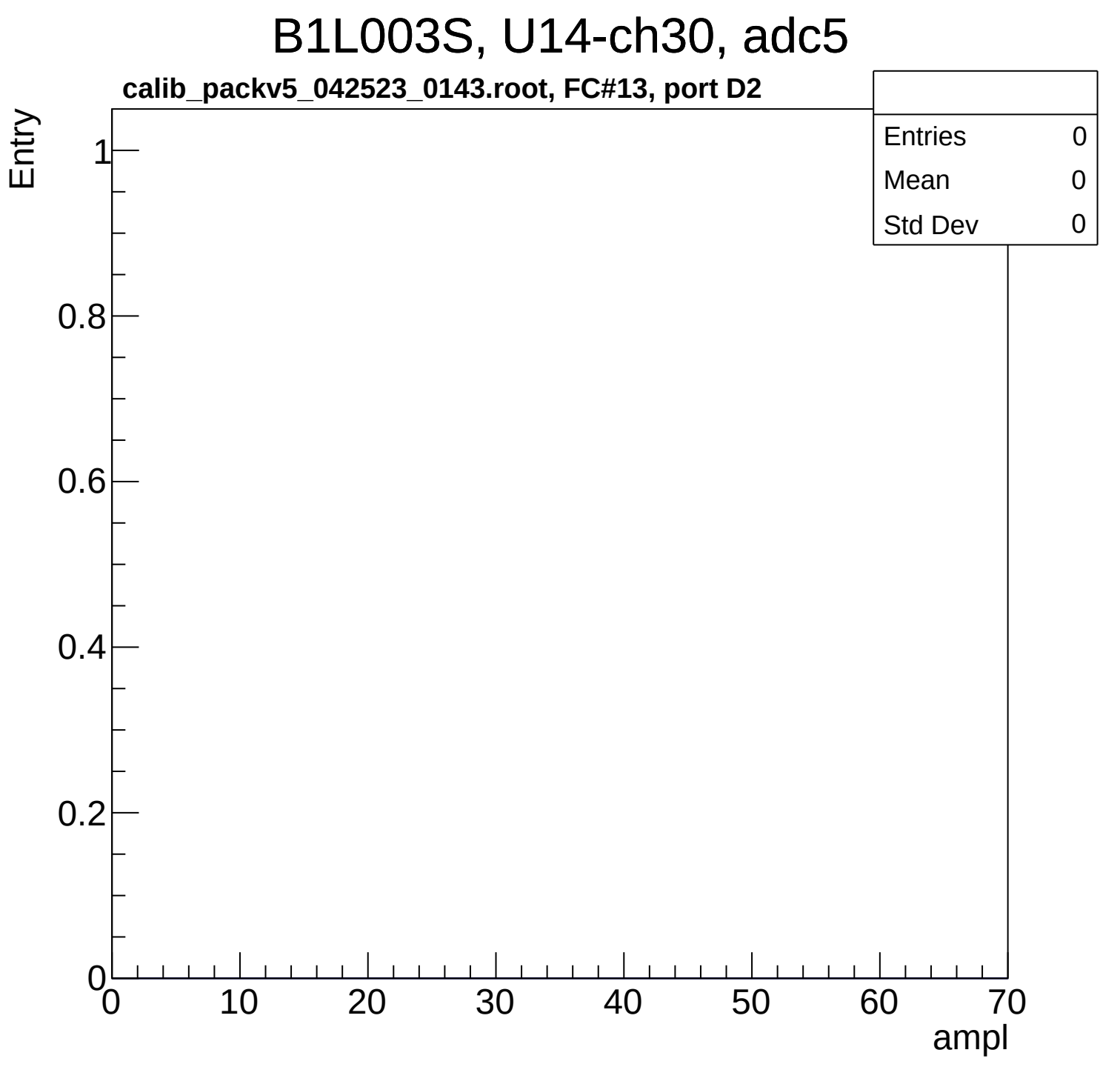

B1L003S, U14-ch30, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

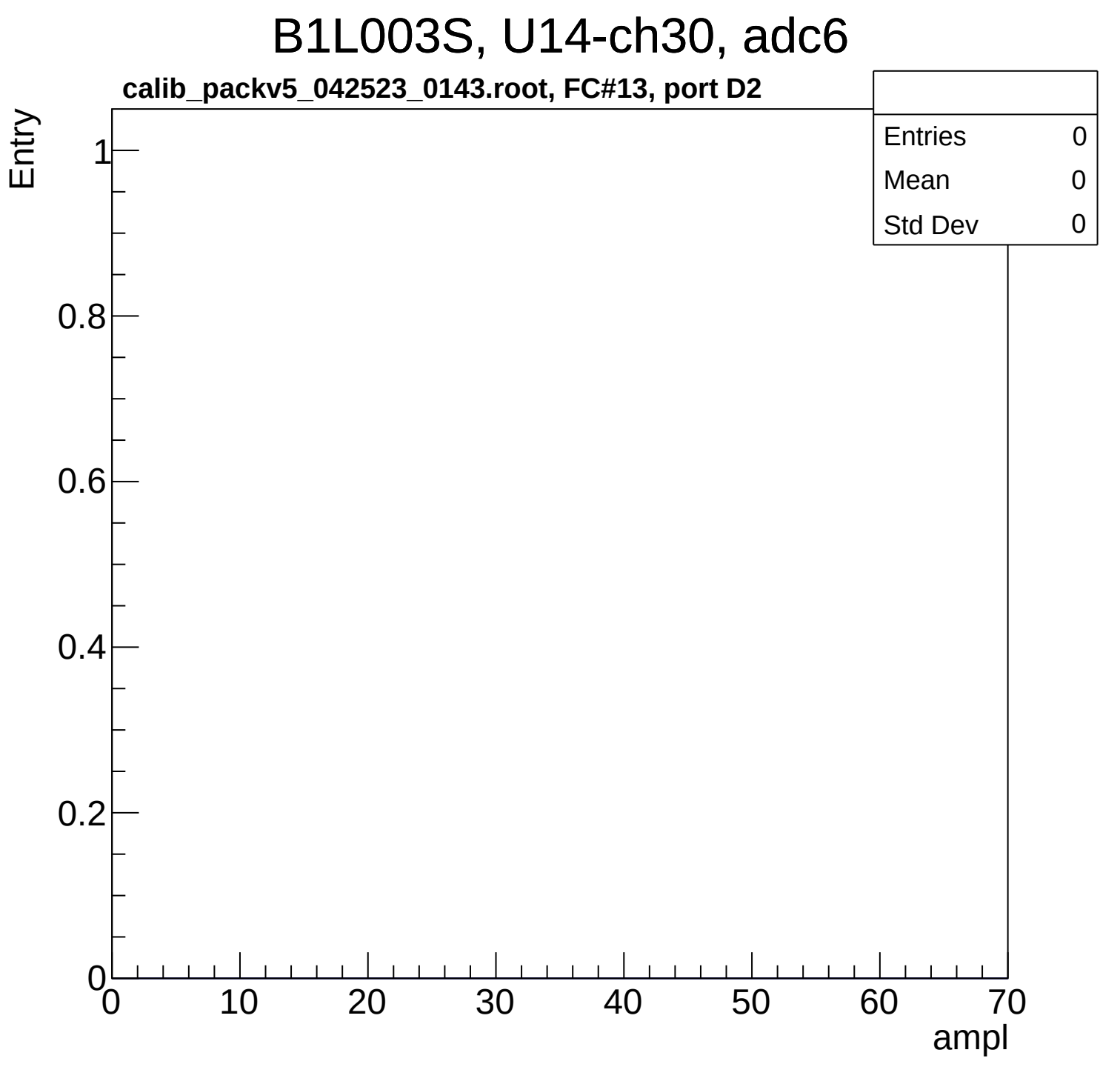

B1L003S, U14-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

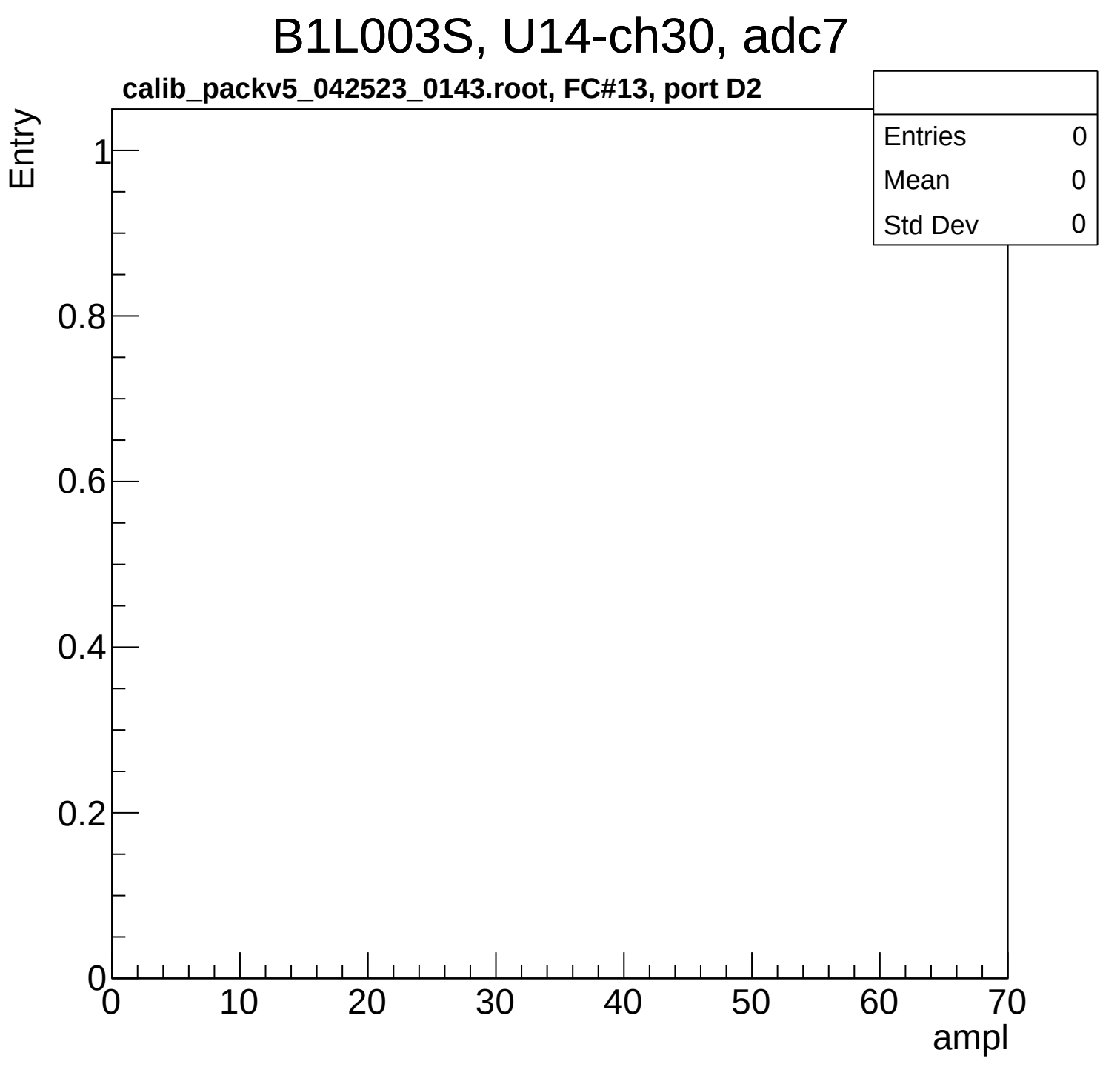

B1L003S, U14-ch30, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

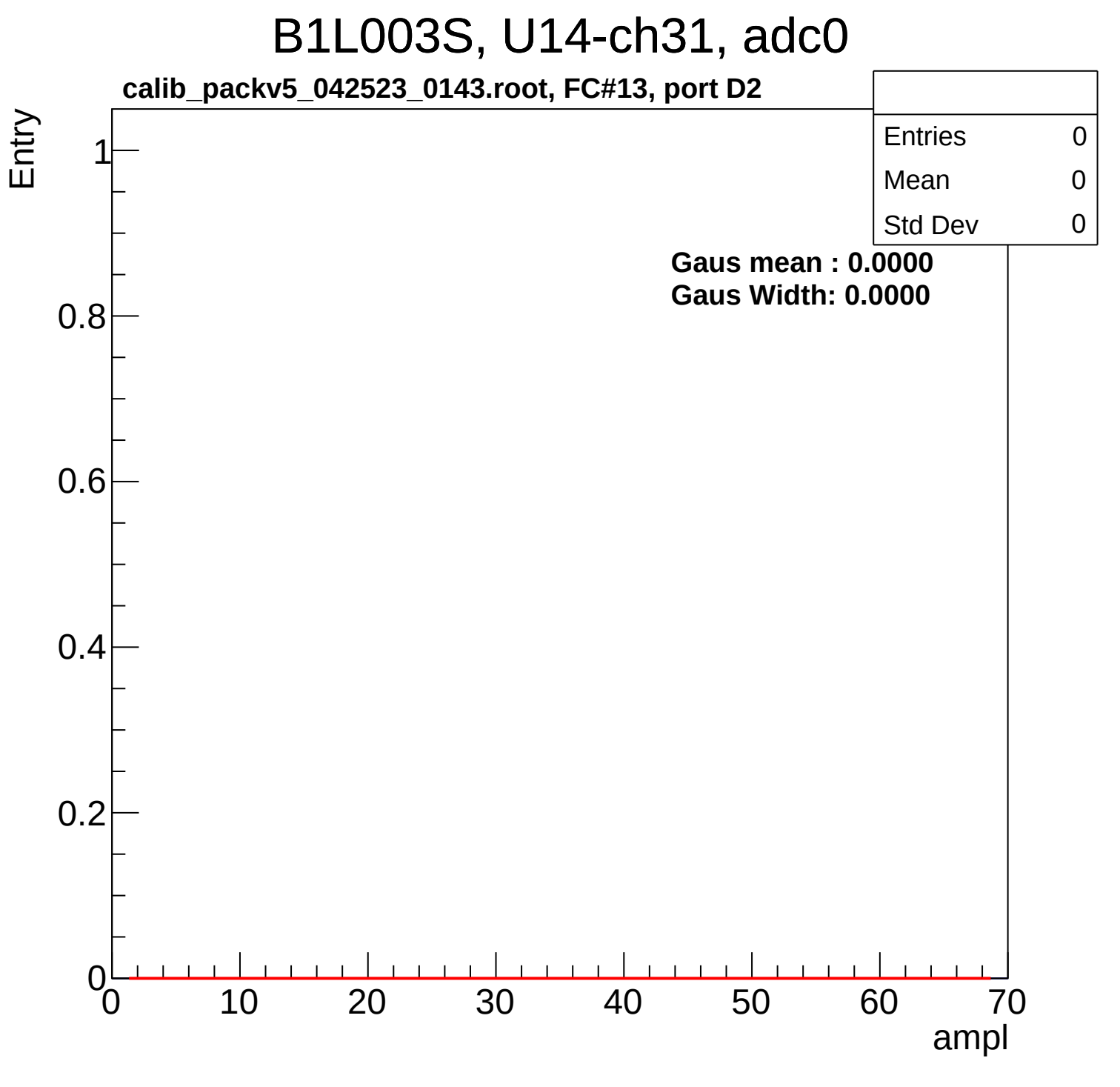

B1L003S, U14-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

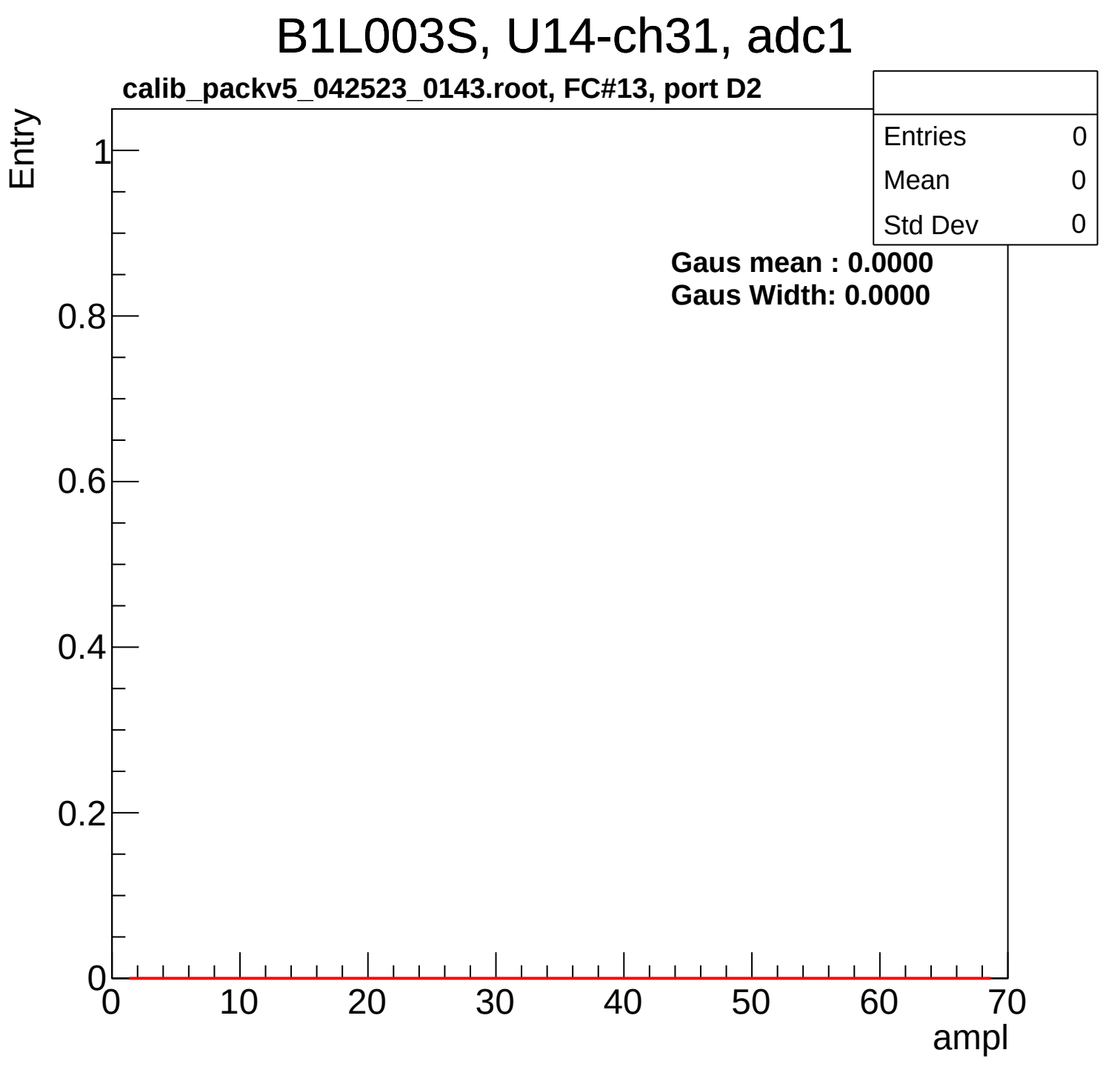

B1L003S, U14-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

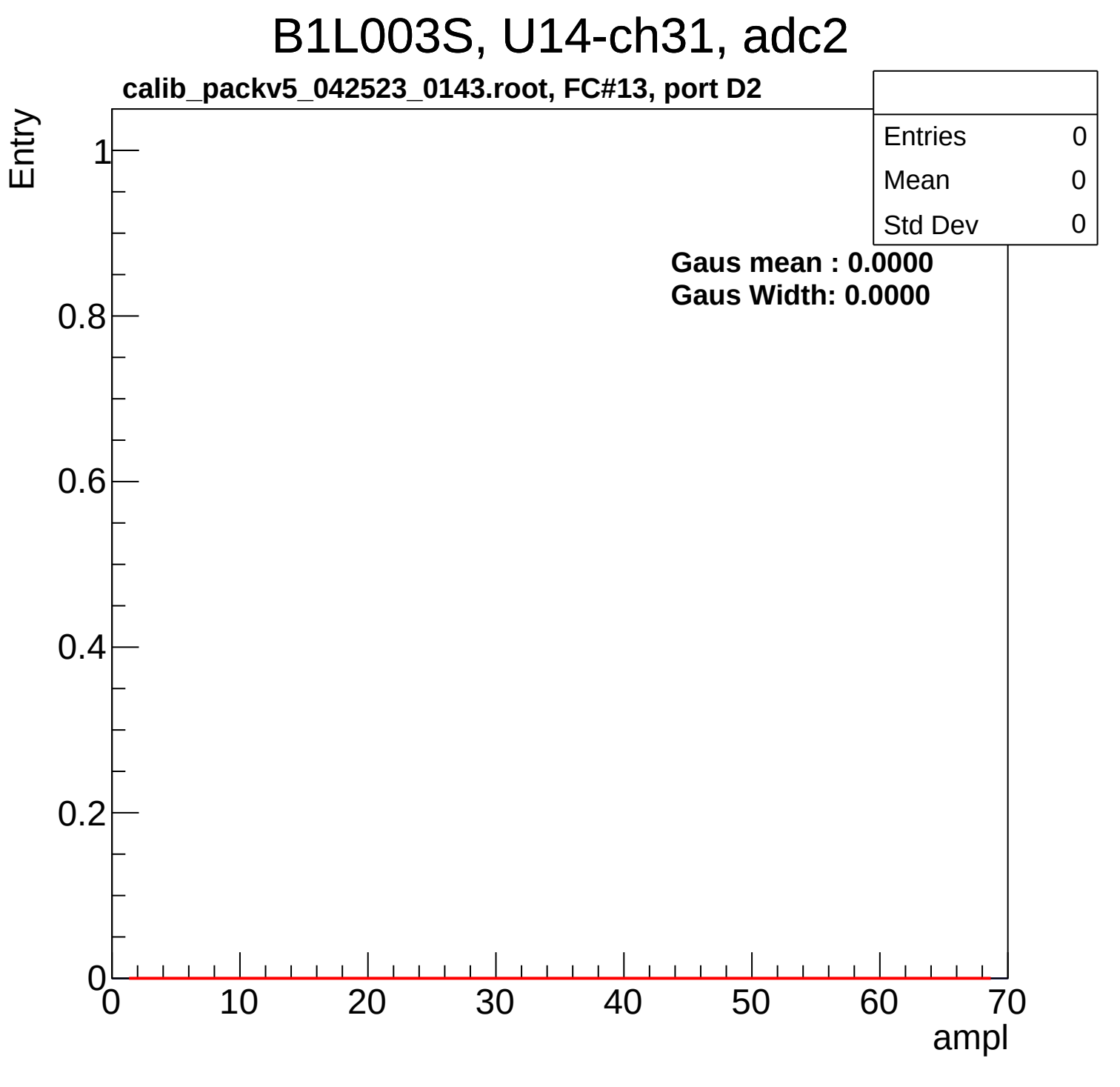

B1L003S, U14-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

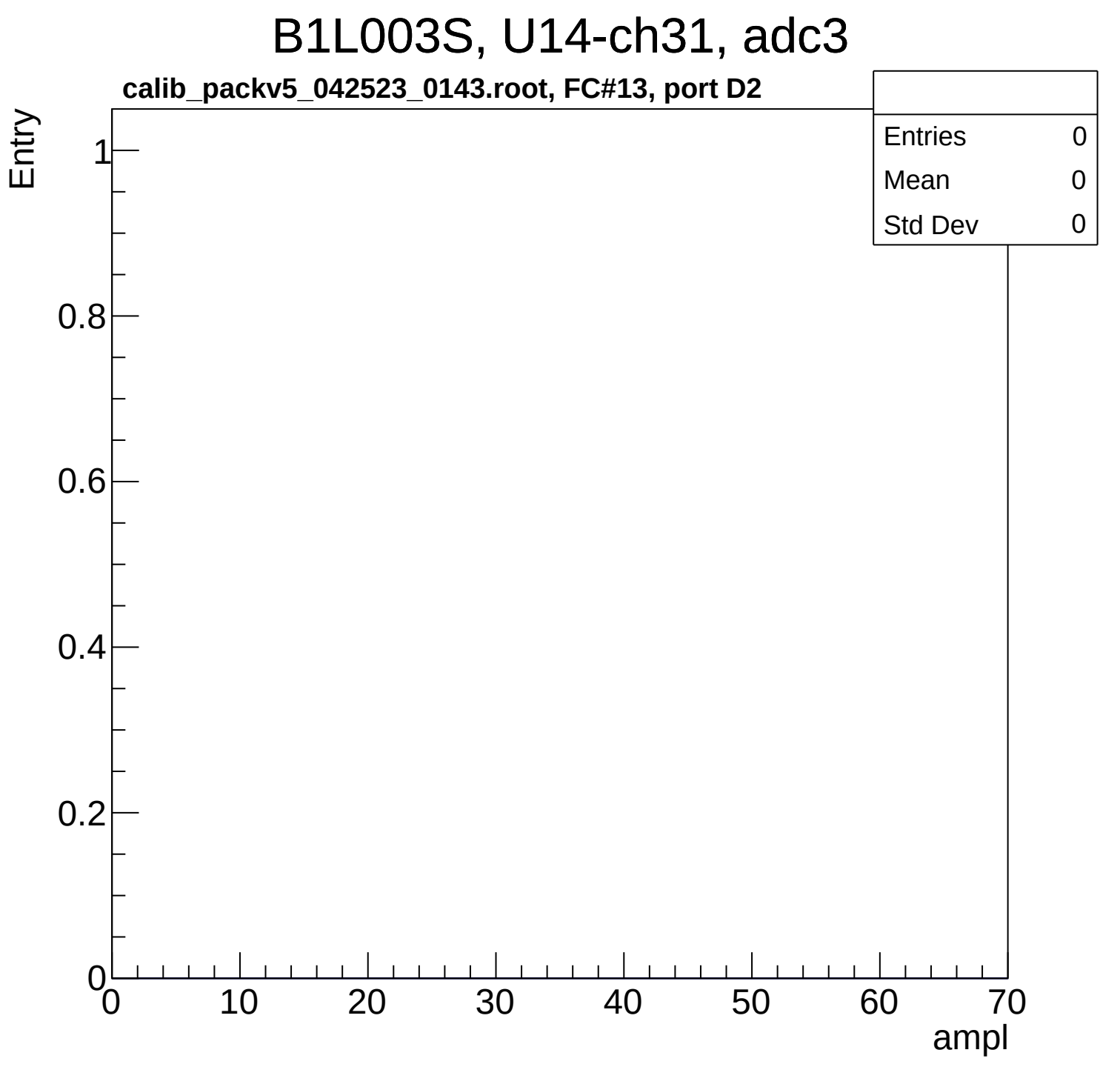

B1L003S, U14-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

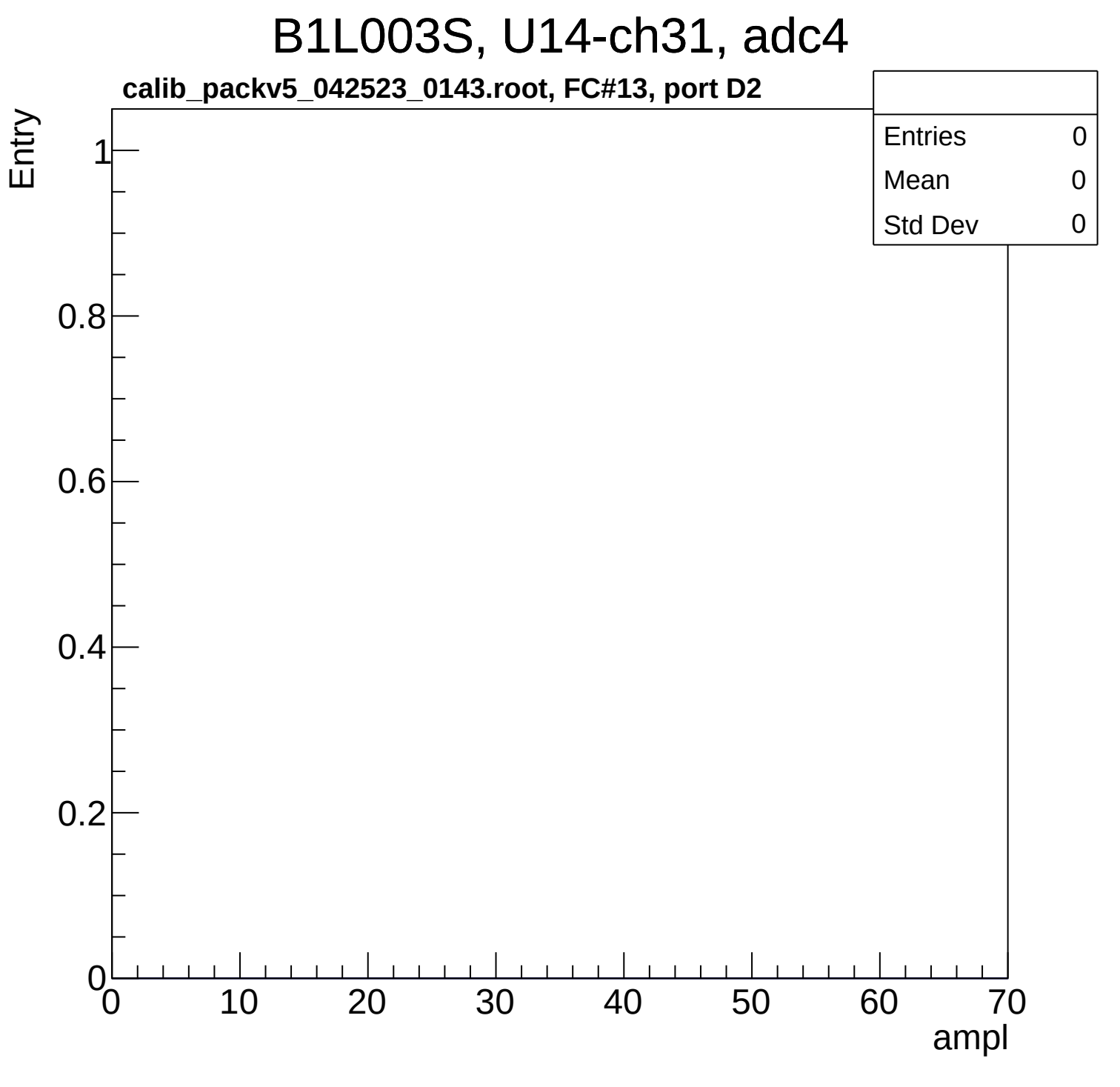

B1L003S, U14-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

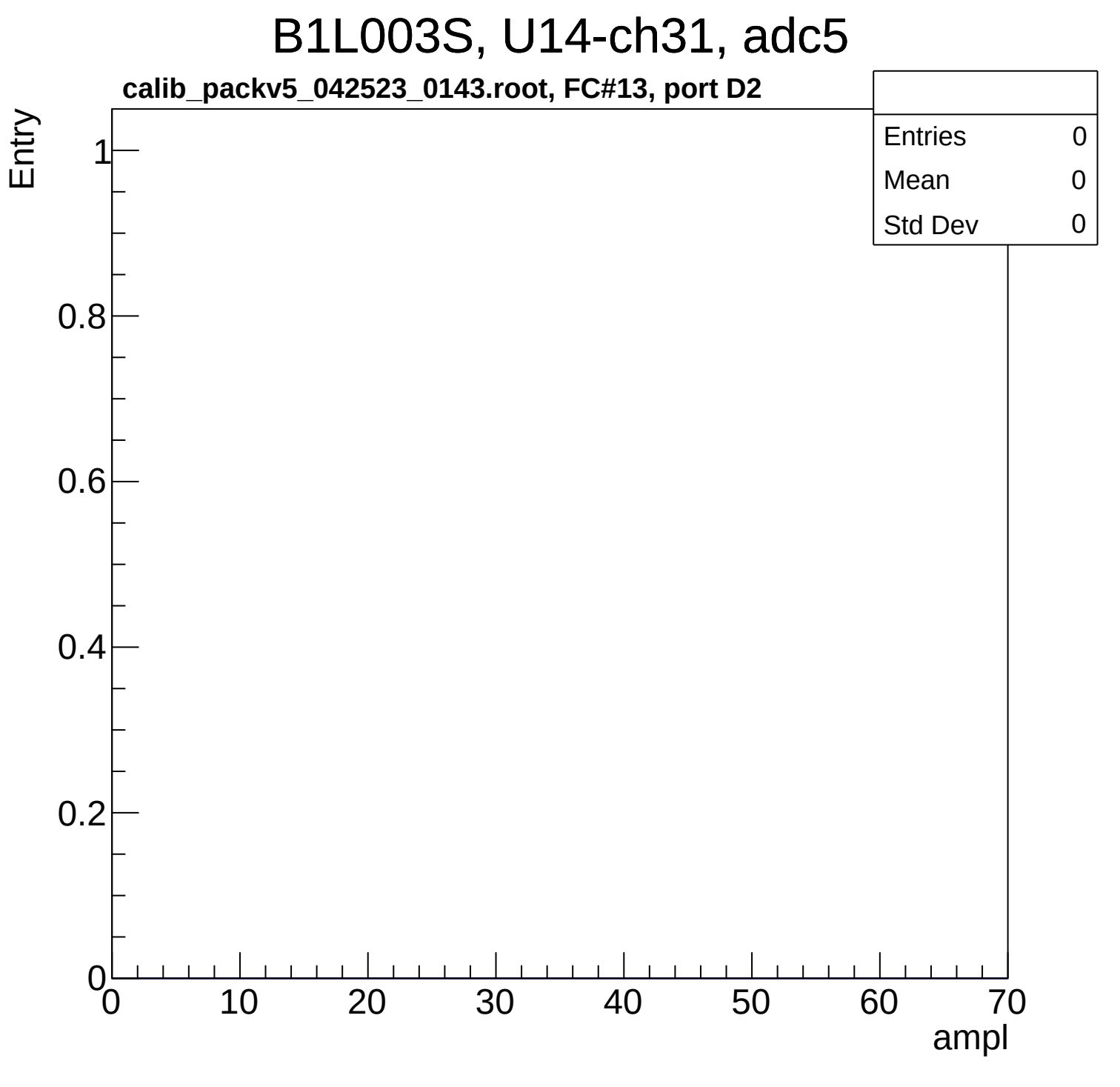

B1L003S, U14-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

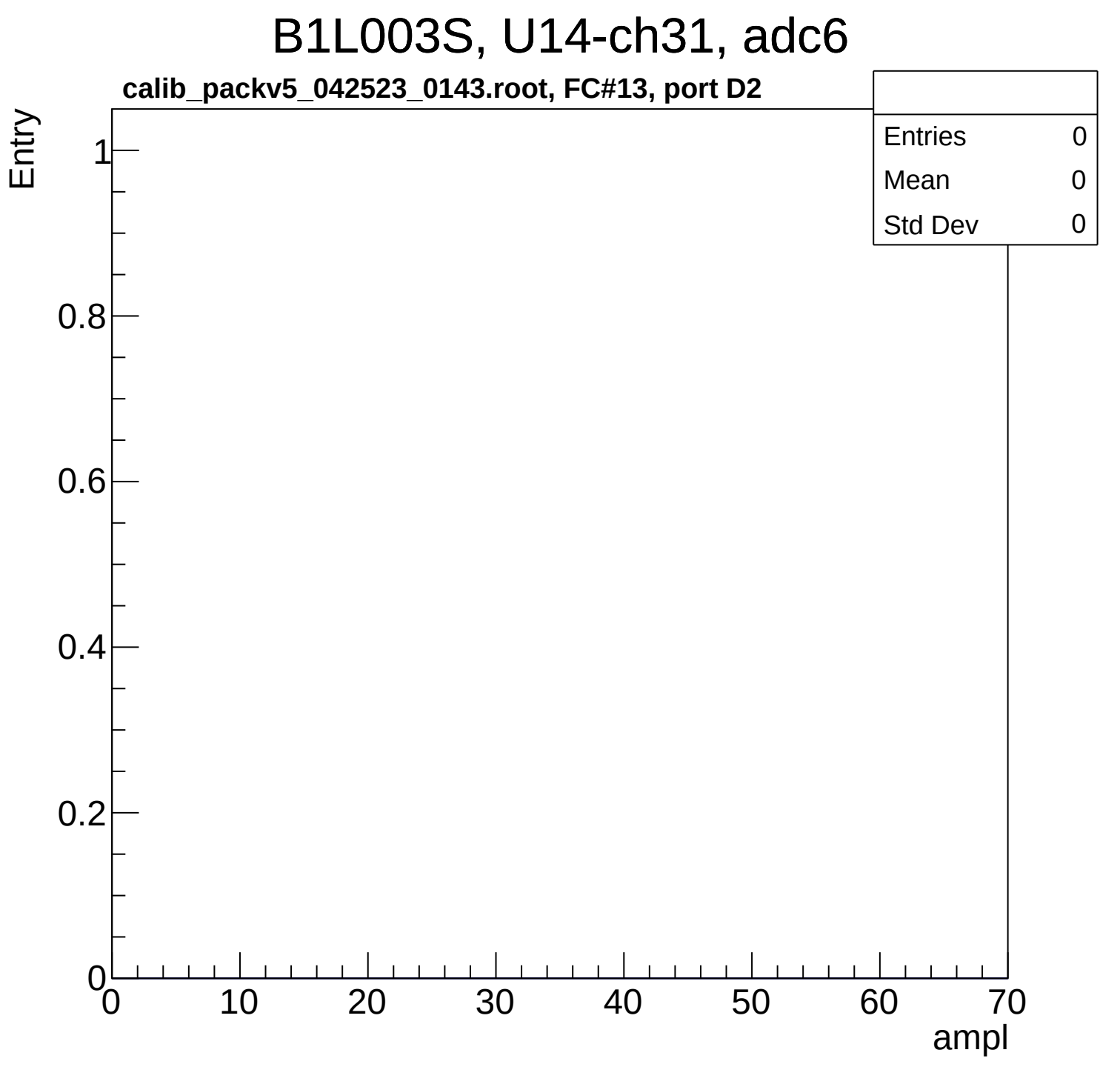

B1L003S, U14-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

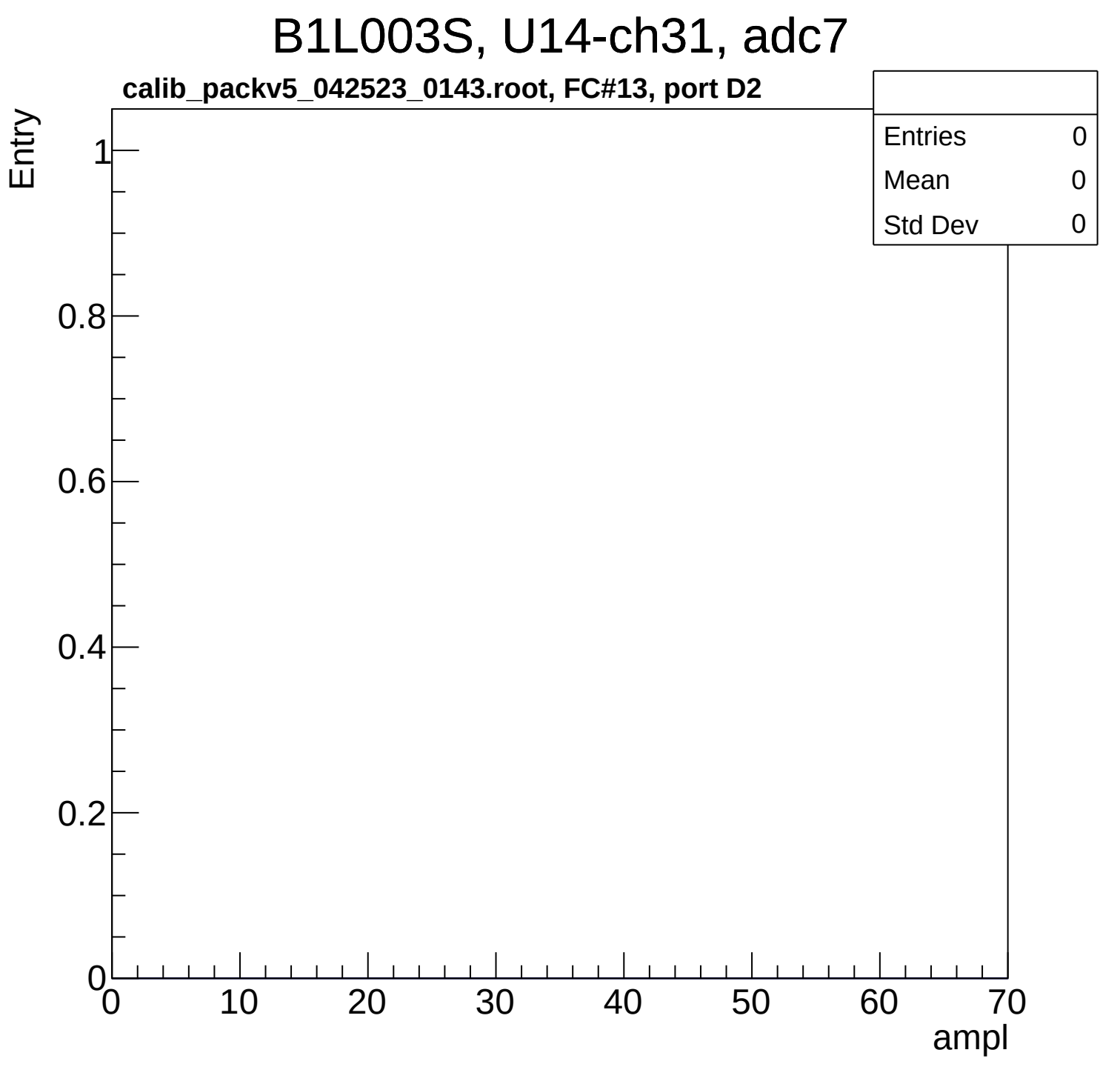

B1L003S, U14-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

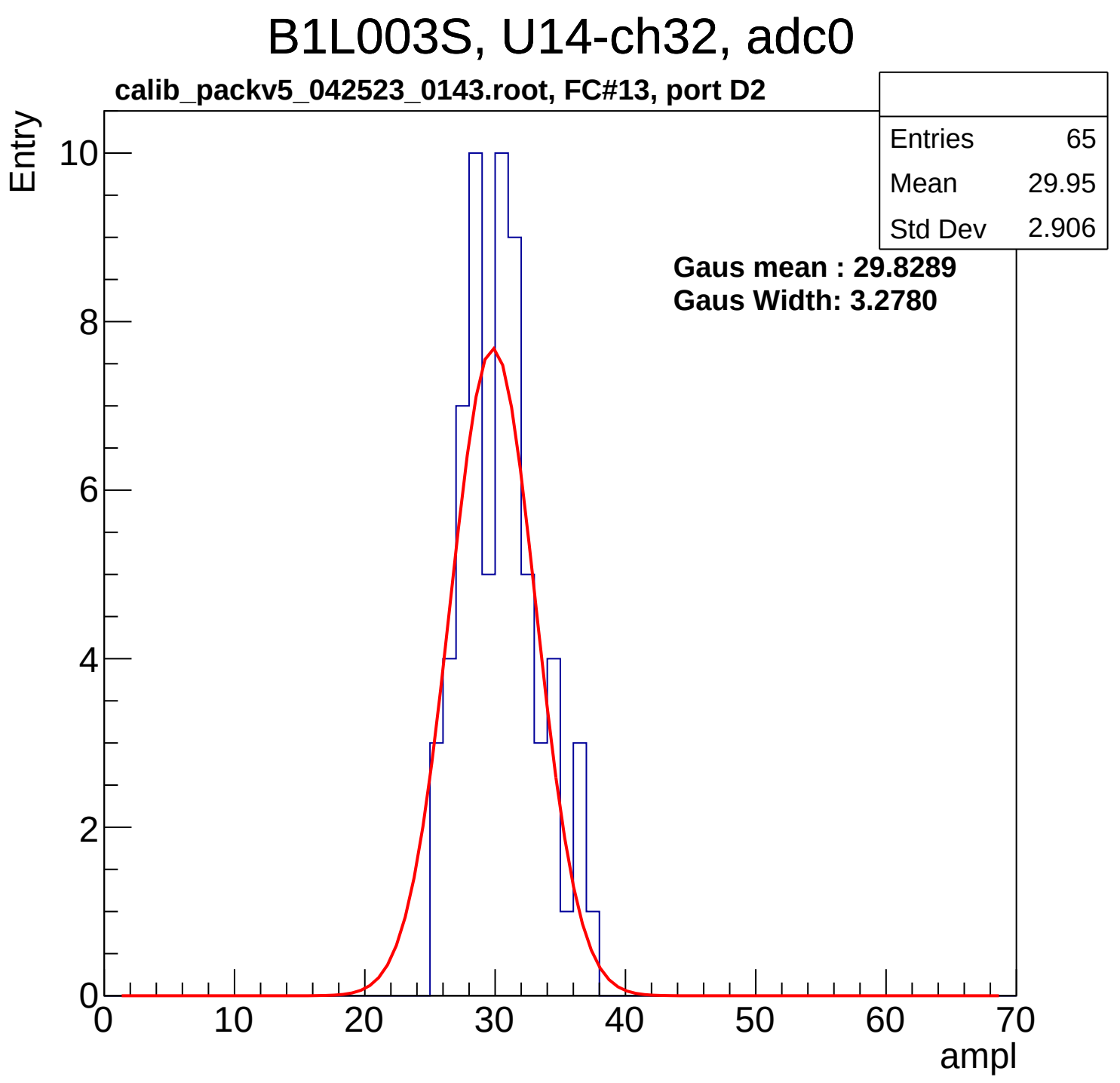

B1L003S, U14-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

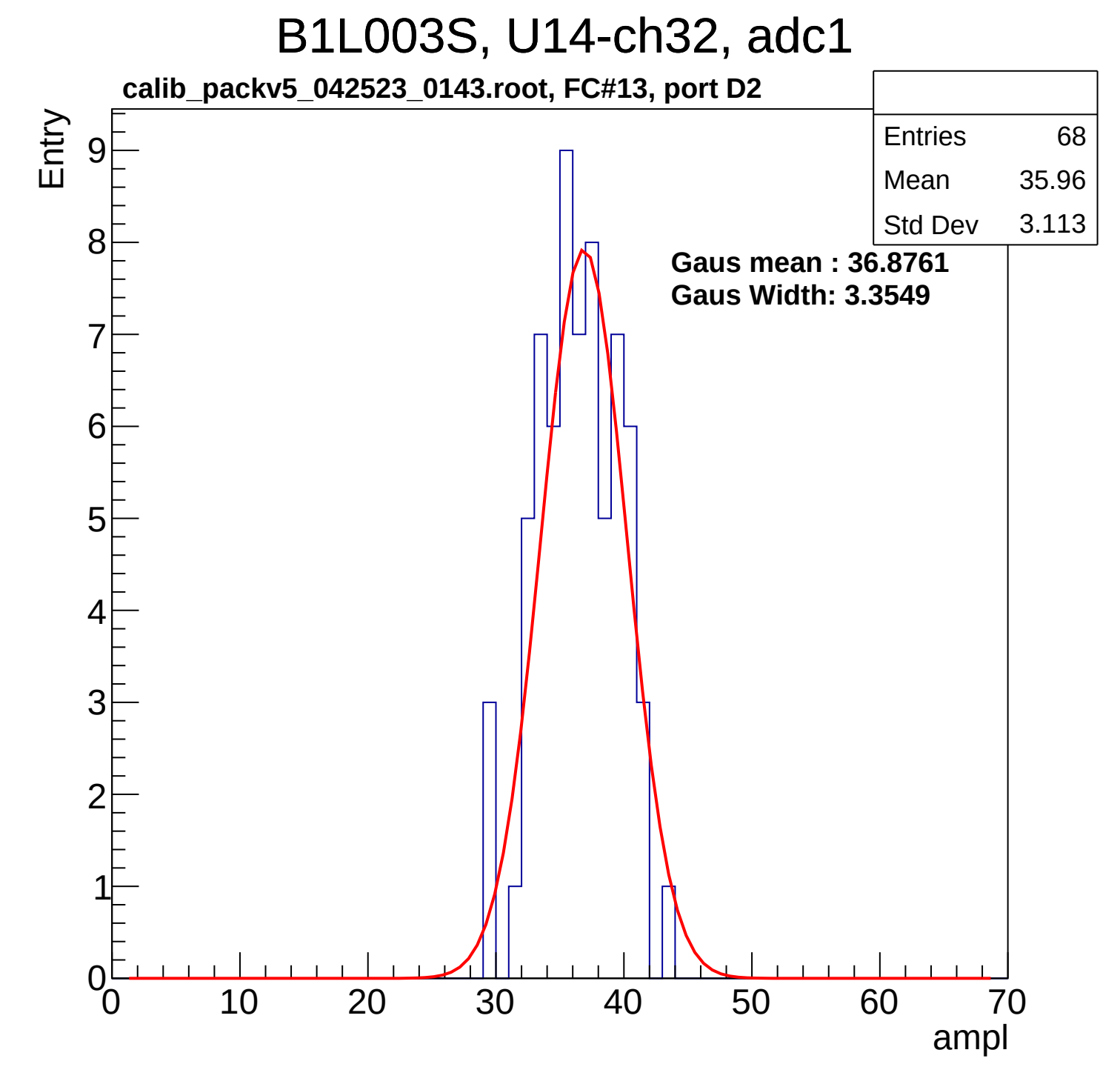

B1L003S, U14-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

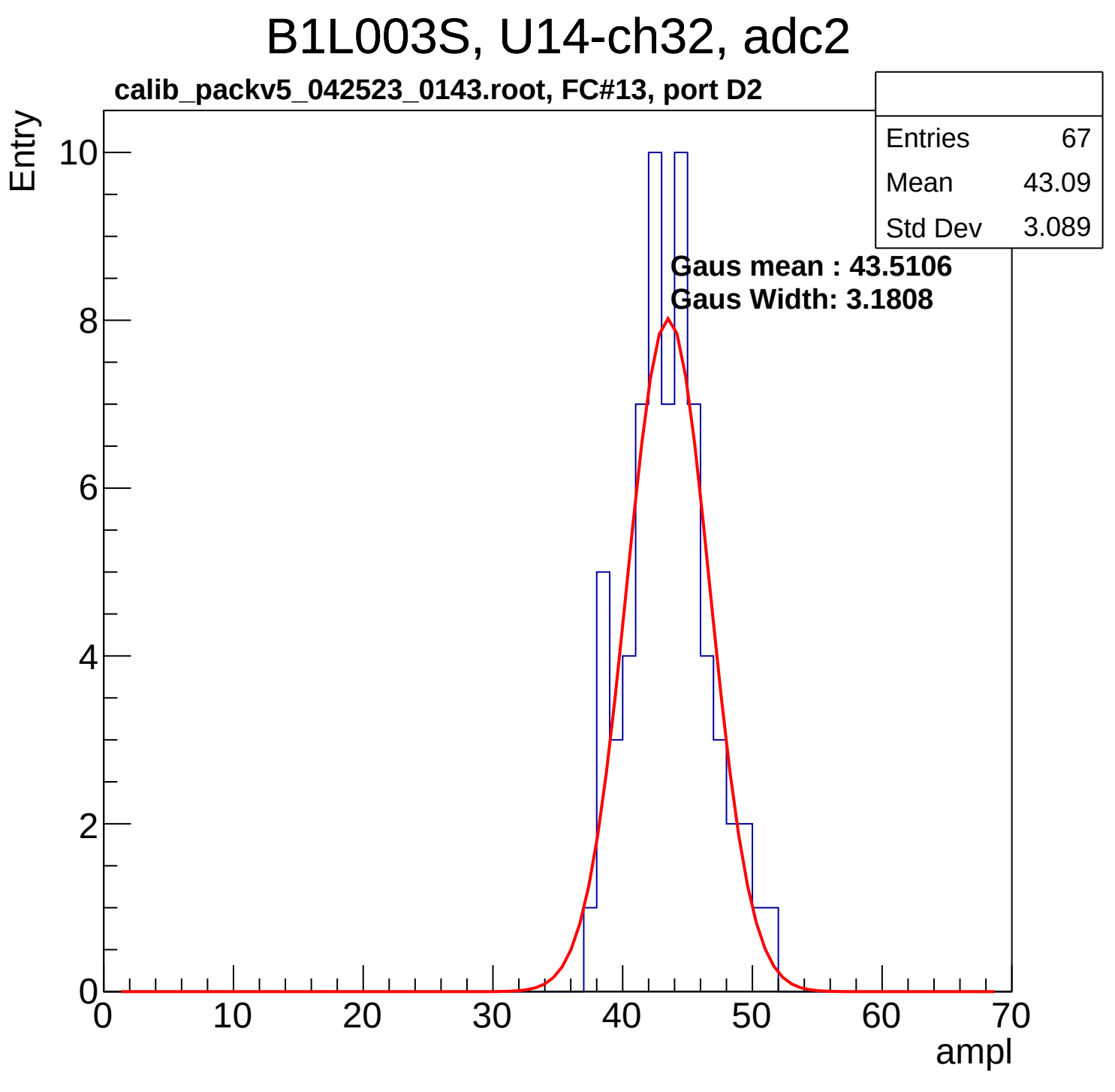

B1L003S, U14-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

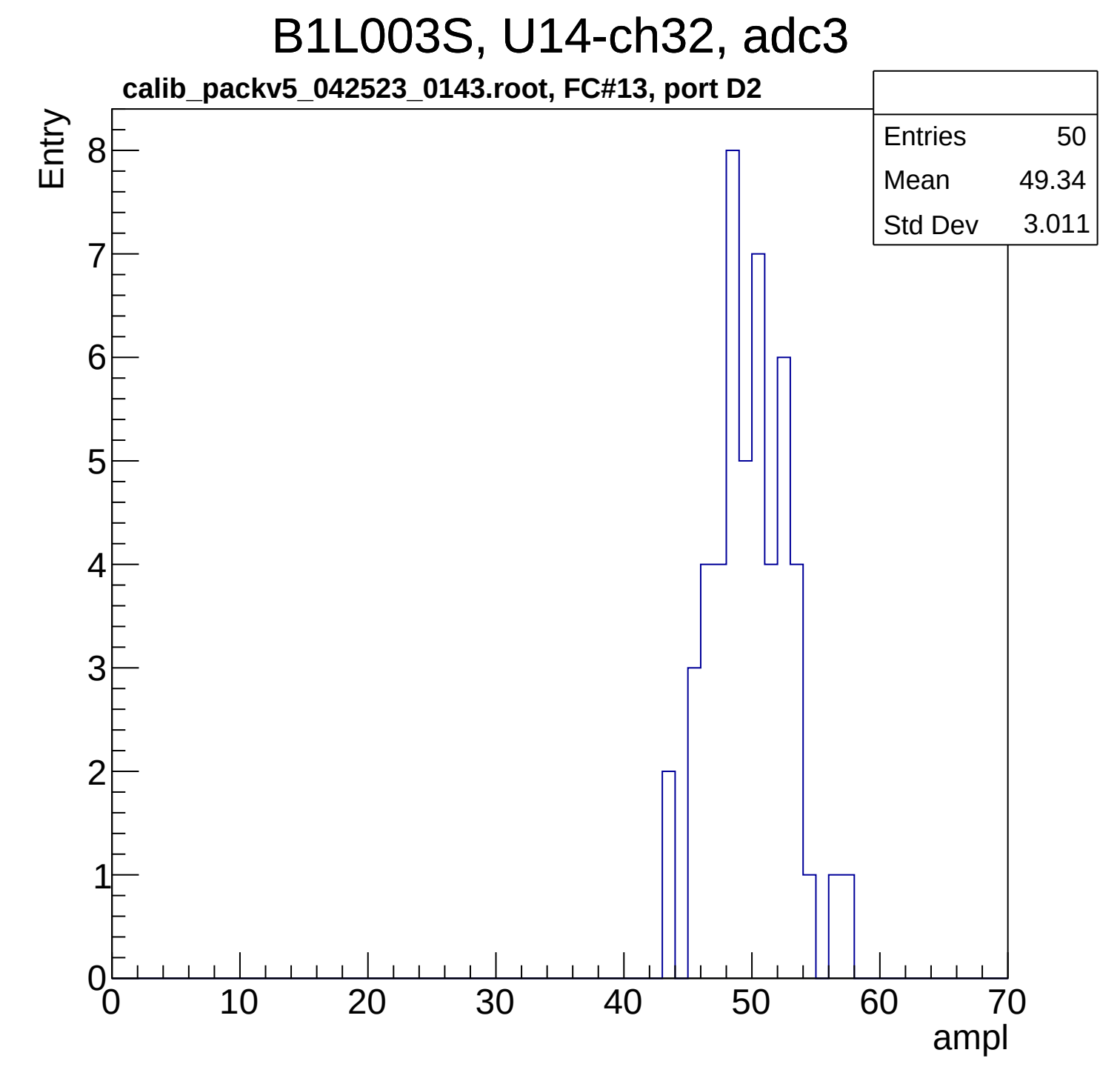

B1L003S, U14-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

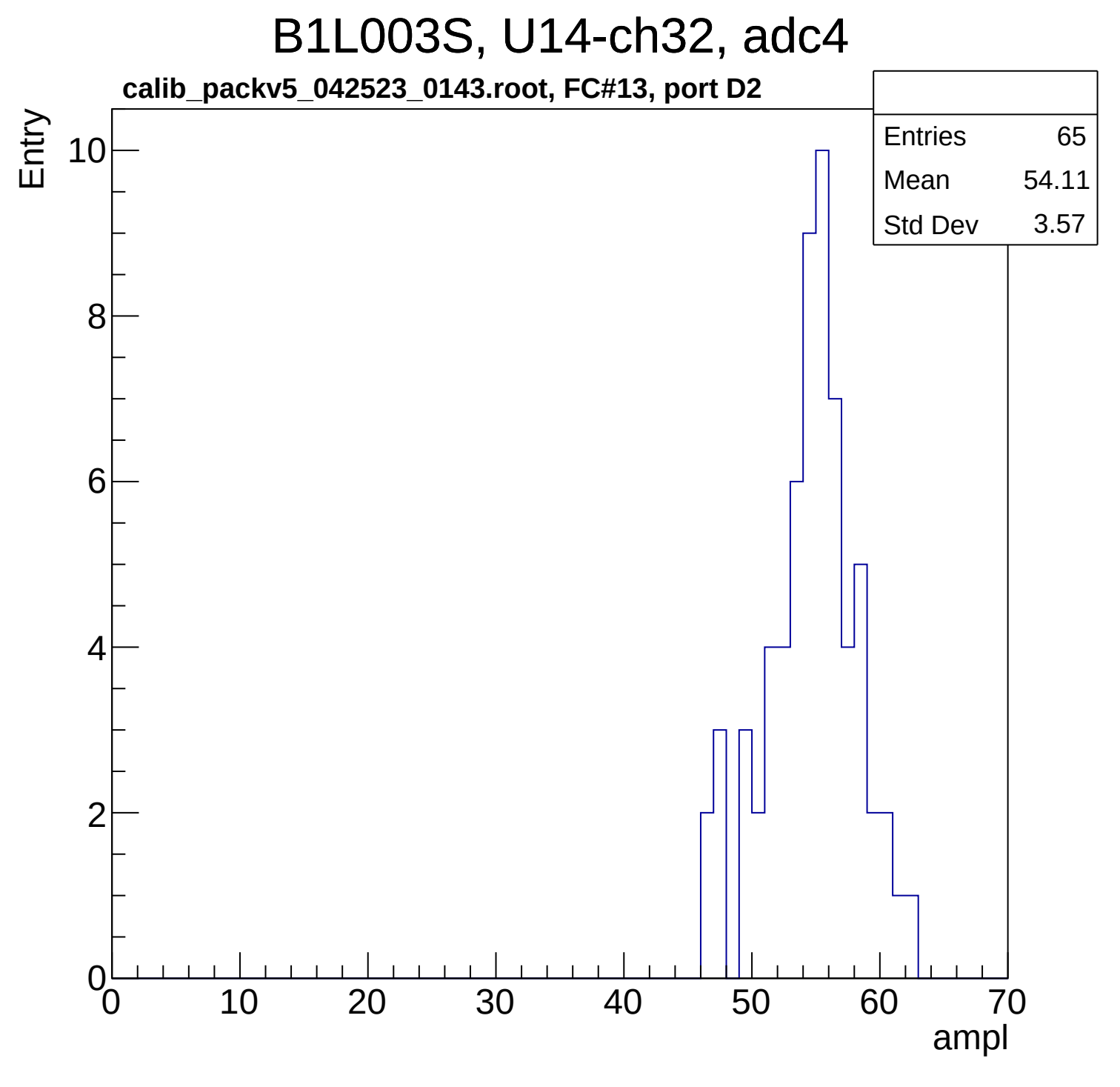

B1L003S, U14-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

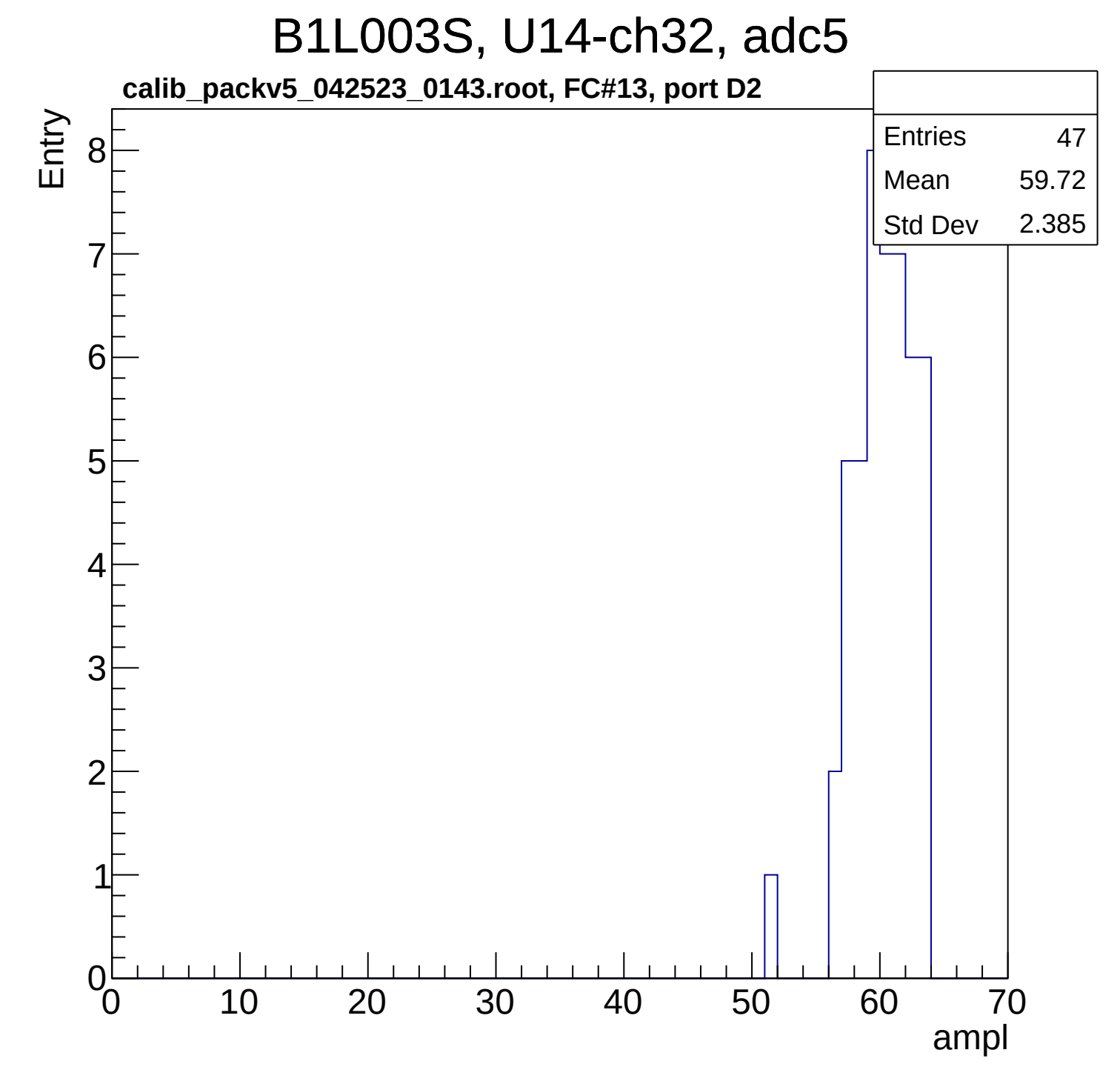

B1L003S, U14-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.95
Std Dev 2.906
Gaus mean : 29.8289
Gaus Width: 3.2780
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

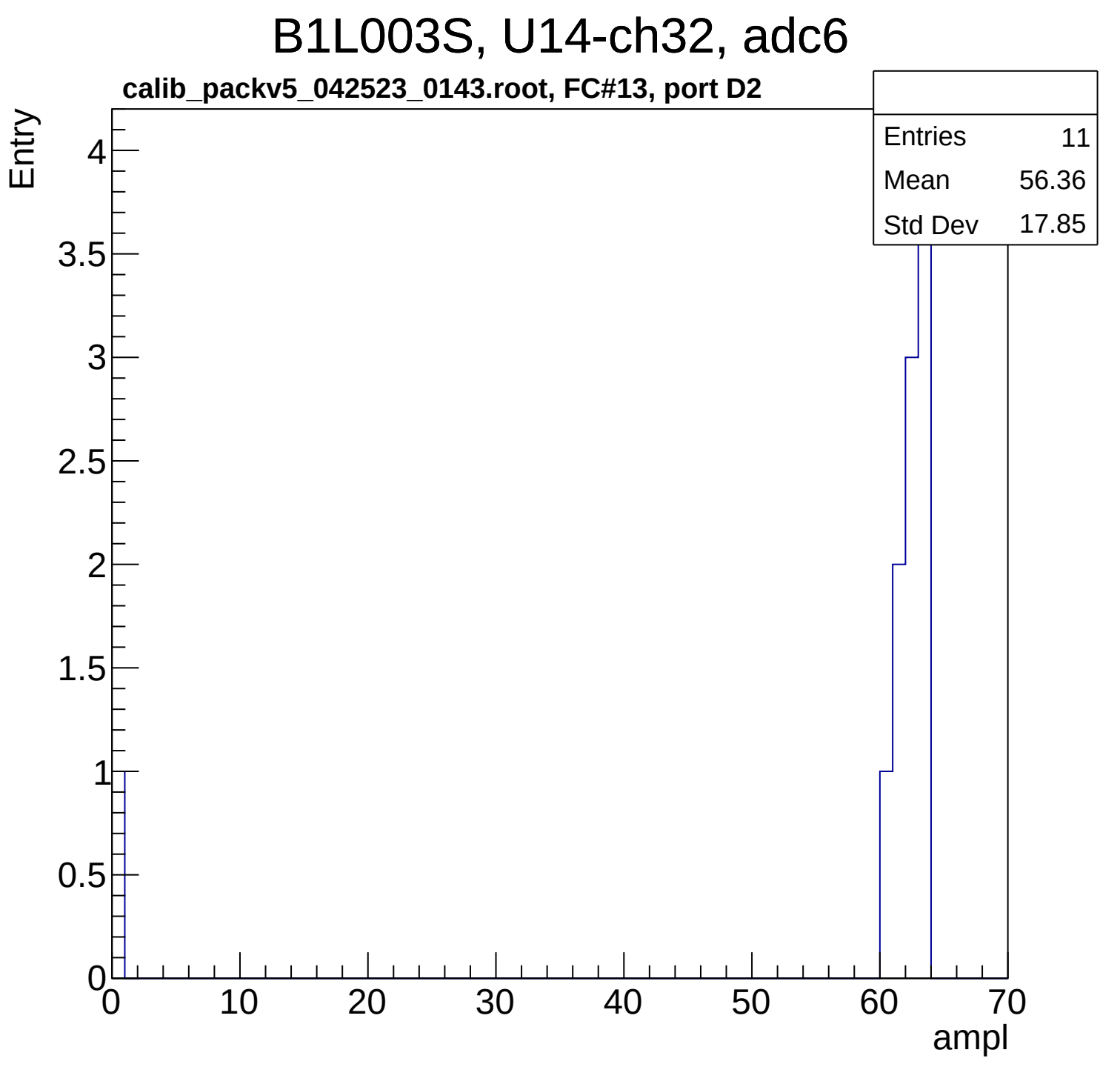

B1L003S, U14-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.96
Std Dev 3.113
Gaus mean : 36.8761
Gaus Width: 3.3549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

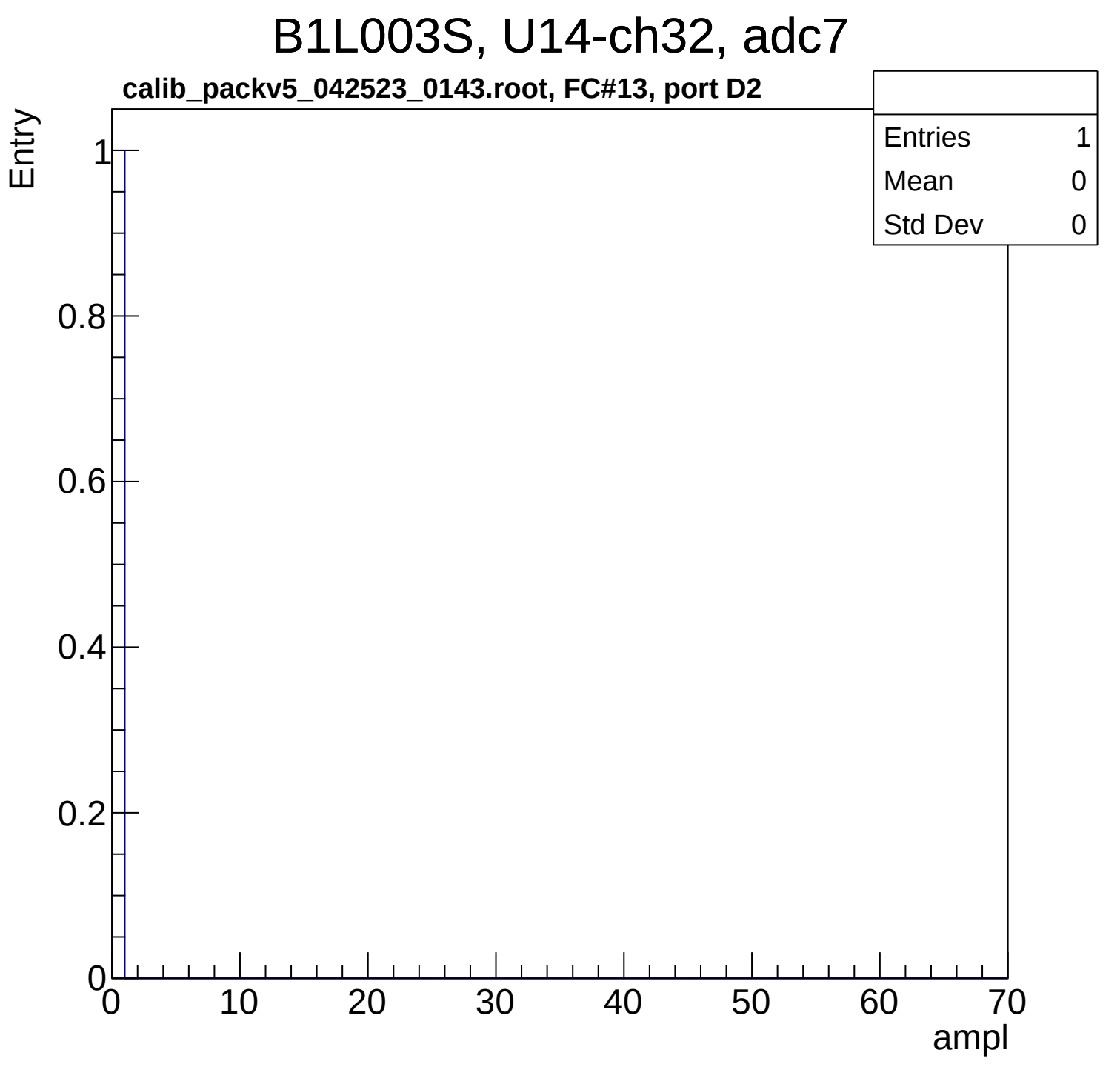

B1L003S, U14-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.09
Std Dev 3.089
Gaus mean : 43.5106
Gaus Width: 3.1808
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

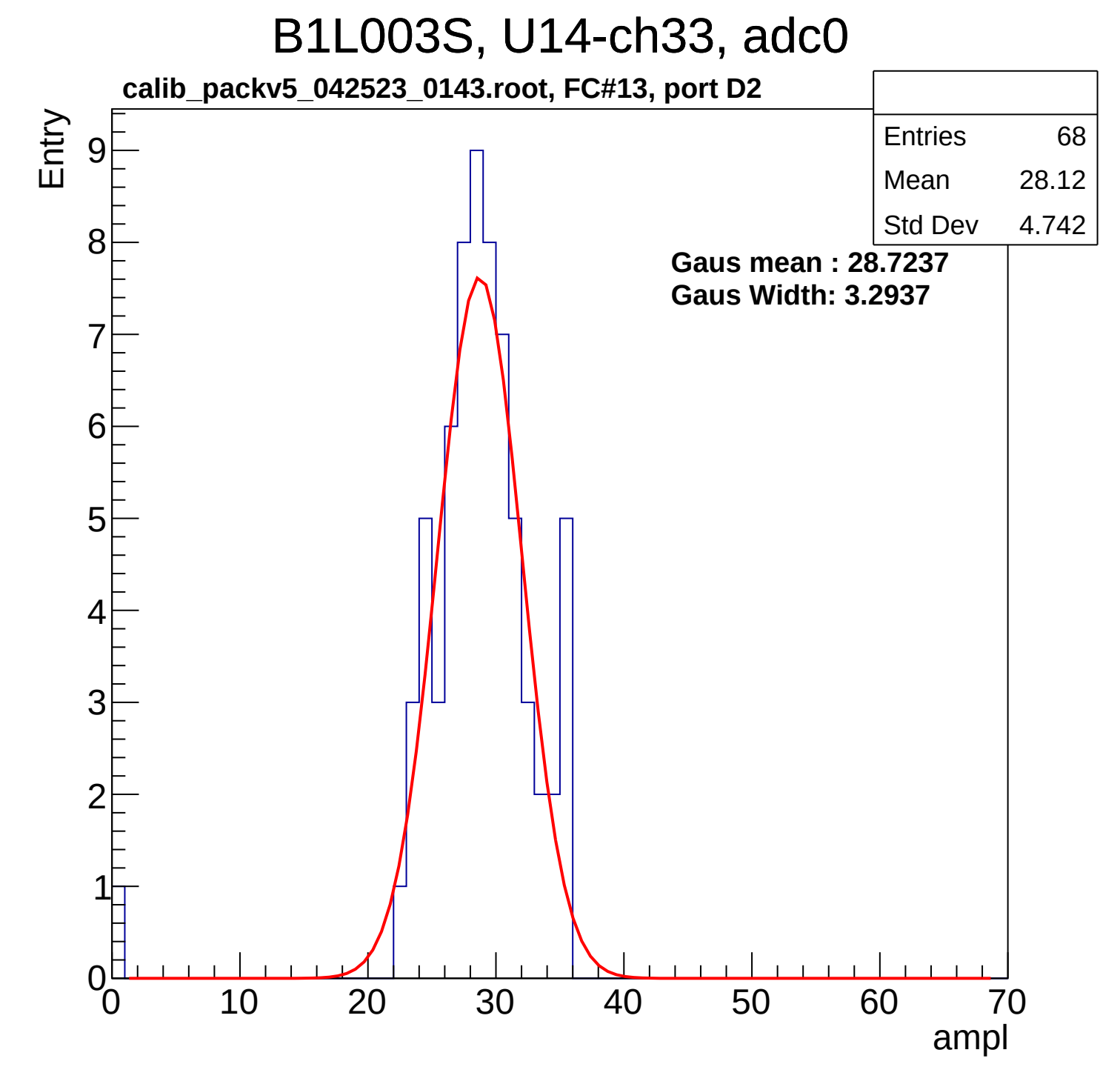

B1L003S, U14-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 49.34
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

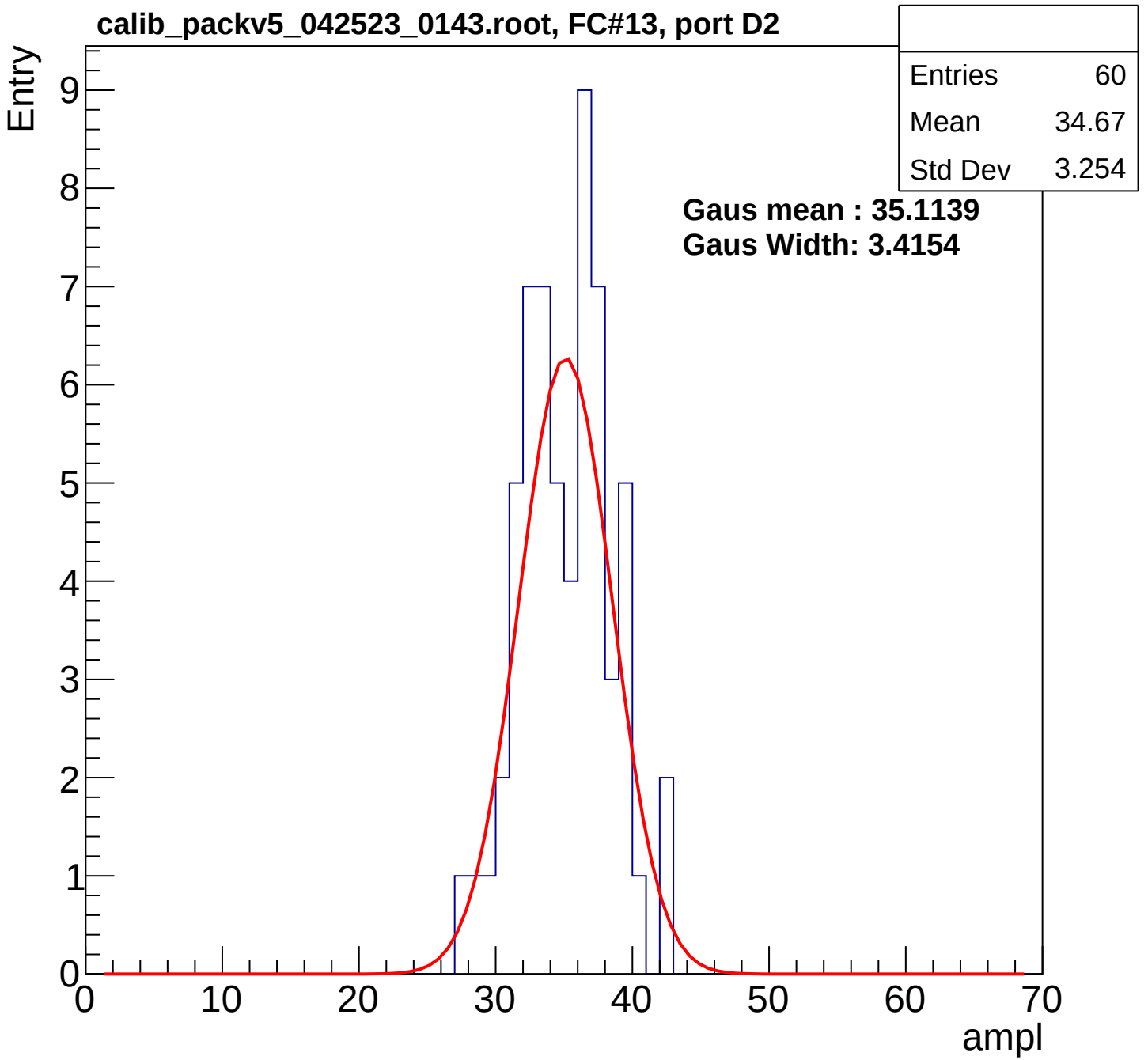

B1L003S, U14-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.11
Std Dev 3.57
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

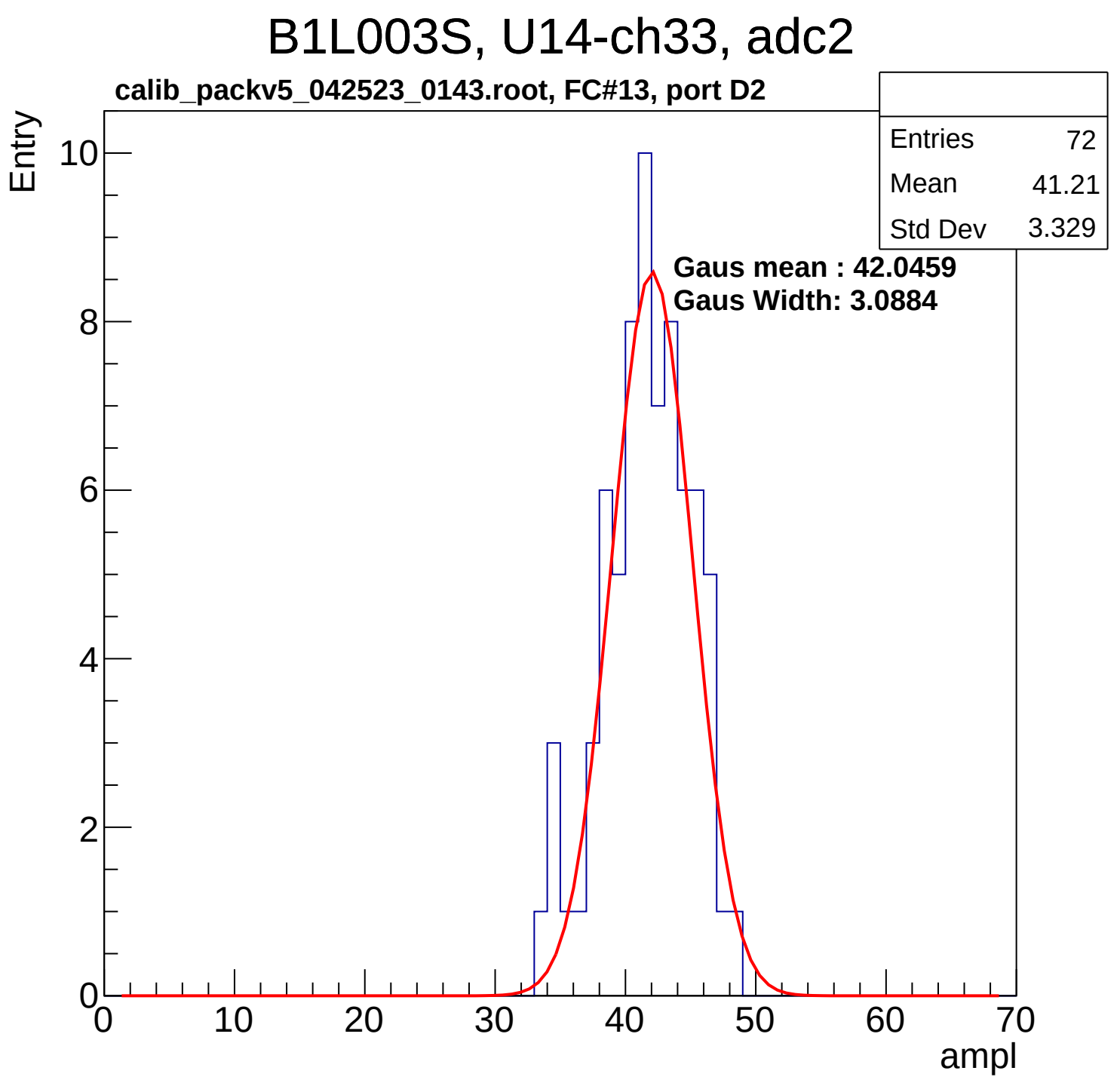

B1L003S, U14-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.72
Std Dev 2.385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

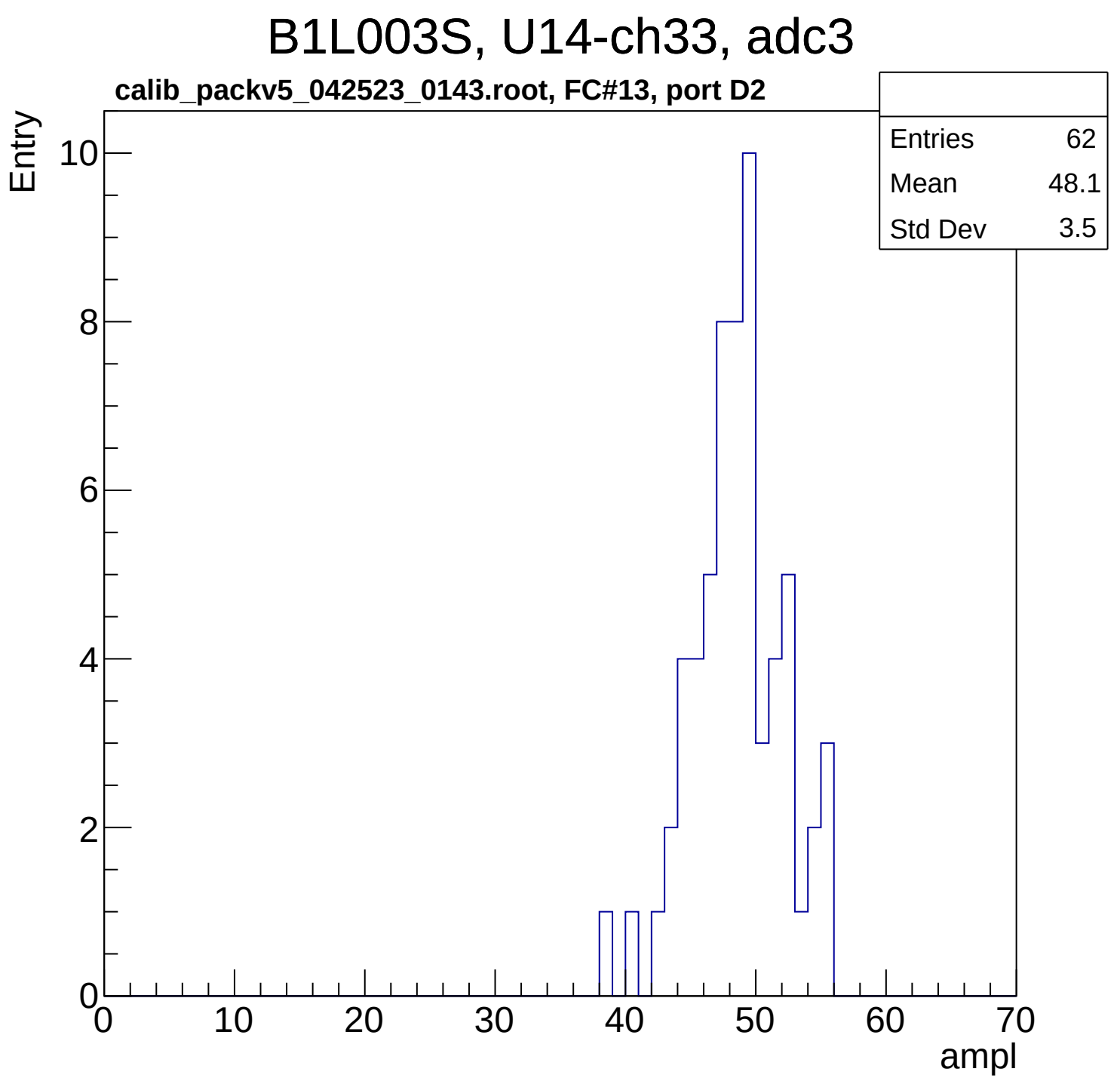

B1L003S, U14-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.36
Std Dev 17.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

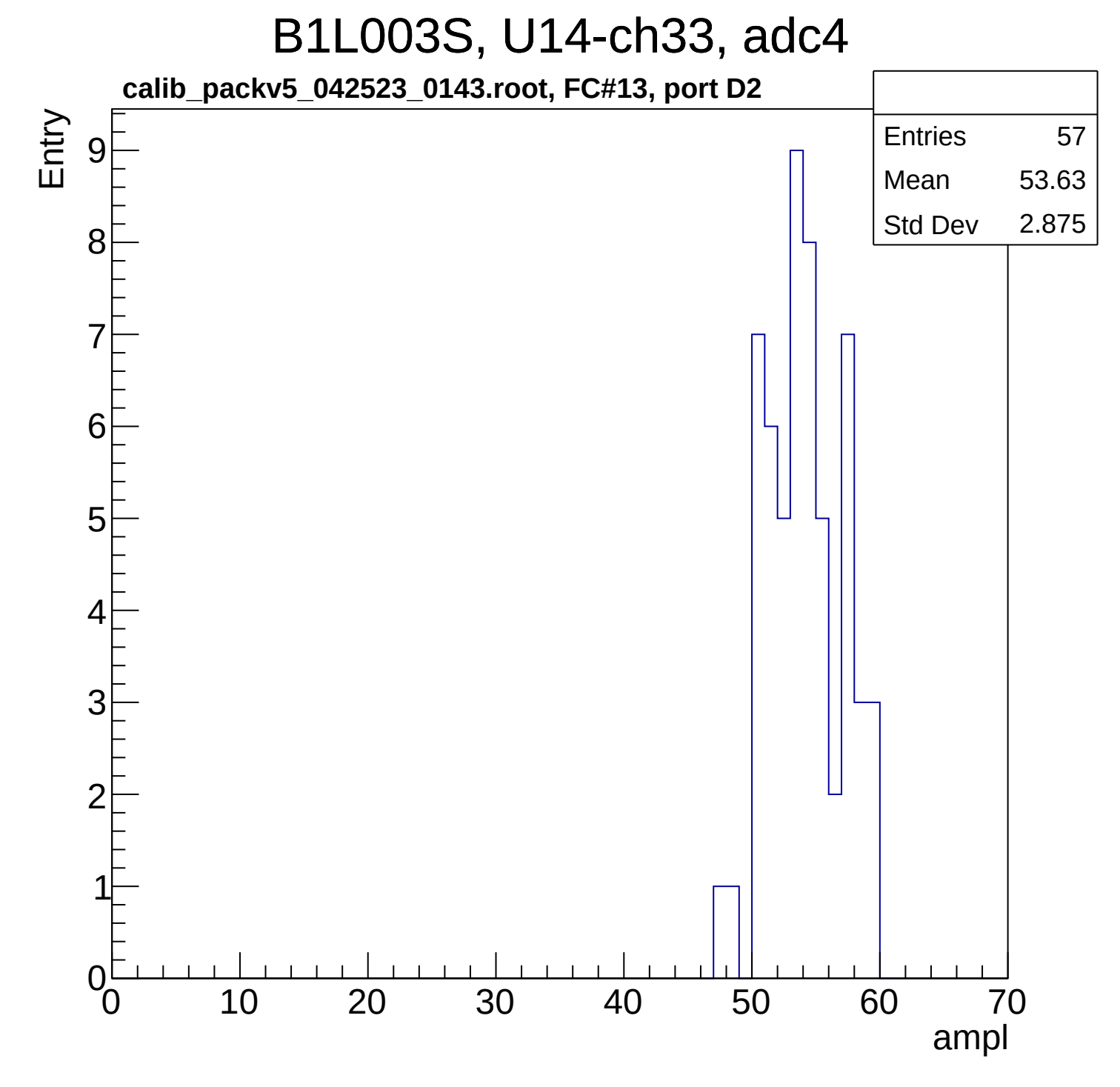

B1L003S, U14-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

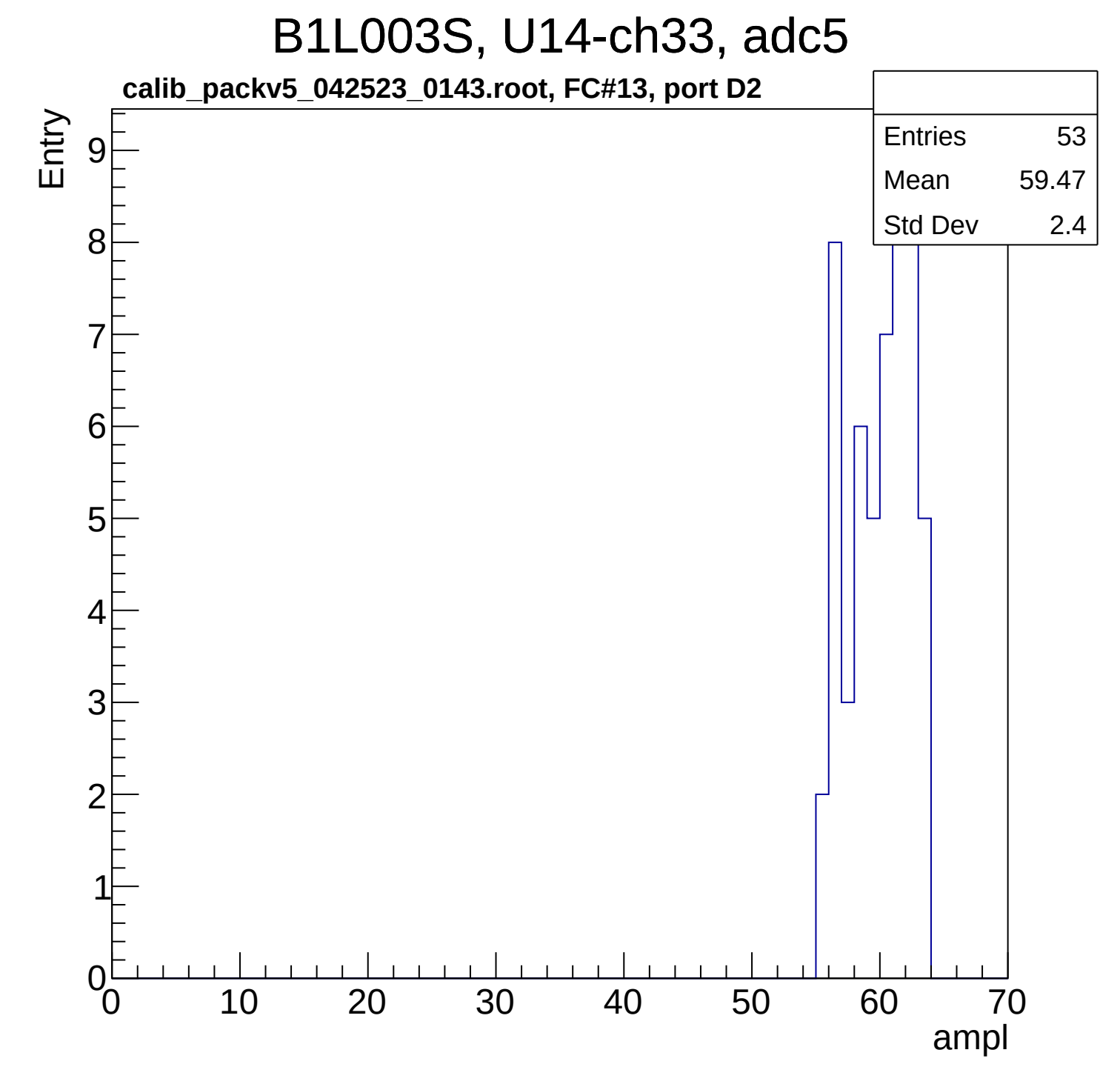

B1L003S, U14-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.12
Std Dev 4.742
Gaus mean : 28.7237
Gaus Width: 3.2937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

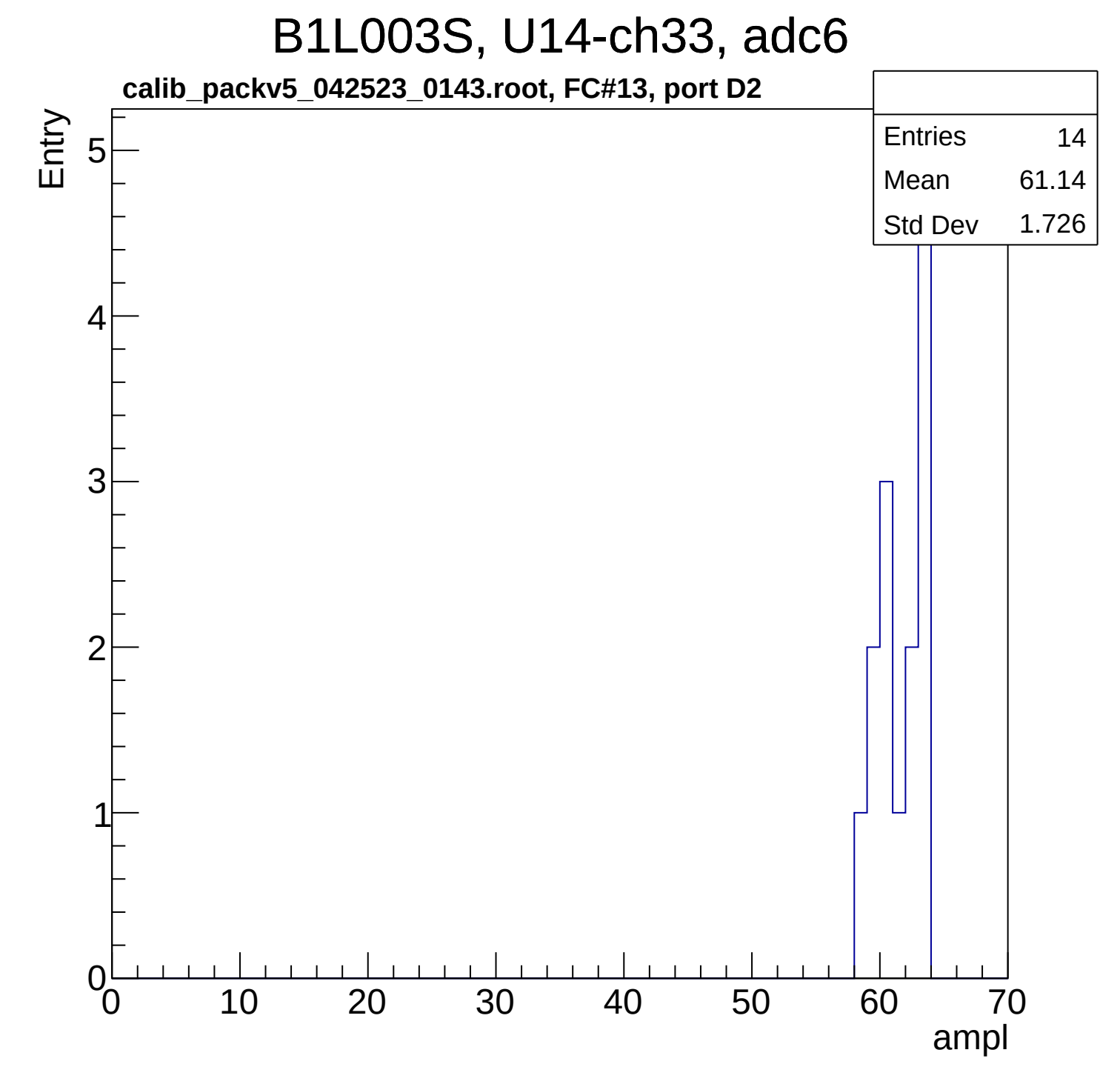

B1L003S, U14-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 34.67
Std Dev 3.254
Gaus mean : 35.1139
Gaus Width: 3.4154
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

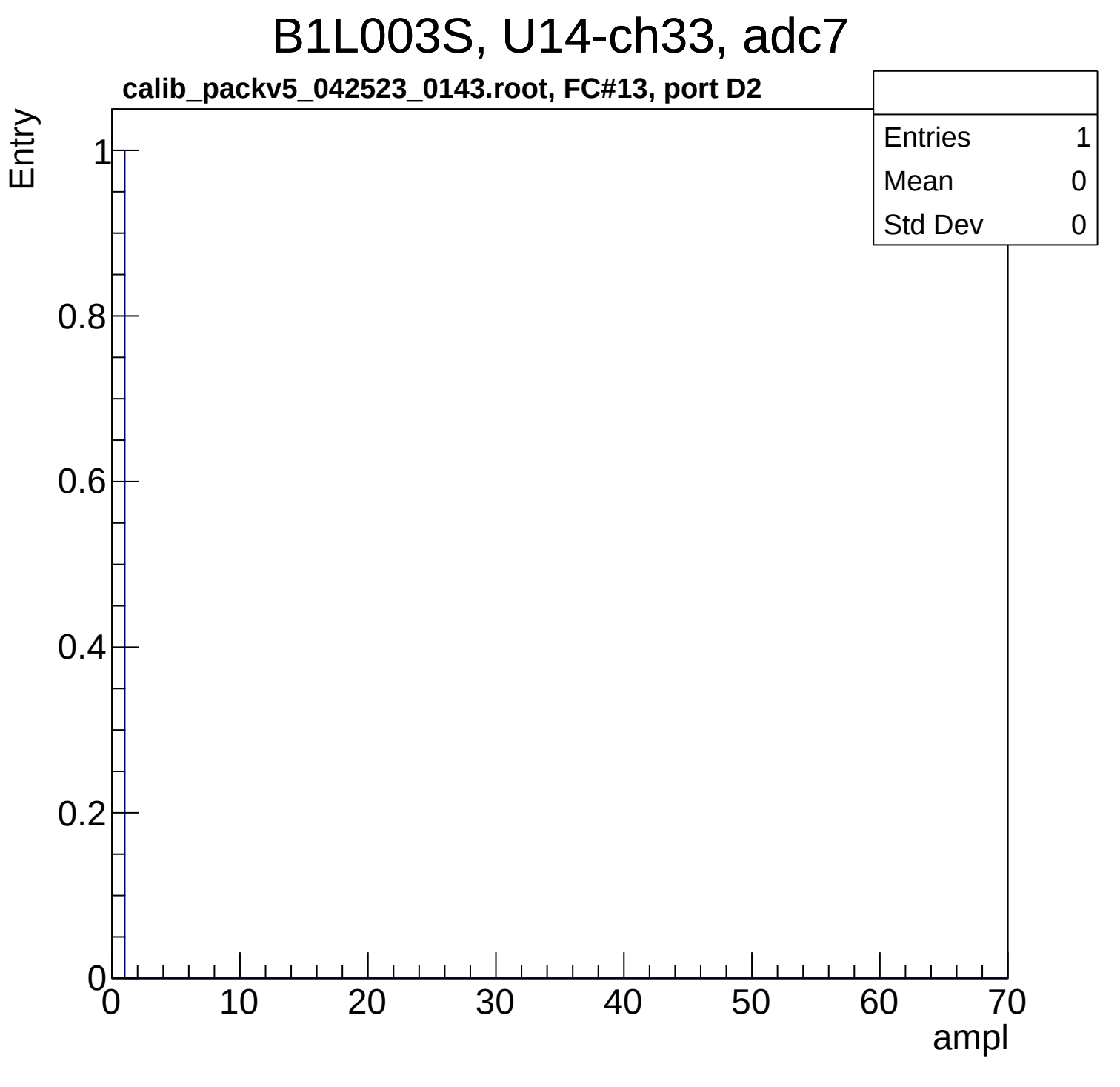

B1L003S, U14-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 41.21
Std Dev 3.329
Gaus mean : 42.0459
Gaus Width: 3.0884
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

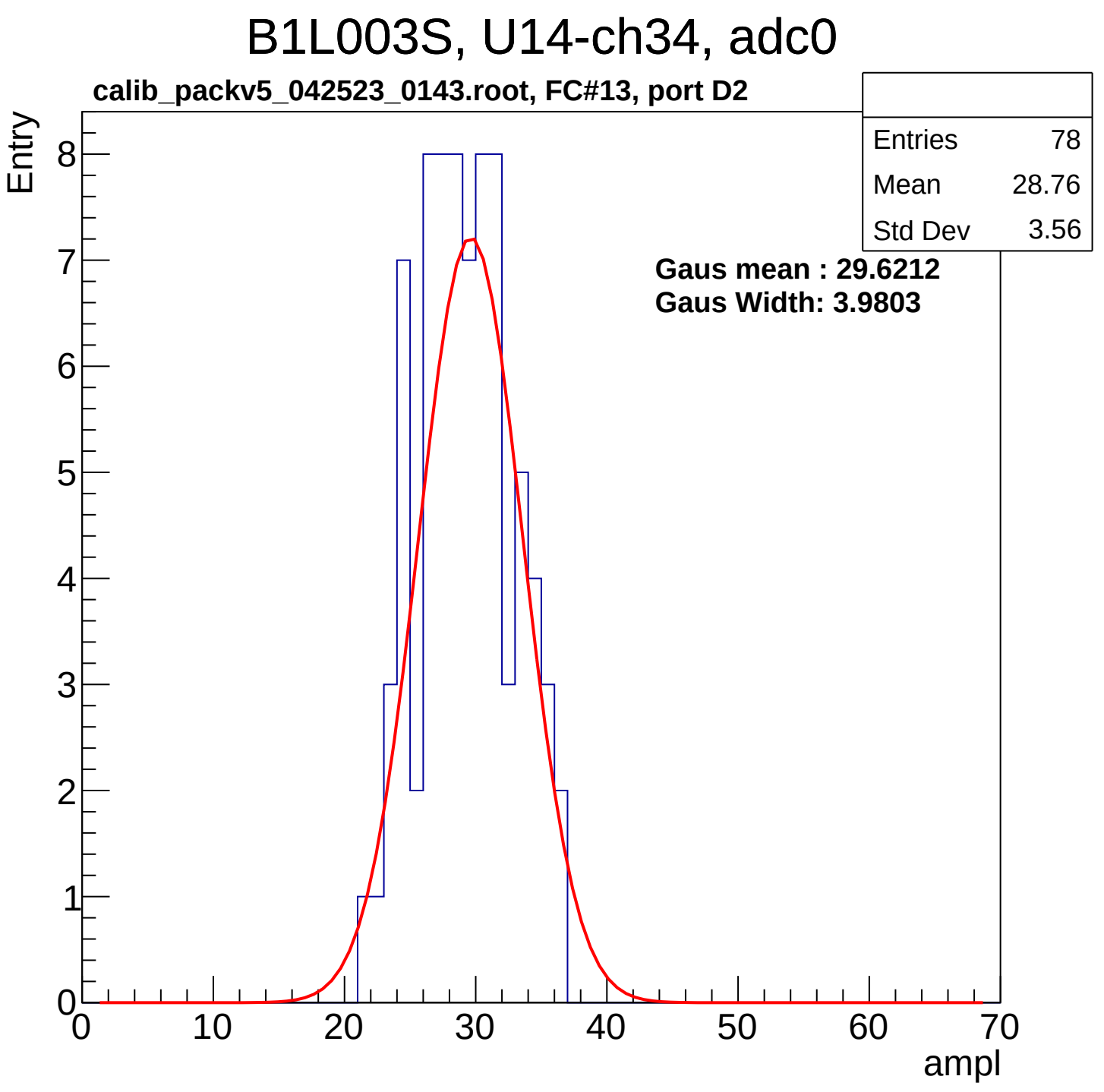

B1L003S, U14-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.1
Std Dev 3.5
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

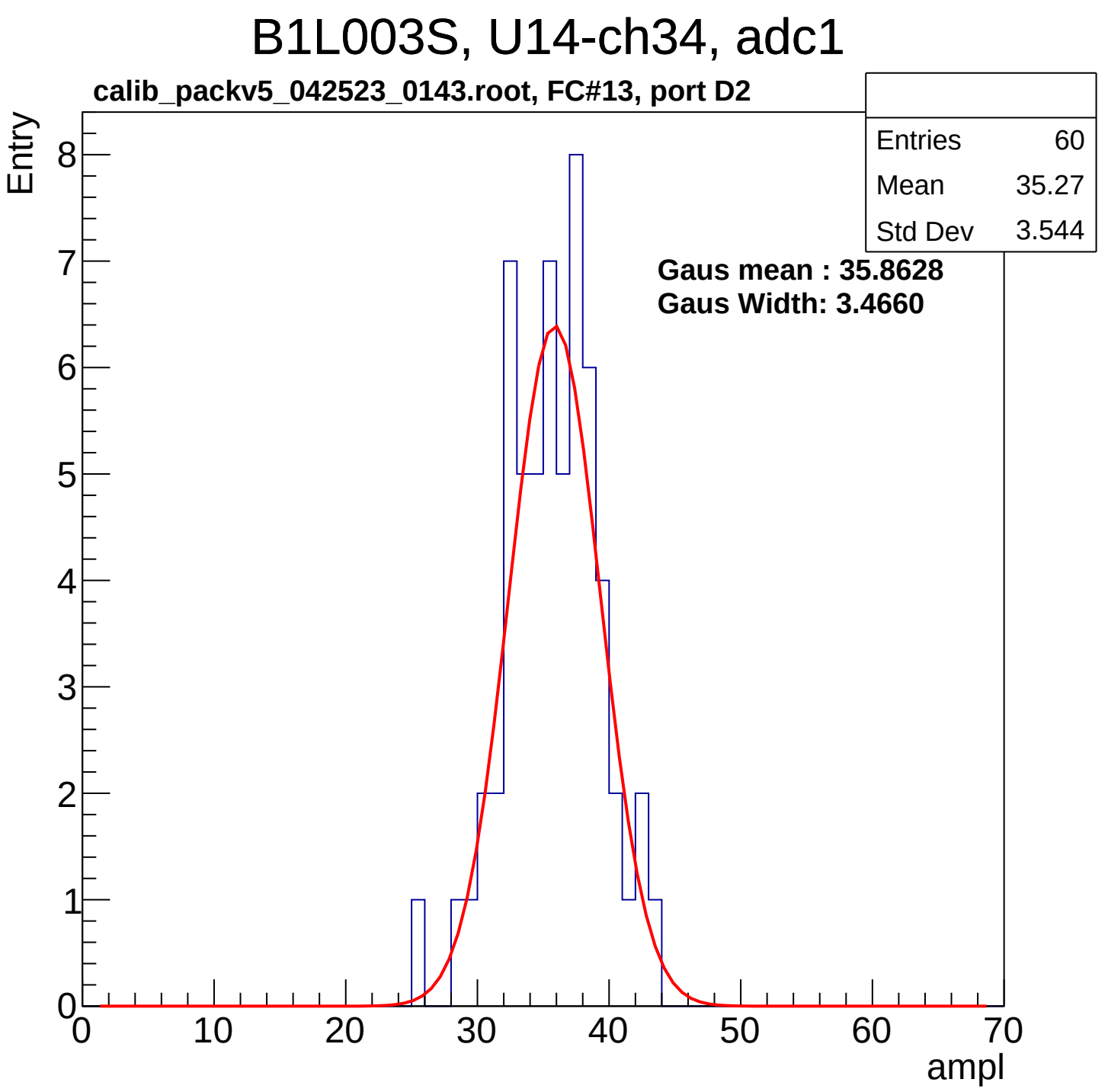

B1L003S, U14-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.63
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

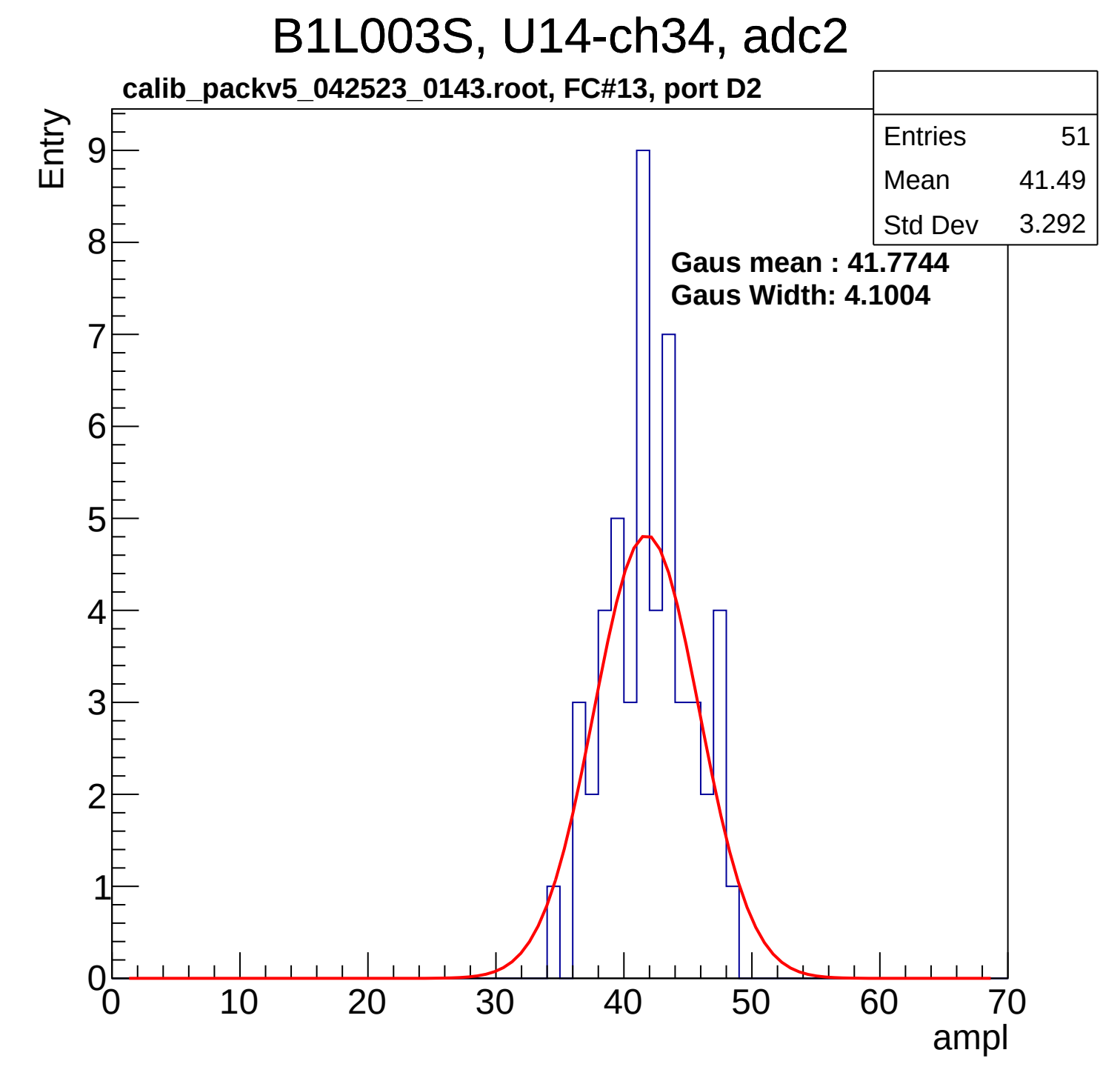

B1L003S, U14-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.47
Std Dev 2.4
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

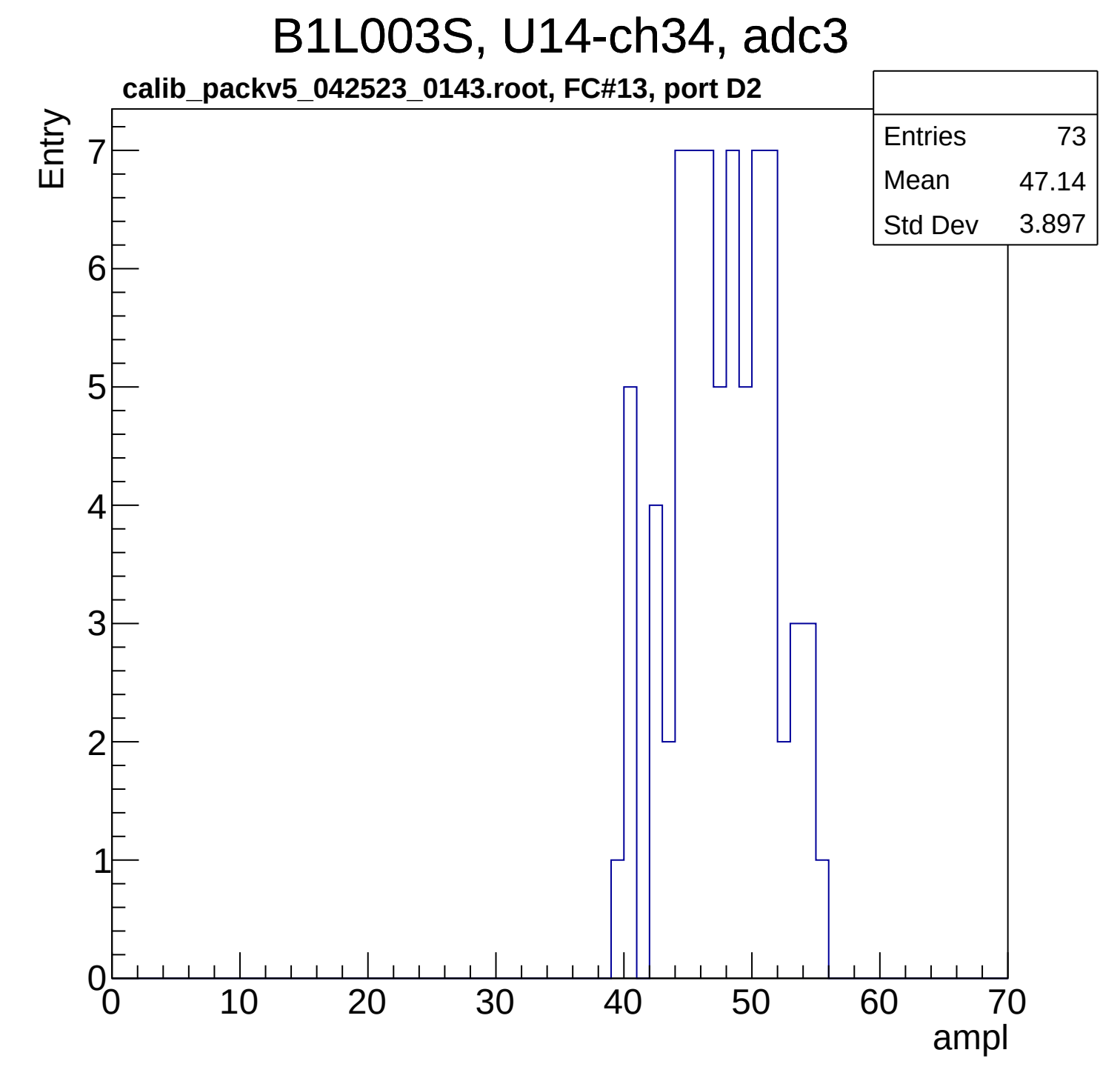

B1L003S, U14-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.14
Std Dev 1.726
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

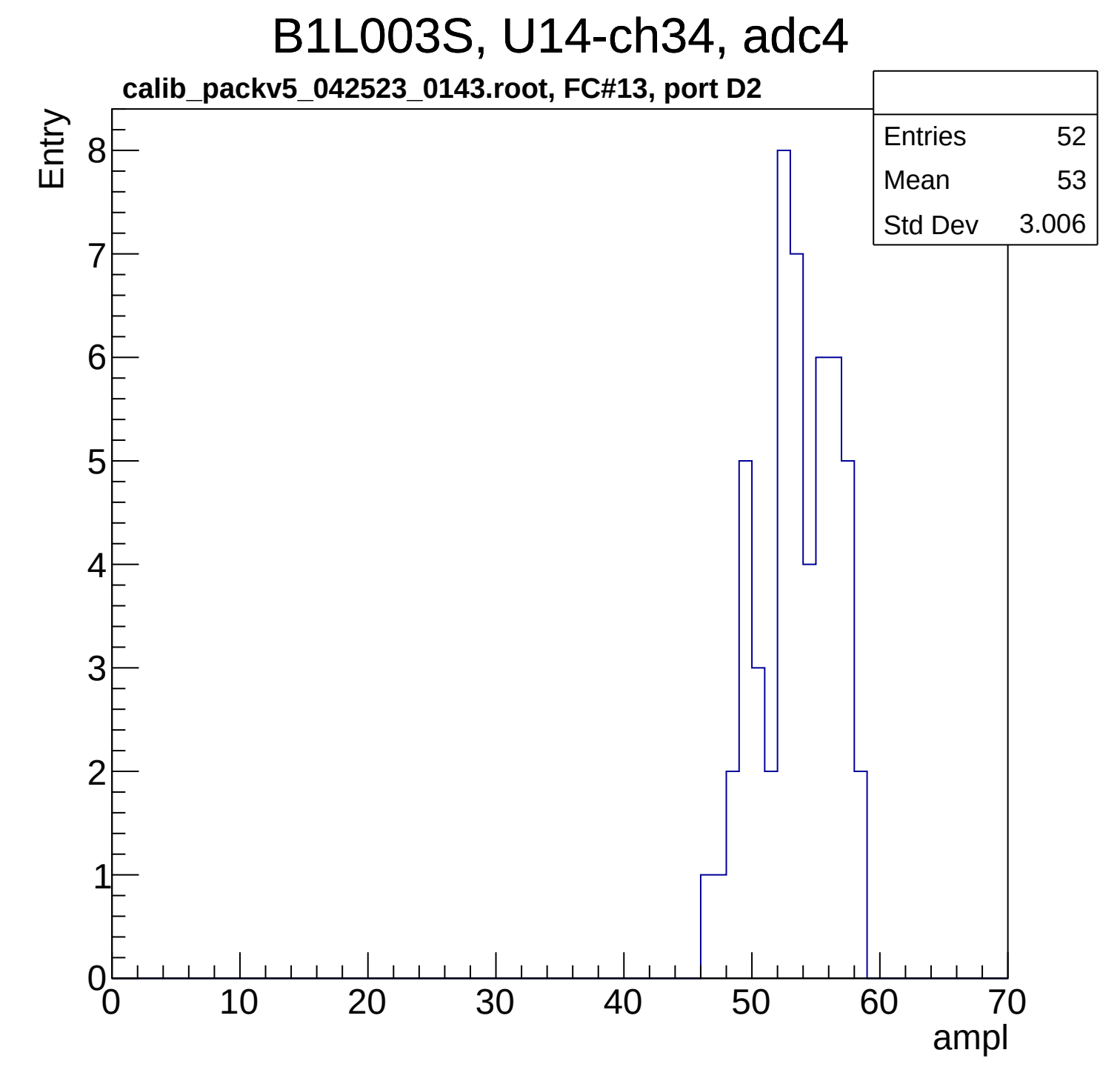

B1L003S, U14-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

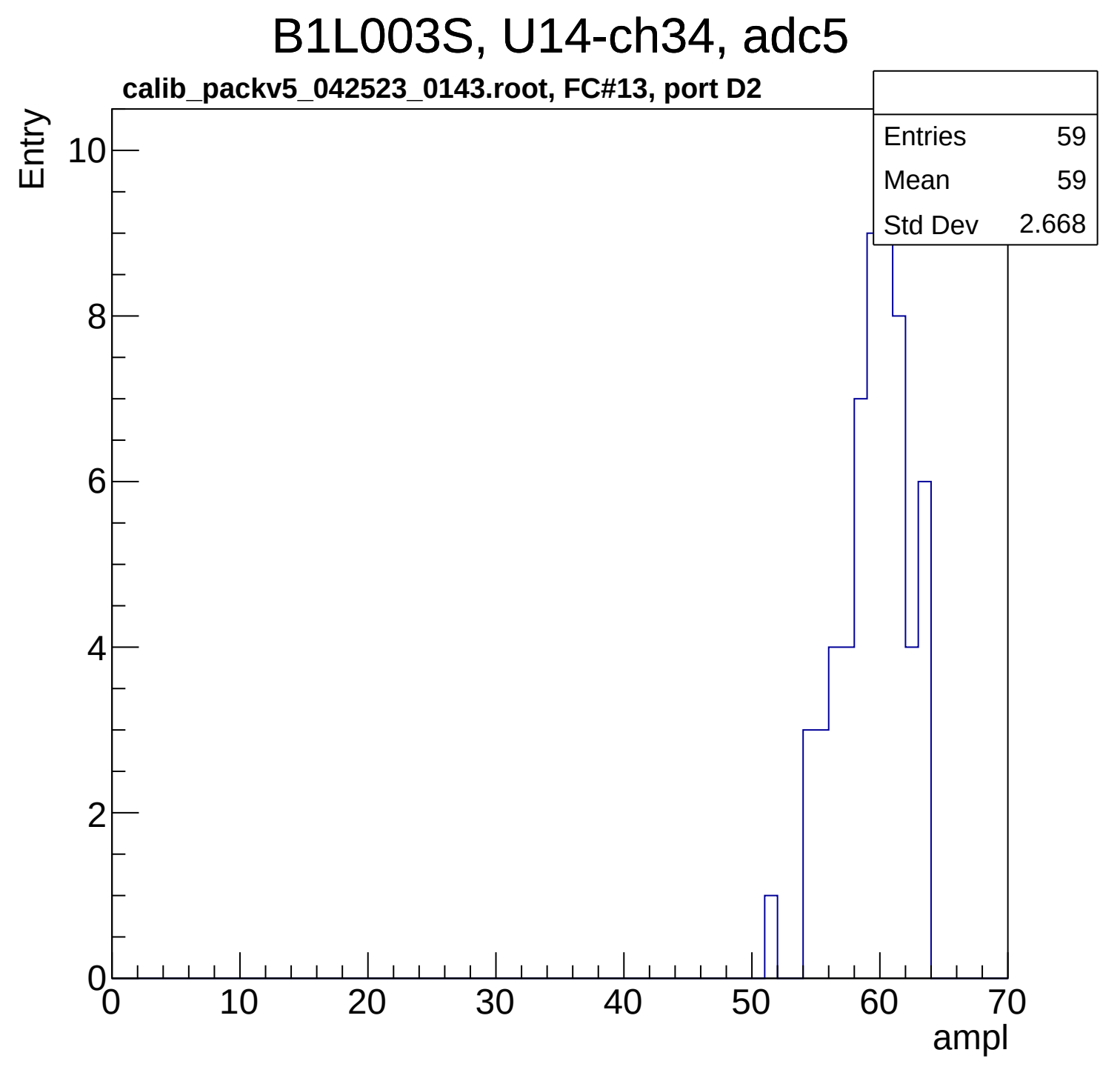

B1L003S, U14-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 28.76
Std Dev 3.56
Gaus mean : 29.6212
Gaus Width: 3.9803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

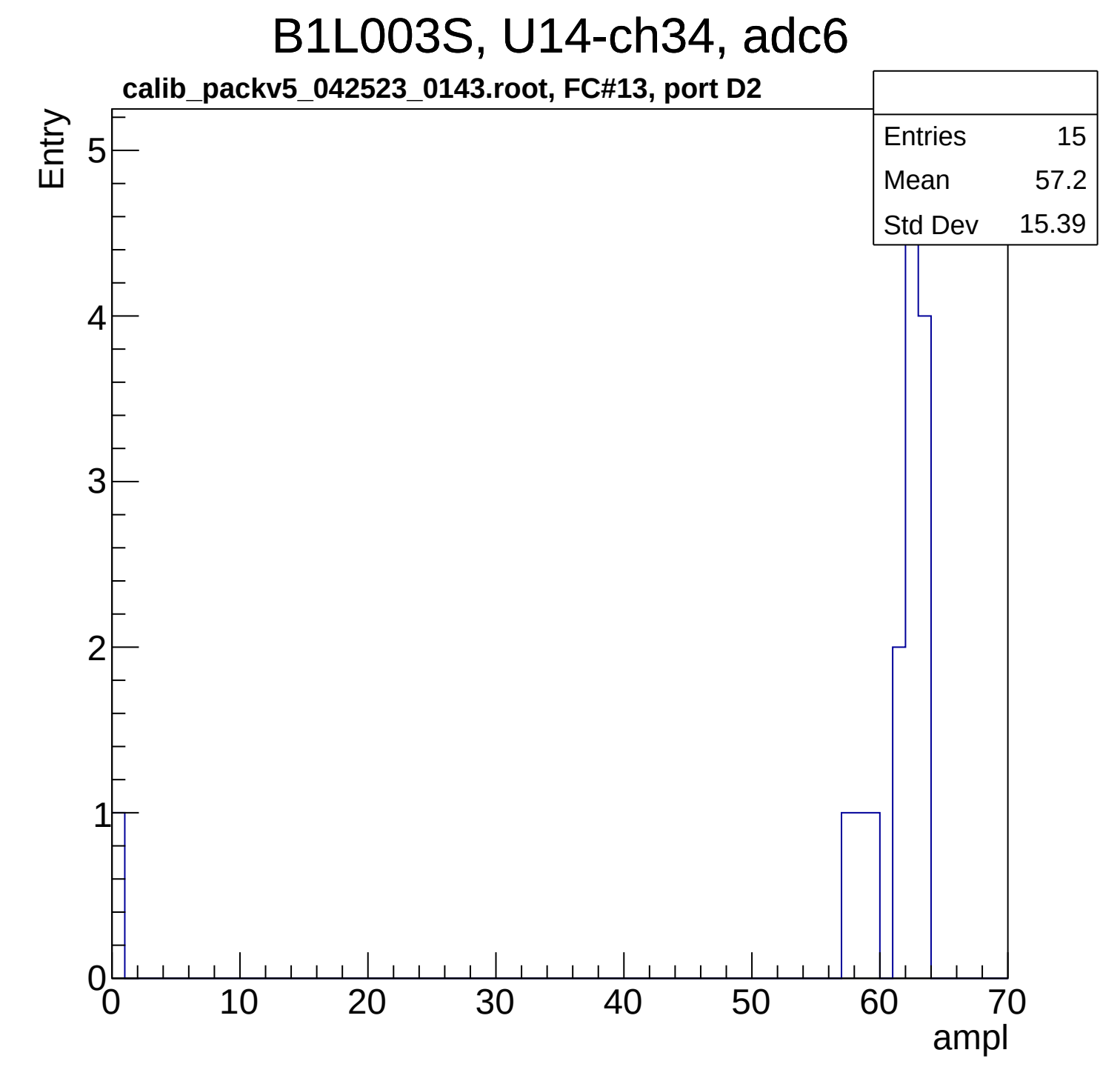

B1L003S, U14-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.27
Std Dev 3.544
Gaus mean : 35.8628
Gaus Width: 3.4660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

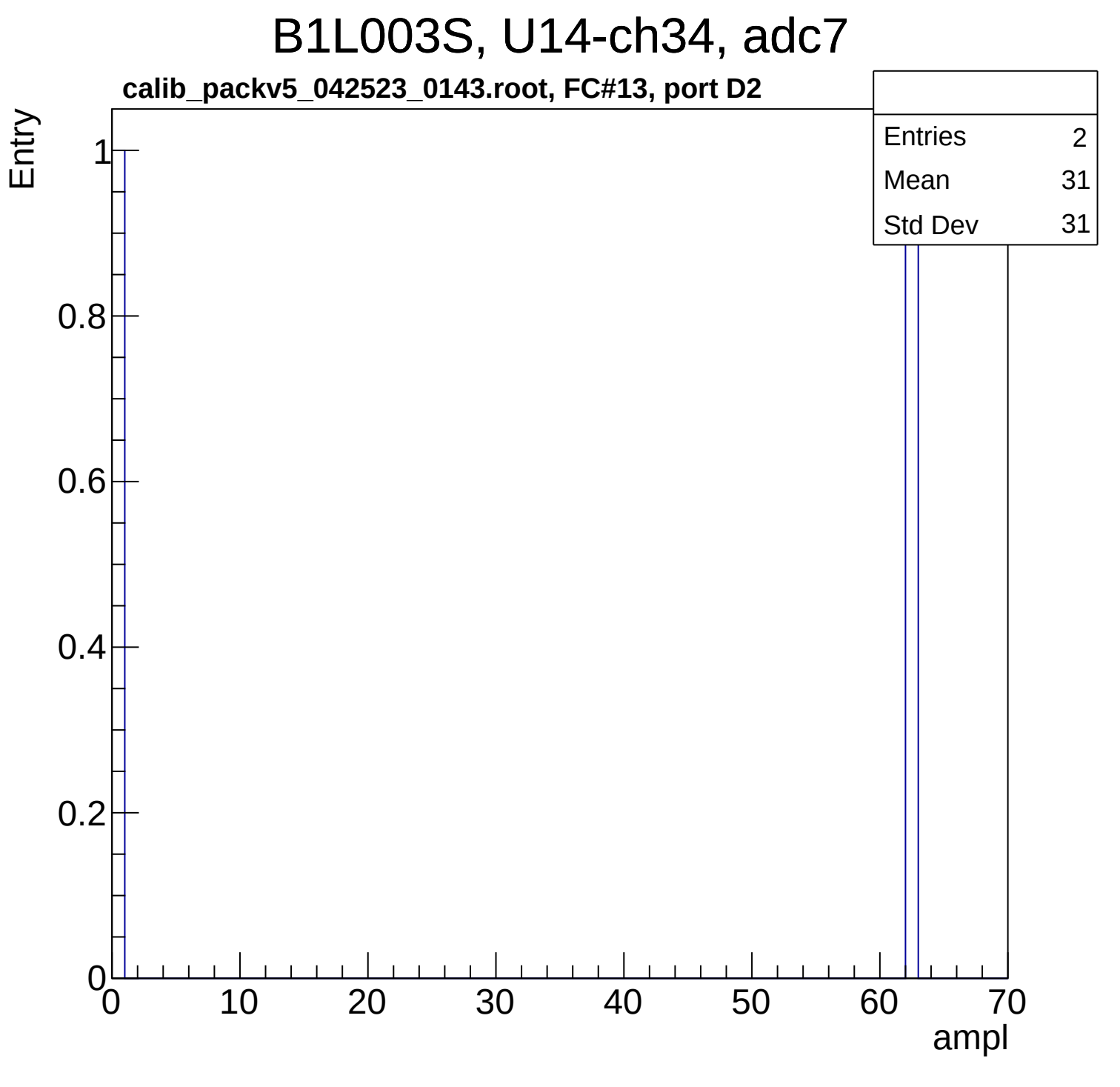

B1L003S, U14-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.49
Std Dev 3.292
Gaus mean : 41.7744
Gaus Width: 4.1004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

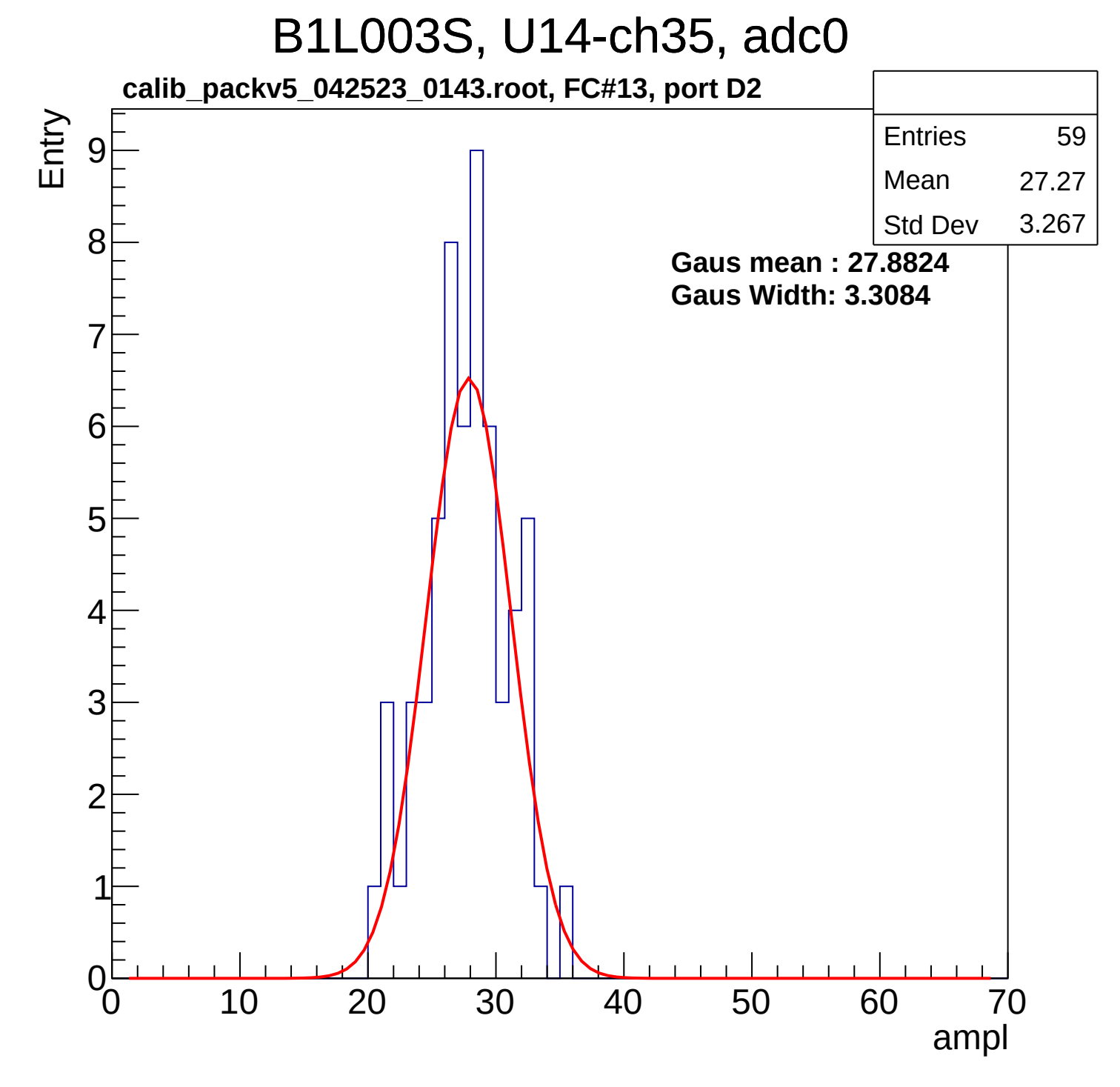

B1L003S, U14-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.14
Std Dev 3.897
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

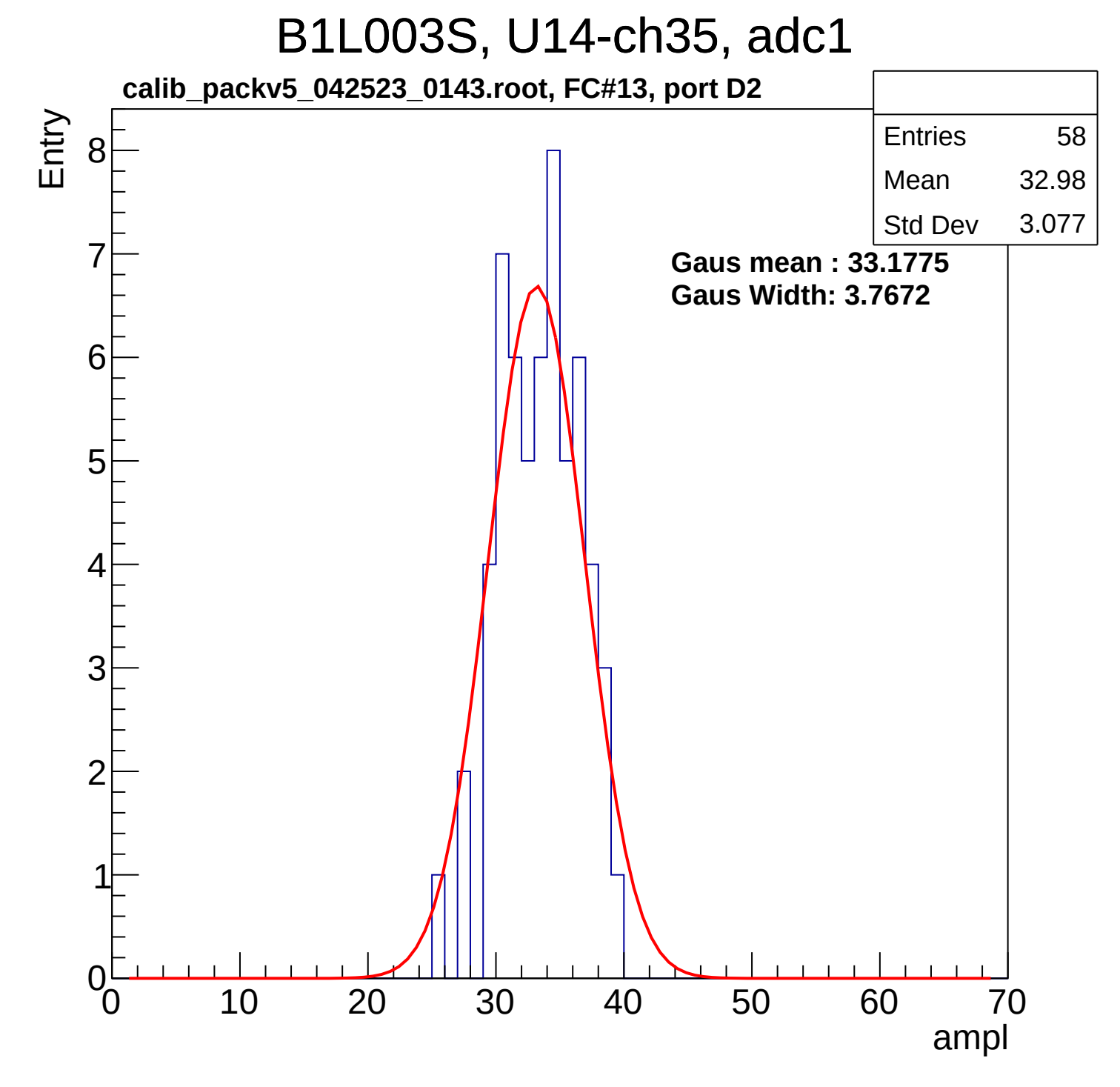

B1L003S, U14-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

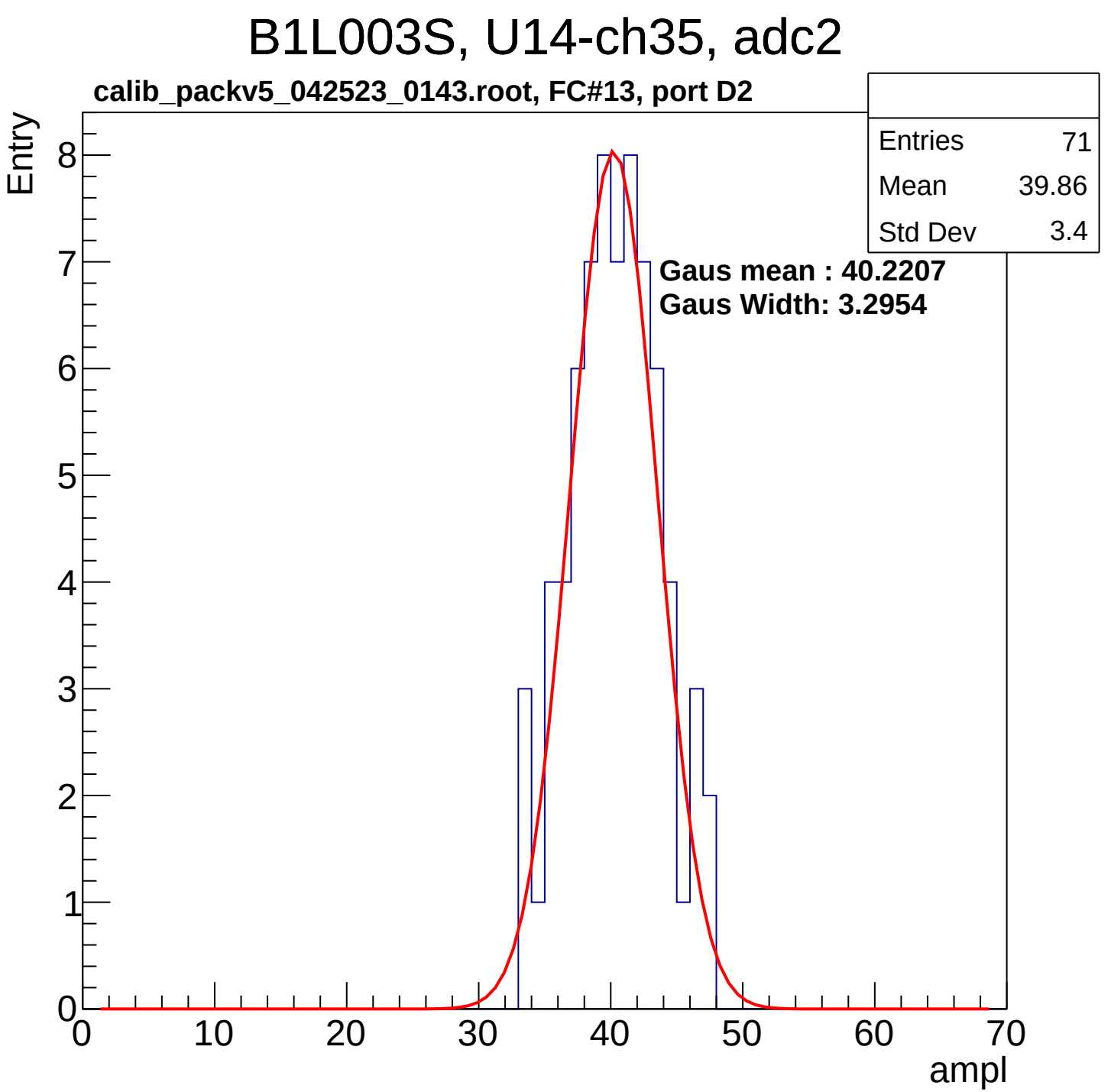

B1L003S, U14-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 59
Std Dev 2.668
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

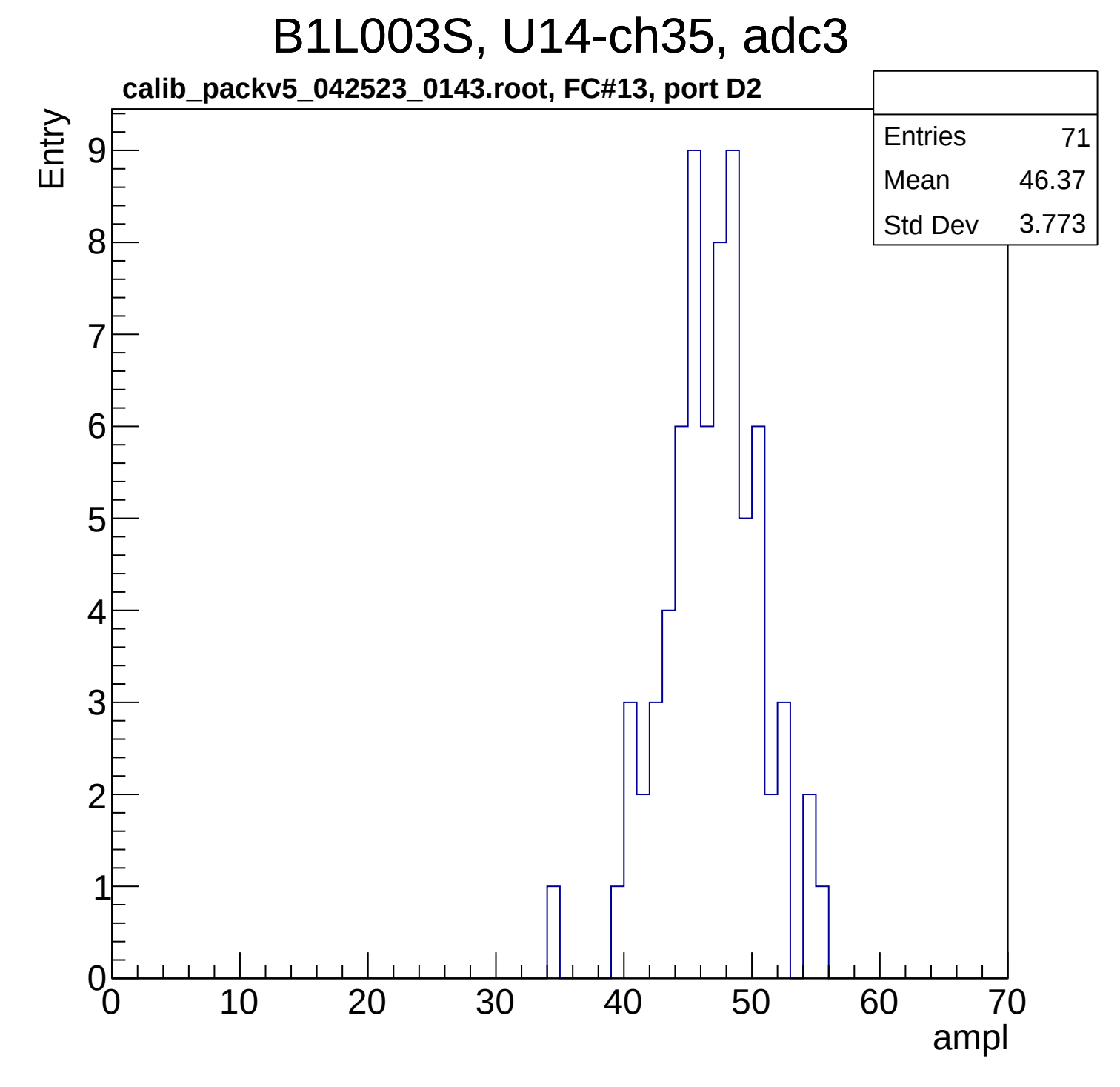

B1L003S, U14-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.2
Std Dev 15.39
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

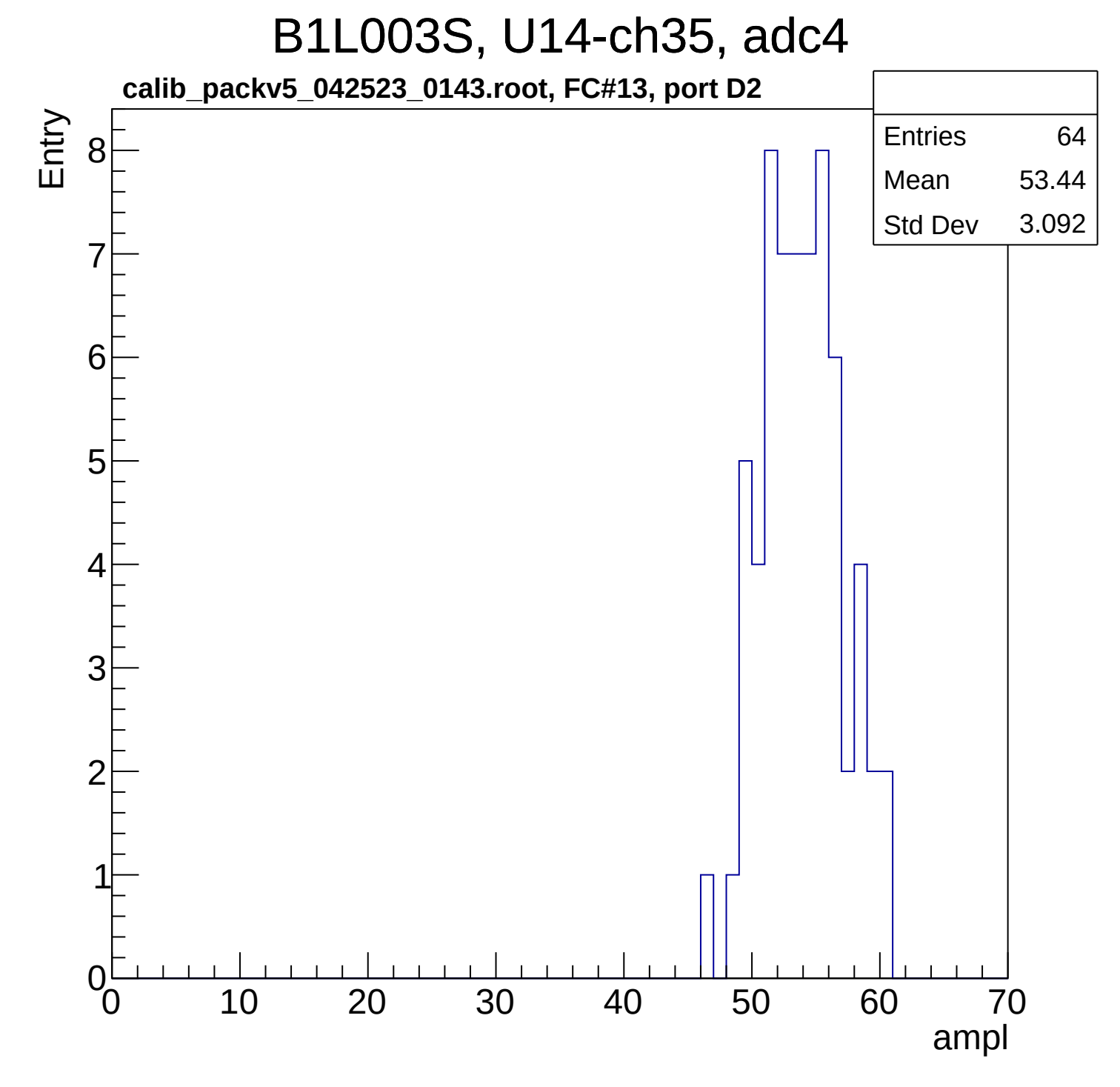

B1L003S, U14-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

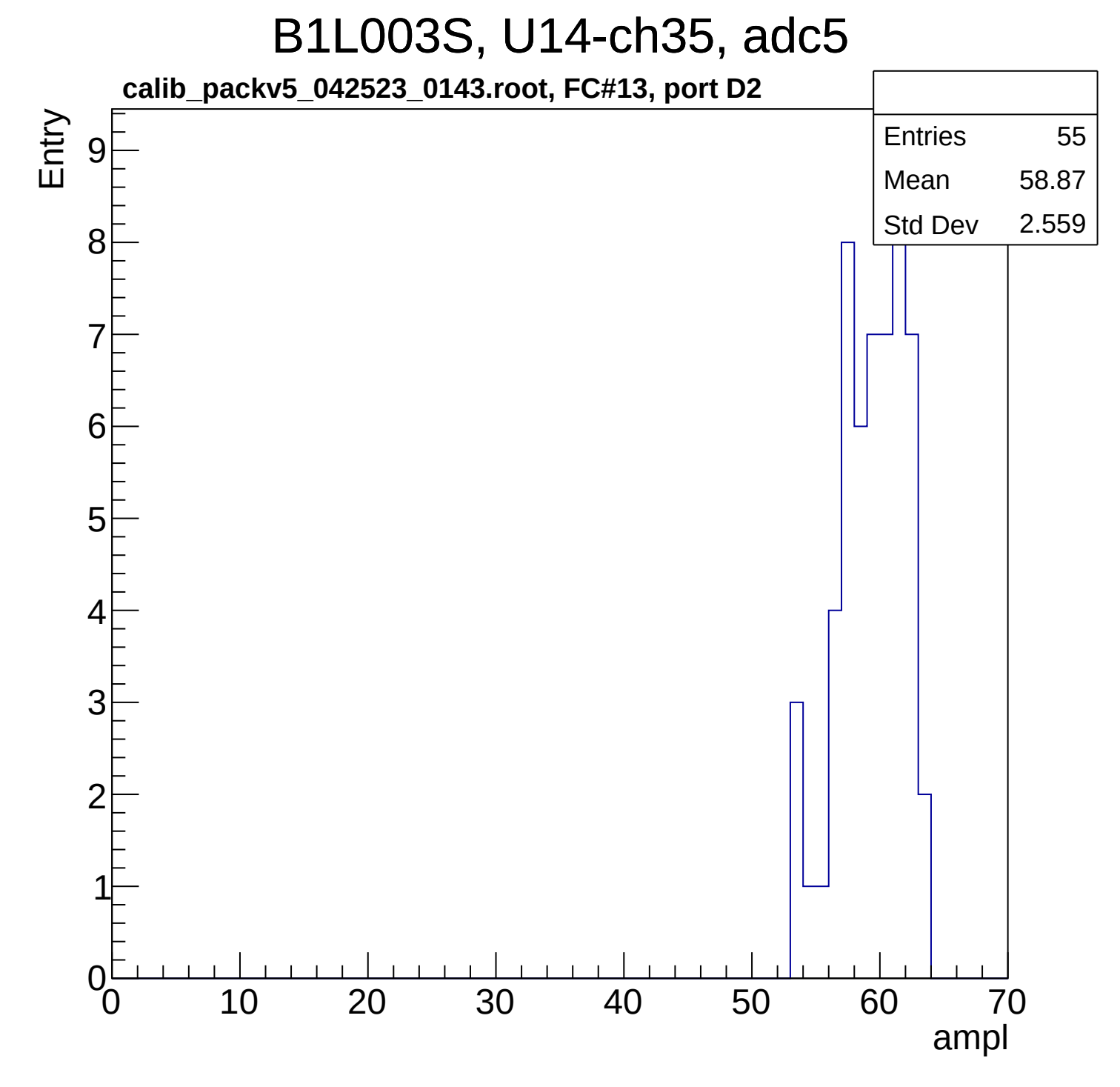

B1L003S, U14-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 27.27
Std Dev 3.267
Gaus mean : 27.8824
Gaus Width: 3.3084
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

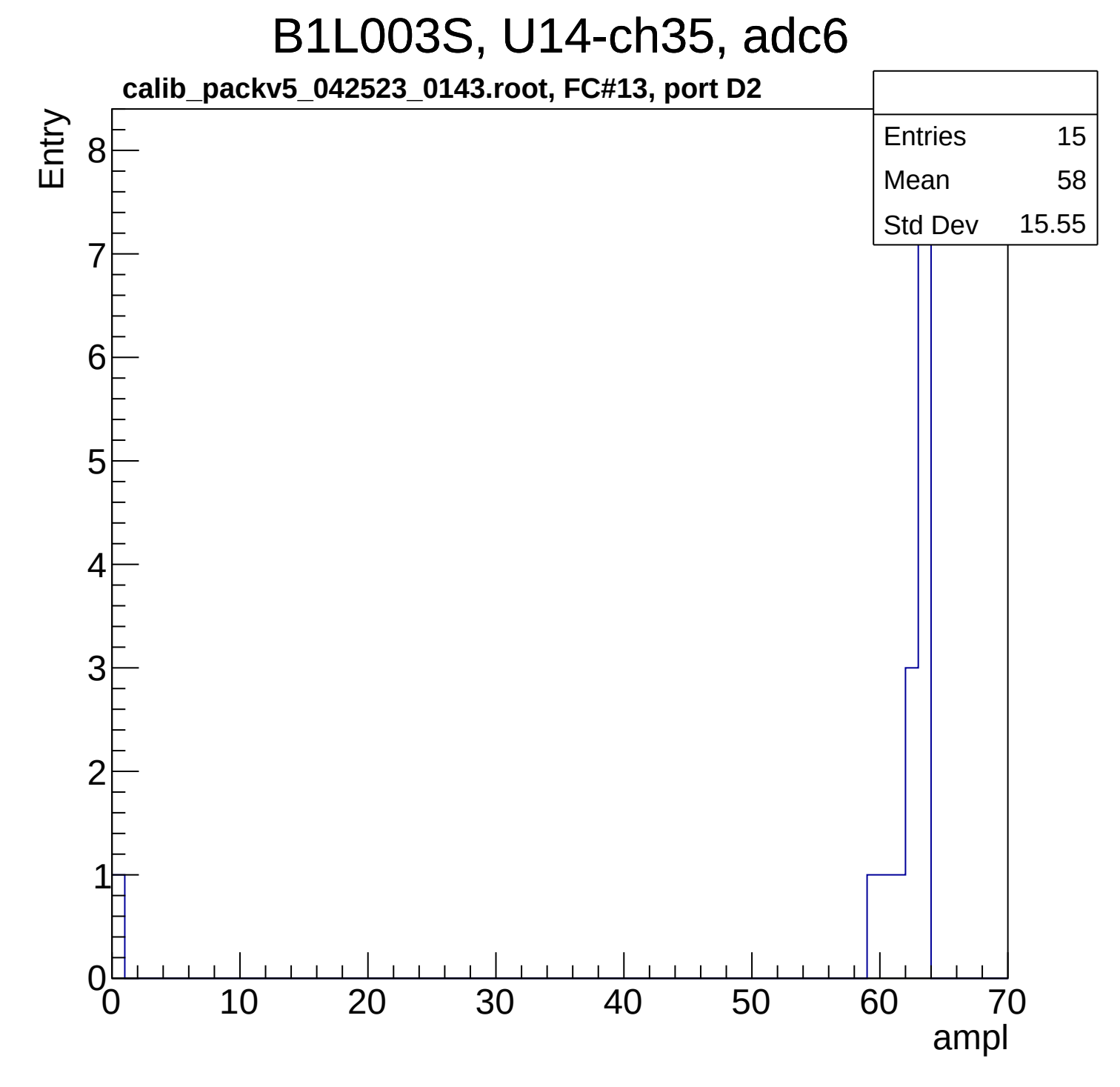

B1L003S, U14-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 32.98
Std Dev 3.077
Gaus mean : 33.1775
Gaus Width: 3.7672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

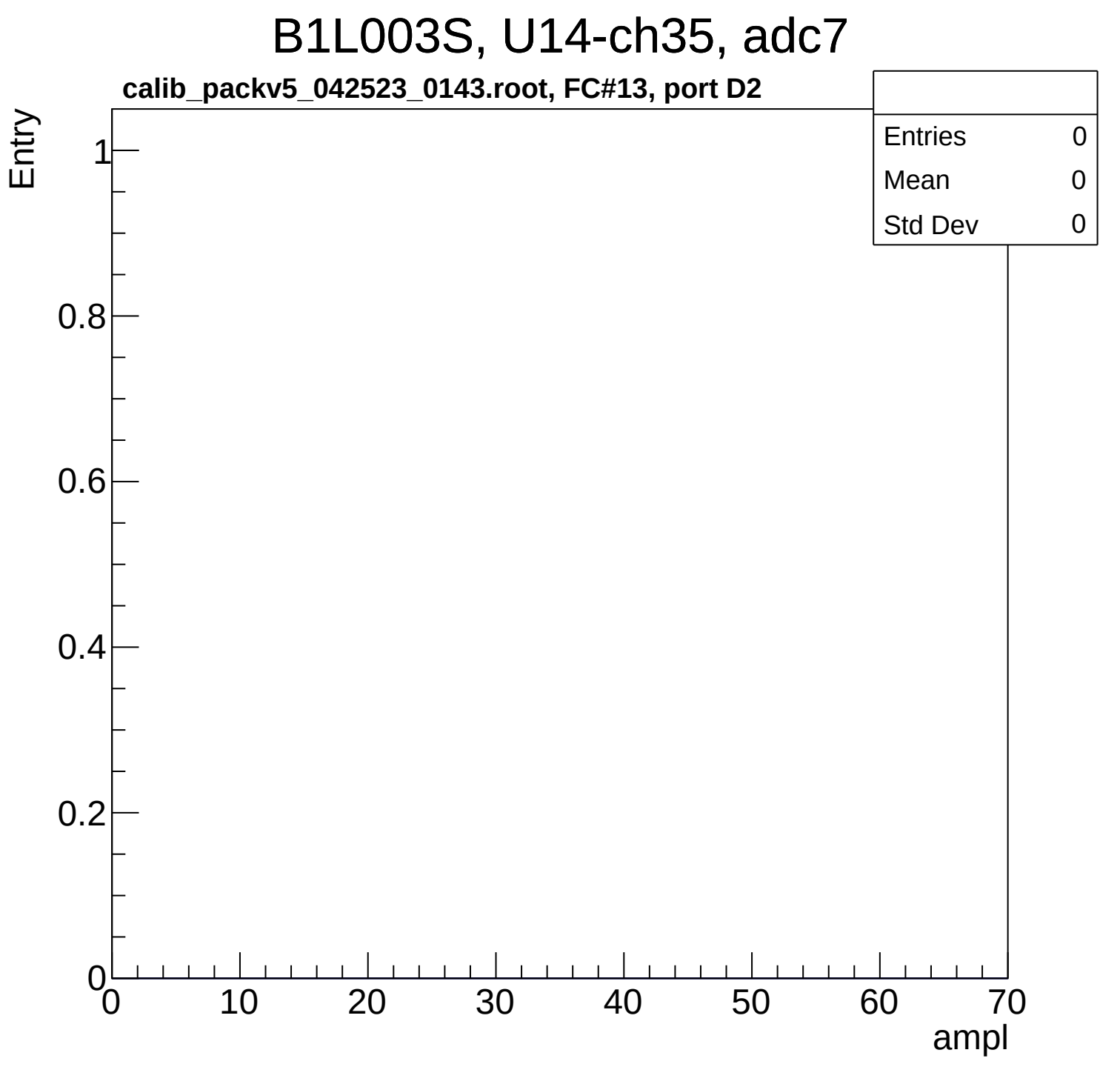

B1L003S, U14-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 39.86
Std Dev 3.4
Gaus mean : 40.2207
Gaus Width: 3.2954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

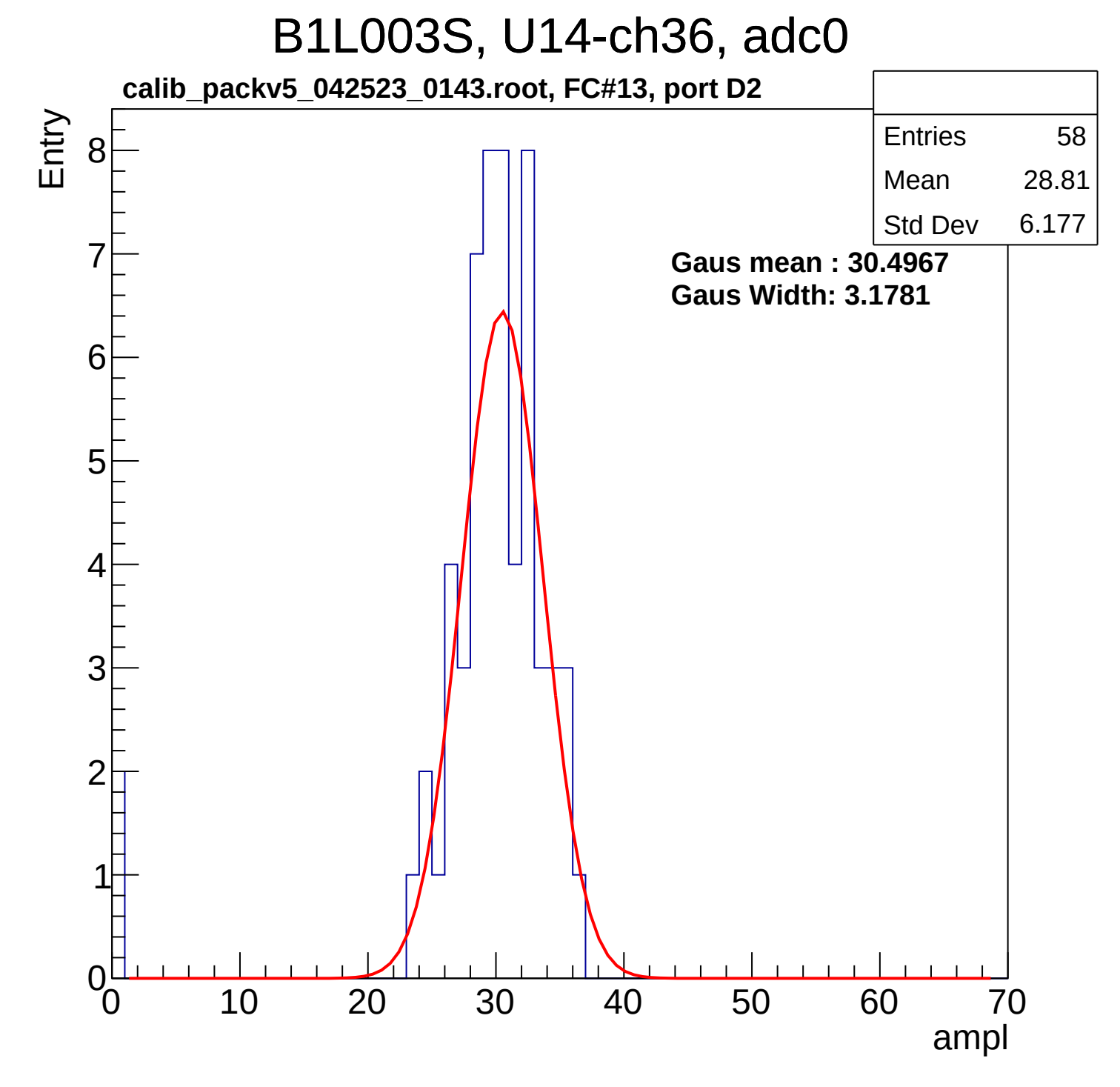

B1L003S, U14-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 46.37
Std Dev 3.773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

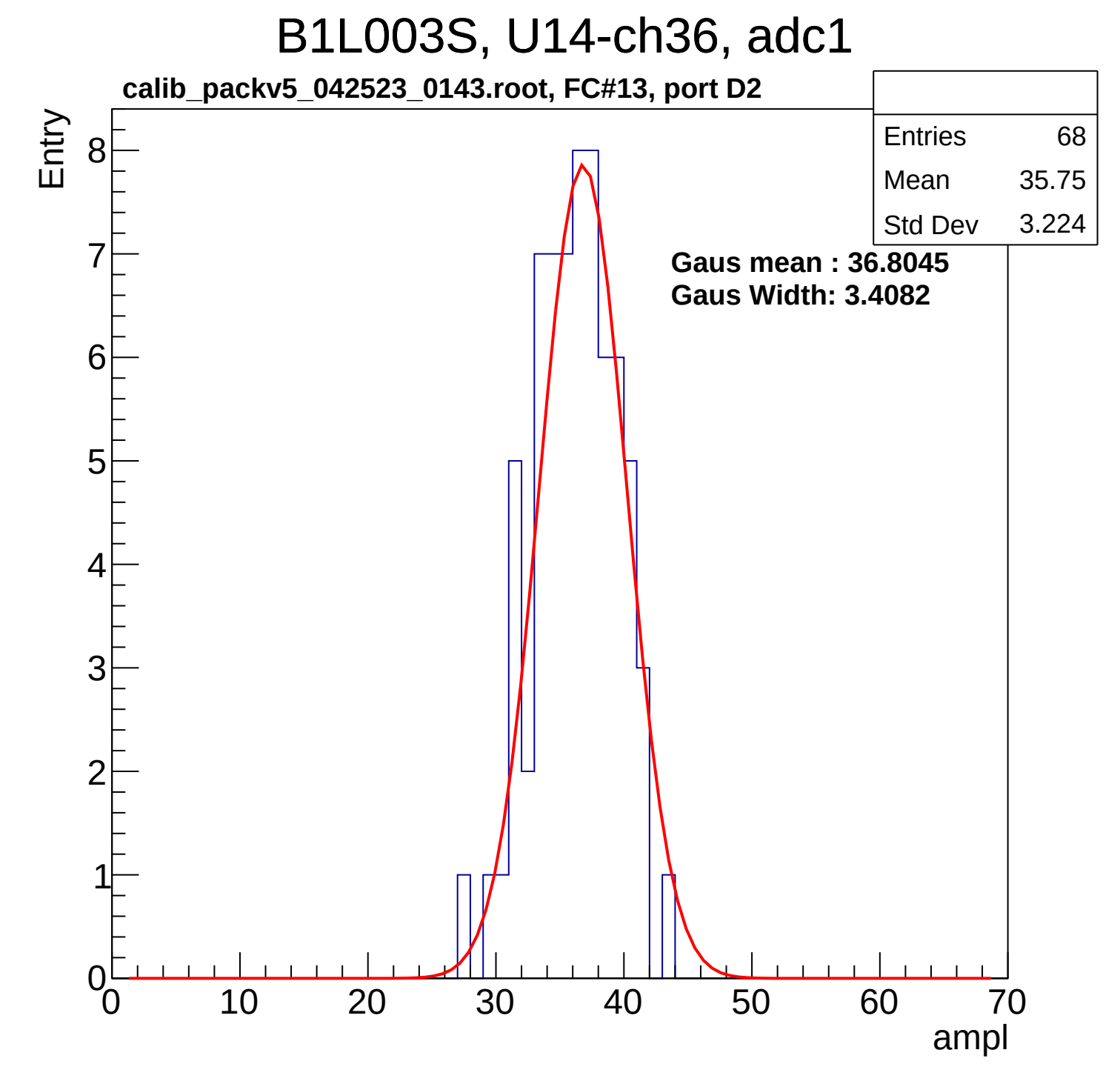

B1L003S, U14-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.44
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

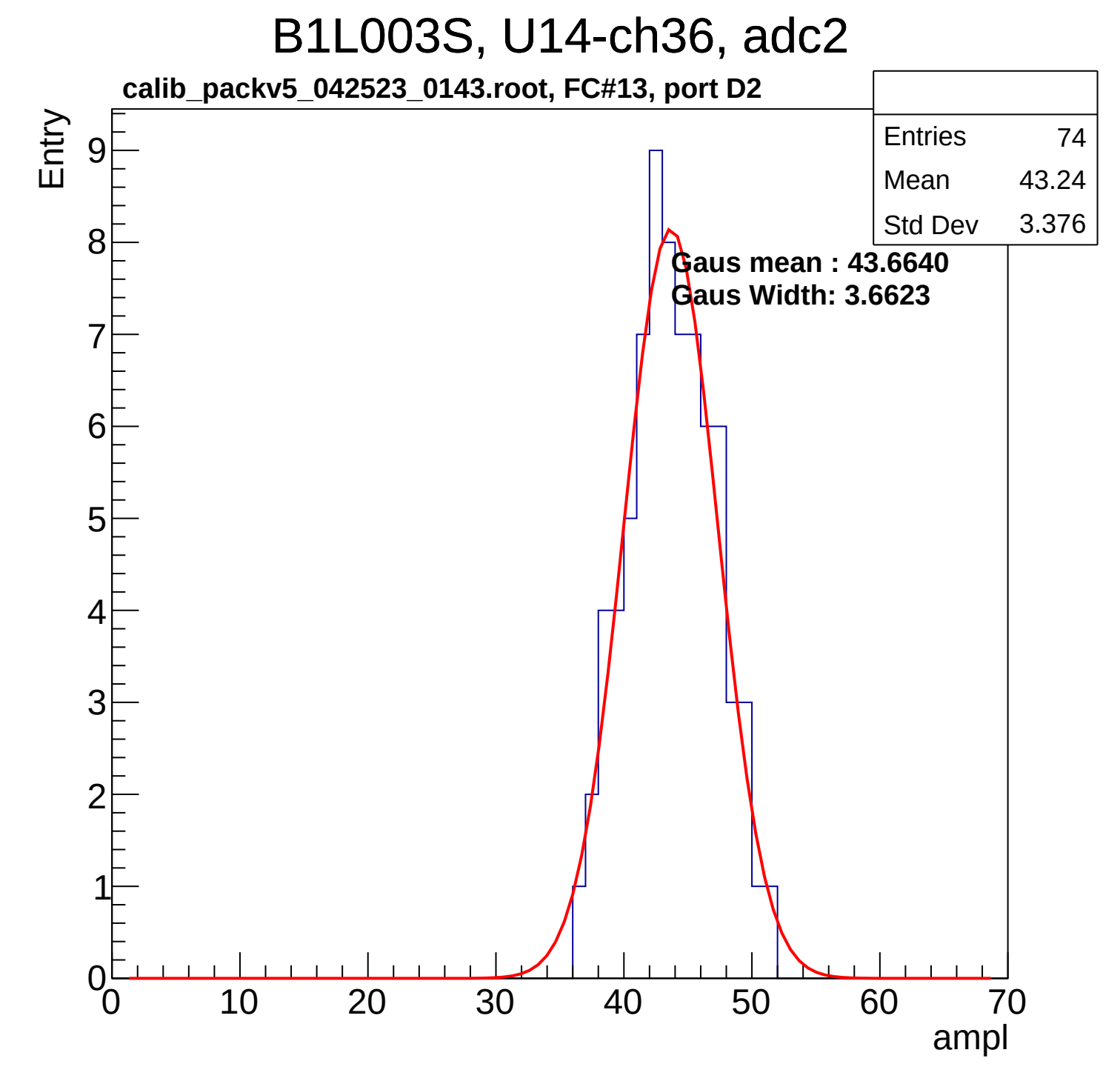

B1L003S, U14-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.87
Std Dev 2.559
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

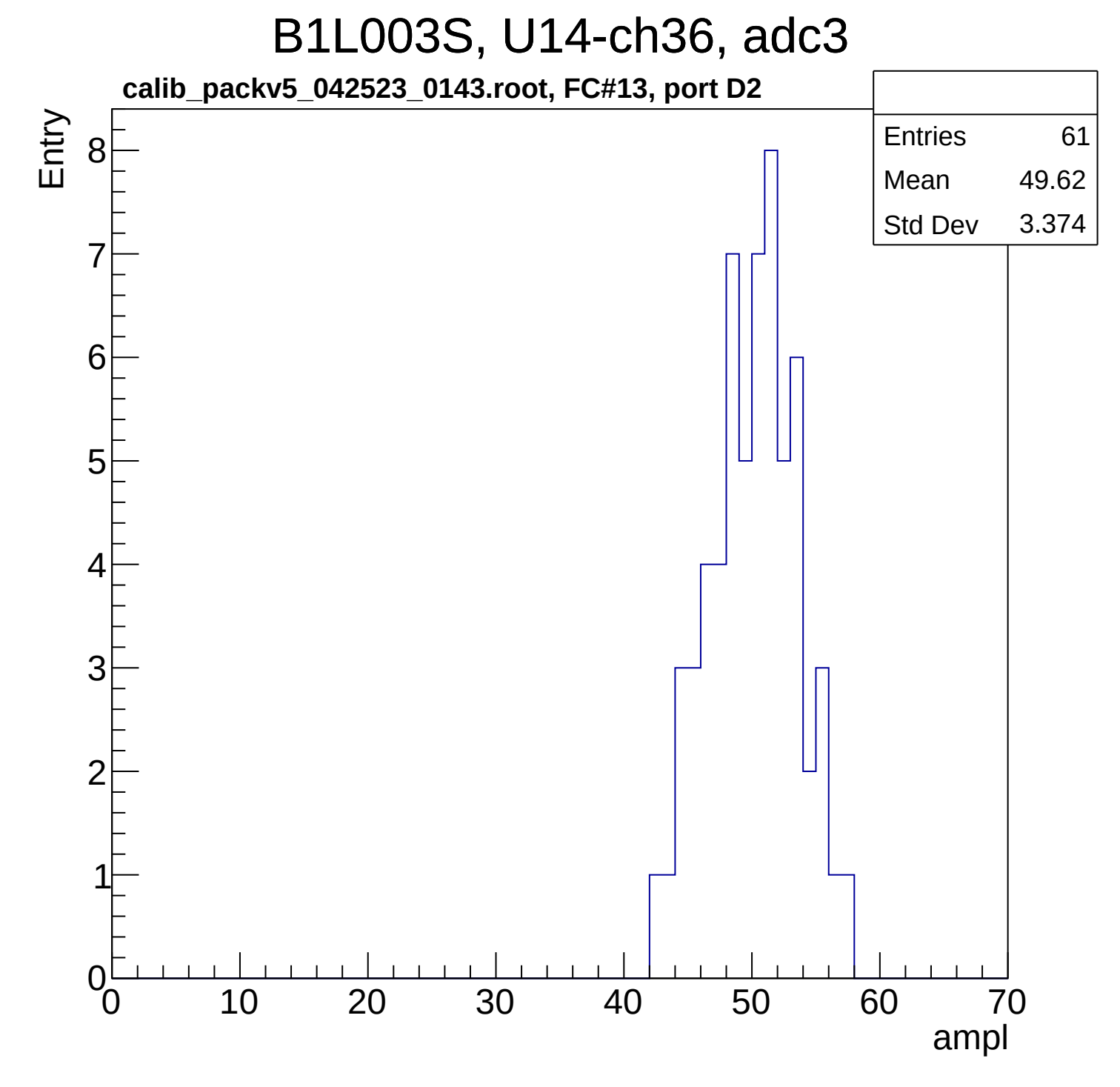

B1L003S, U14-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 58
Std Dev 15.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

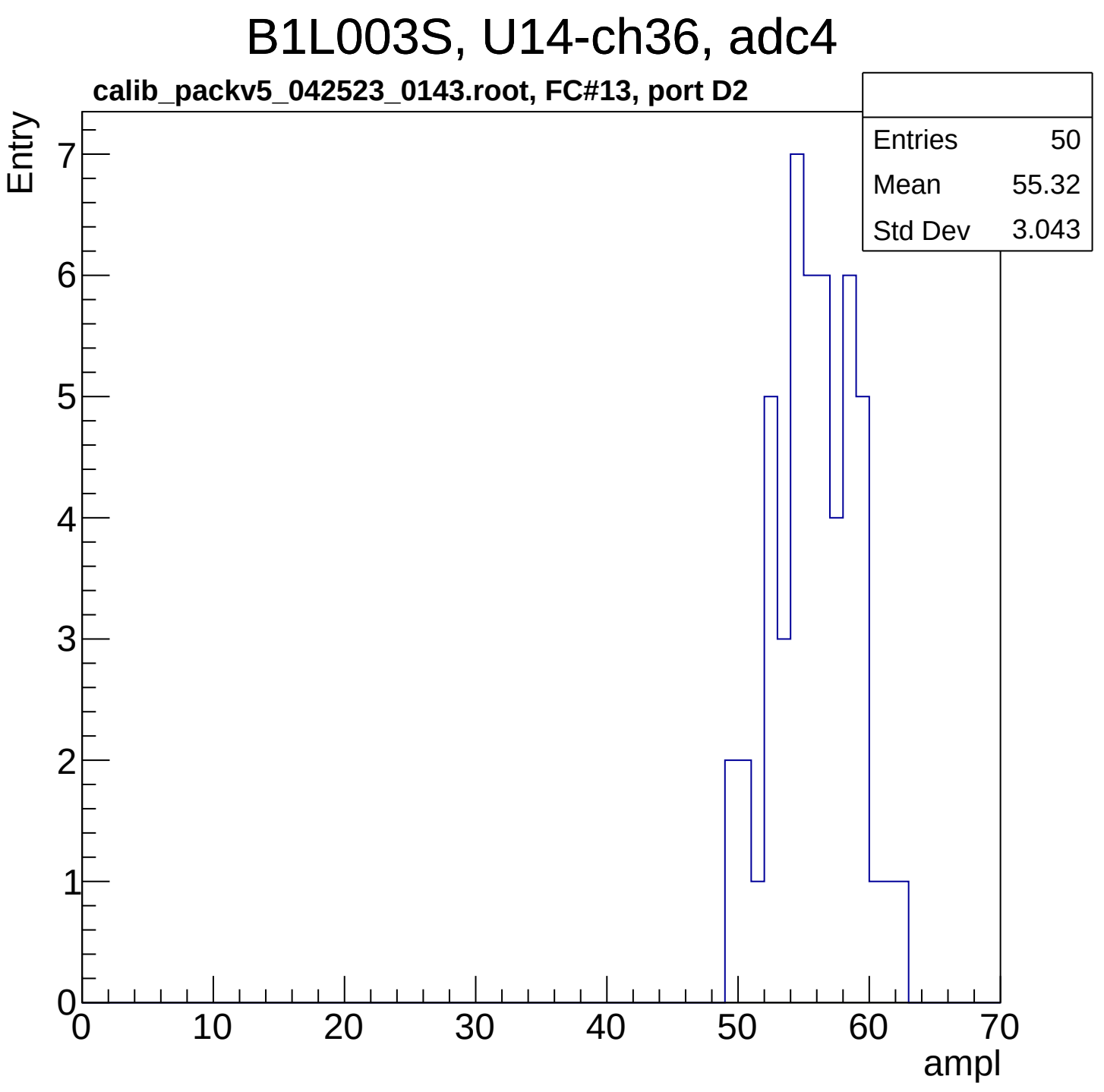

B1L003S, U14-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

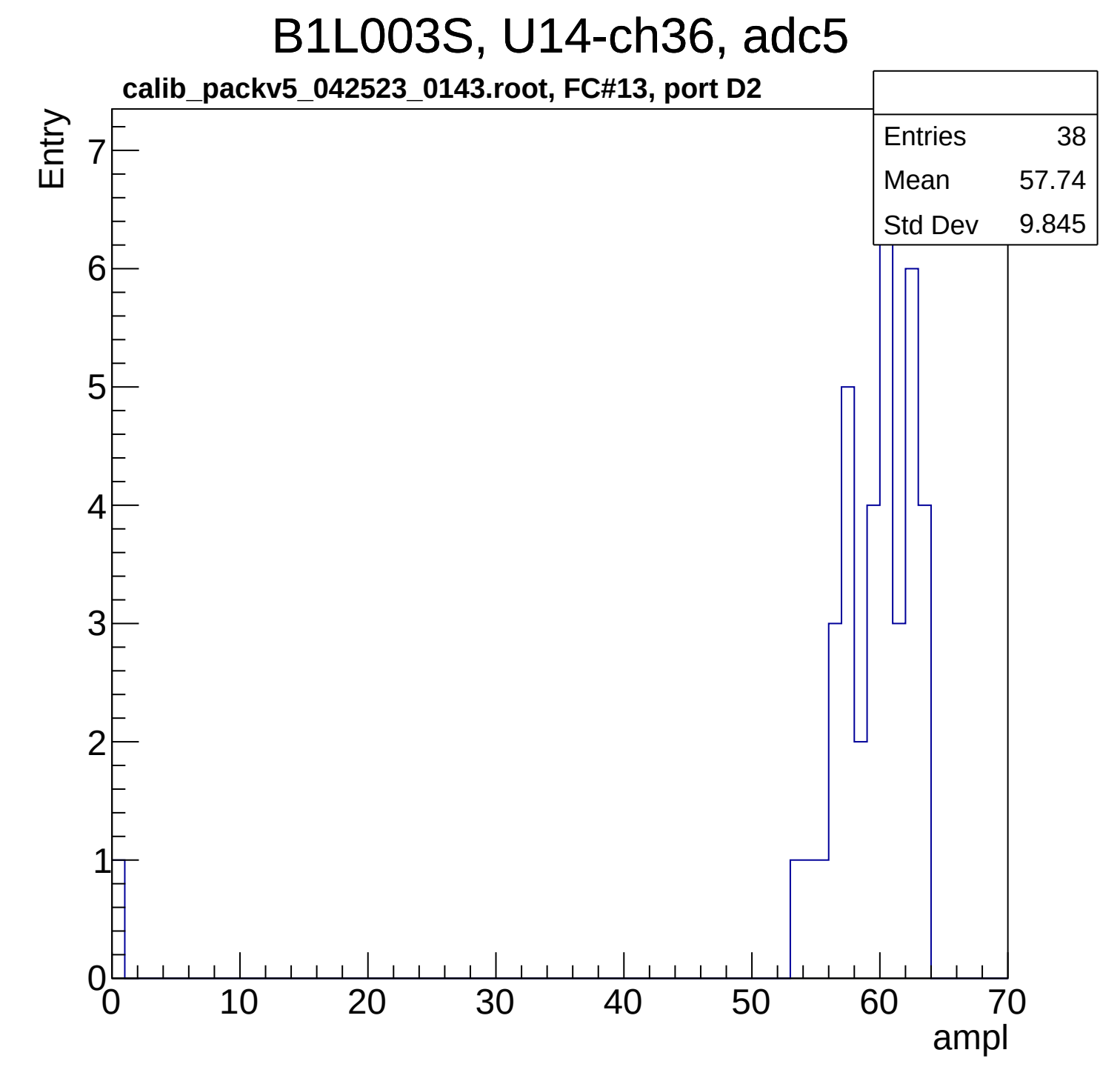

B1L003S, U14-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.81
Std Dev 6.177
Gaus mean : 30.4967
Gaus Width: 3.1781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

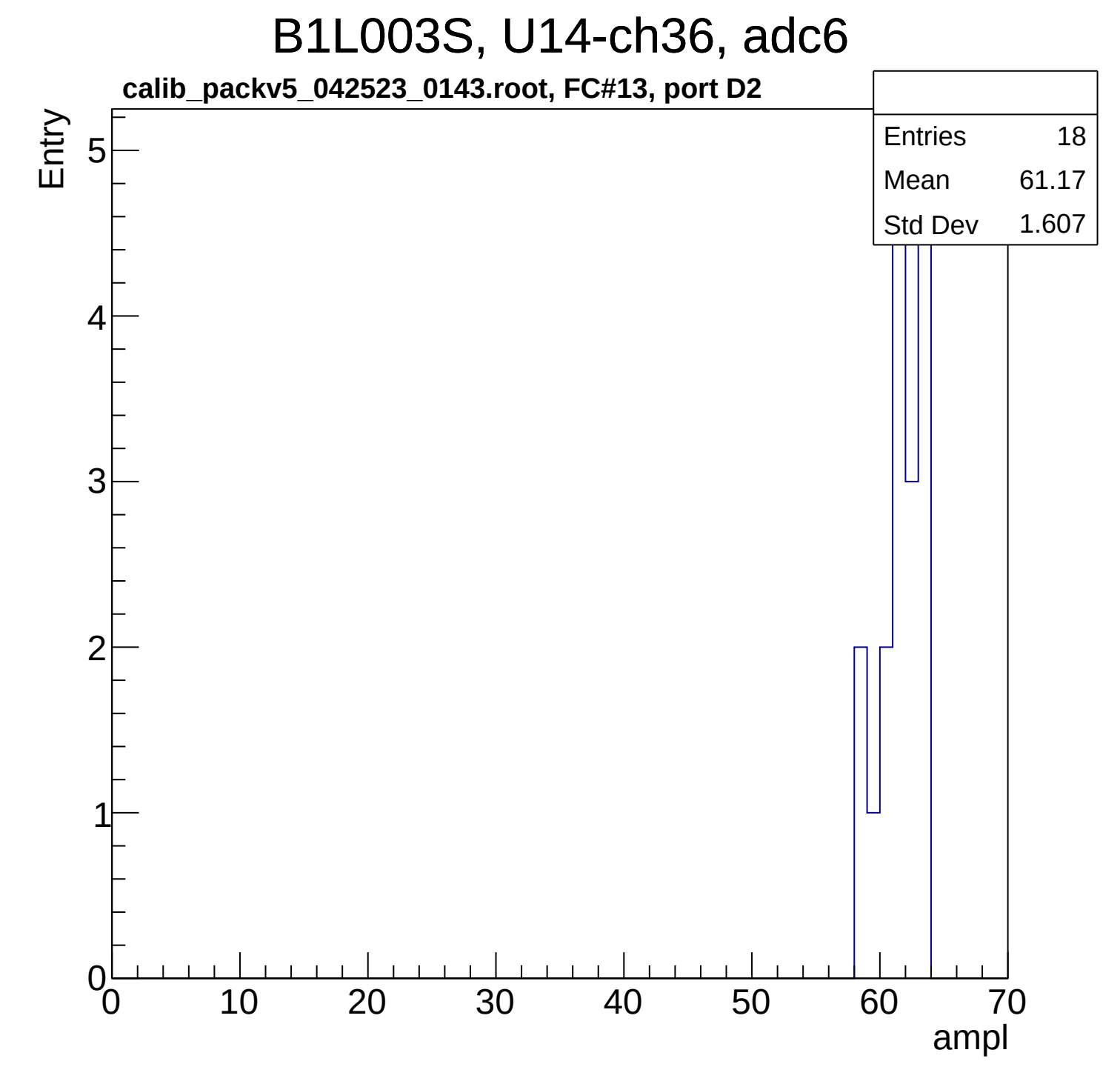

B1L003S, U14-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.75
Std Dev 3.224
Gaus mean : 36.8045
Gaus Width: 3.4082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

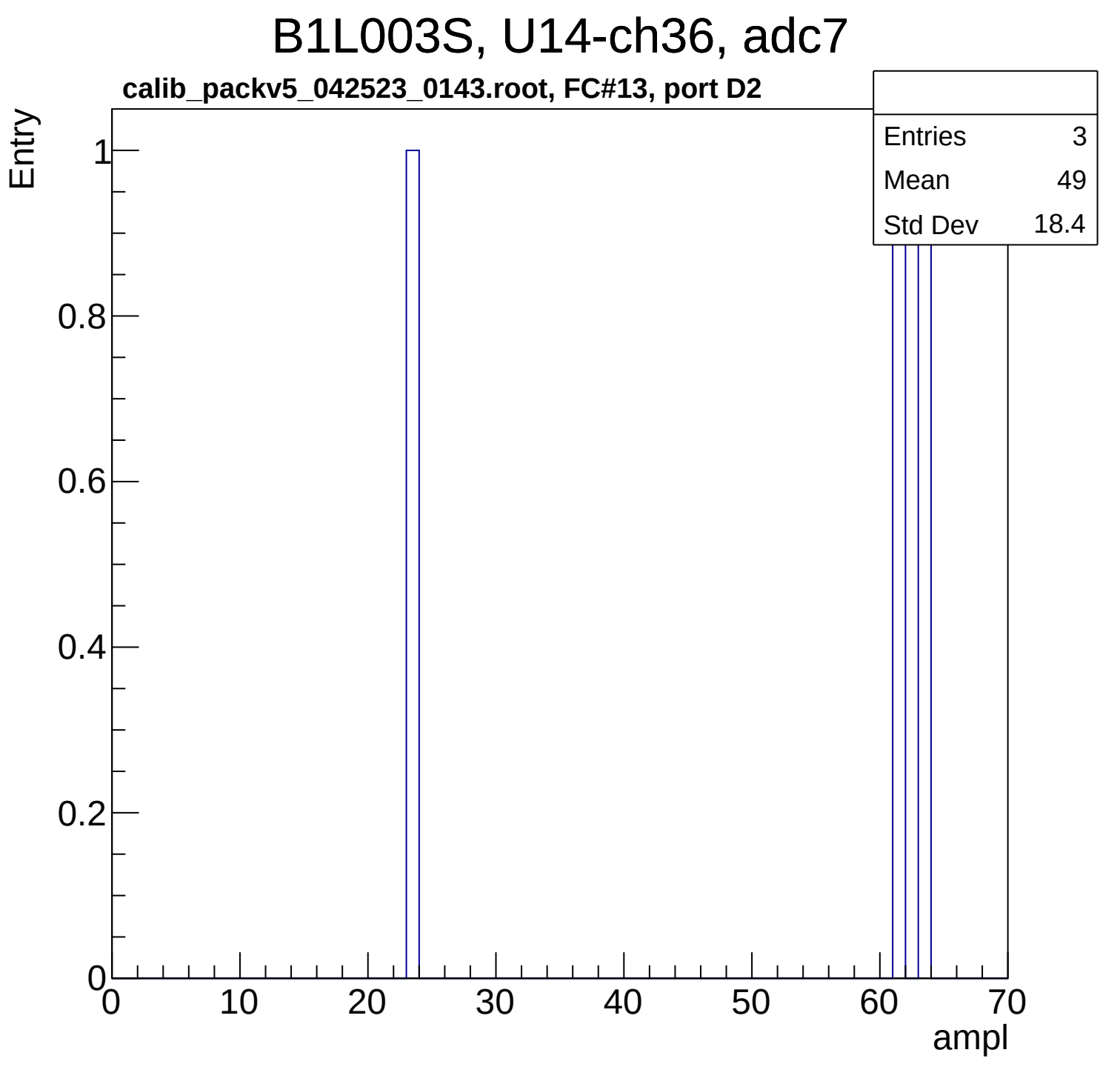

B1L003S, U14-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.24
Std Dev 3.376
Gaus mean : 43.6640
Gaus Width: 3.6623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

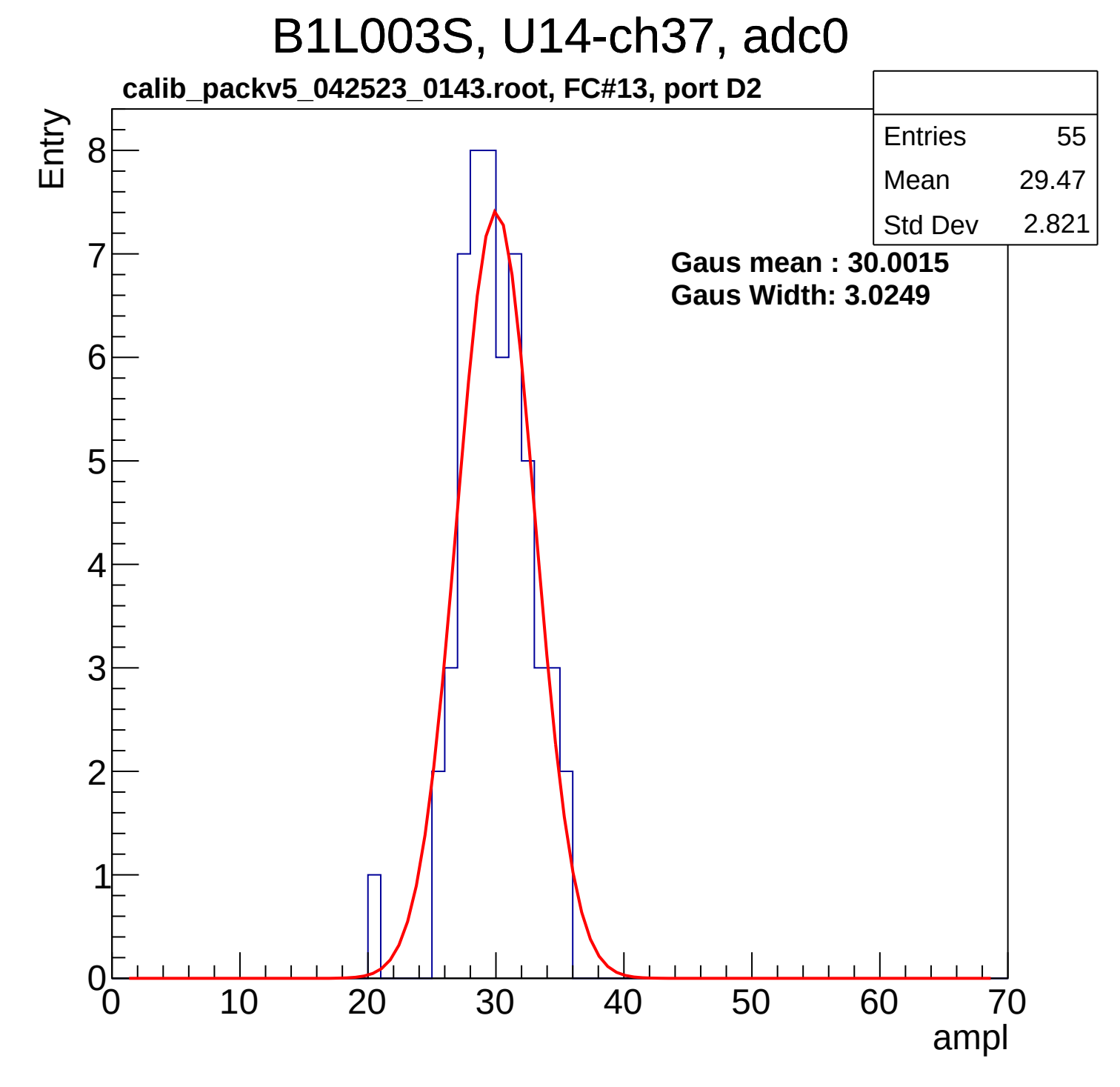

B1L003S, U14-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.62
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

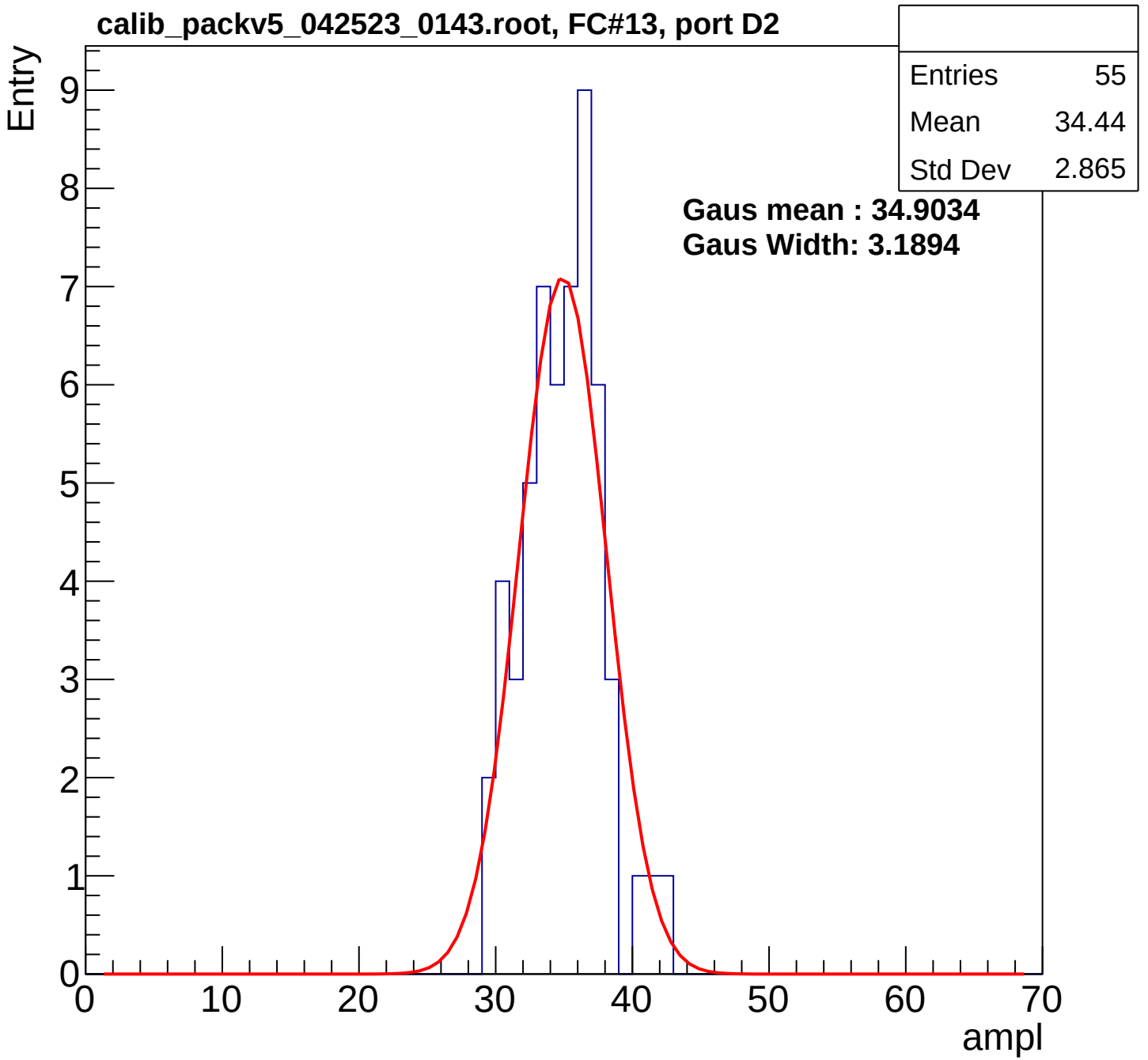

B1L003S, U14-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.32
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

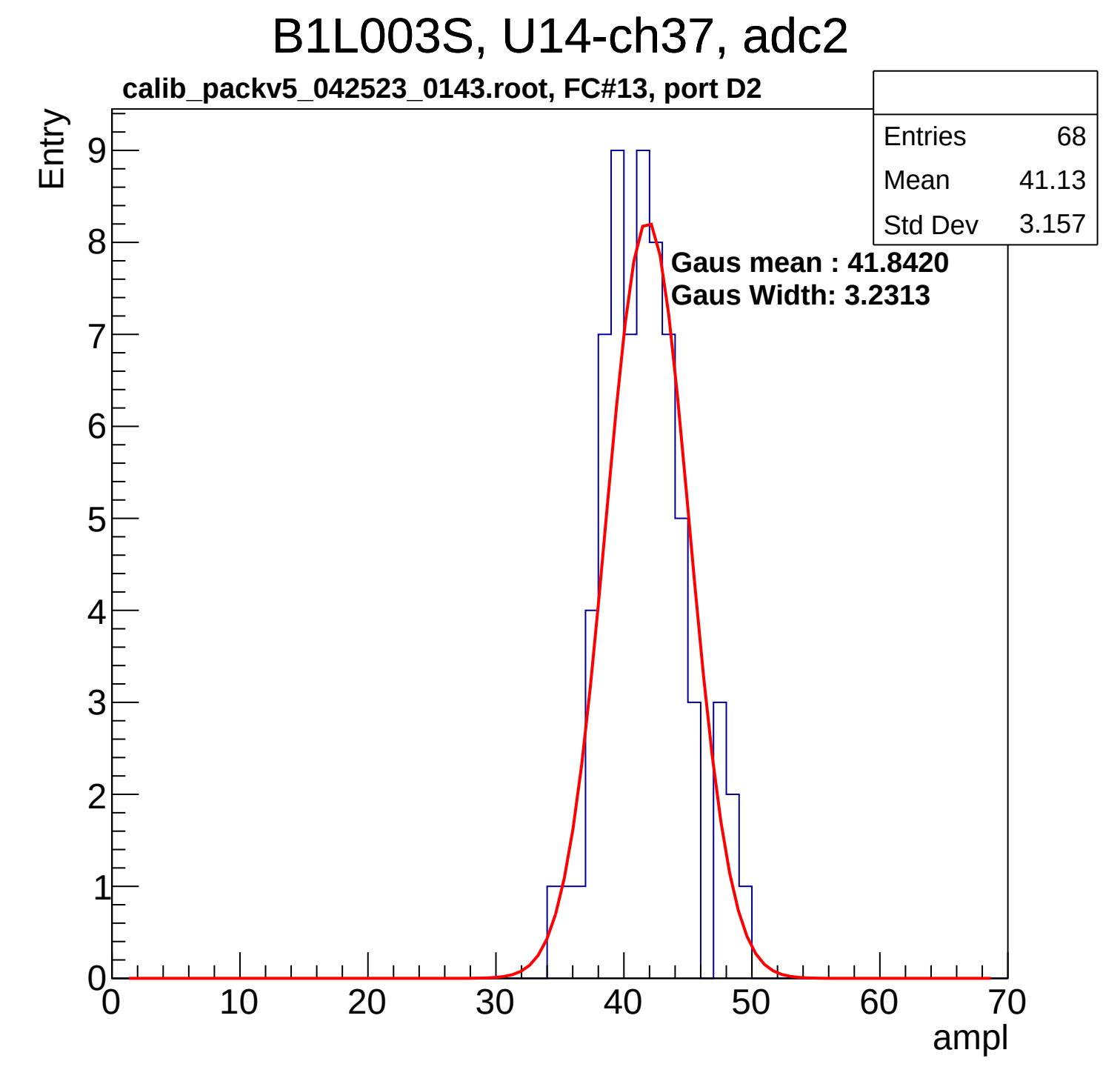

B1L003S, U14-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 57.74
Std Dev 9.845
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

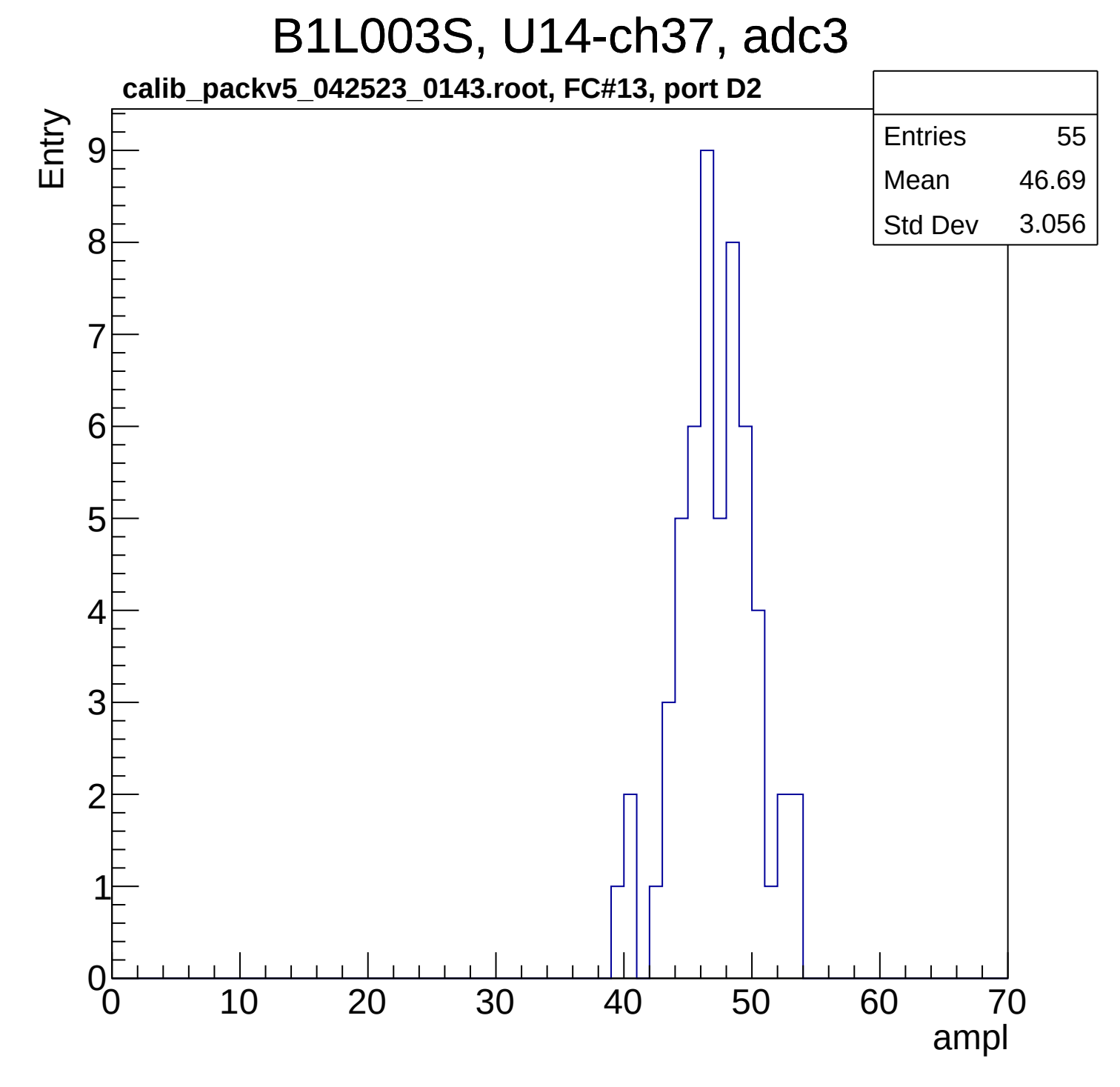

B1L003S, U14-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.17
Std Dev 1.607
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

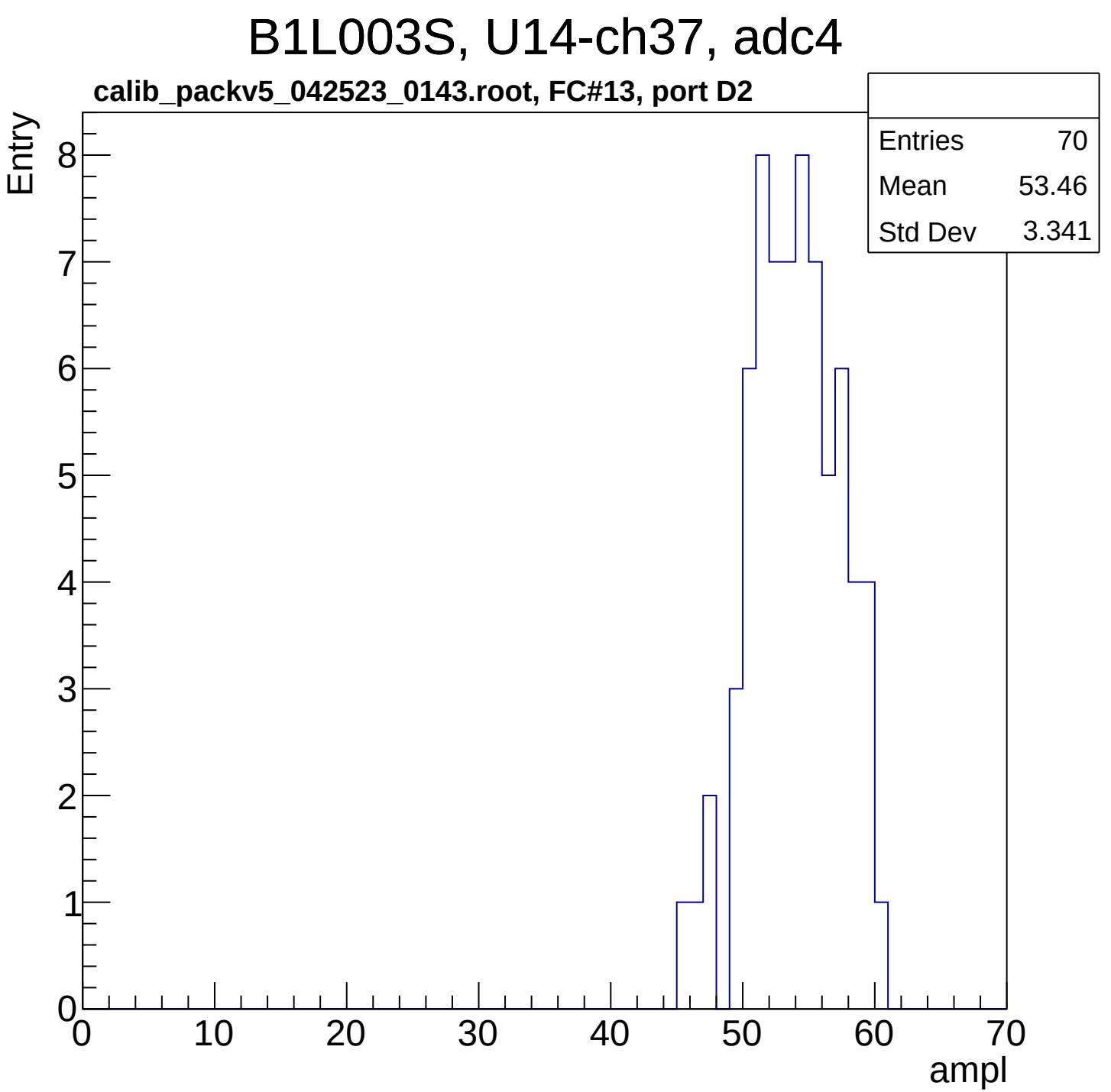

B1L003S, U14-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 49
Std Dev 18.4
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

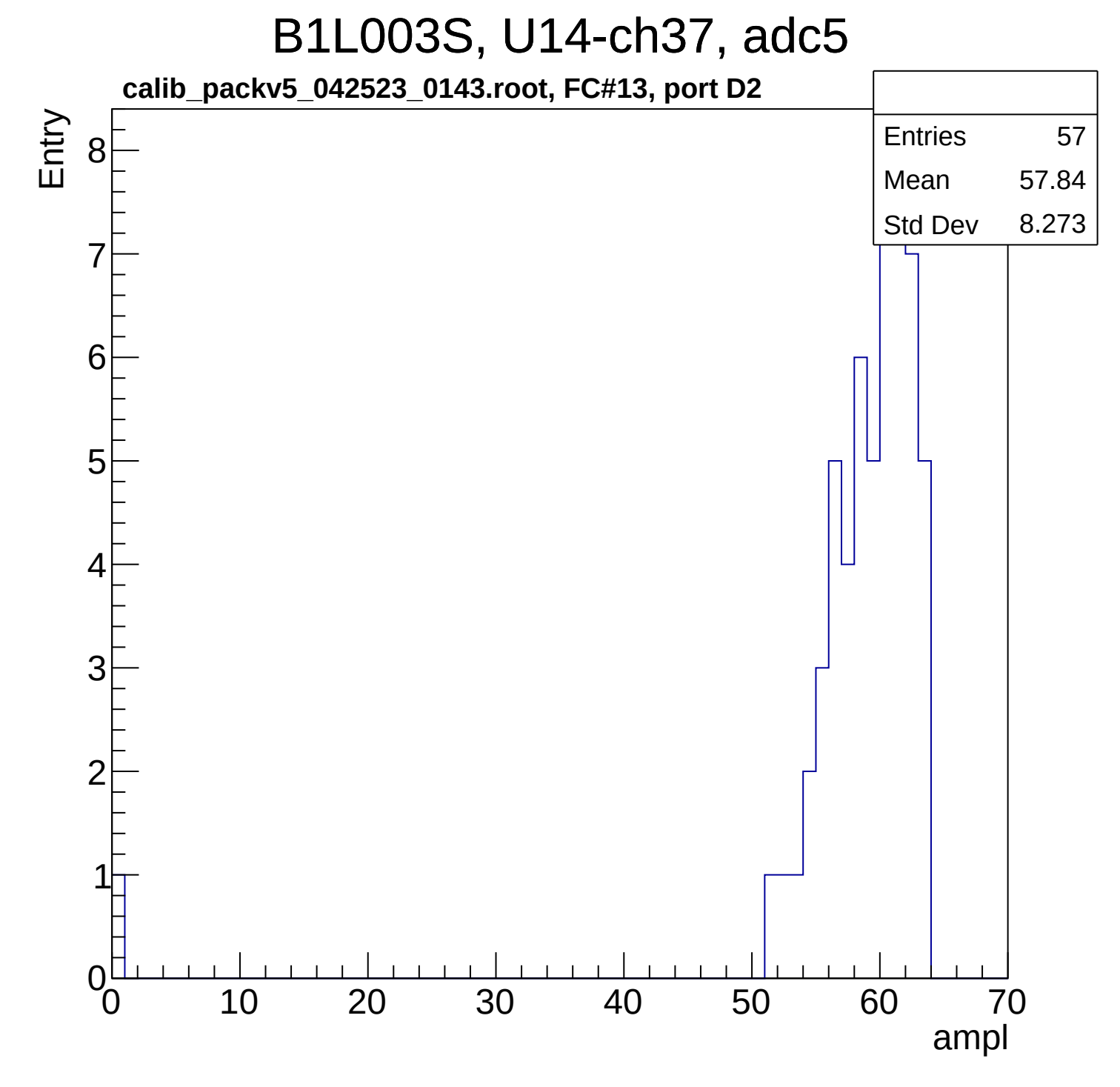

B1L003S, U14-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.47
Std Dev 2.821
Gaus mean : 30.0015
Gaus Width: 3.0249
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

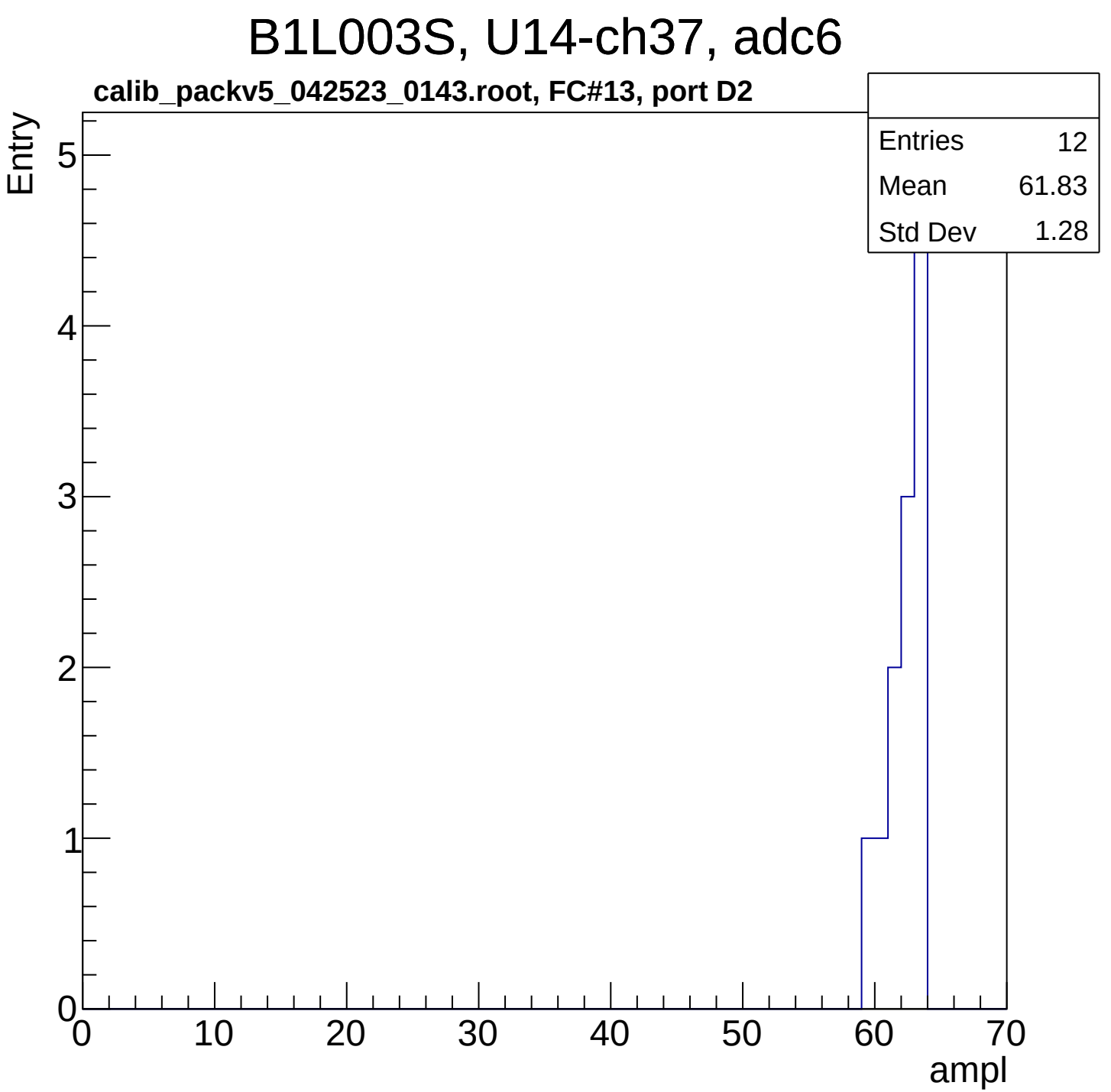

B1L003S, U14-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.44
Std Dev 2.865
Gaus mean : 34.9034
Gaus Width: 3.1894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

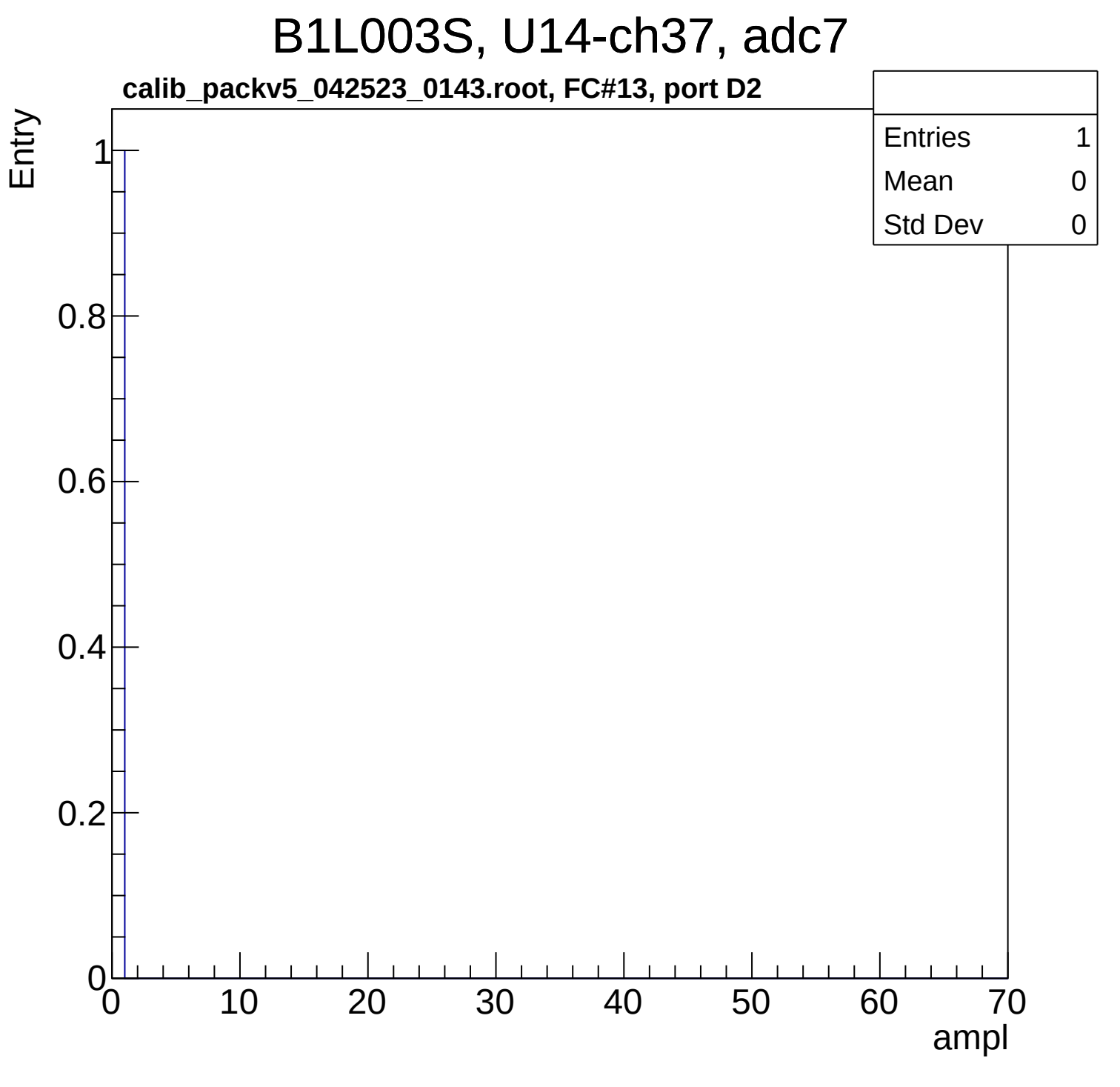

B1L003S, U14-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 41.13
Std Dev 3.157
Gaus mean : 41.8420
Gaus Width: 3.2313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

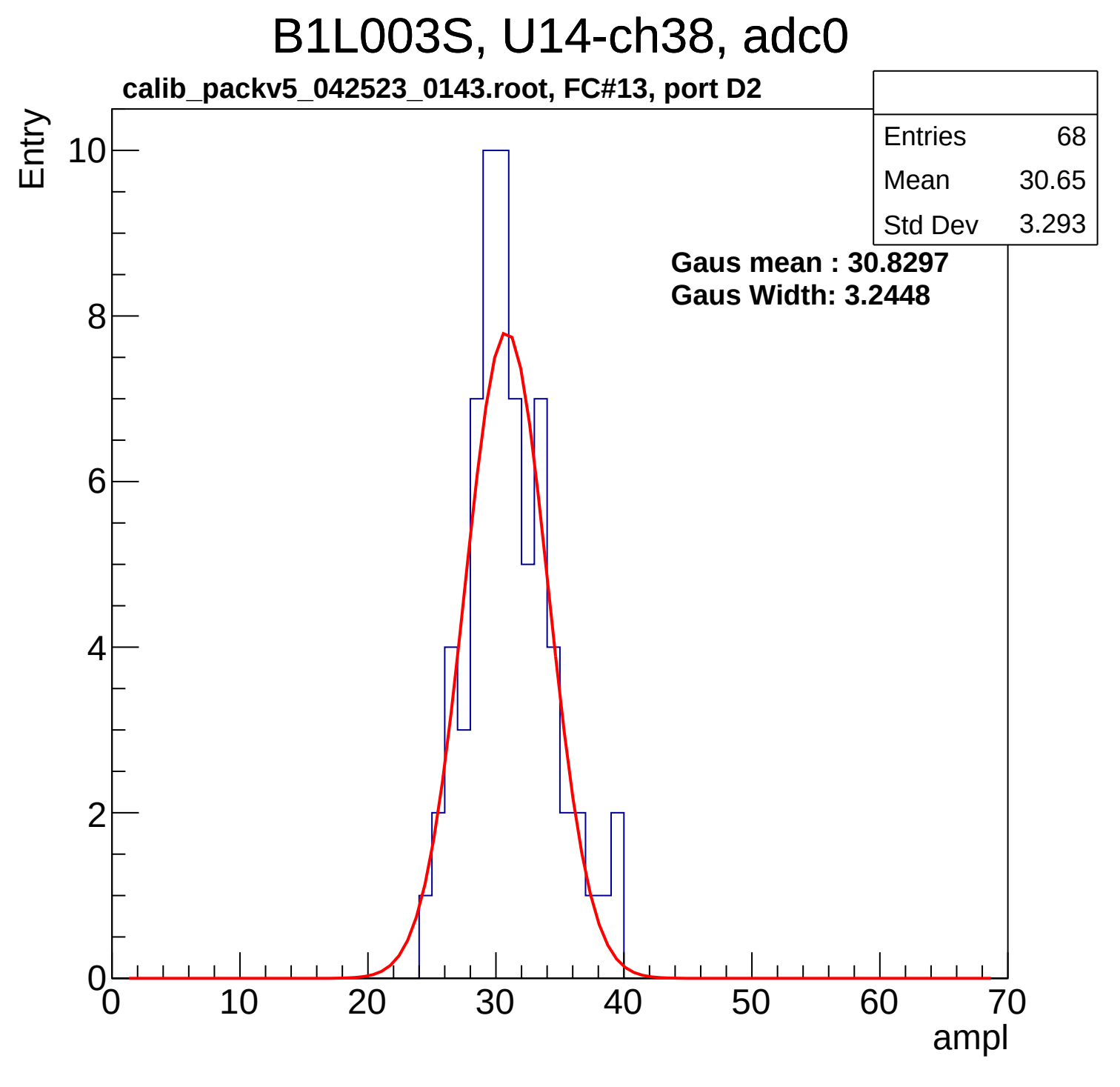

B1L003S, U14-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 46.69
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

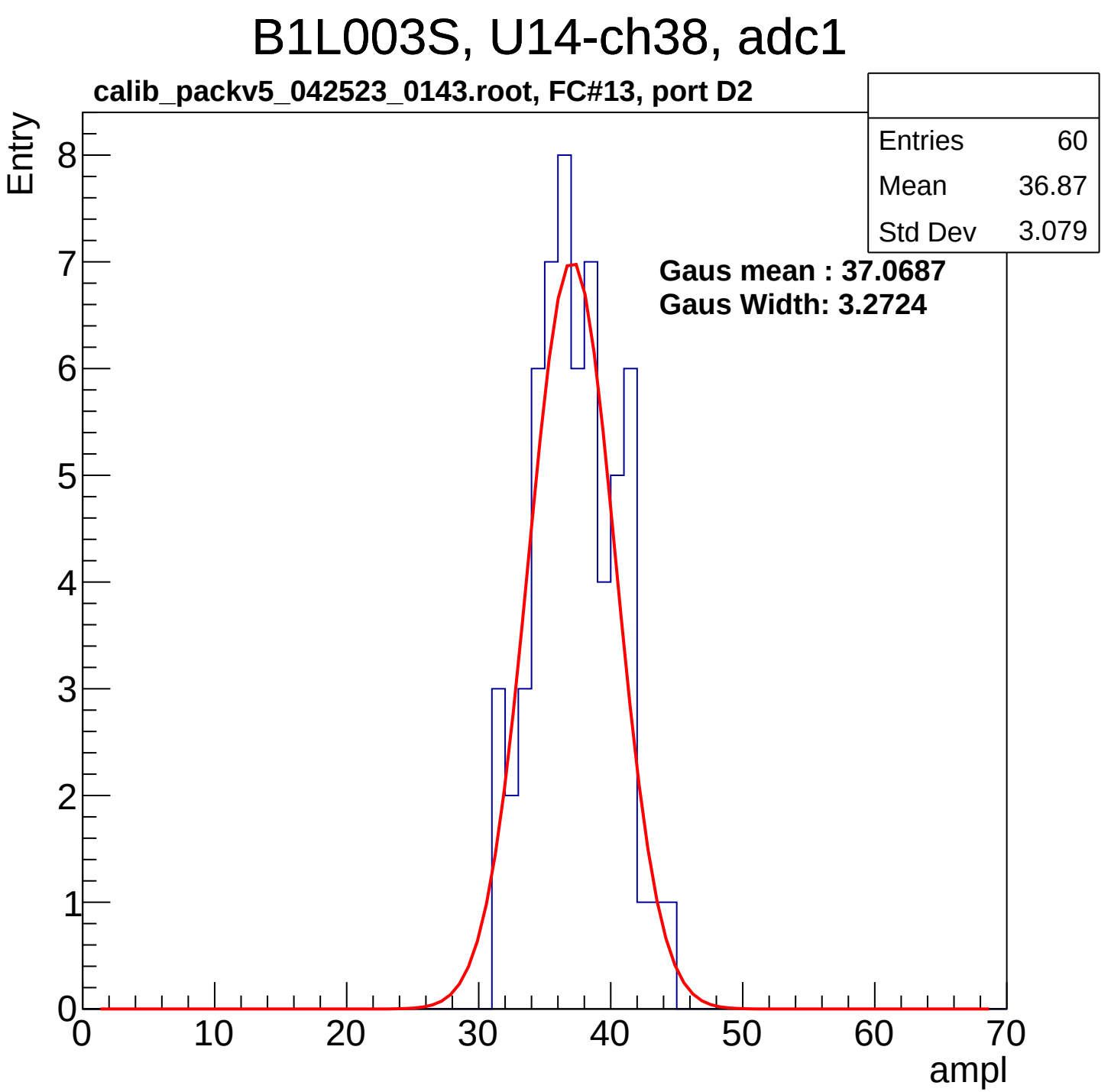

B1L003S, U14-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 53.46
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

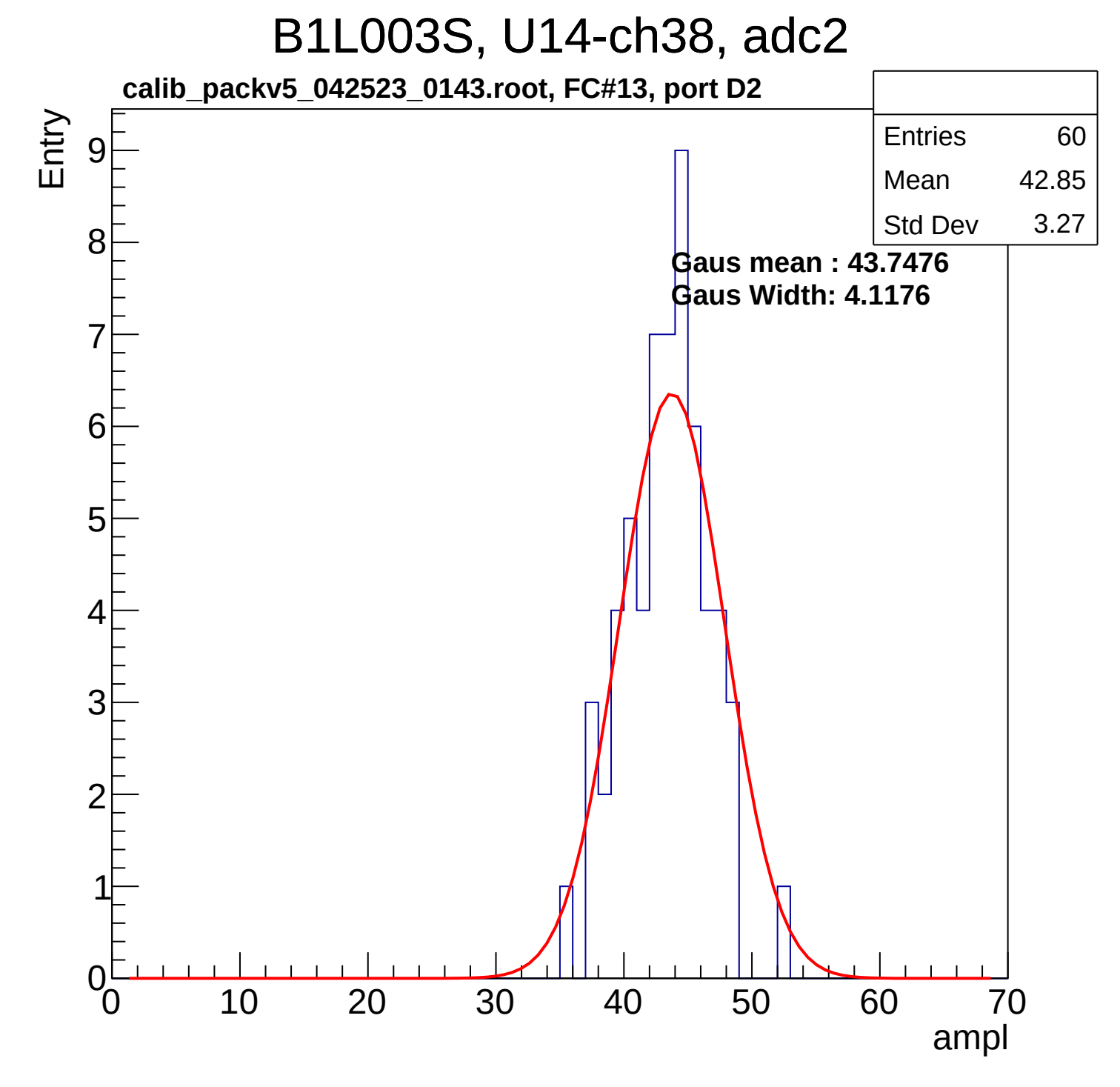

B1L003S, U14-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.84
Std Dev 8.273
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

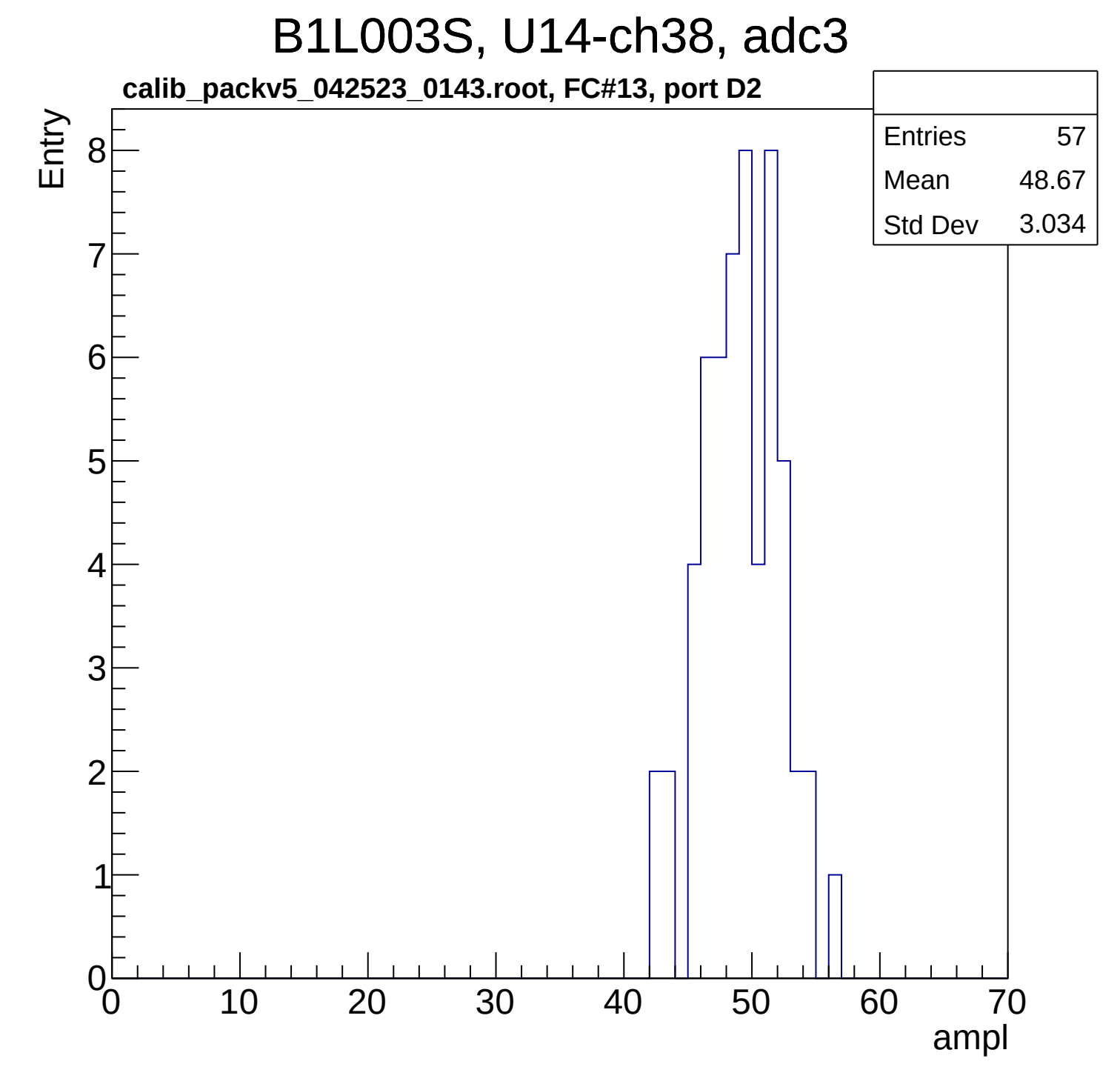

B1L003S, U14-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.83
Std Dev 1.28
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

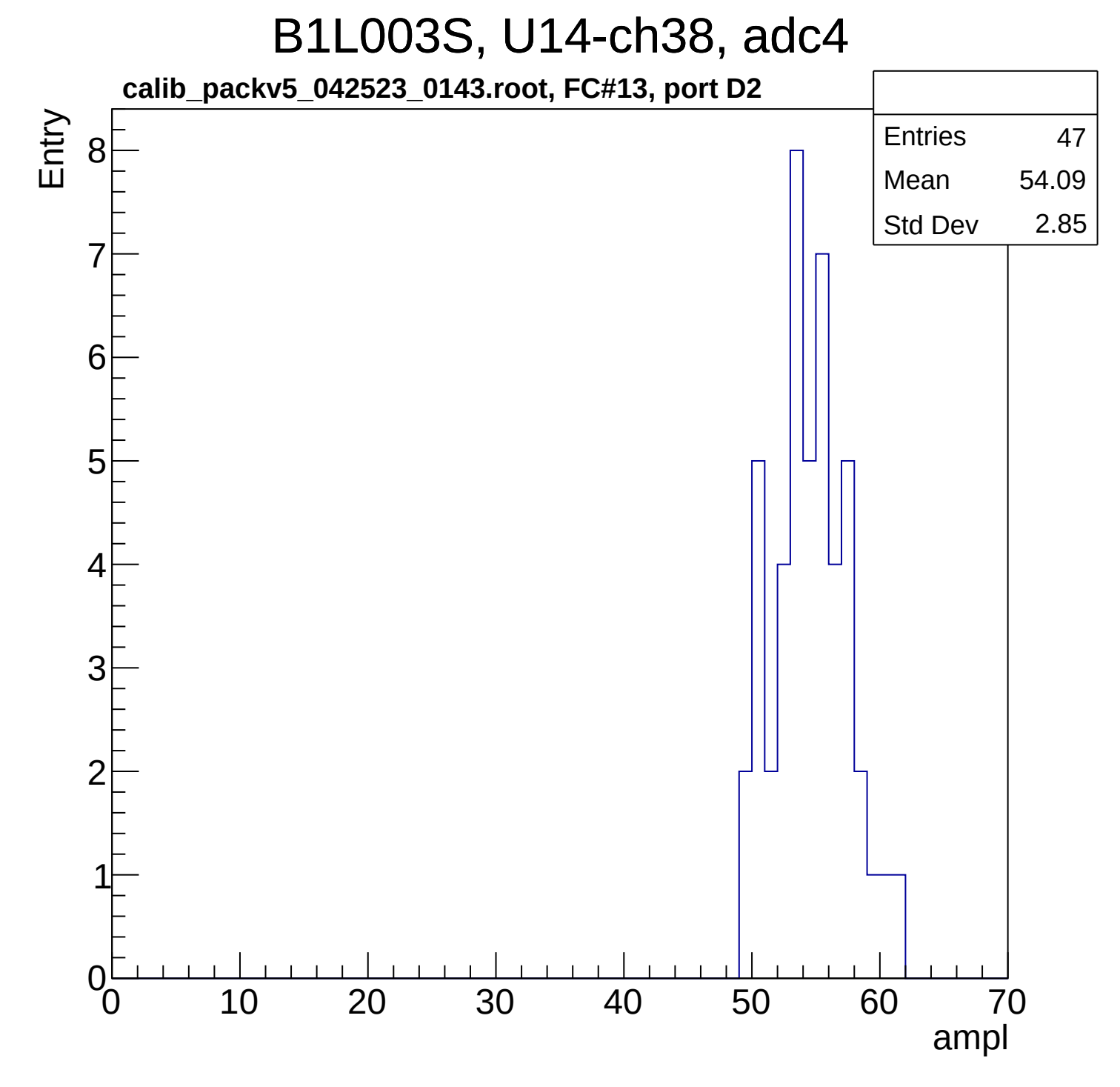

B1L003S, U14-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

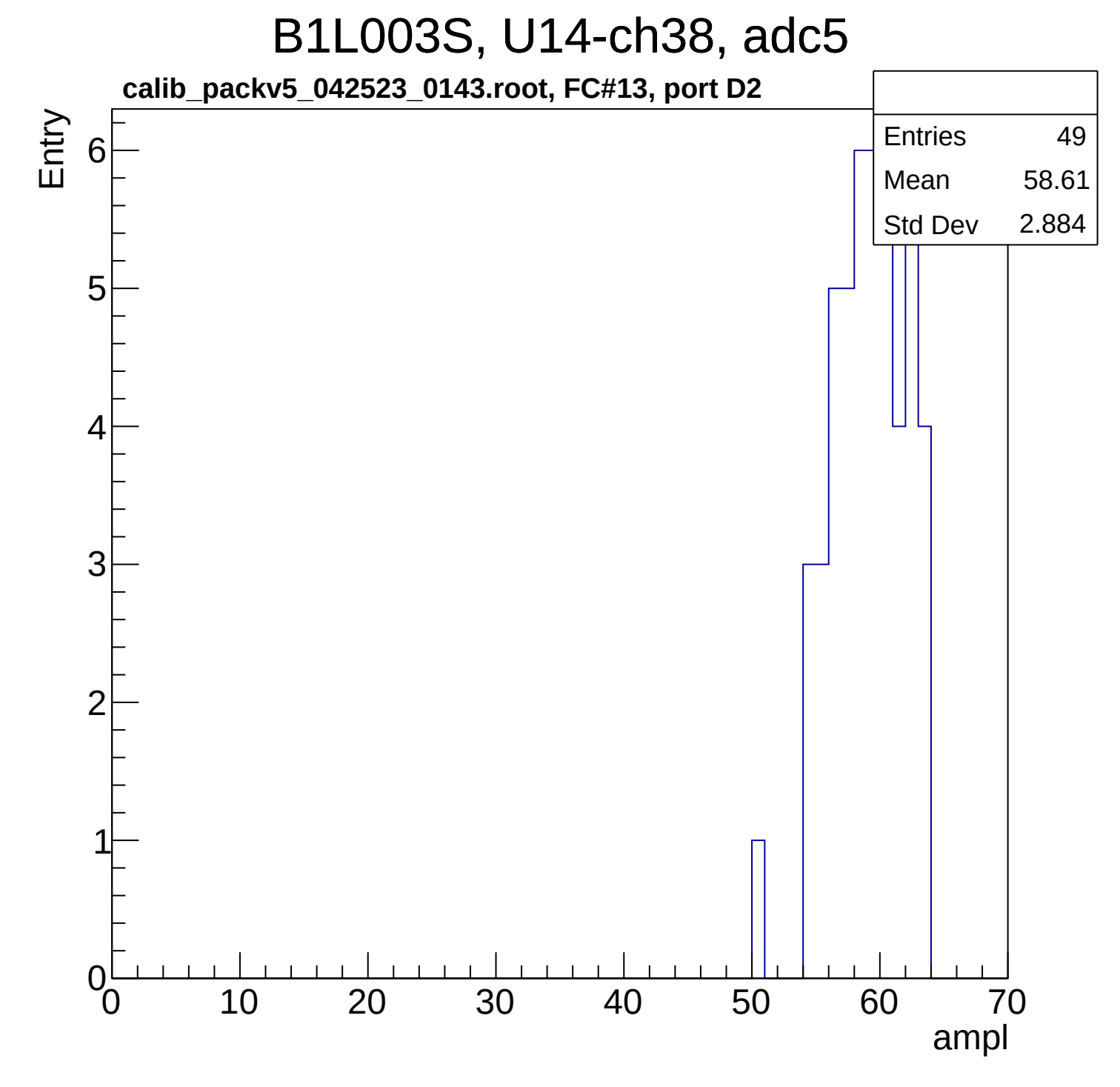

B1L003S, U14-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.65
Std Dev 3.293
Gaus mean : 30.8297
Gaus Width: 3.2448
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

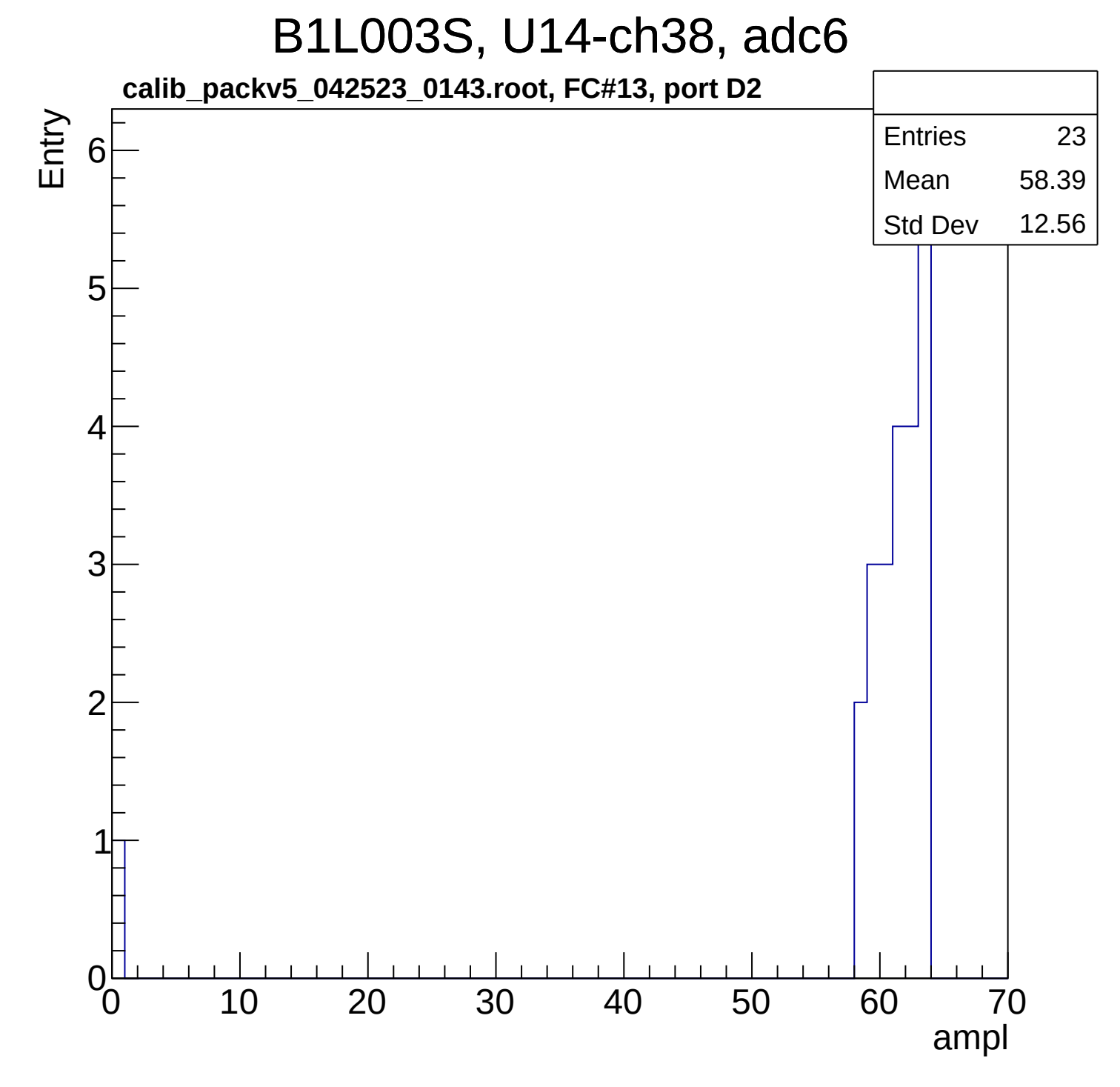

B1L003S, U14-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.87
Std Dev 3.079
Gaus mean : 37.0687
Gaus Width: 3.2724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

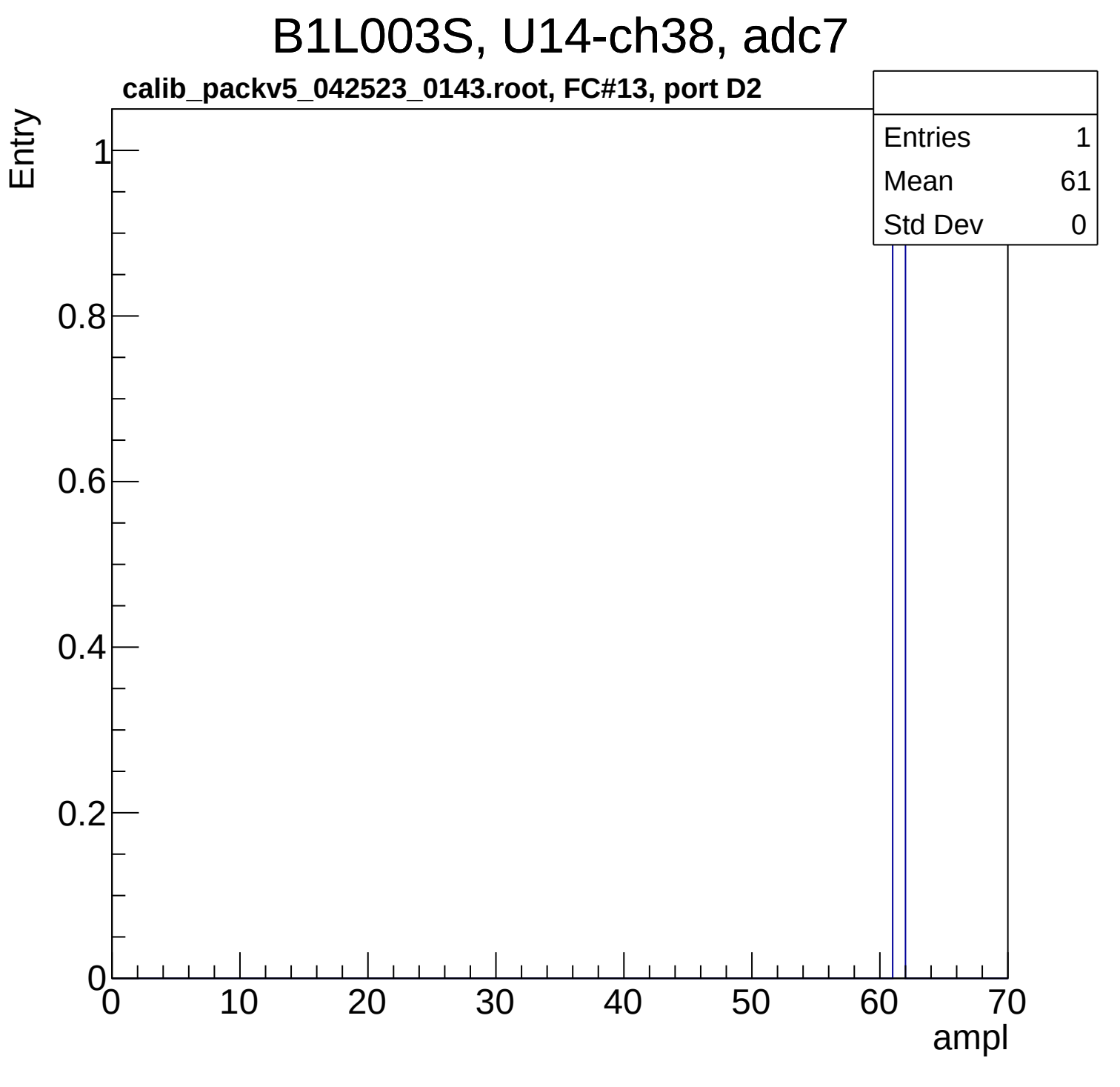

B1L003S, U14-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.85
Std Dev 3.27
Gaus mean : 43.7476
Gaus Width: 4.1176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

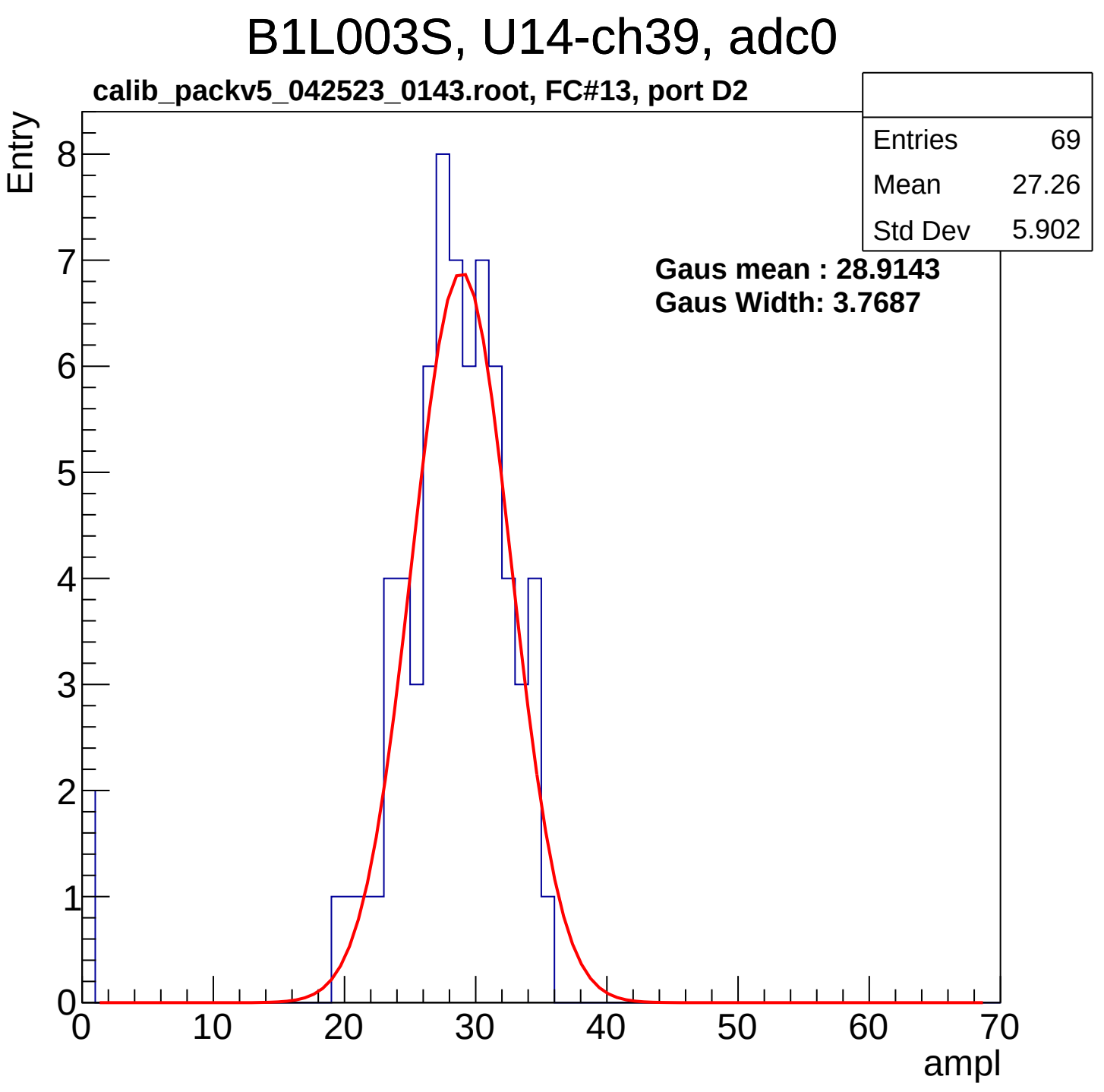

B1L003S, U14-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.67
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

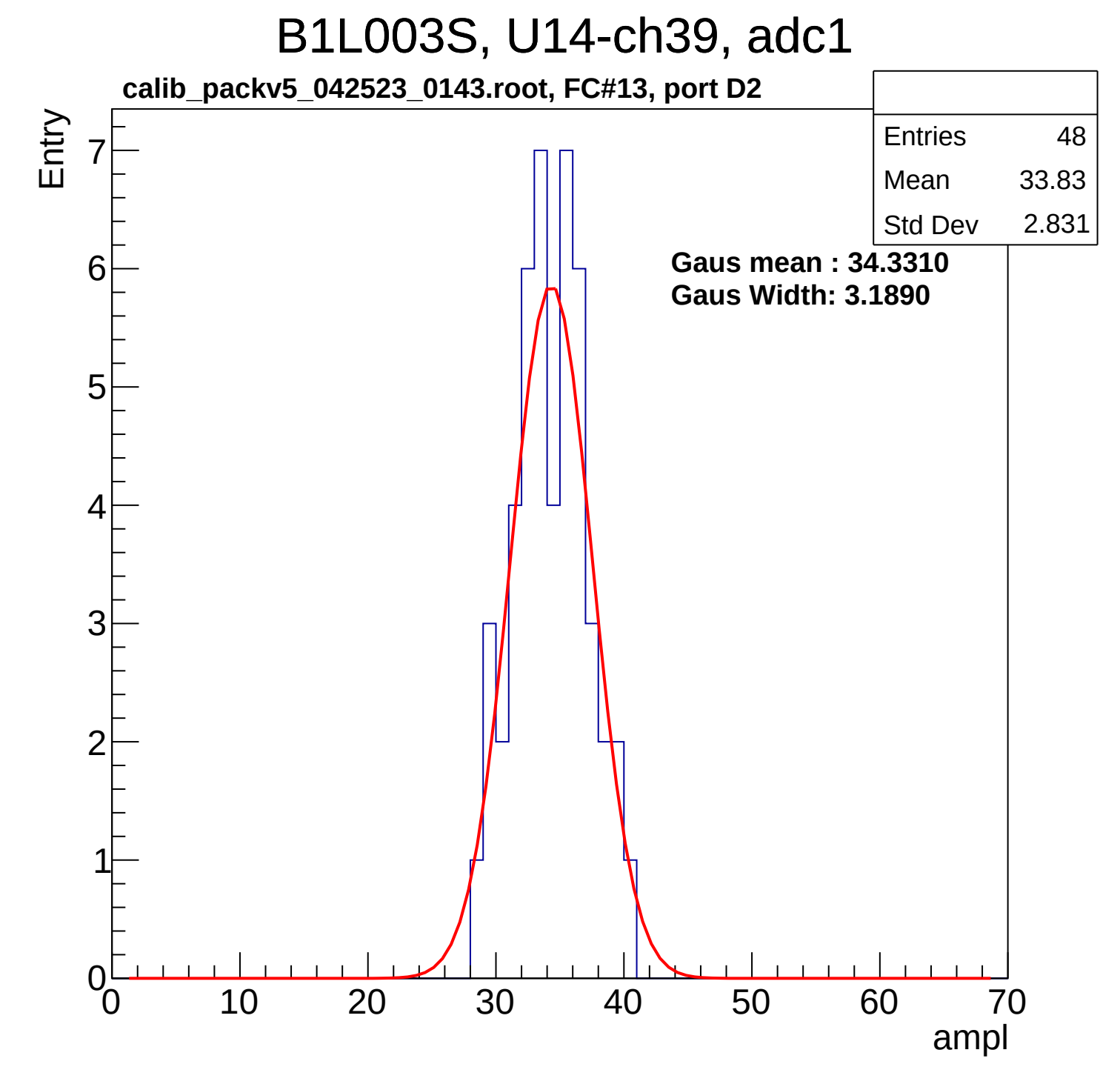

B1L003S, U14-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.09
Std Dev 2.85
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

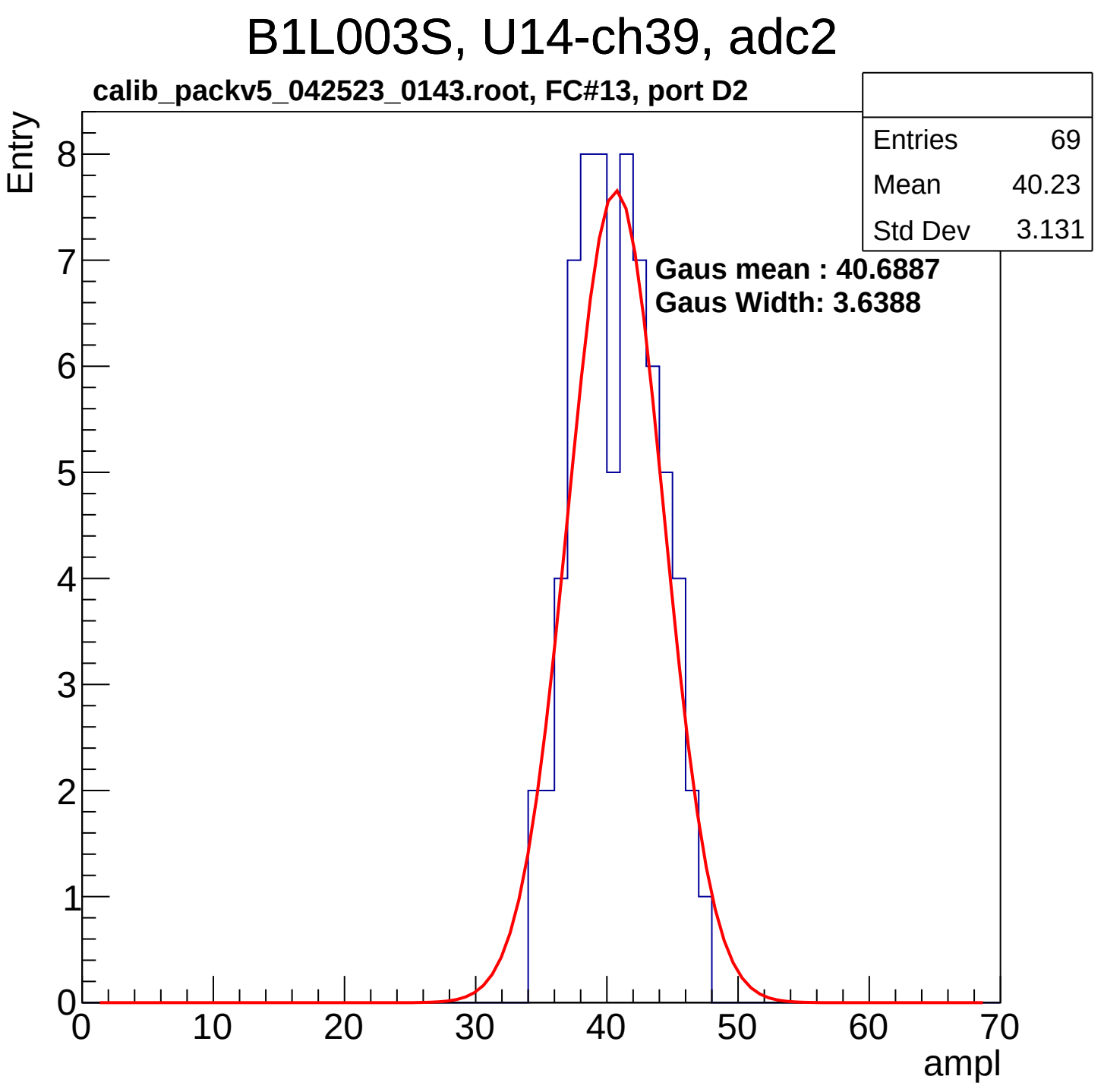

B1L003S, U14-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.61
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

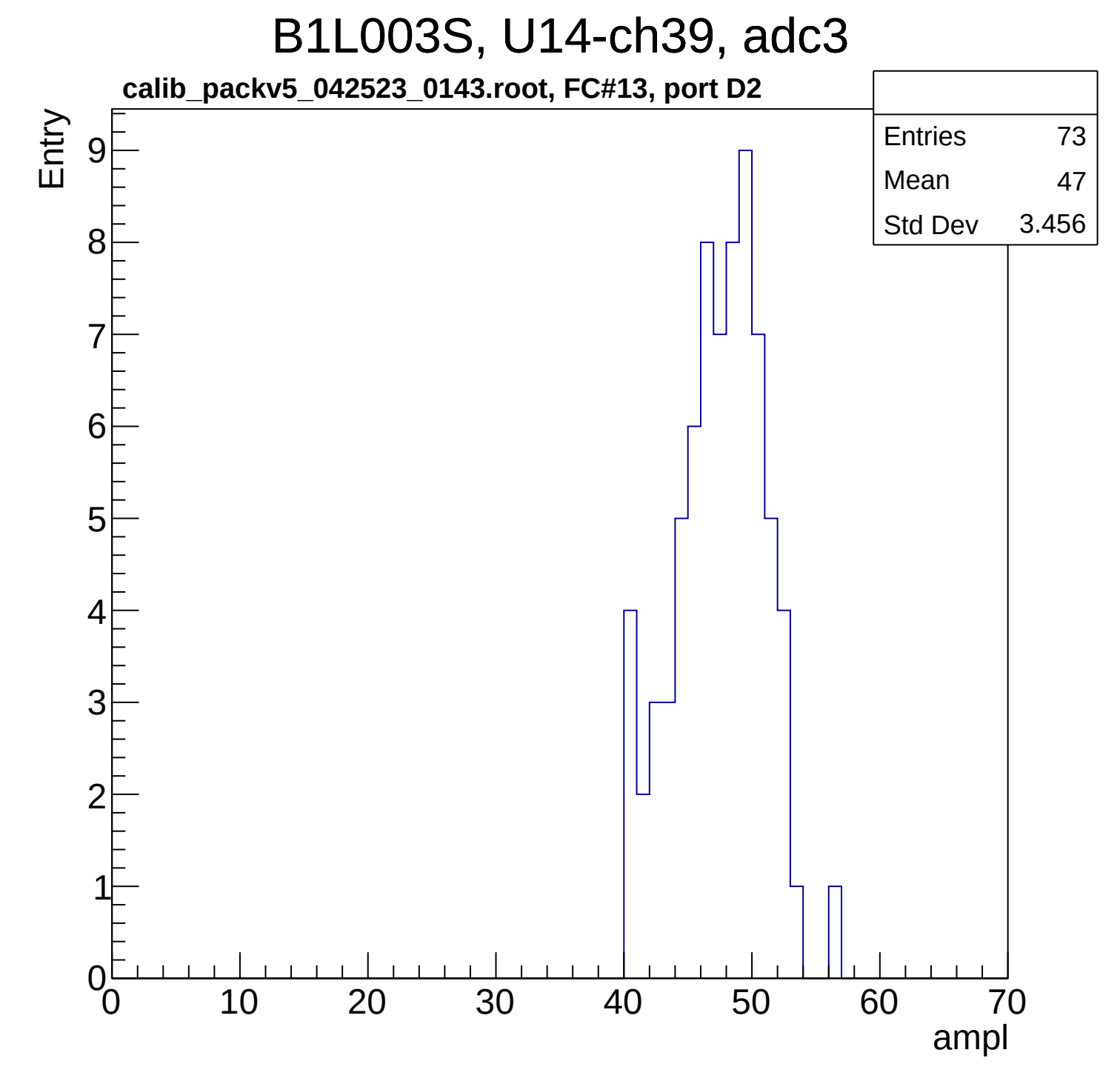

B1L003S, U14-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 58.39
Std Dev 12.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

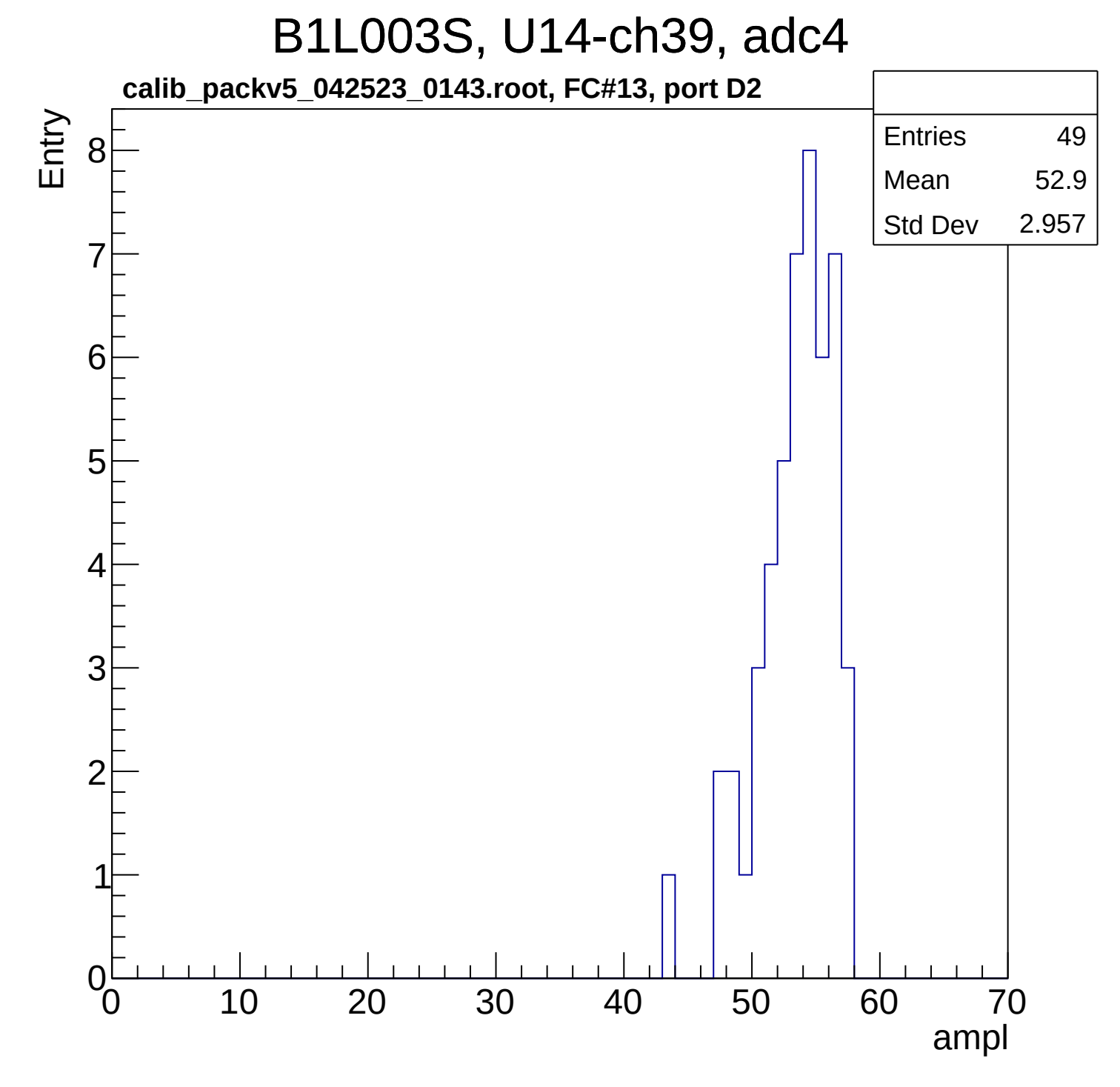

B1L003S, U14-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

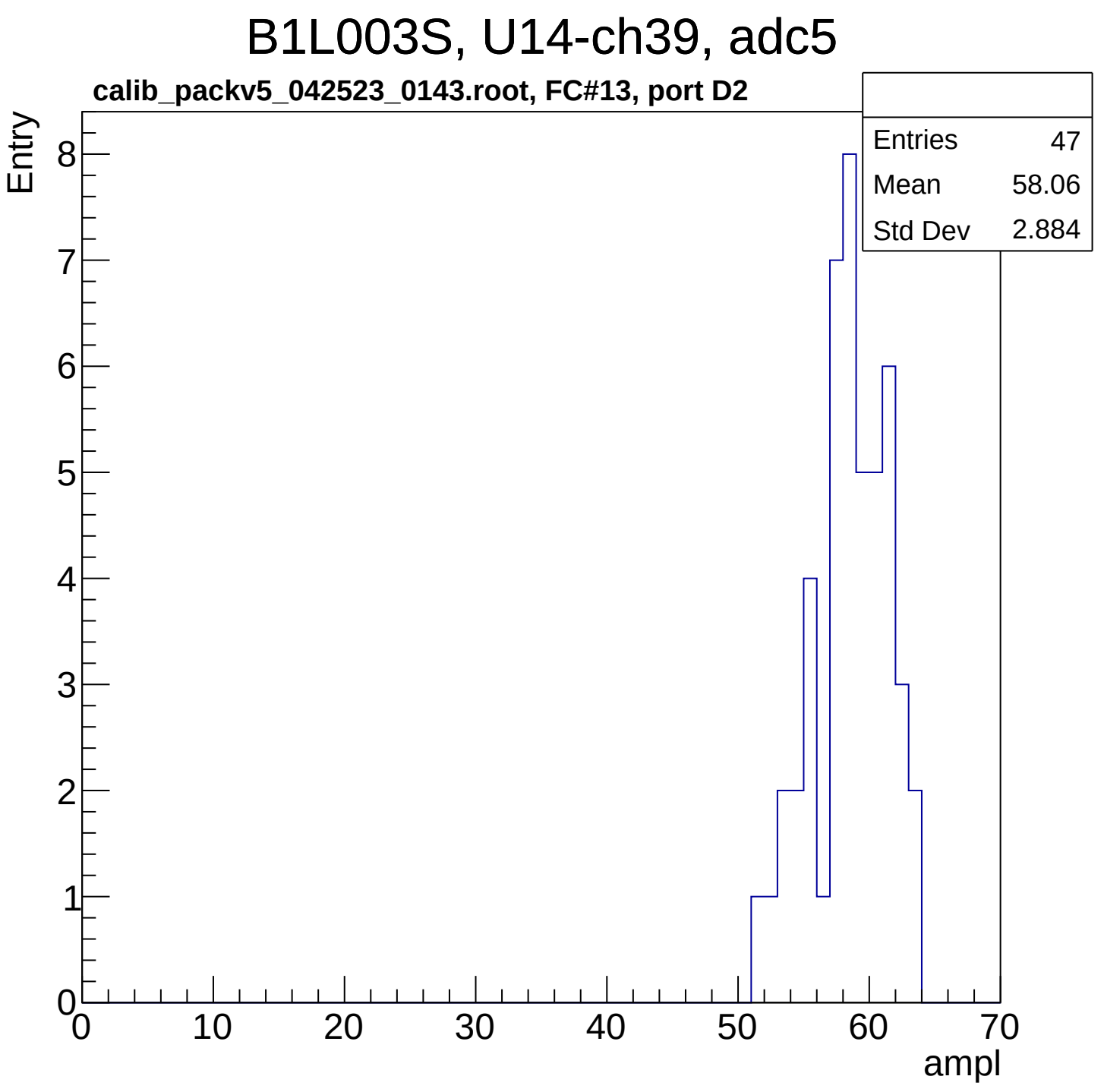

B1L003S, U14-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.26
Std Dev 5.902
Gaus mean : 28.9143
Gaus Width: 3.7687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

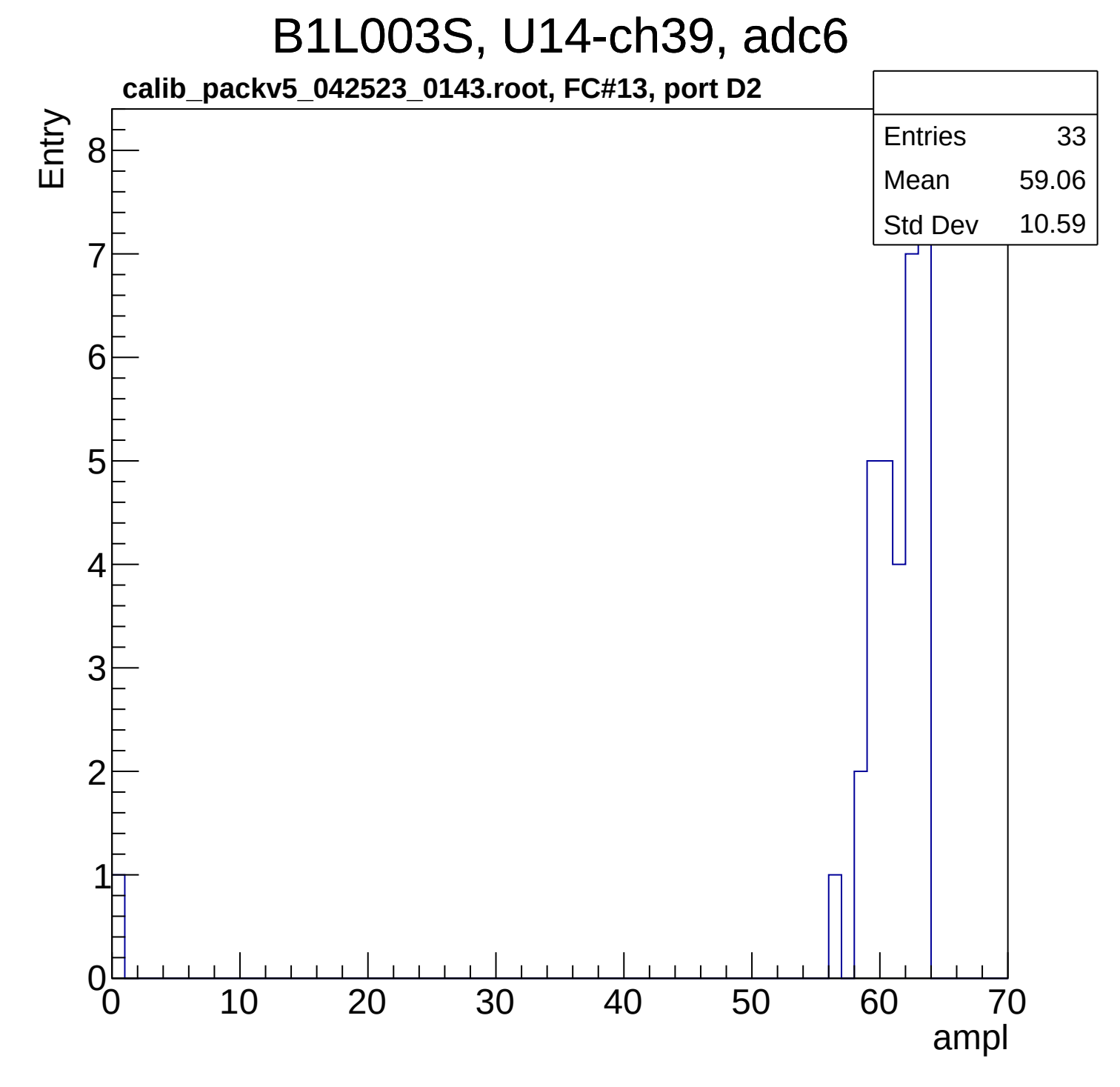

B1L003S, U14-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 33.83
Std Dev 2.831
Gaus mean : 34.3310
Gaus Width: 3.1890
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

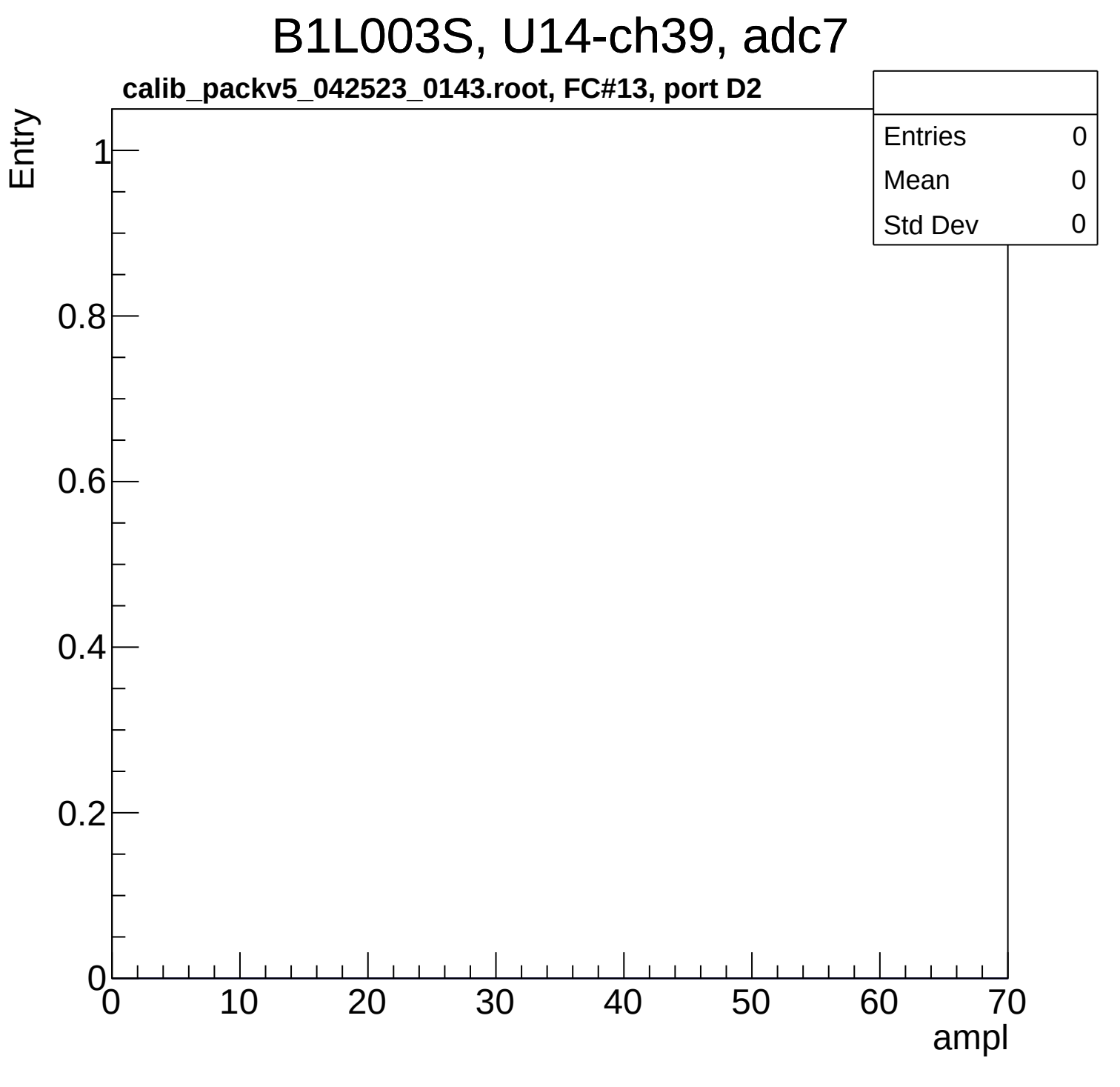

B1L003S, U14-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 40.23
Std Dev 3.131
Gaus mean : 40.6887
Gaus Width: 3.6388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

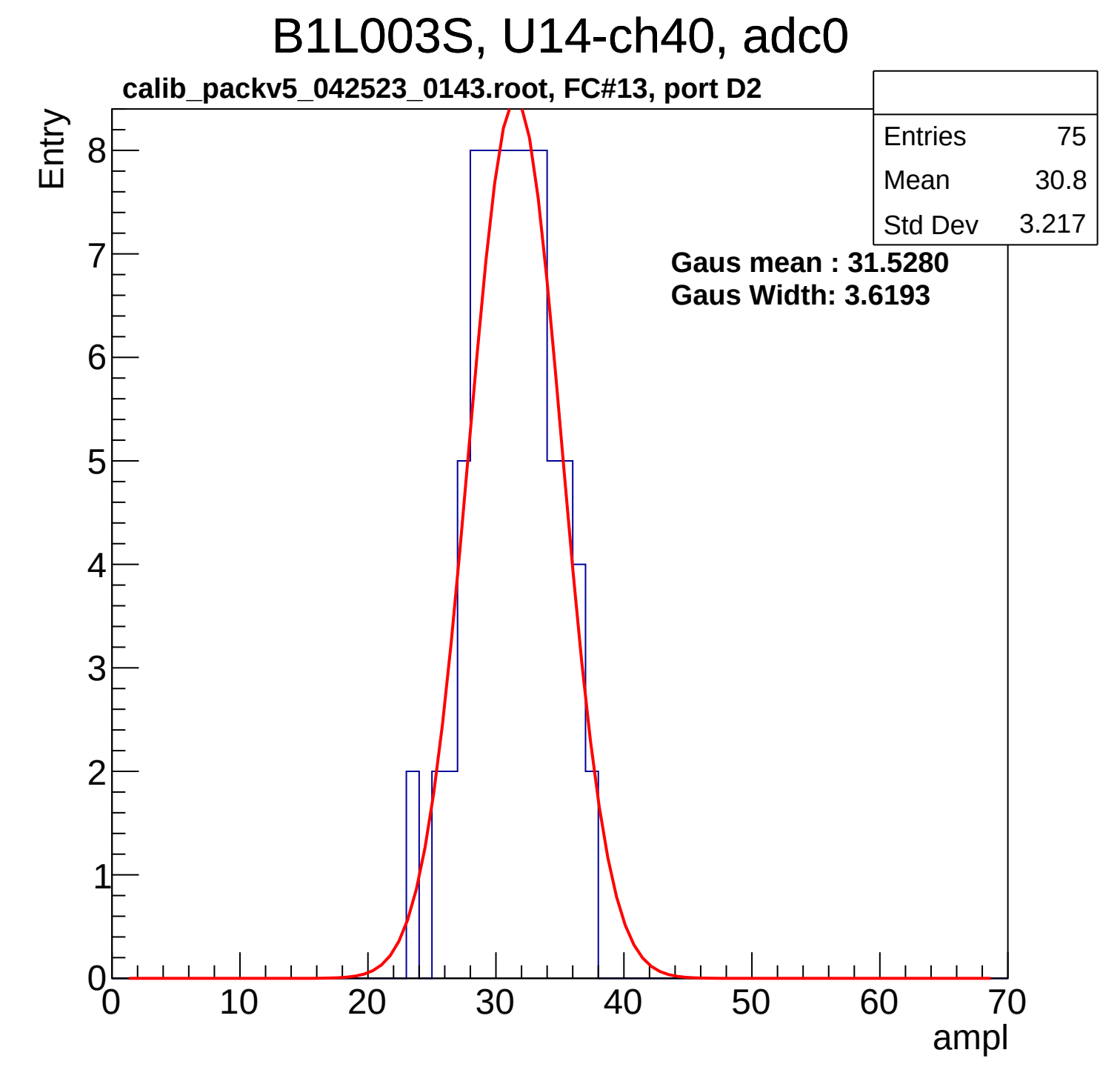

B1L003S, U14-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47
Std Dev 3.456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

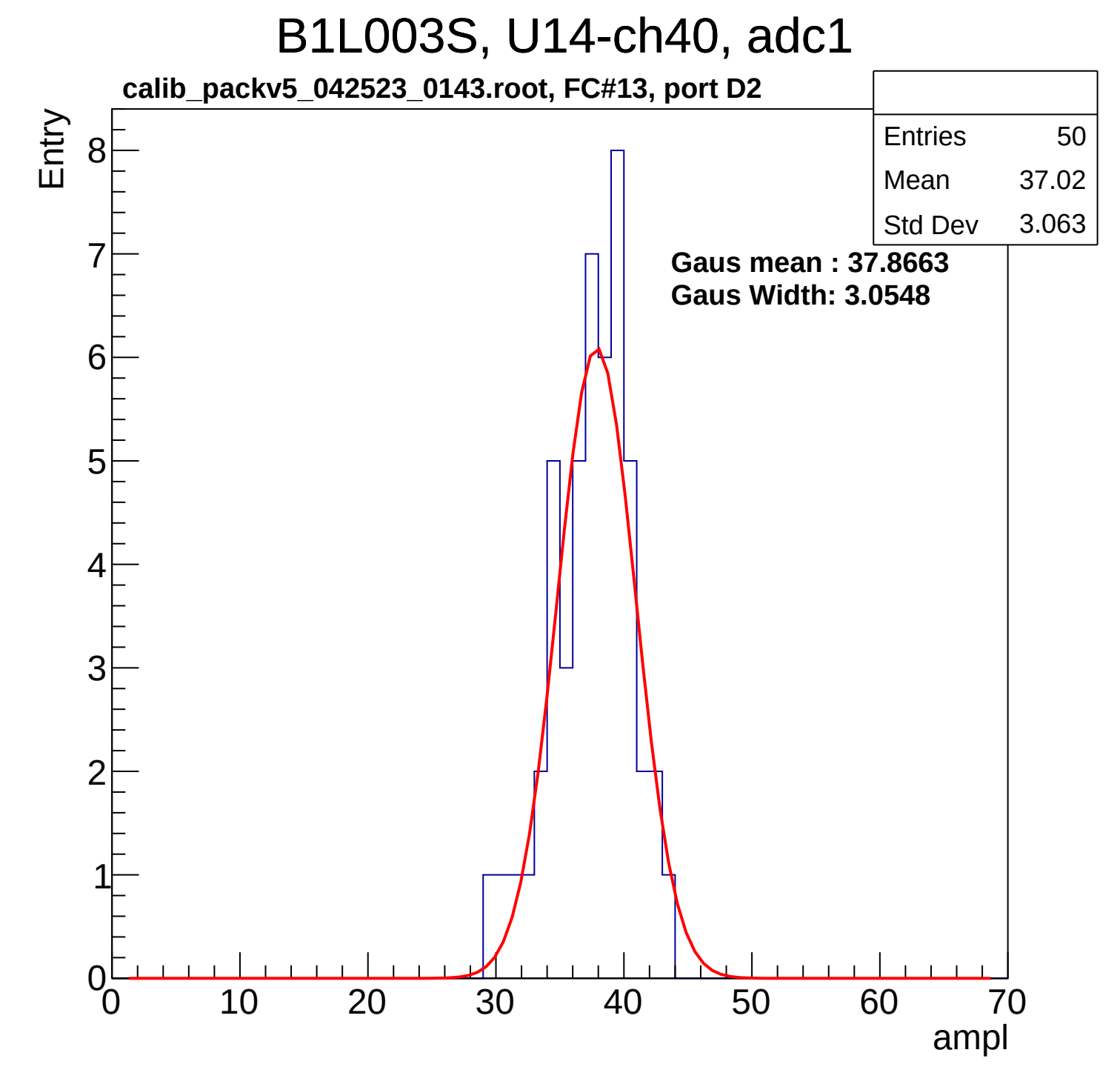

B1L003S, U14-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 52.9
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

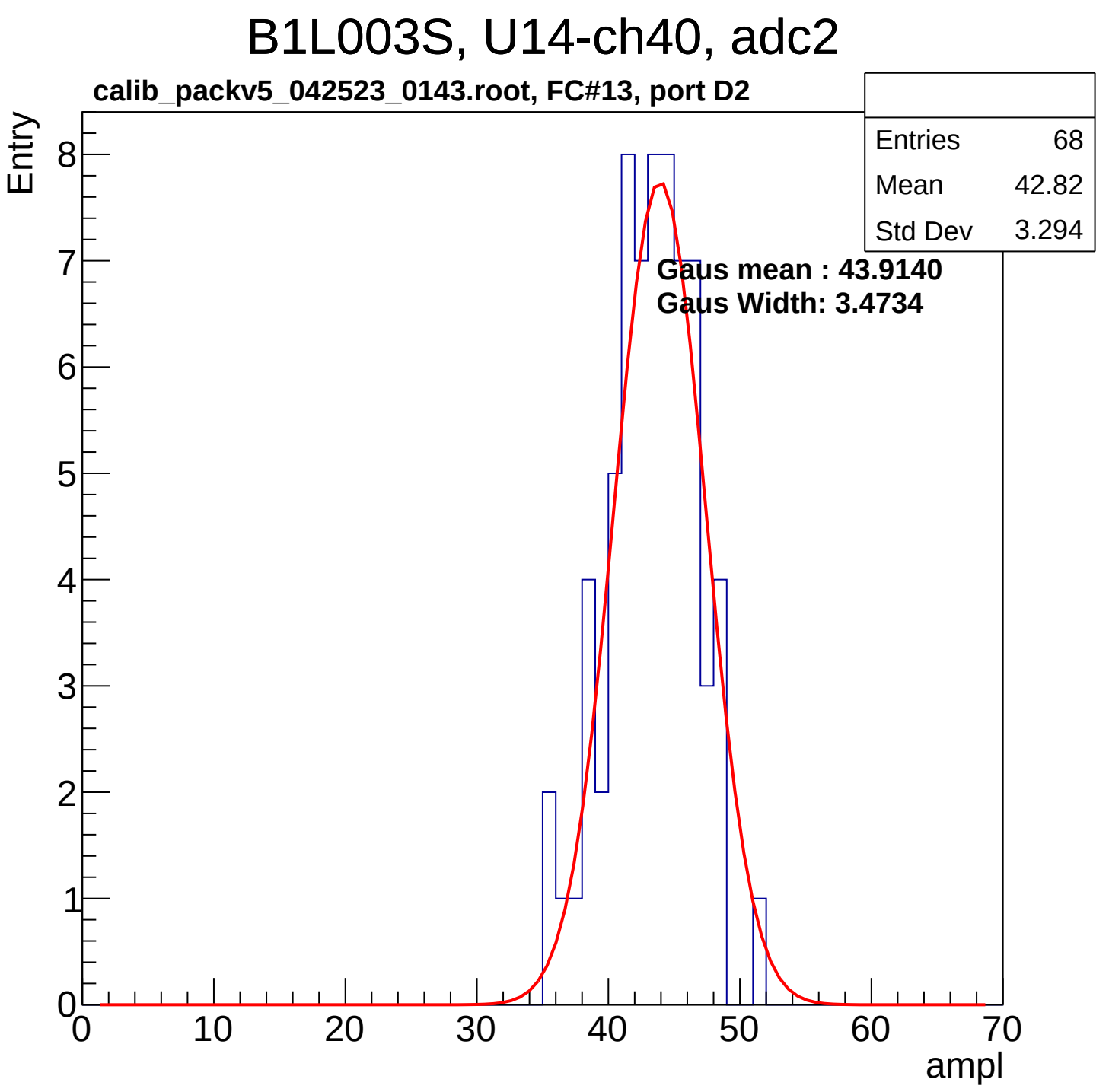

B1L003S, U14-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.06
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

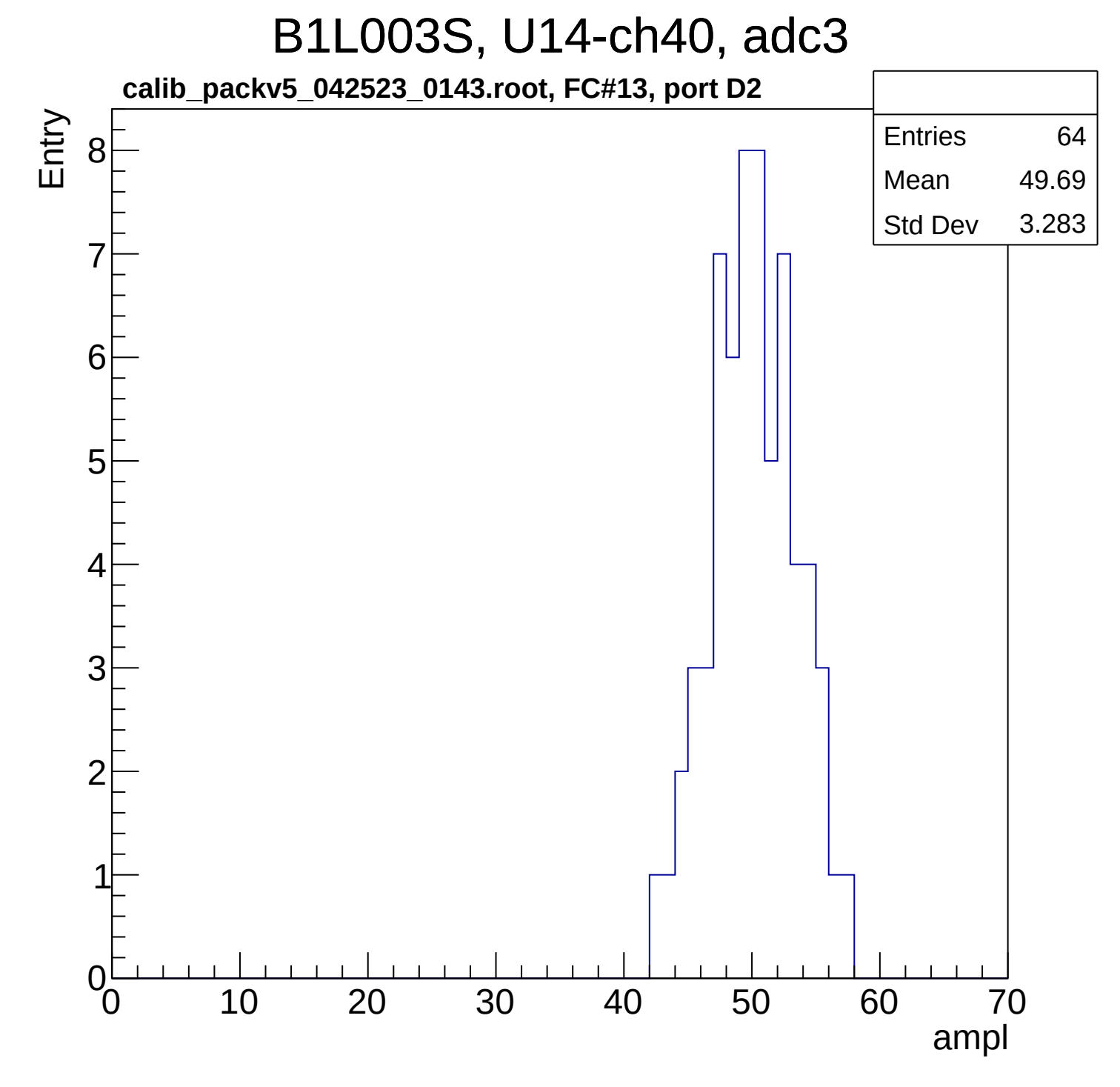

B1L003S, U14-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 59.06
Std Dev 10.59
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

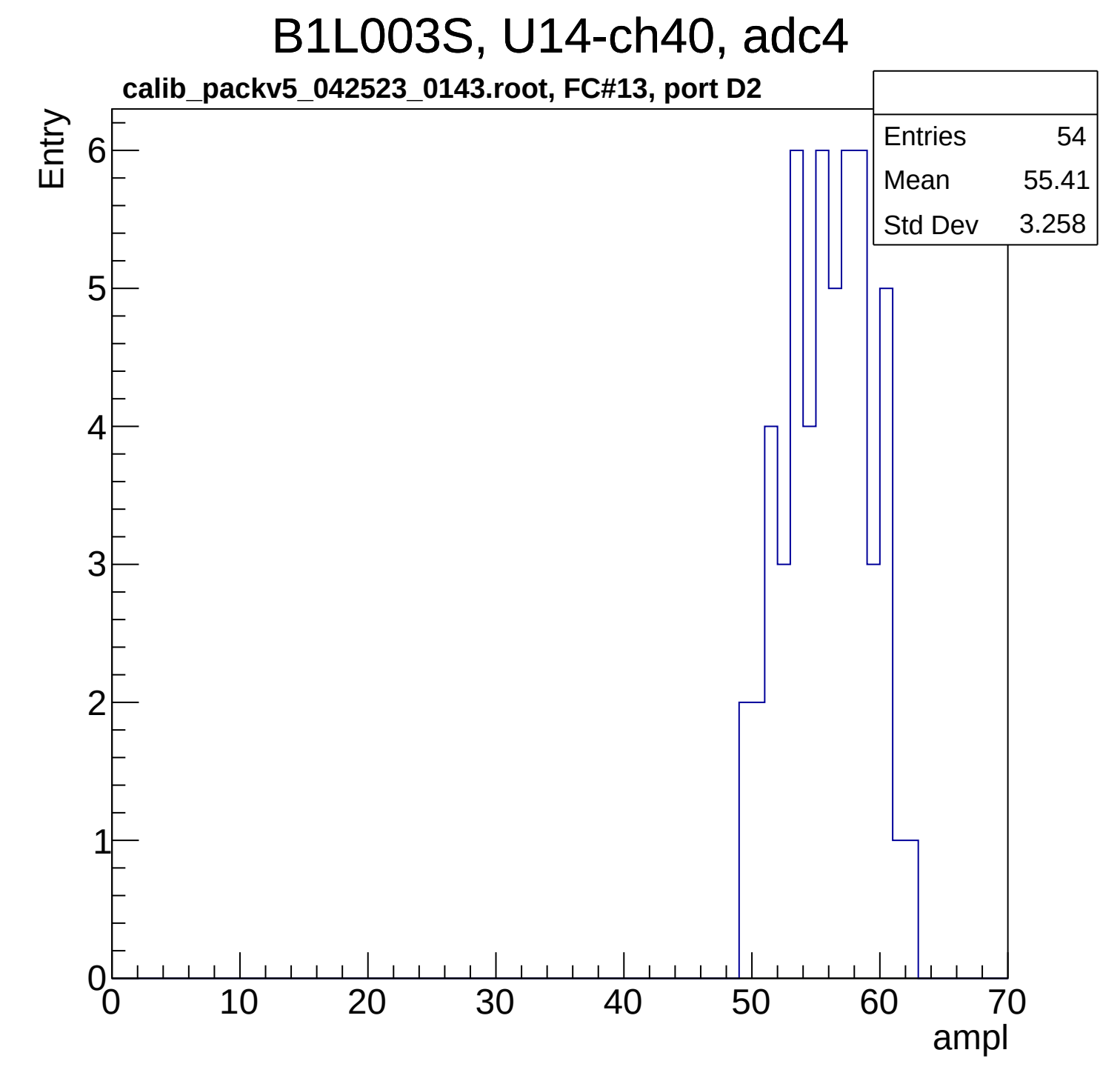

B1L003S, U14-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

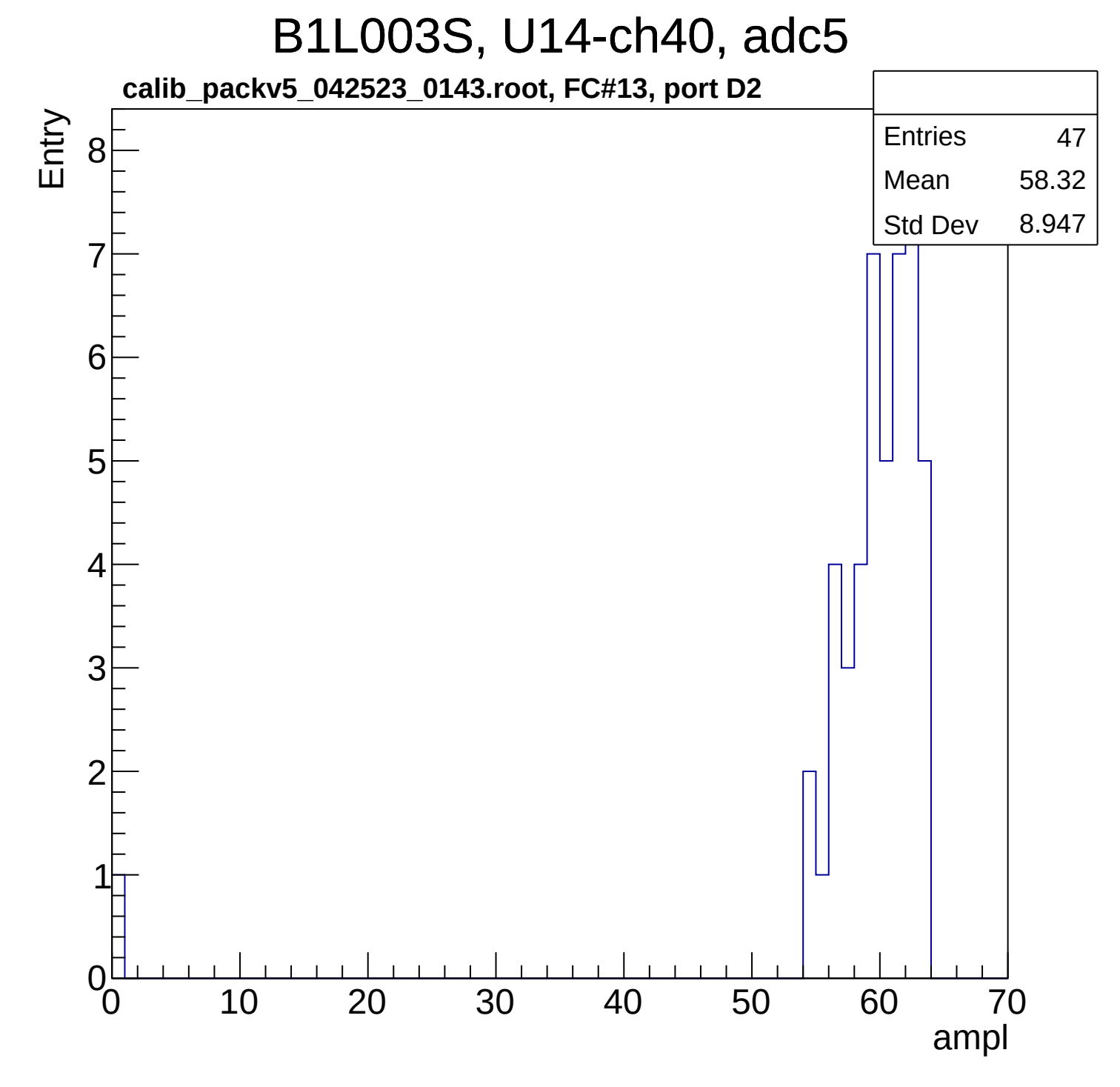

B1L003S, U14-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 30.8
Std Dev 3.217
Gaus mean : 31.5280
Gaus Width: 3.6193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

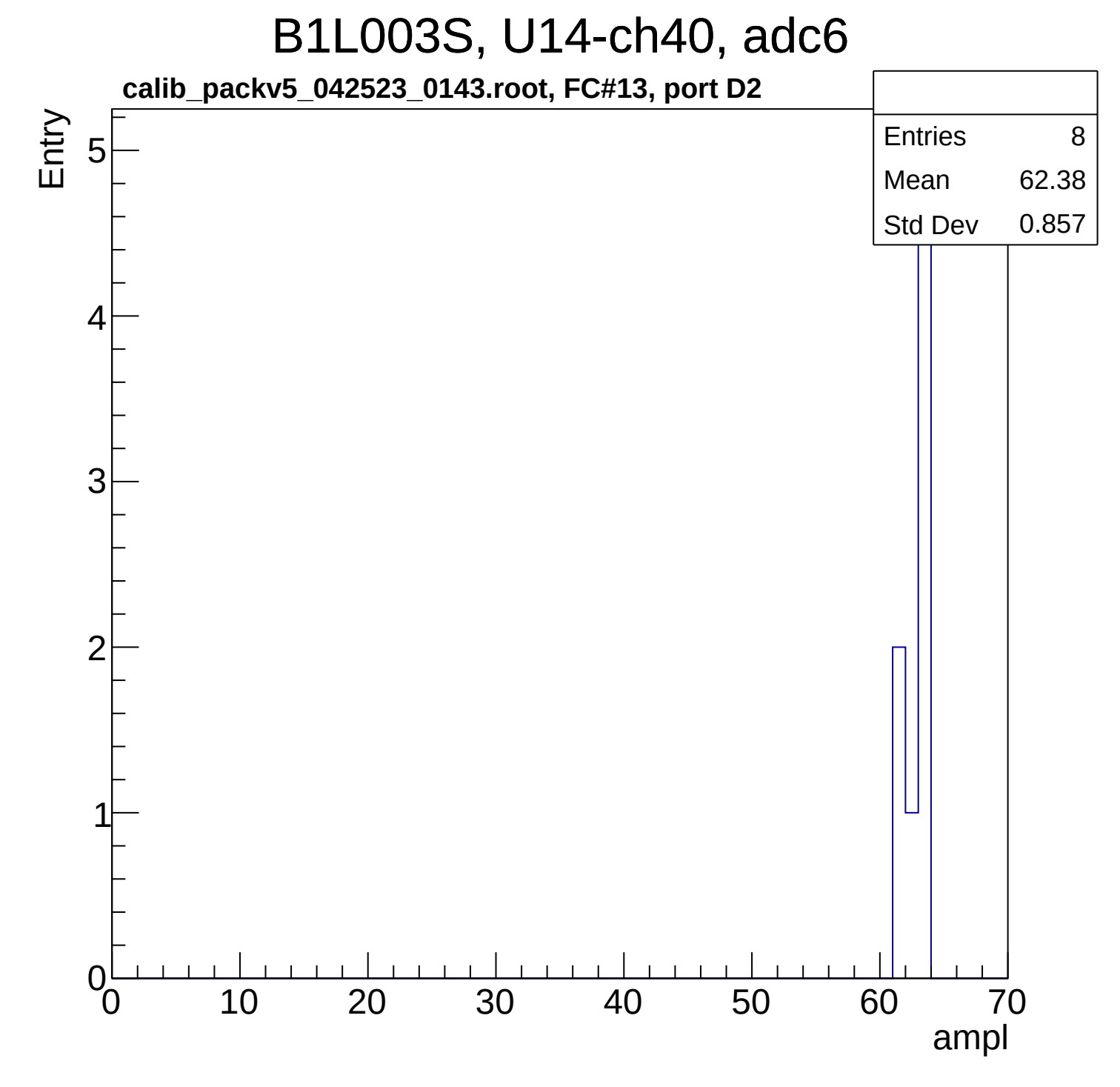

B1L003S, U14-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 37.02
Std Dev 3.063
Gaus mean : 37.8663
Gaus Width: 3.0548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

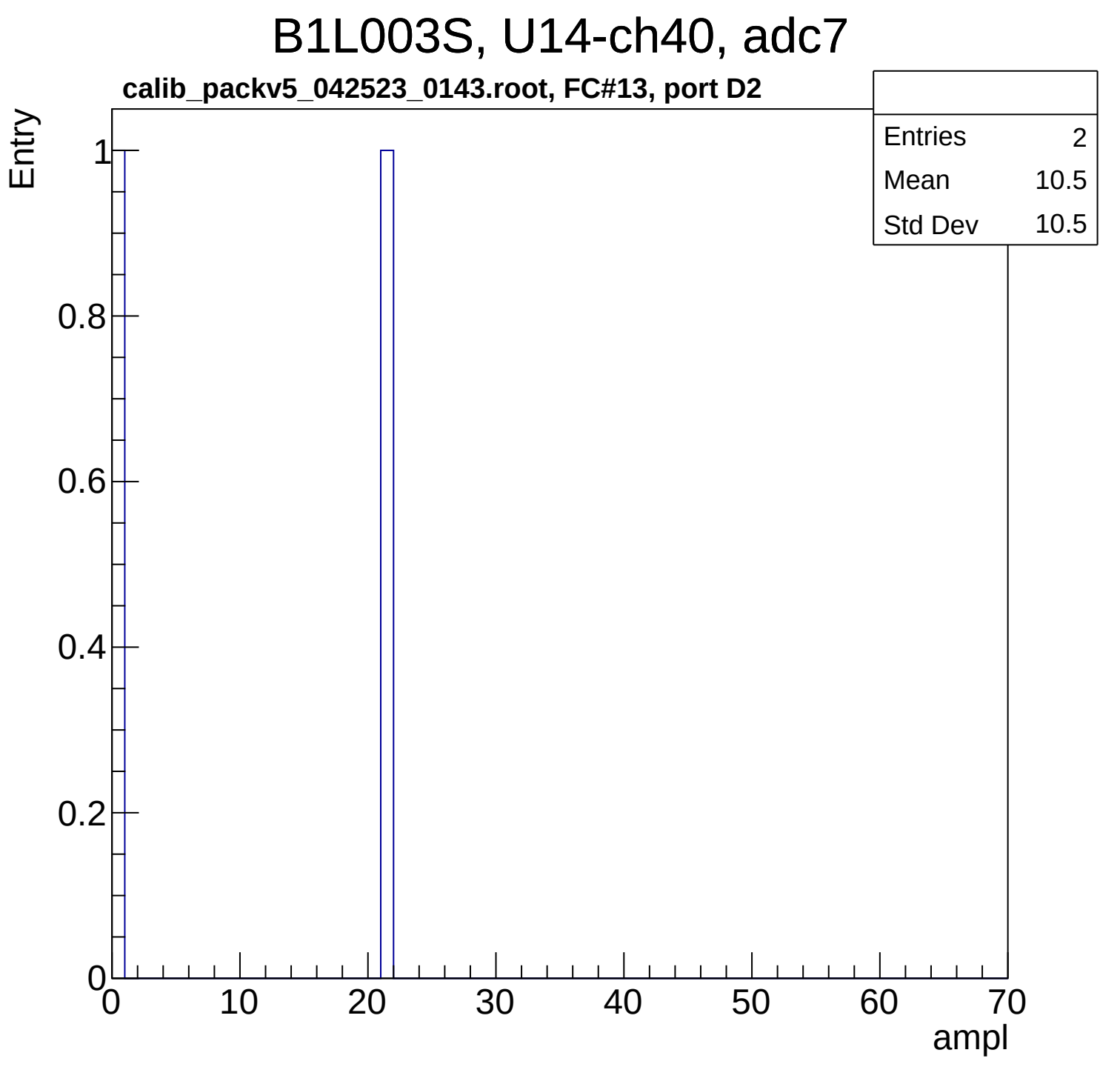

B1L003S, U14-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.82
Std Dev 3.294
Gaus mean : 43.9140
Gaus Width: 3.4734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

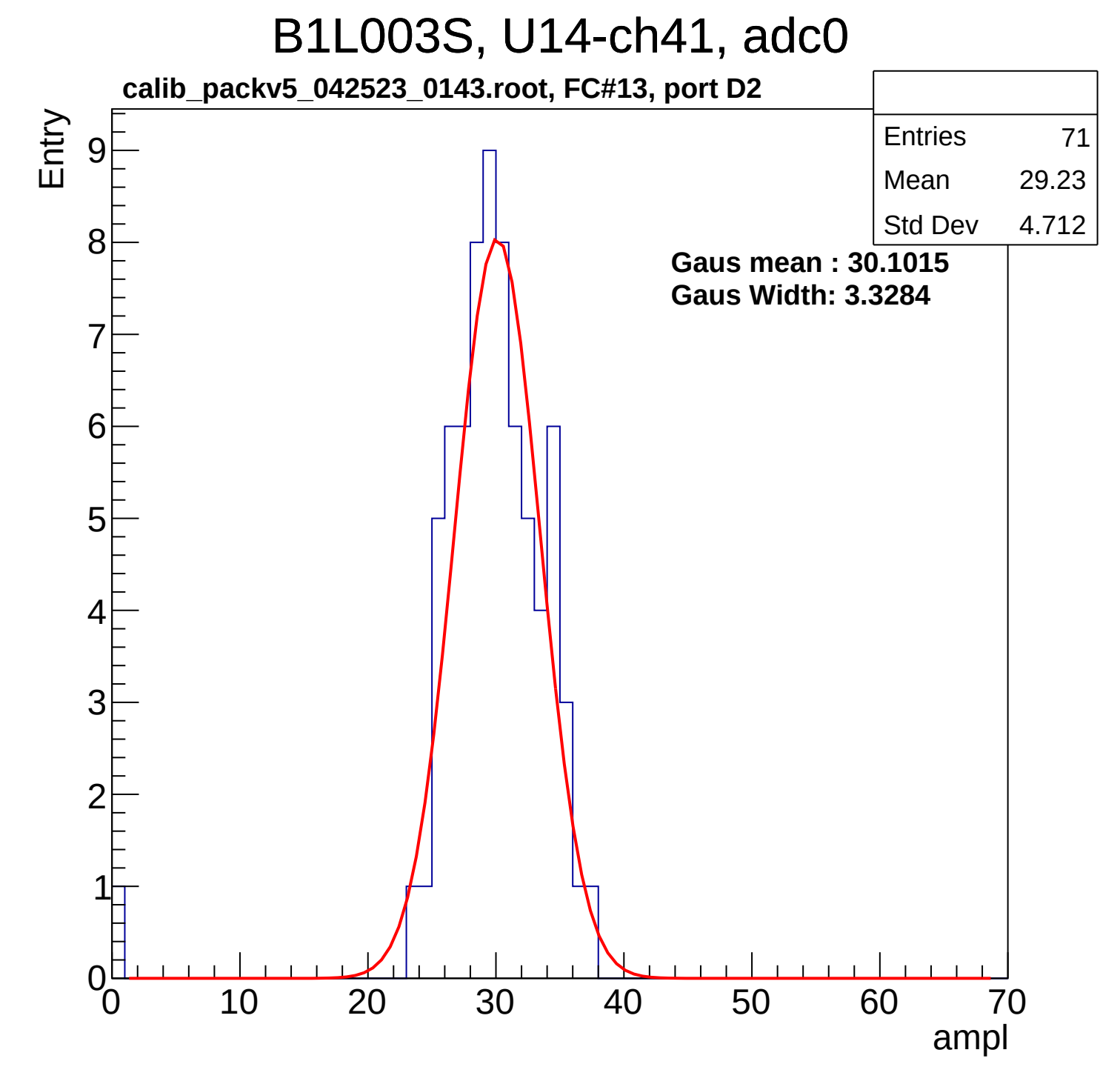

B1L003S, U14-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.69
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

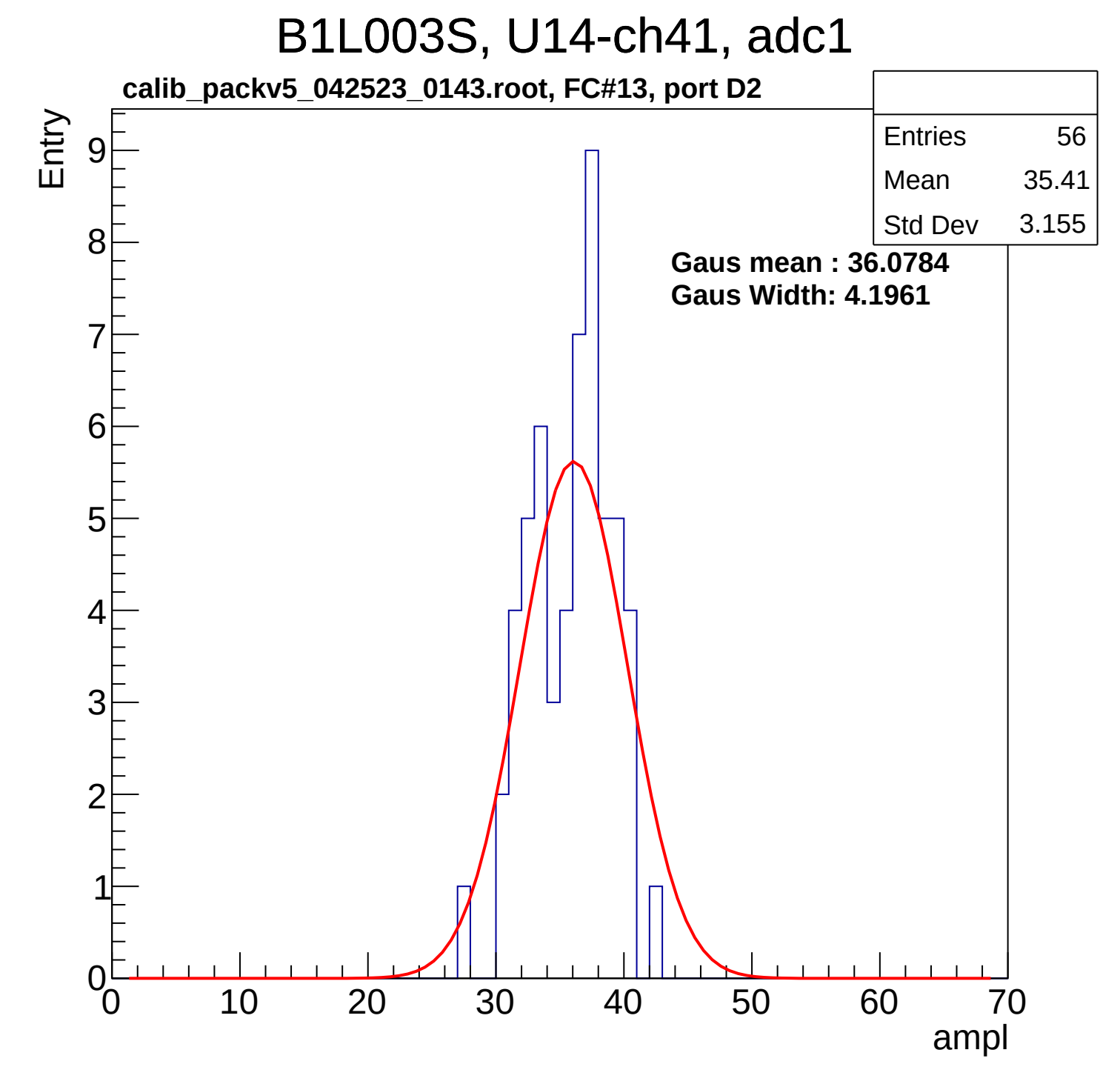

B1L003S, U14-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.41
Std Dev 3.258
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

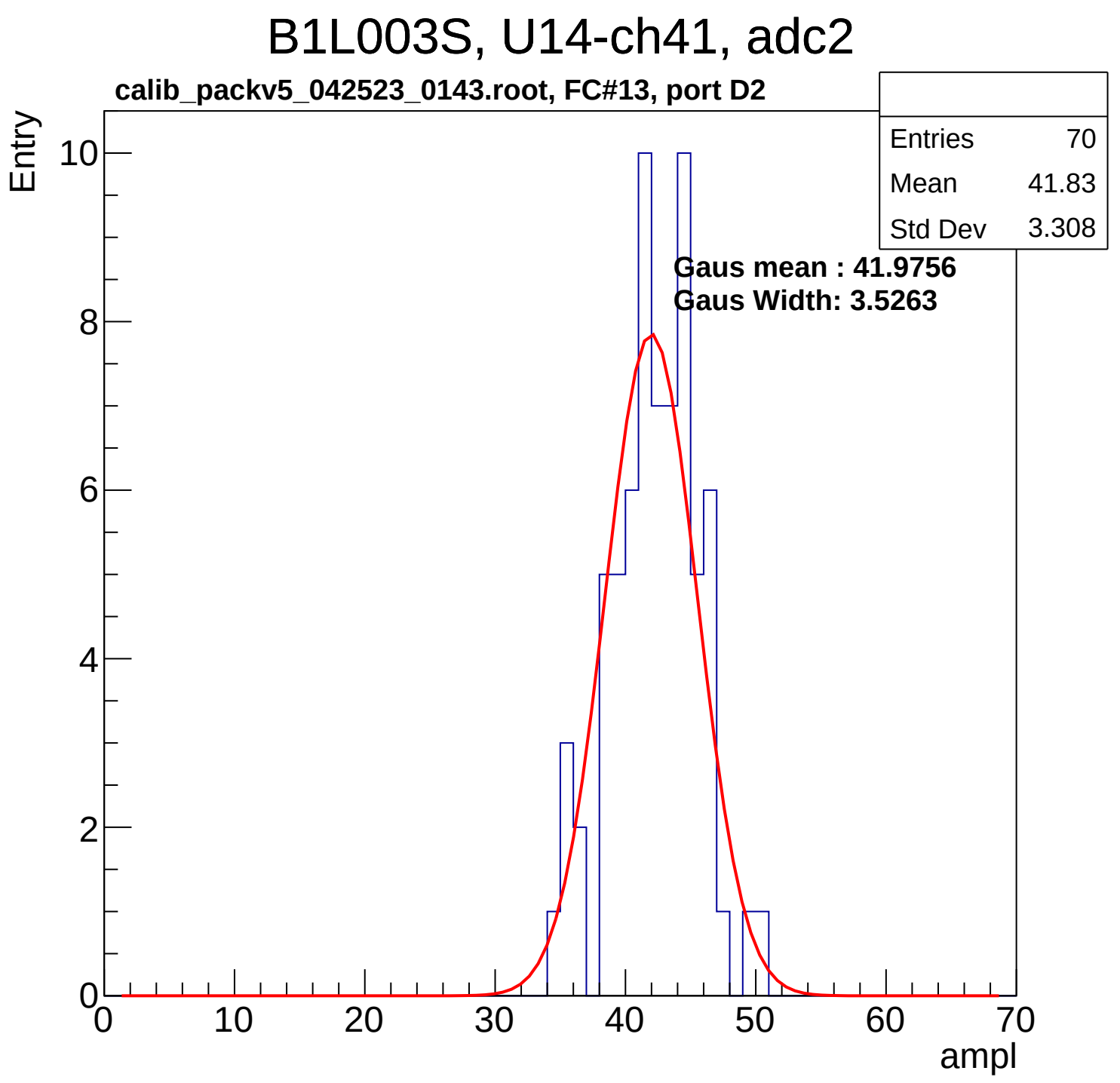

B1L003S, U14-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.32
Std Dev 8.947
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

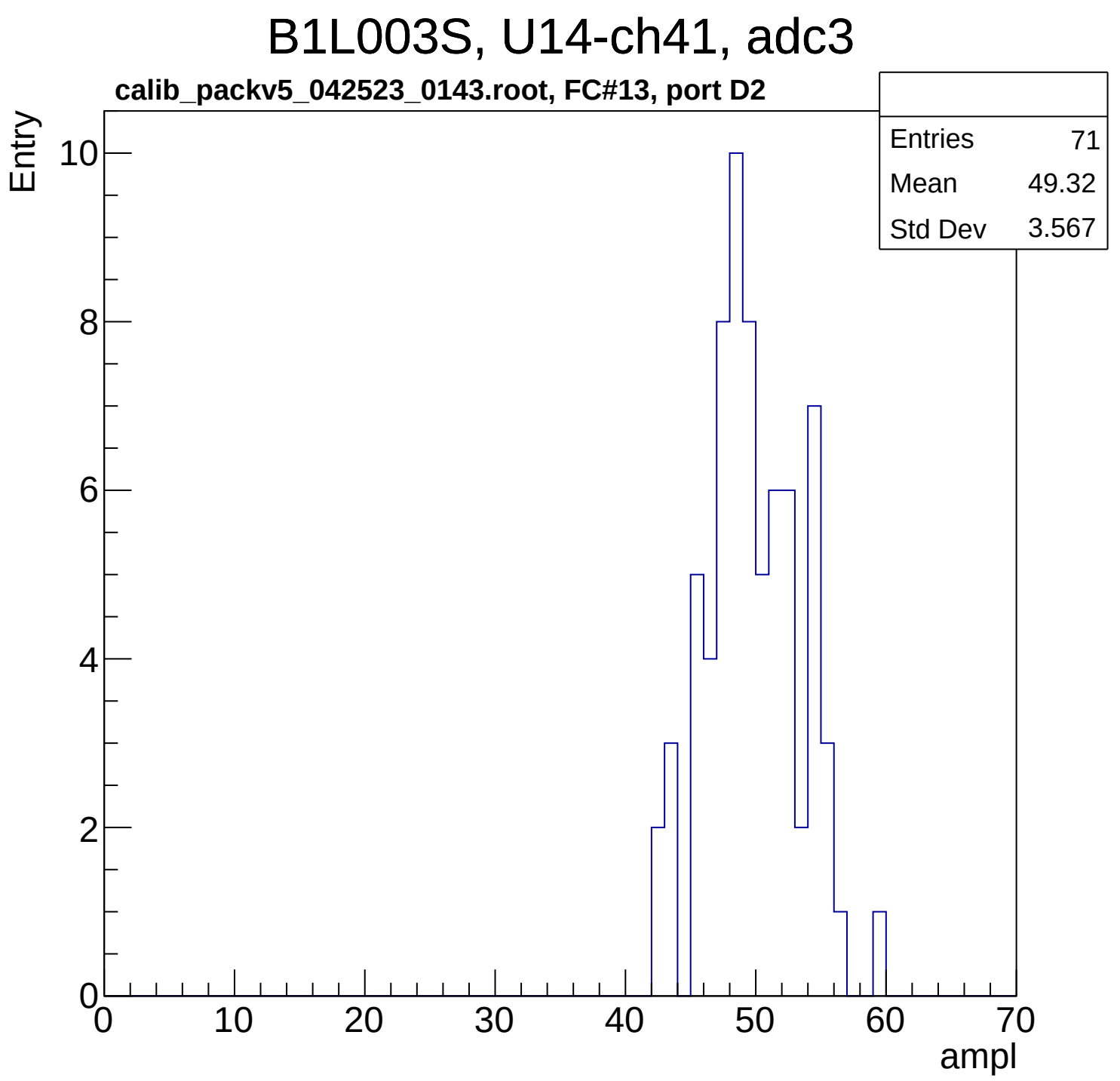

B1L003S, U14-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.38
Std Dev 0.857
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

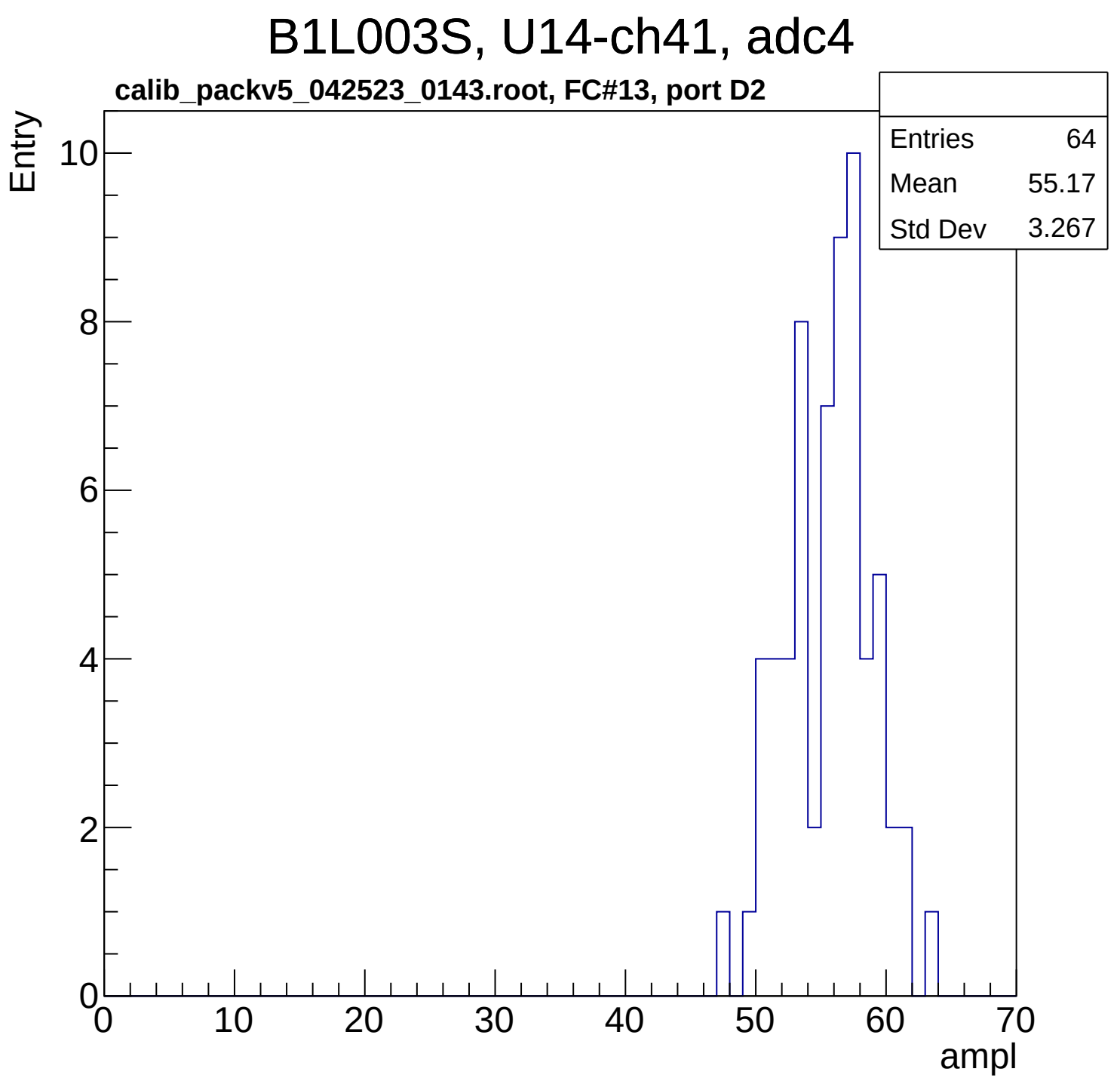

B1L003S, U14-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

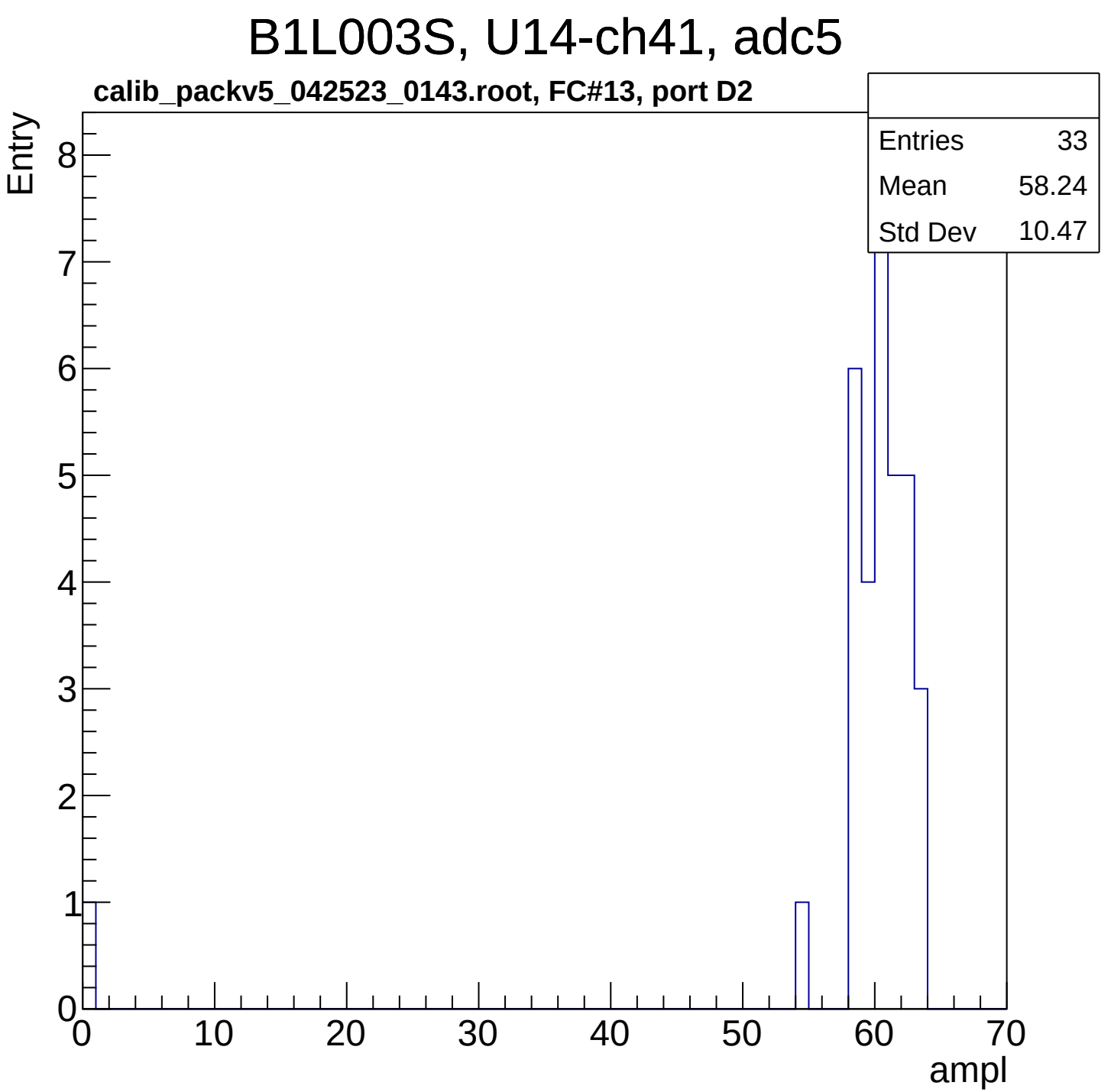

B1L003S, U14-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.23
Std Dev 4.712
Gaus mean : 30.1015
Gaus Width: 3.3284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

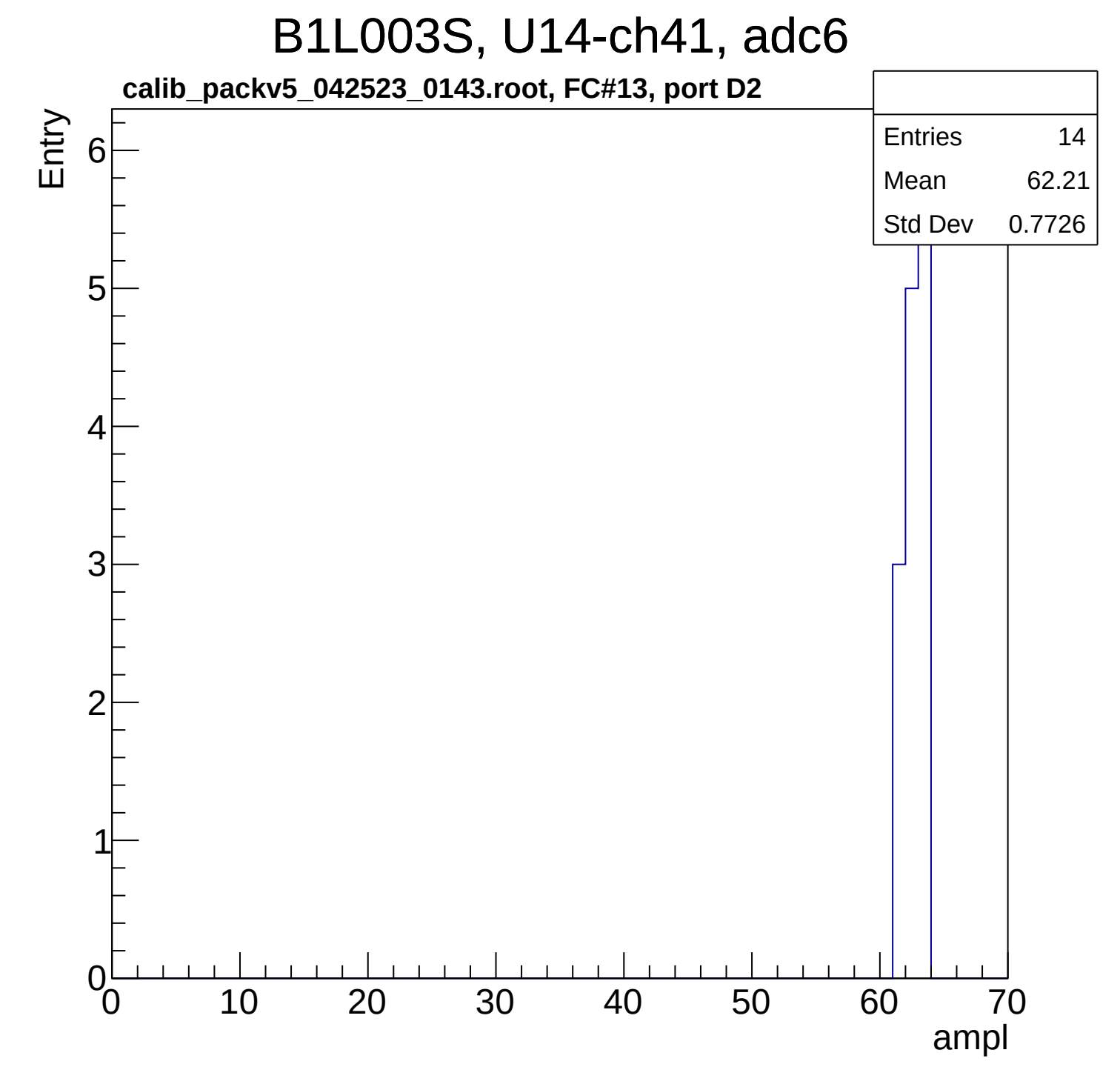

B1L003S, U14-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 35.41
Std Dev 3.155
Gaus mean : 36.0784
Gaus Width: 4.1961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

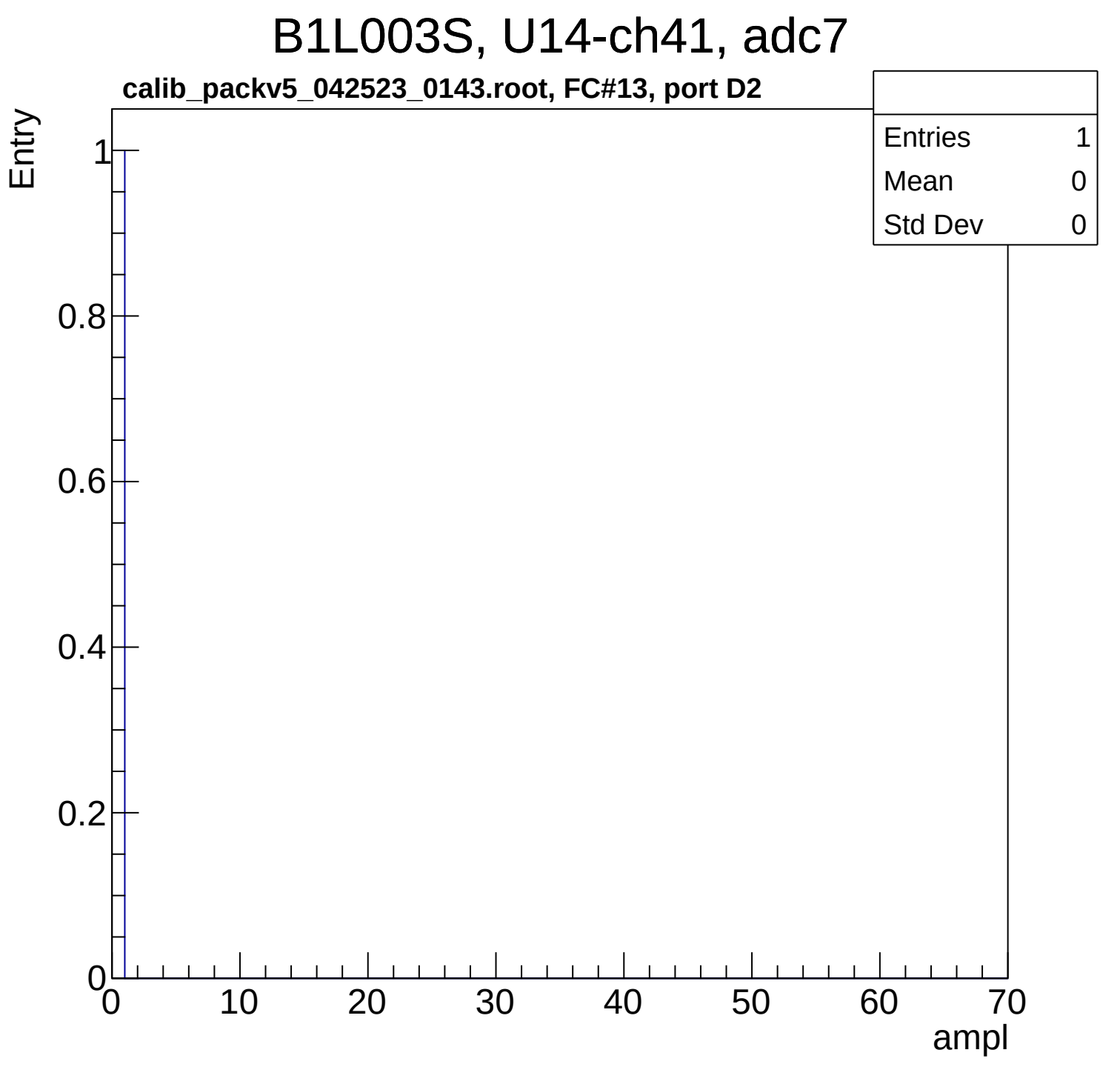

B1L003S, U14-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.83
Std Dev 3.308
Gaus mean : 41.9756
Gaus Width: 3.5263
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

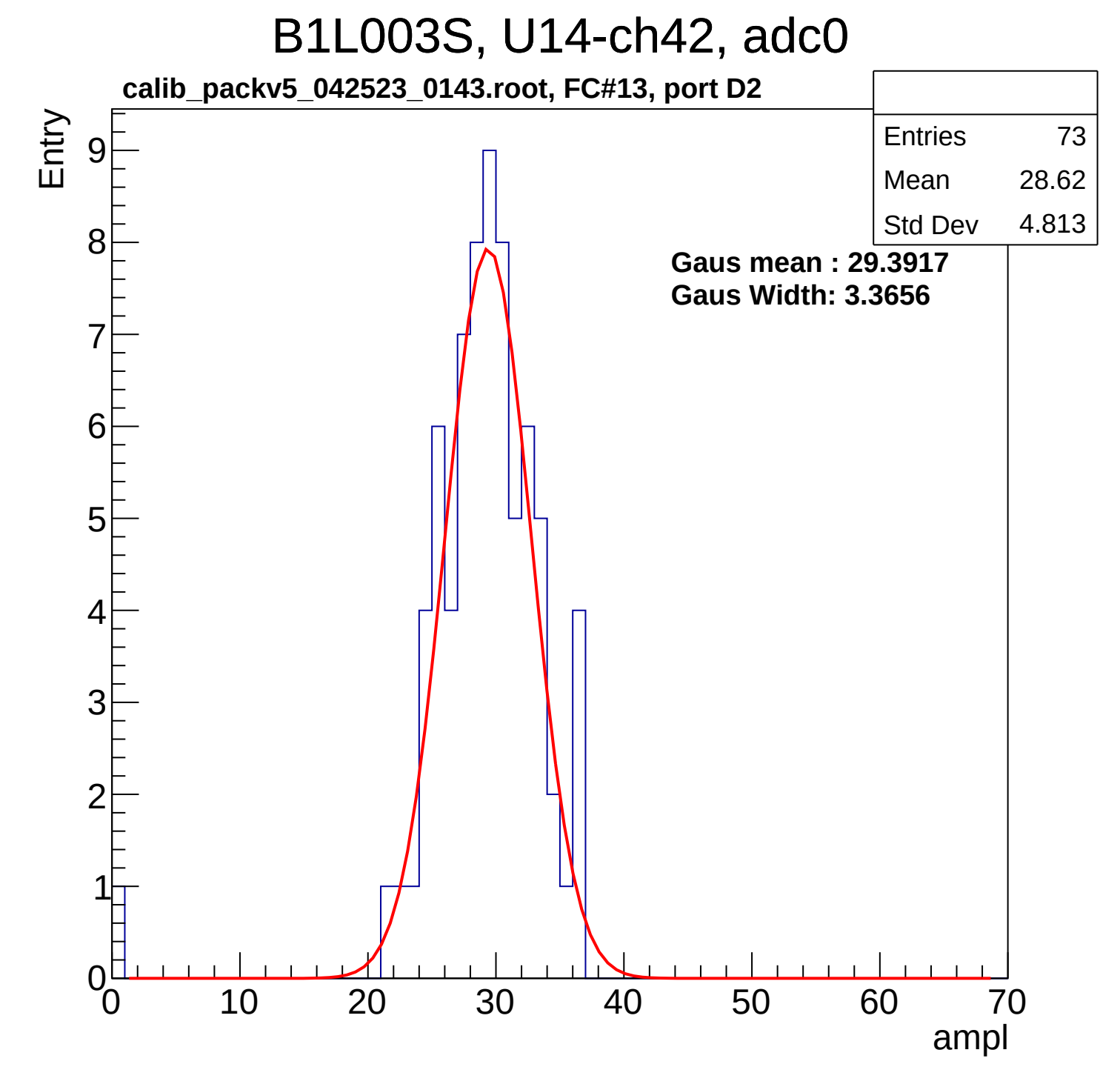

B1L003S, U14-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.32
Std Dev 3.567
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

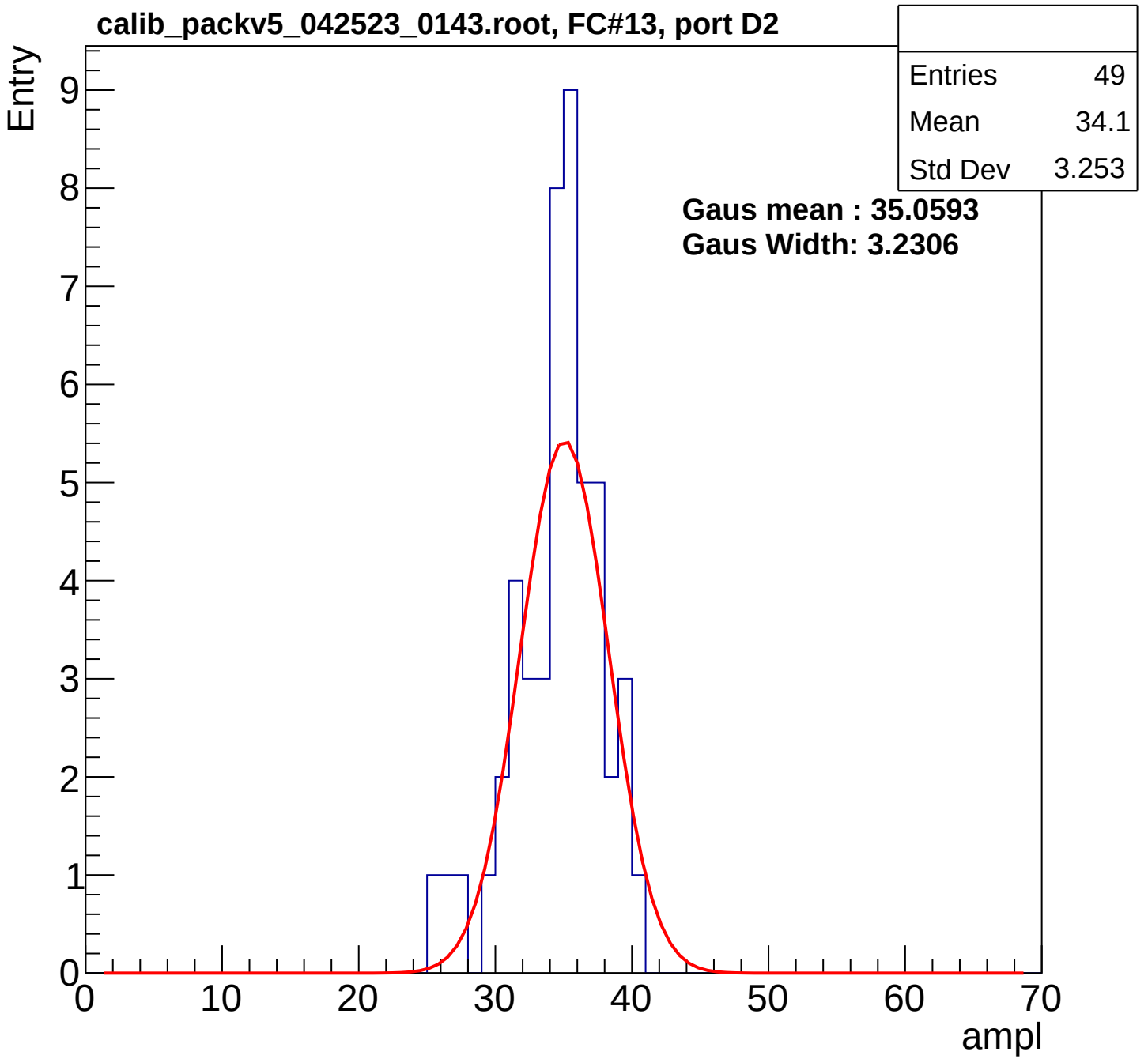

B1L003S, U14-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 55.17
Std Dev 3.267
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

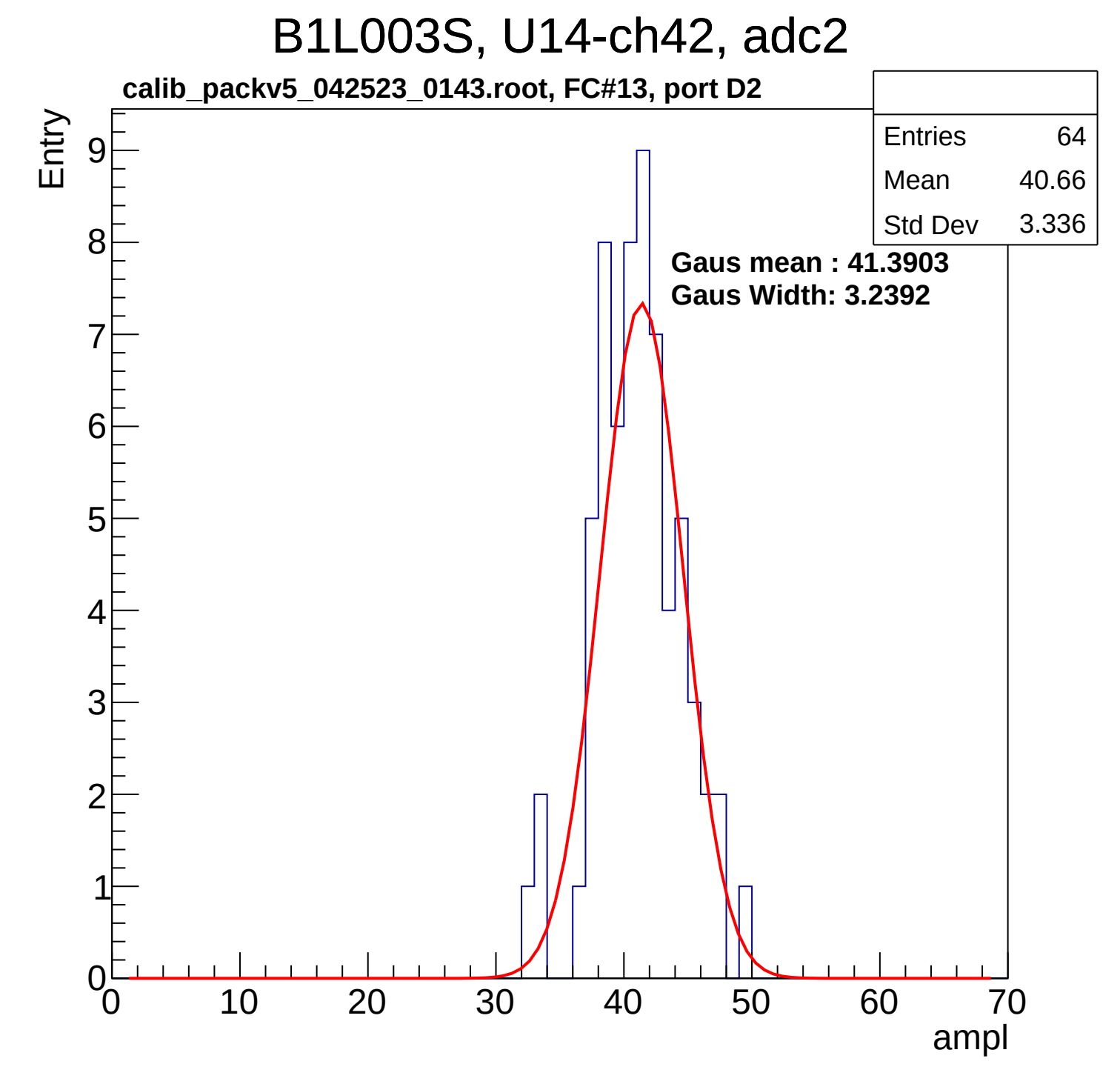

B1L003S, U14-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 58.24
Std Dev 10.47
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

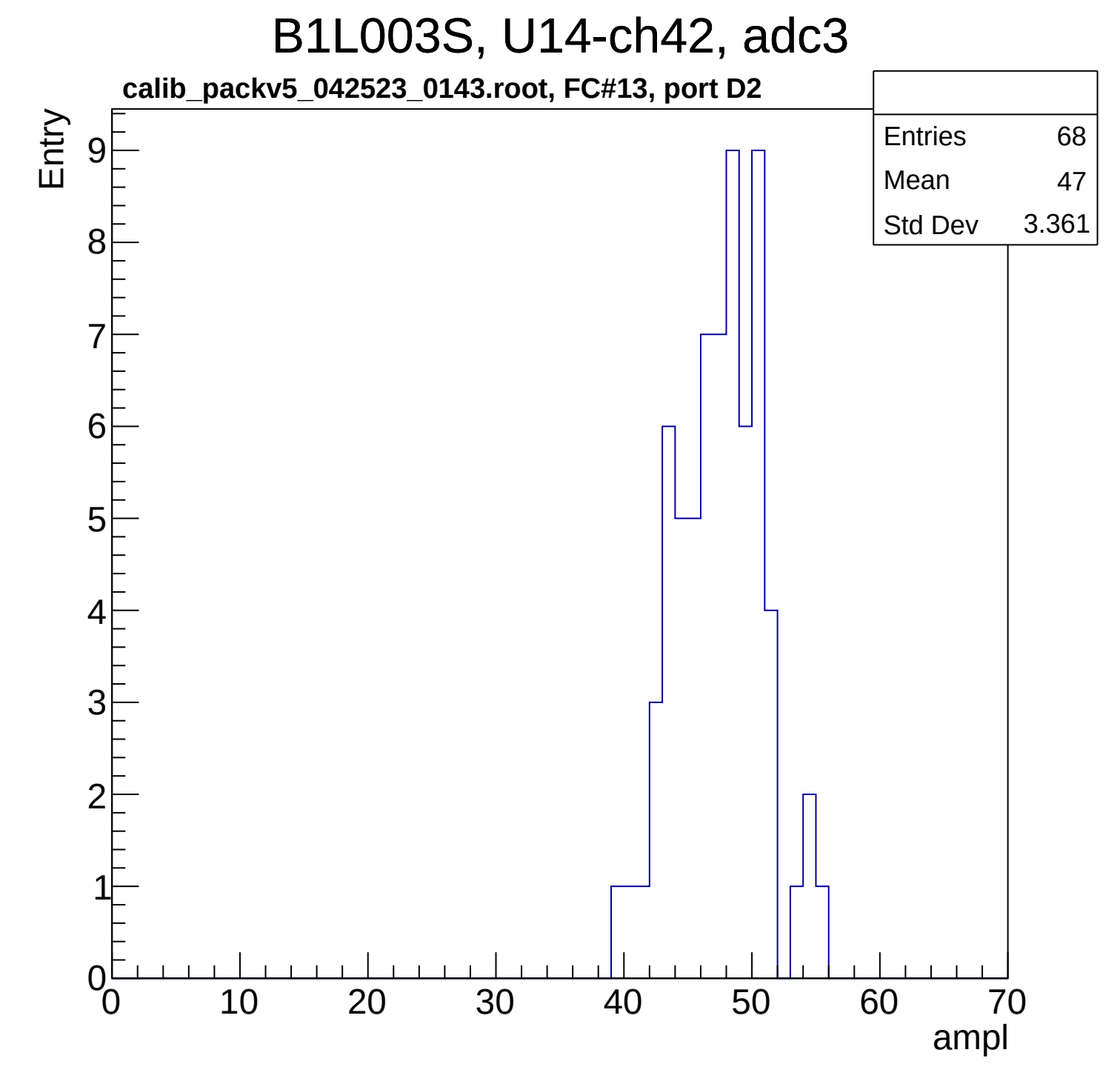

B1L003S, U14-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 62.21
Std Dev 0.7726
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

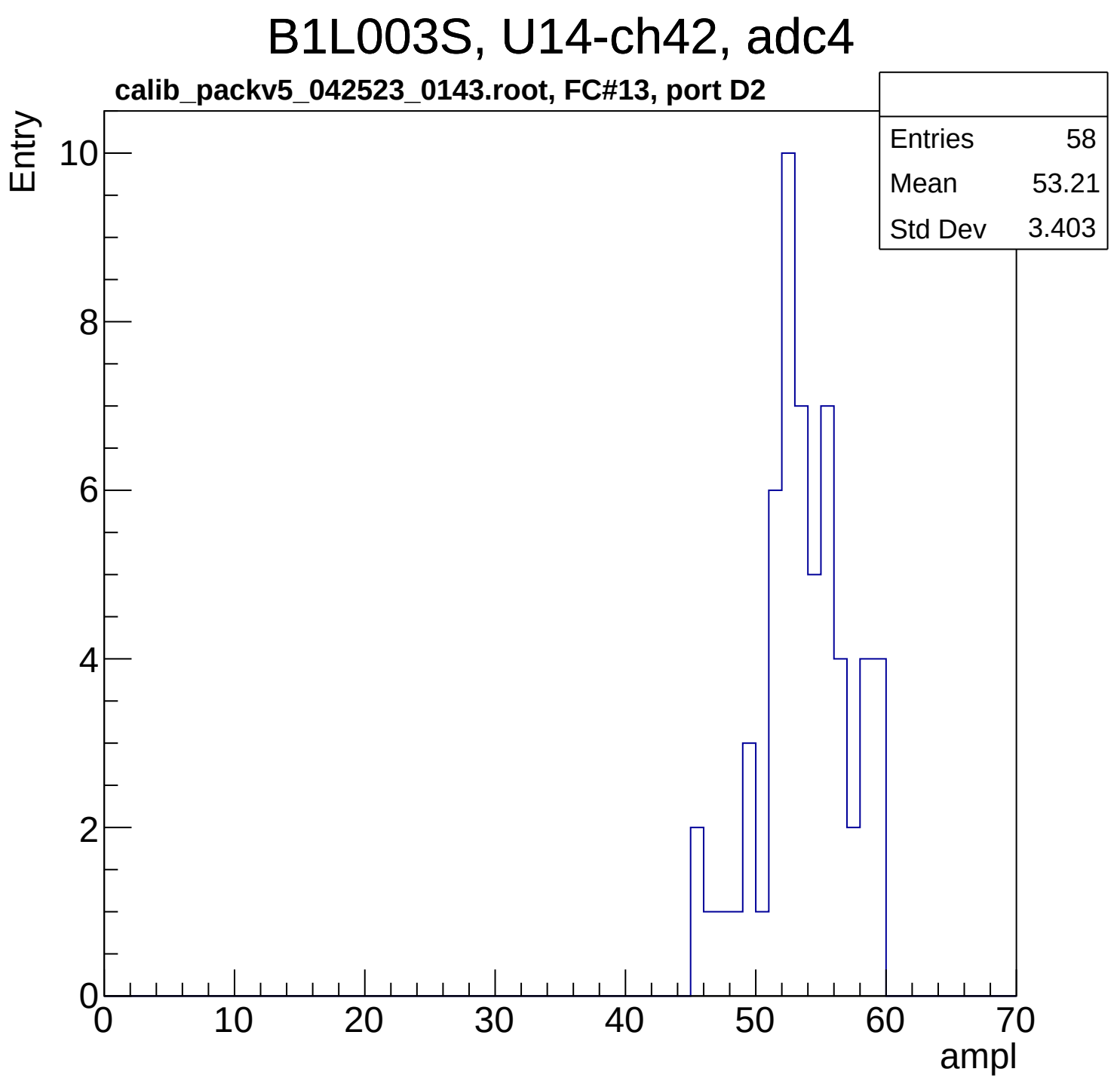

B1L003S, U14-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

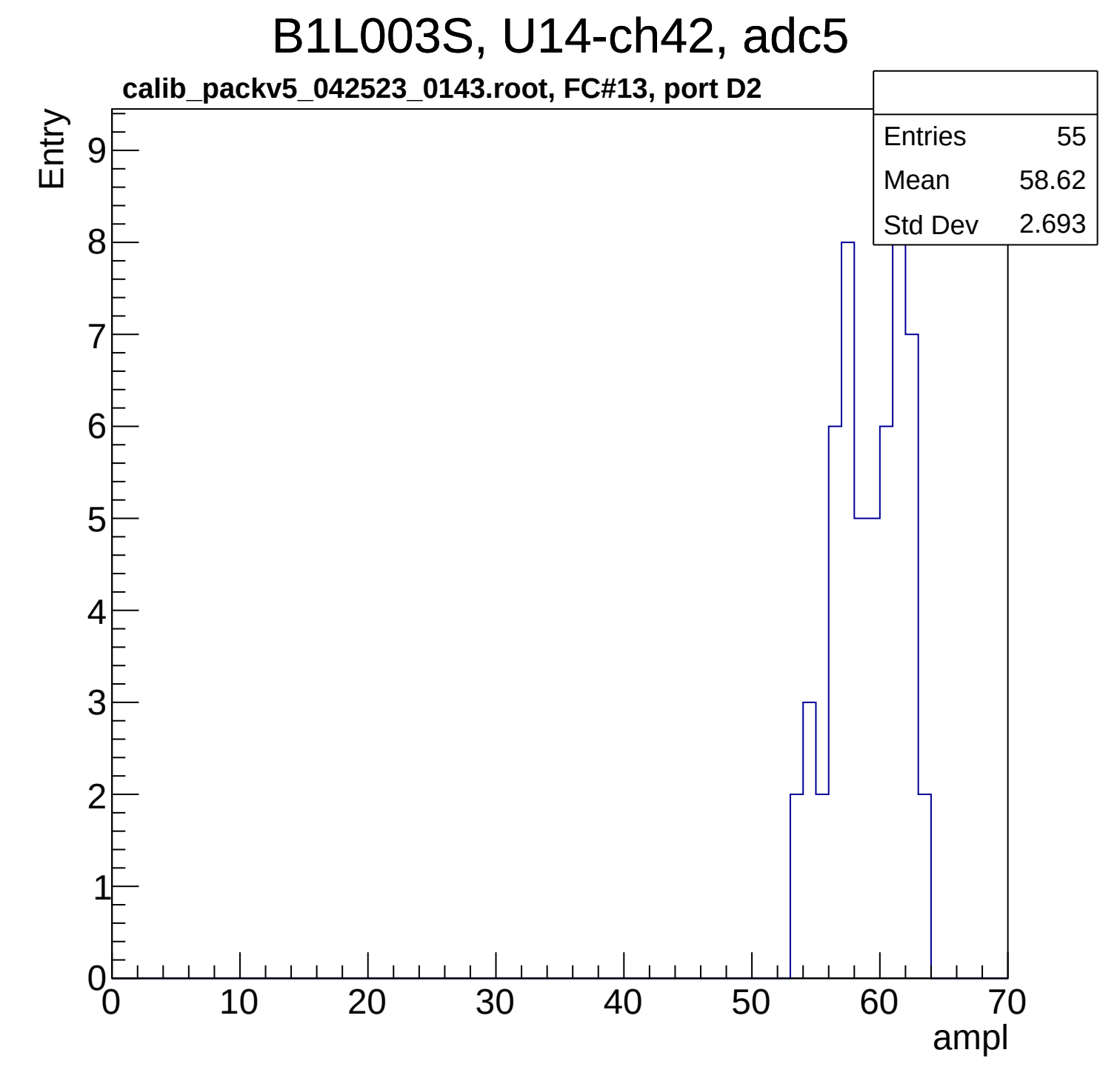

B1L003S, U14-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.62
Std Dev 4.813
Gaus mean : 29.3917
Gaus Width: 3.3656
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

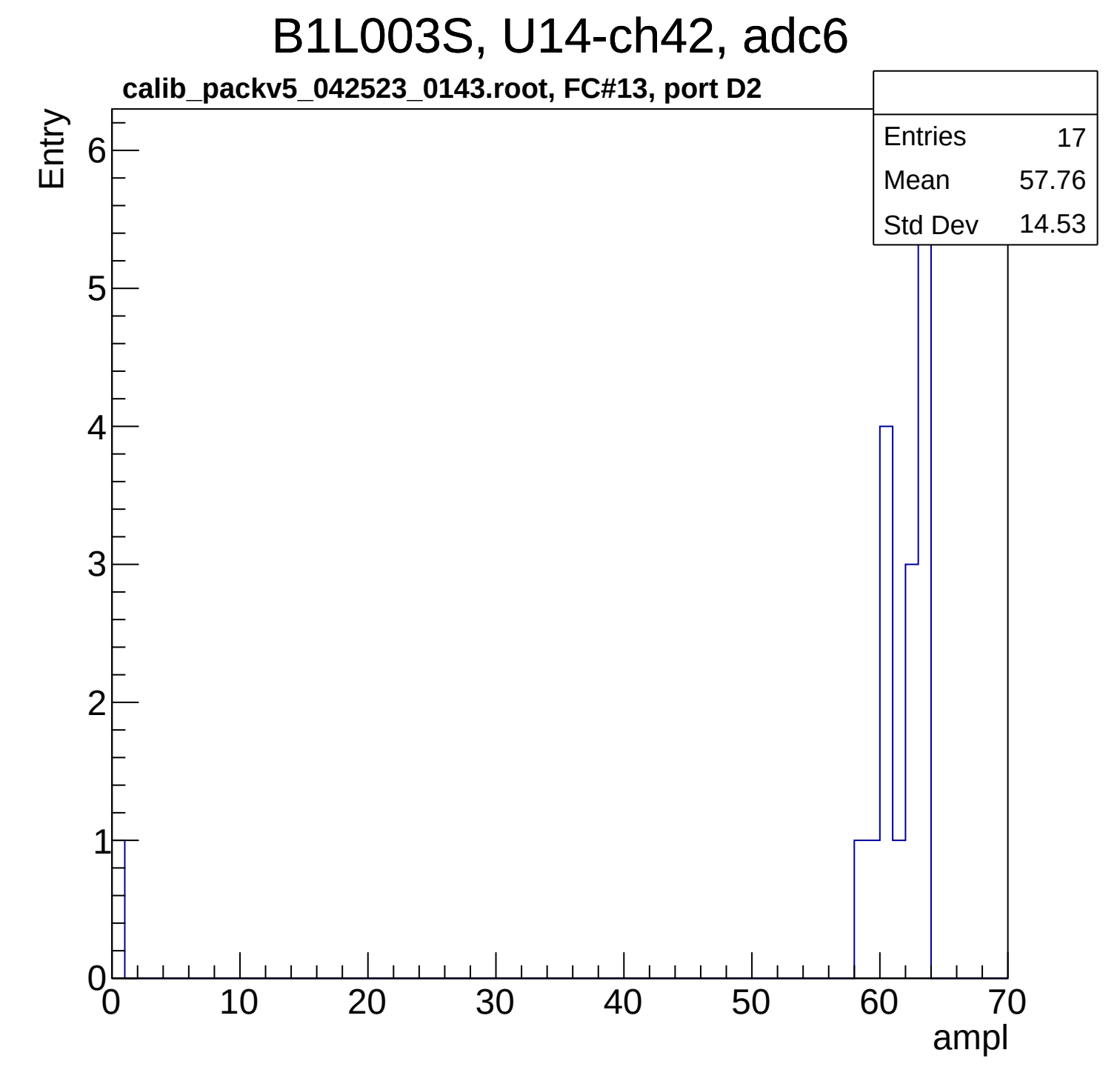

B1L003S, U14-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 34.1
Std Dev 3.253
Gaus mean : 35.0593
Gaus Width: 3.2306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

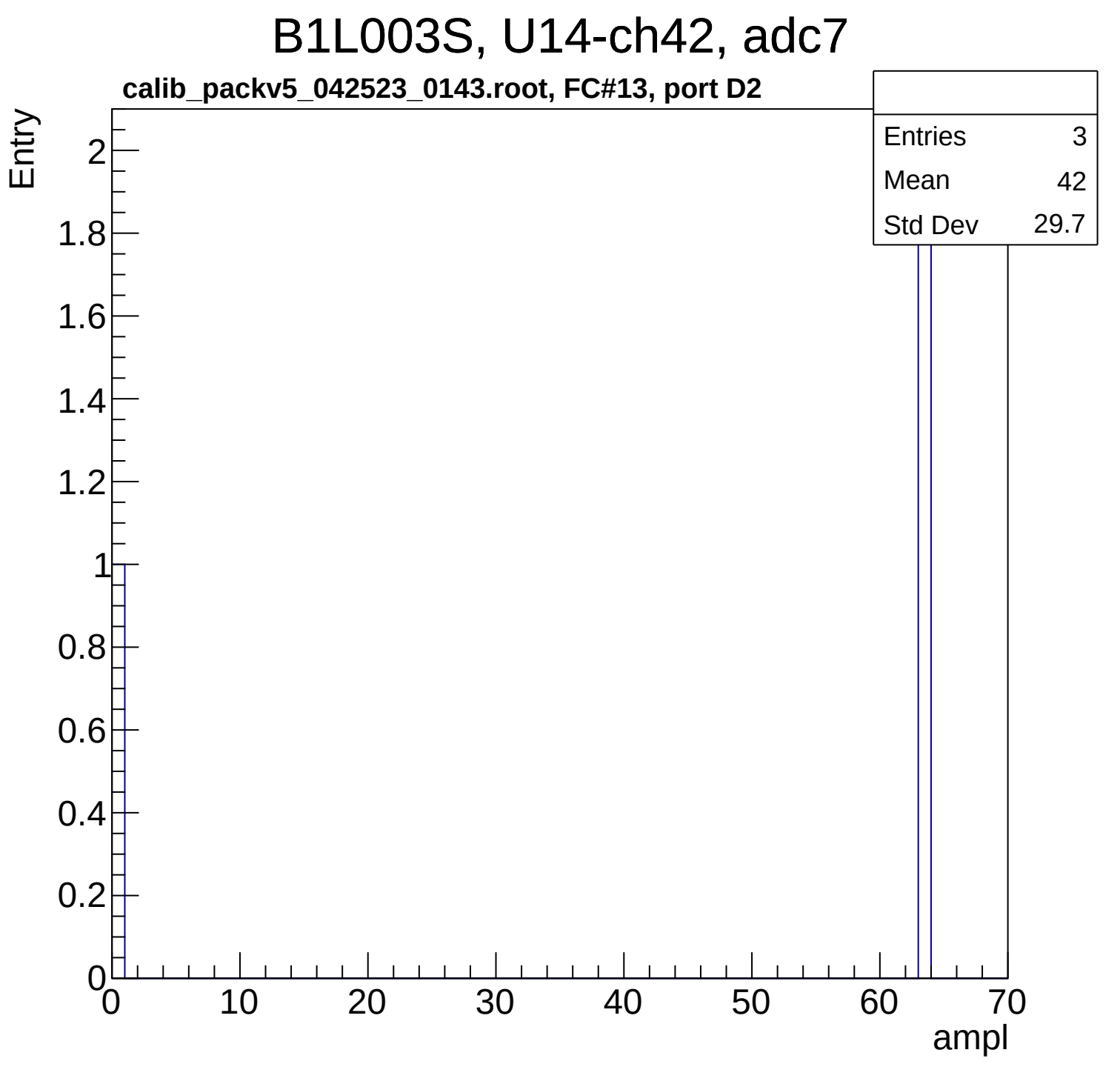

B1L003S, U14-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 40.66
Std Dev 3.336
Gaus mean : 41.3903
Gaus Width: 3.2392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

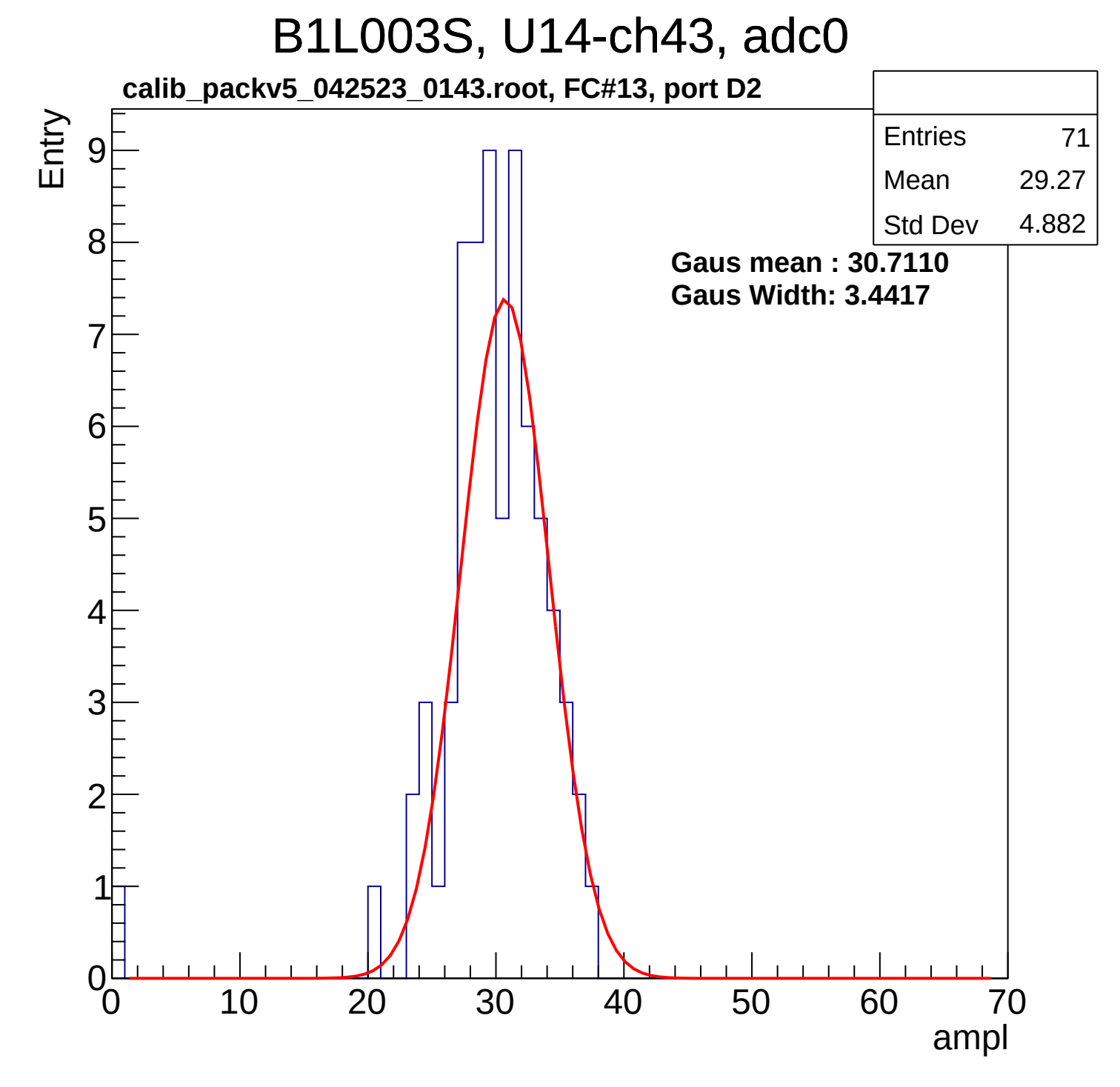

B1L003S, U14-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 47
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

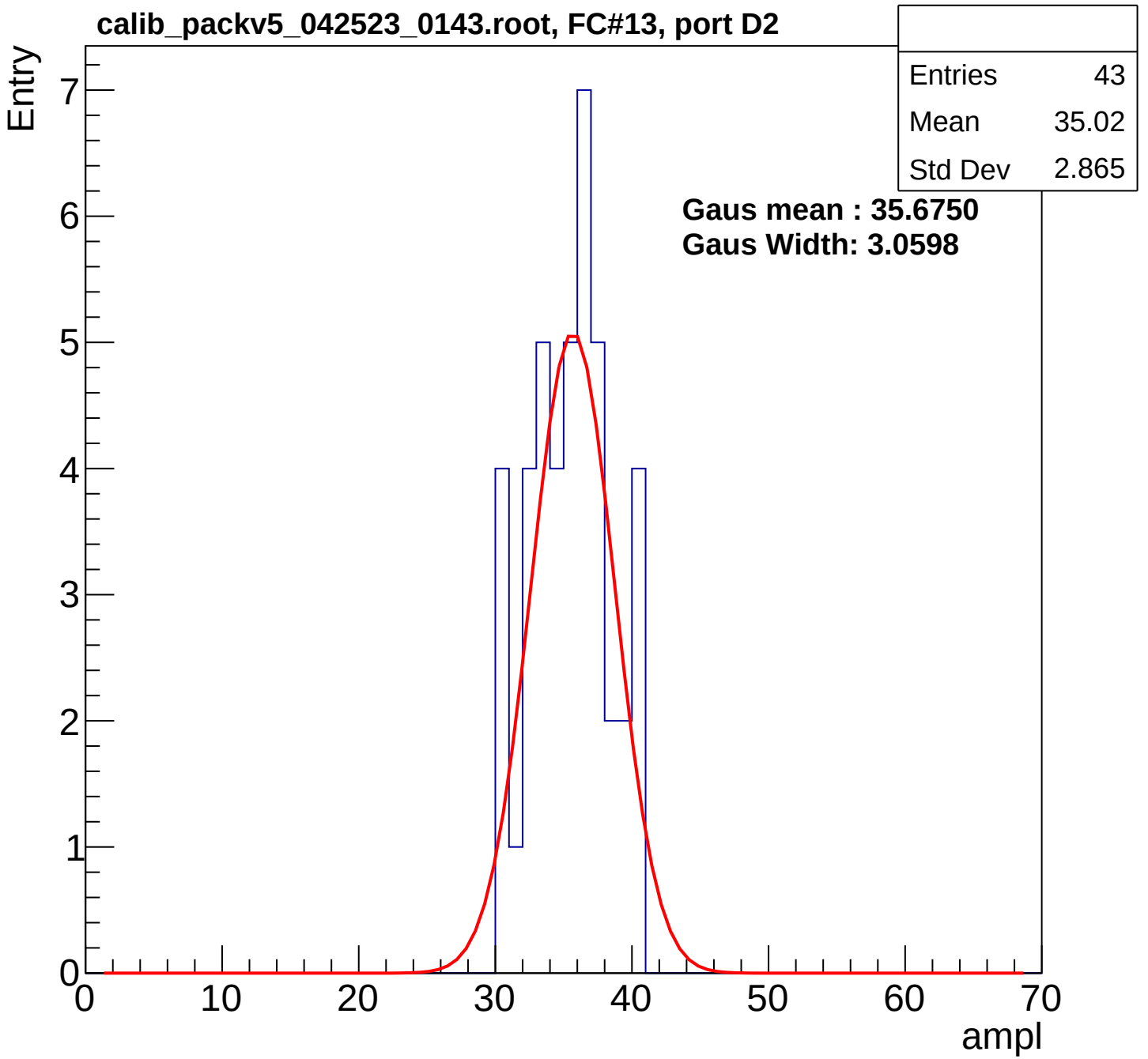

B1L003S, U14-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.21
Std Dev 3.403
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

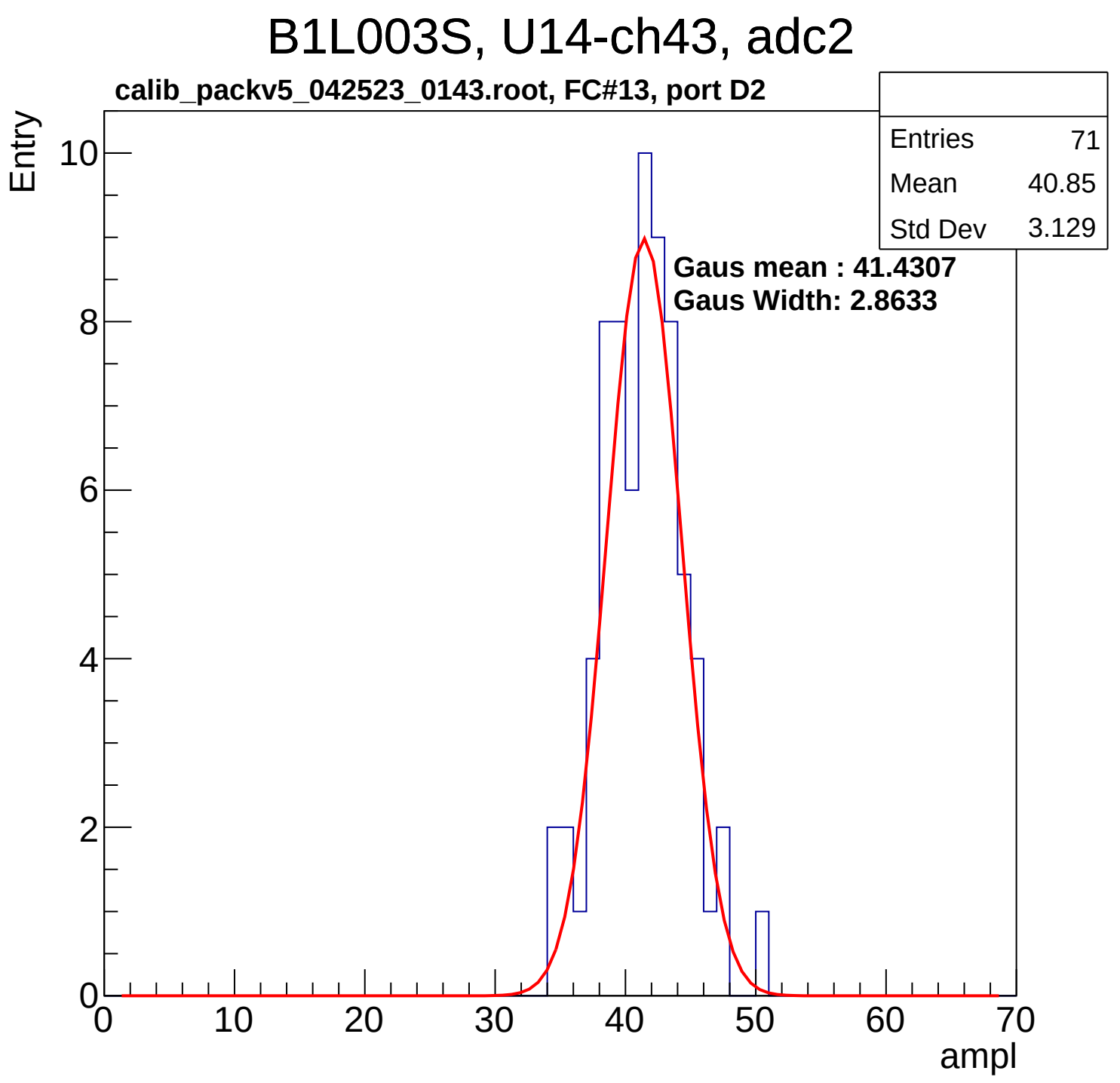

B1L003S, U14-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.62
Std Dev 2.693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

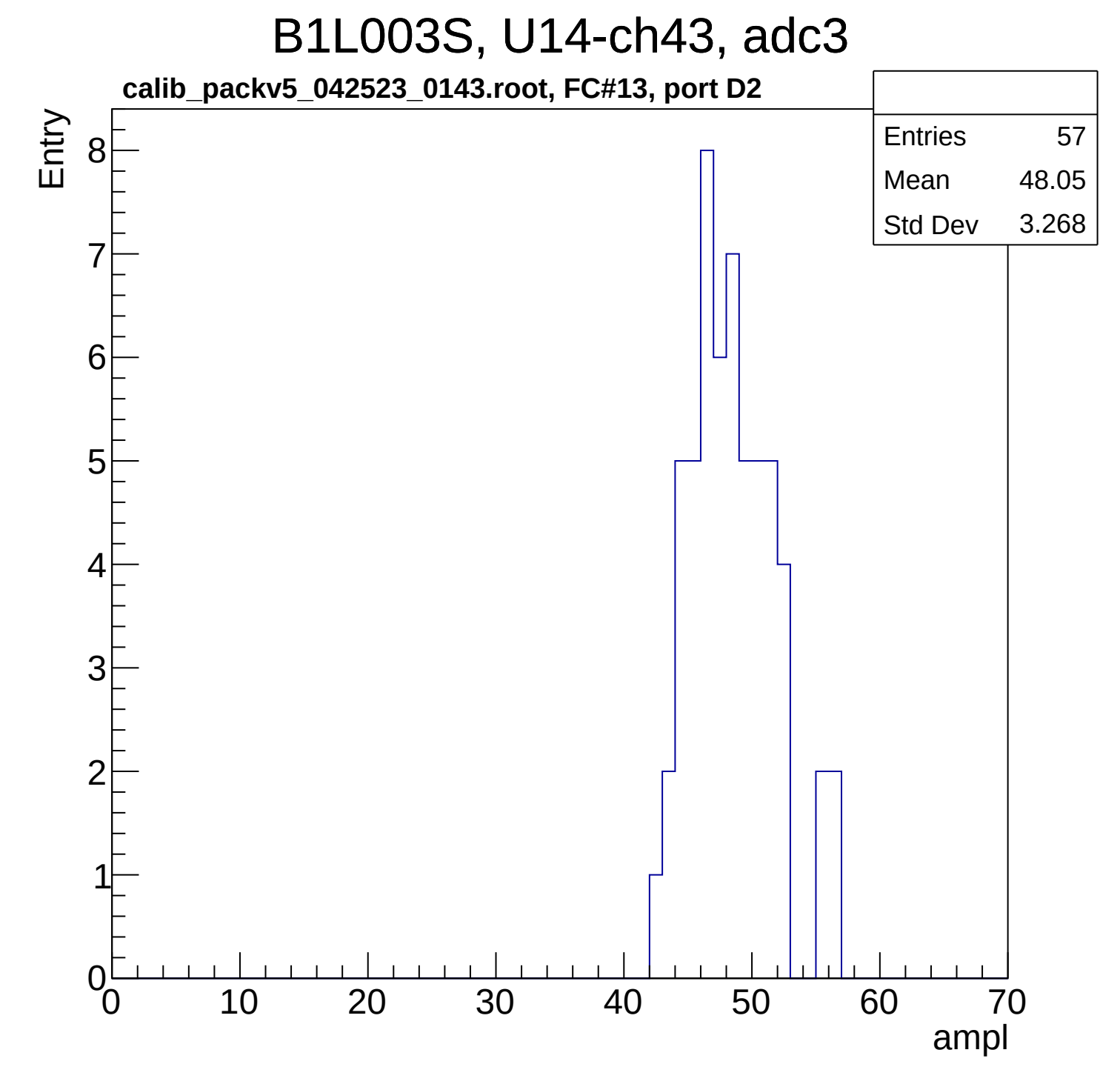

B1L003S, U14-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.76
Std Dev 14.53
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

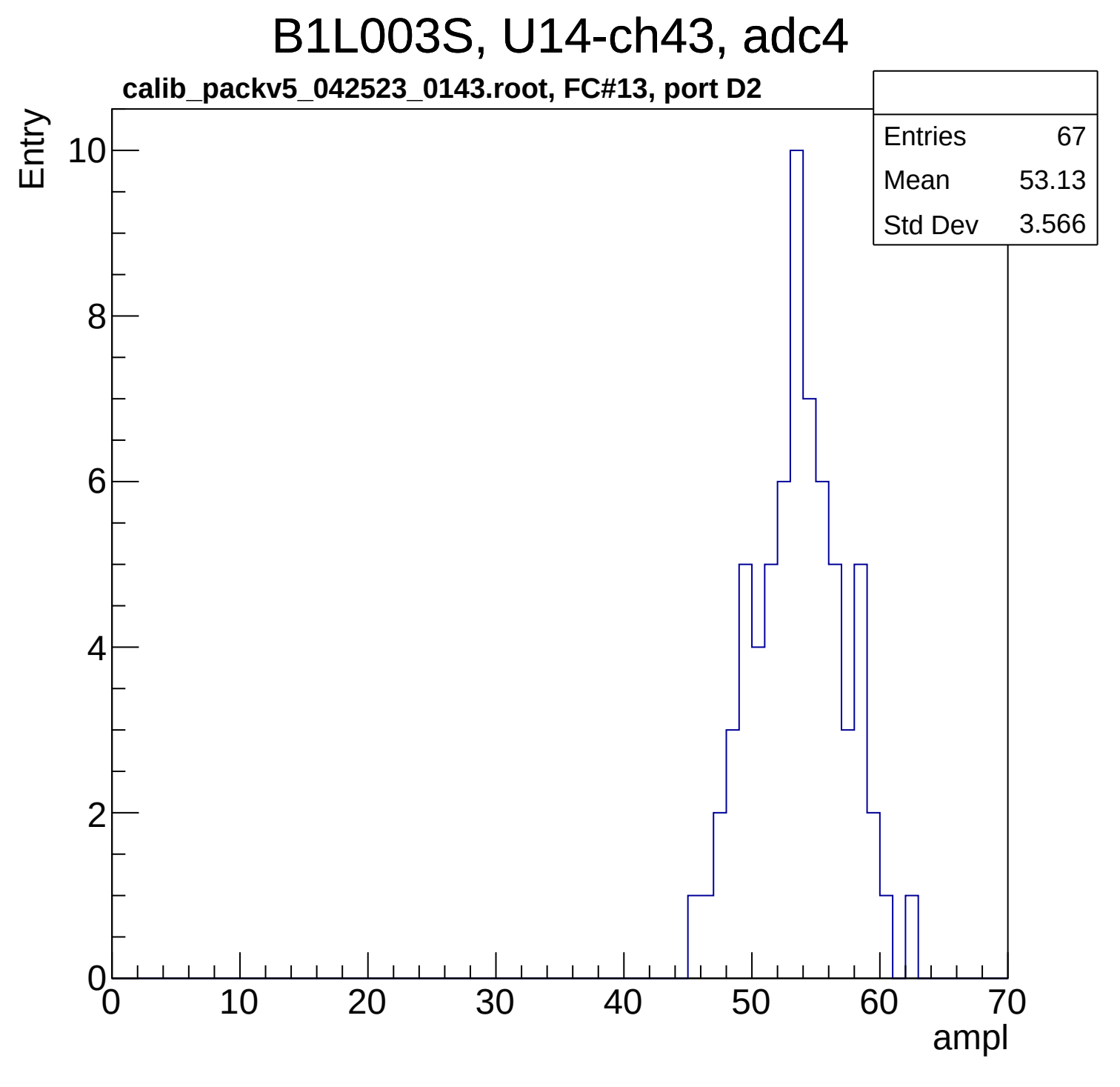

B1L003S, U14-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

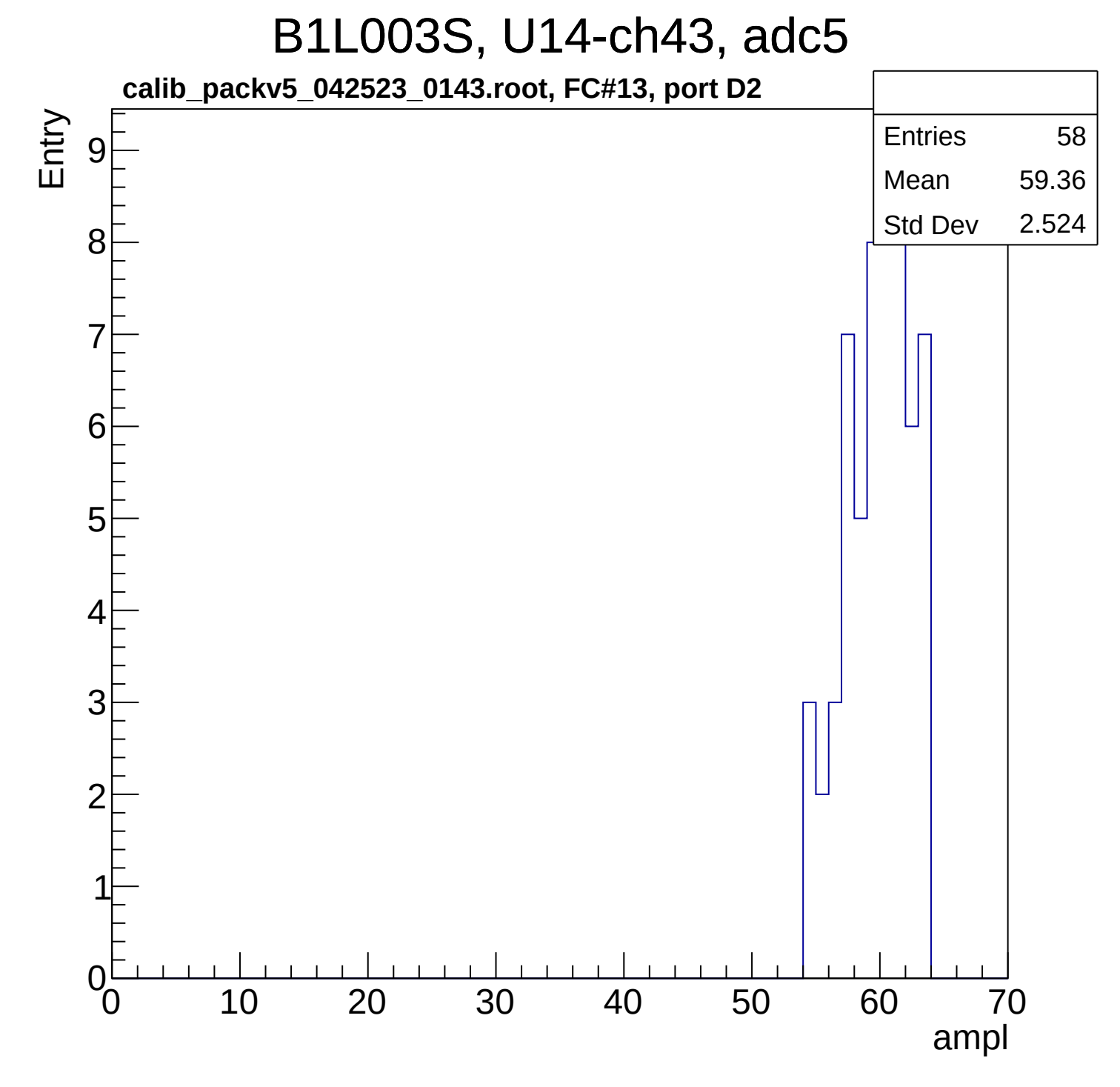

B1L003S, U14-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.27
Std Dev 4.882
Gaus mean : 30.7110
Gaus Width: 3.4417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

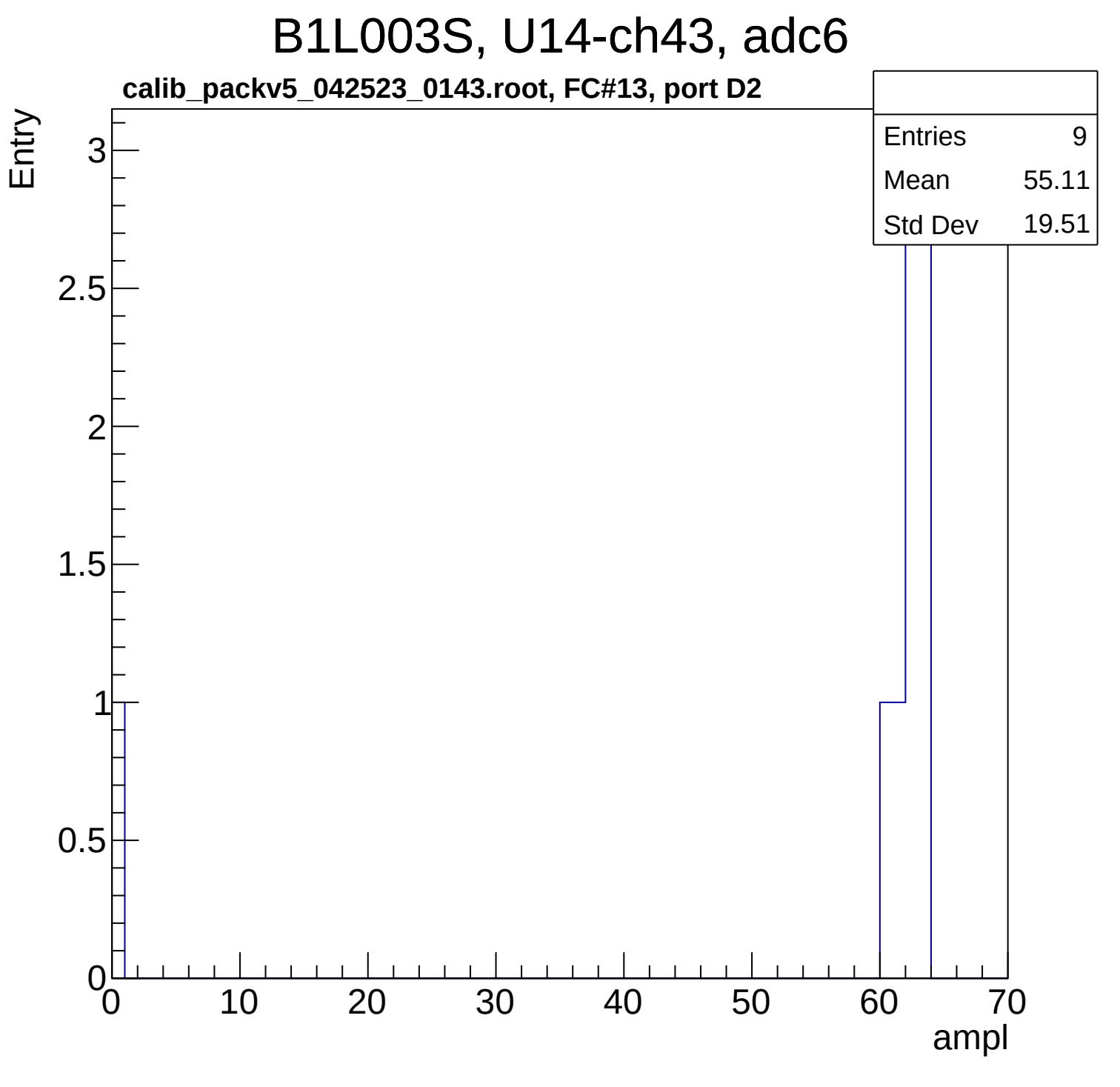

B1L003S, U14-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 35.02
Std Dev 2.865
Gaus mean : 35.6750
Gaus Width: 3.0598
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

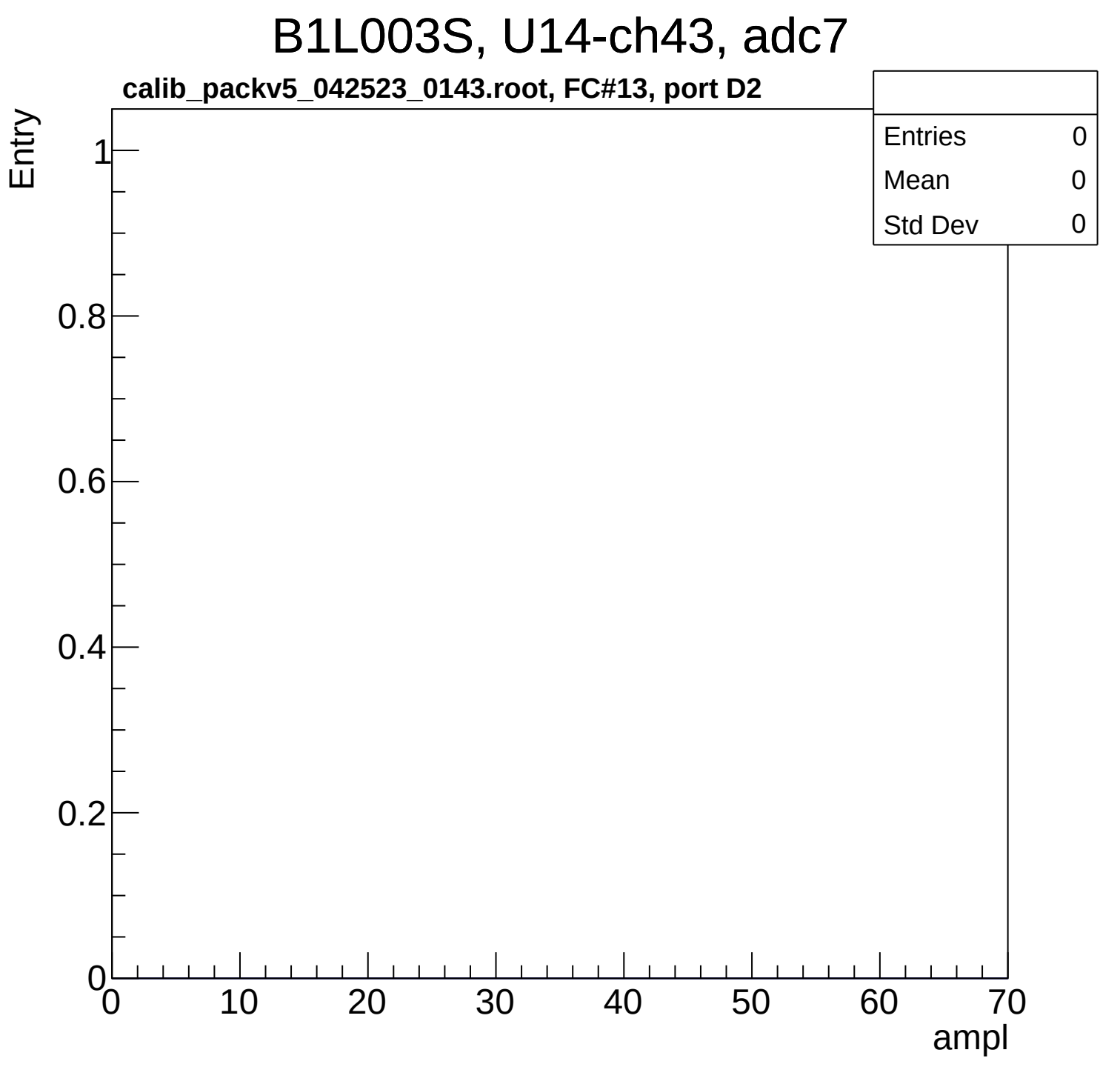

B1L003S, U14-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 40.85
Std Dev 3.129
Gaus mean : 41.4307
Gaus Width: 2.8633
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

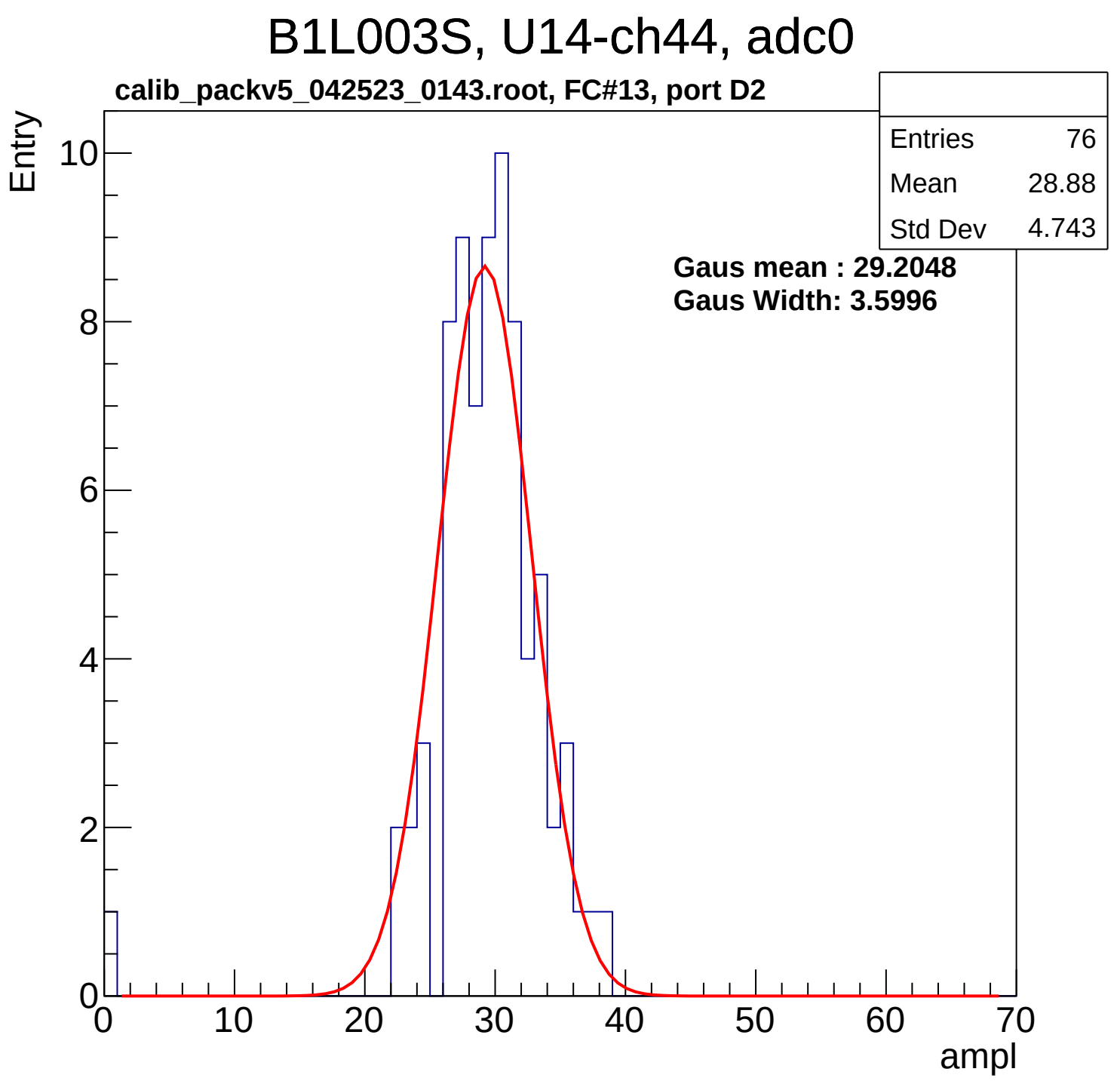

B1L003S, U14-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.05
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

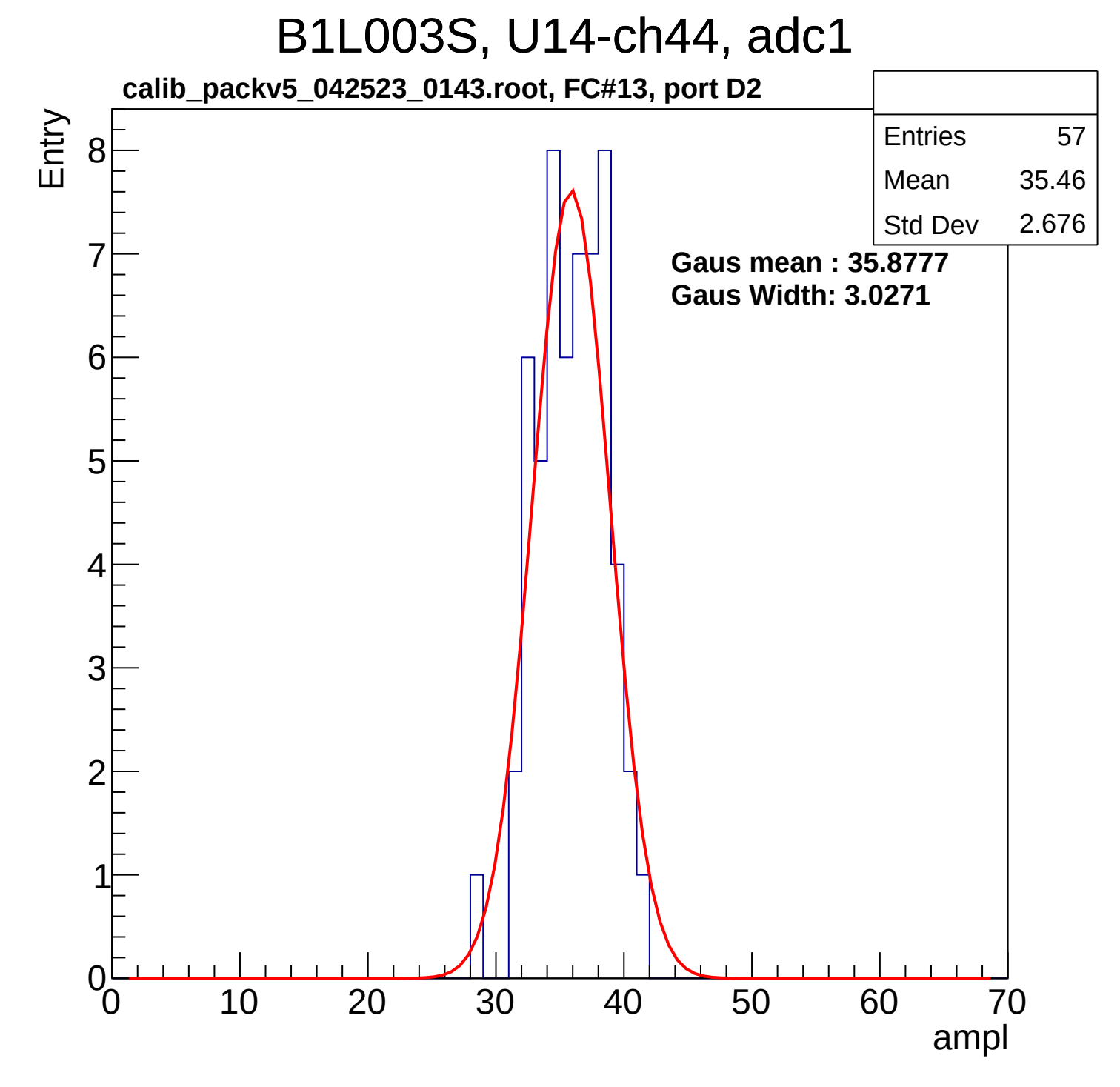

B1L003S, U14-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 53.13
Std Dev 3.566
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

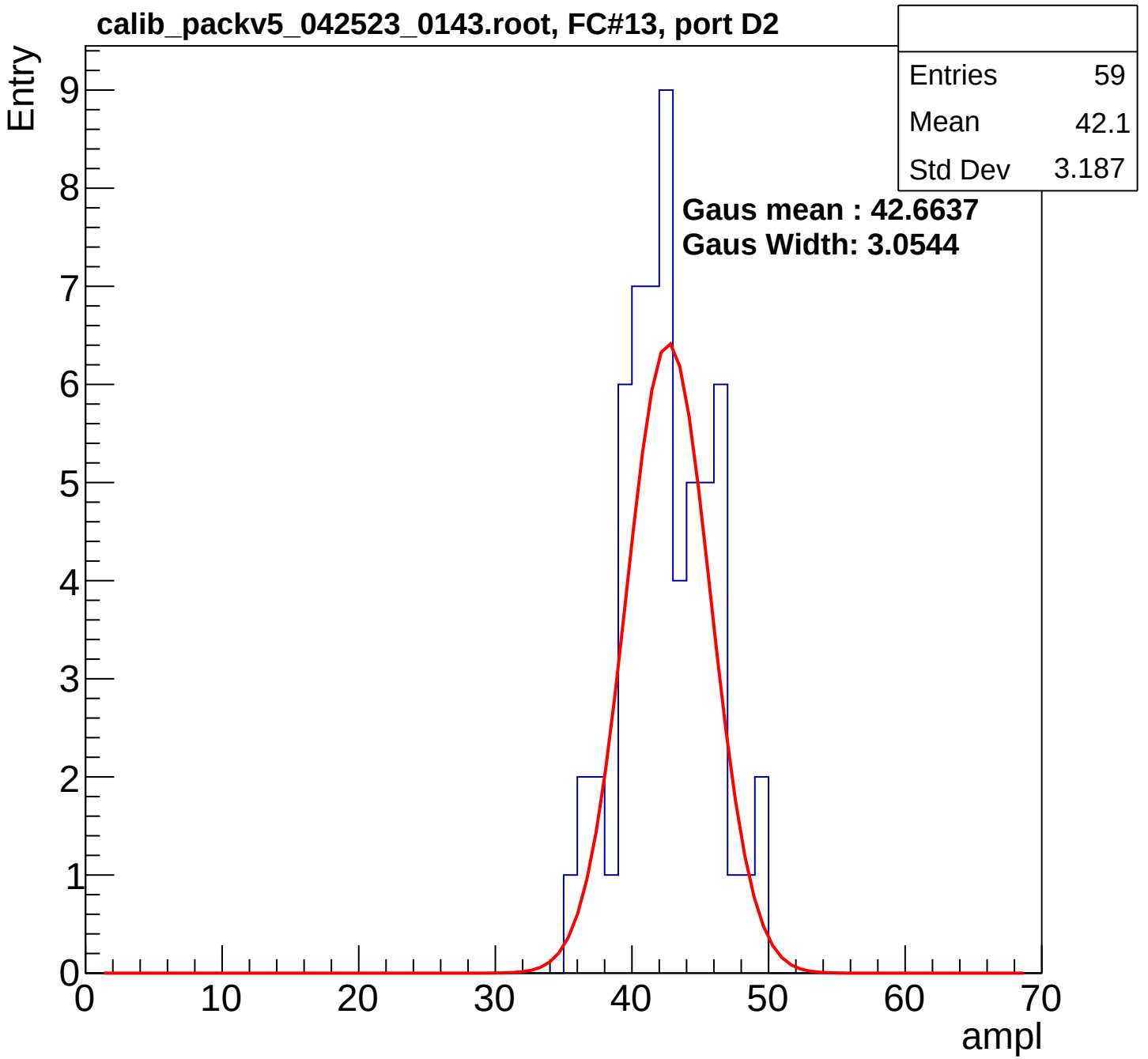

B1L003S, U14-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59.36
Std Dev 2.524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

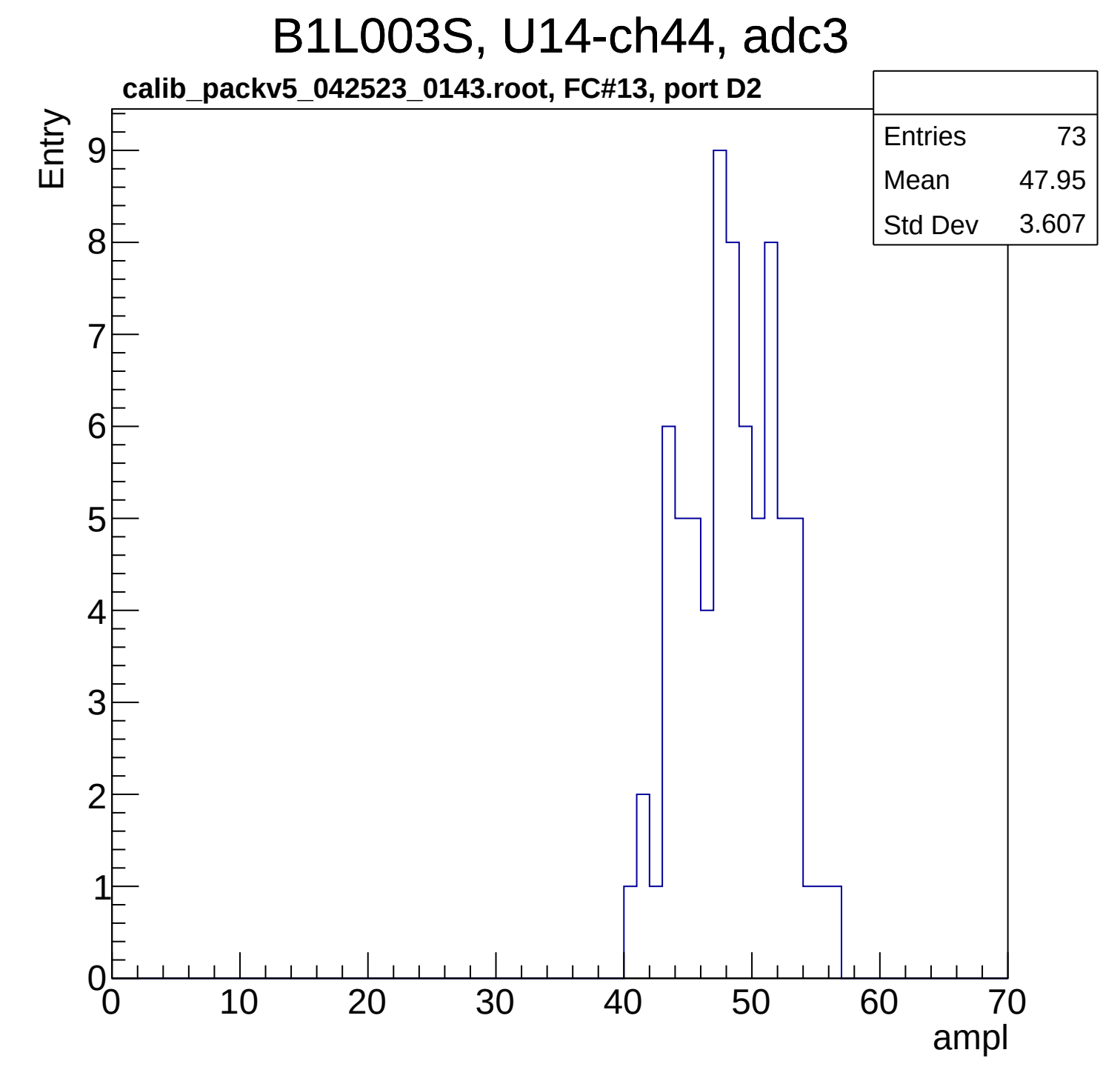

B1L003S, U14-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.11
Std Dev 19.51
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

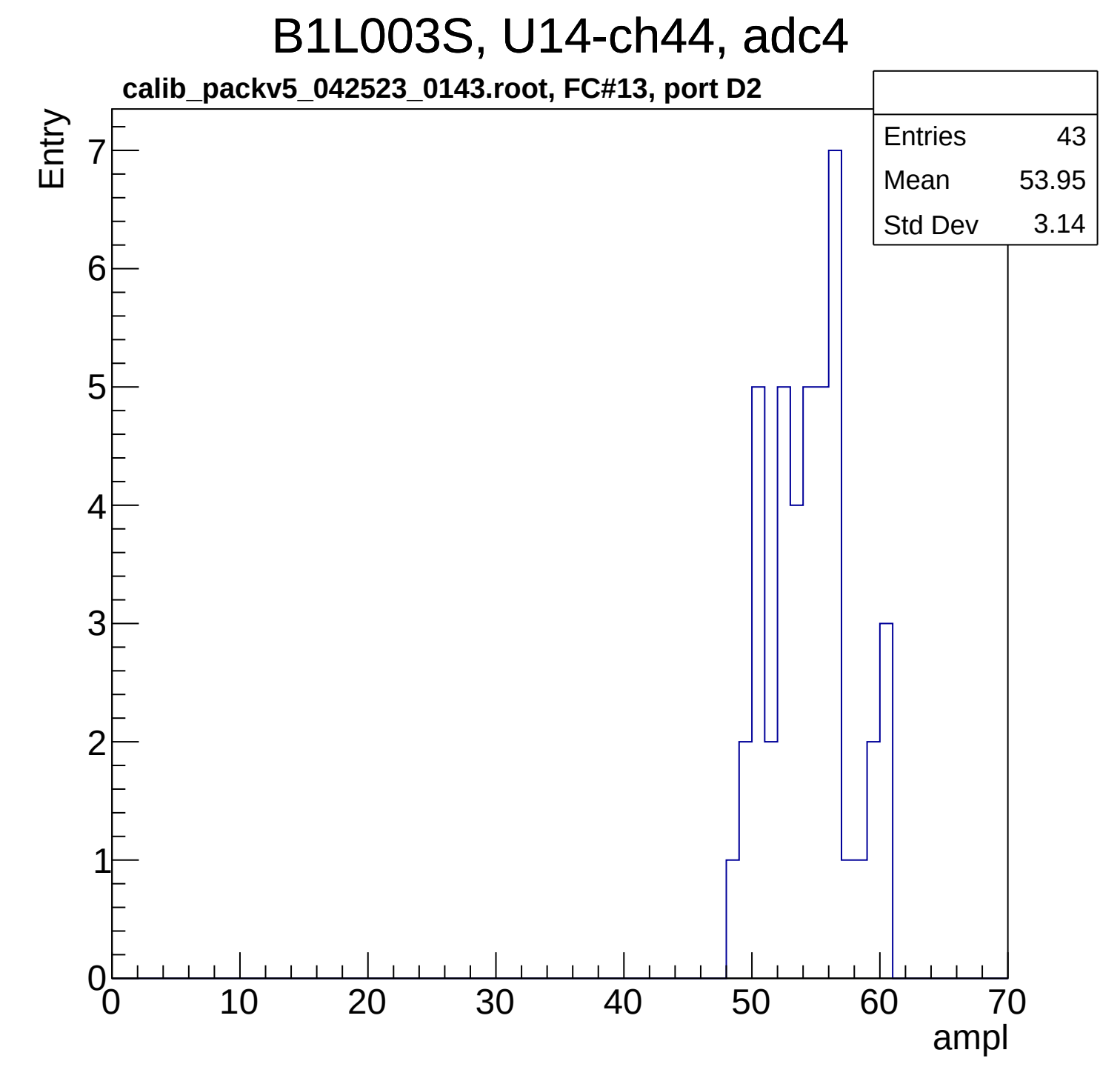

B1L003S, U14-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

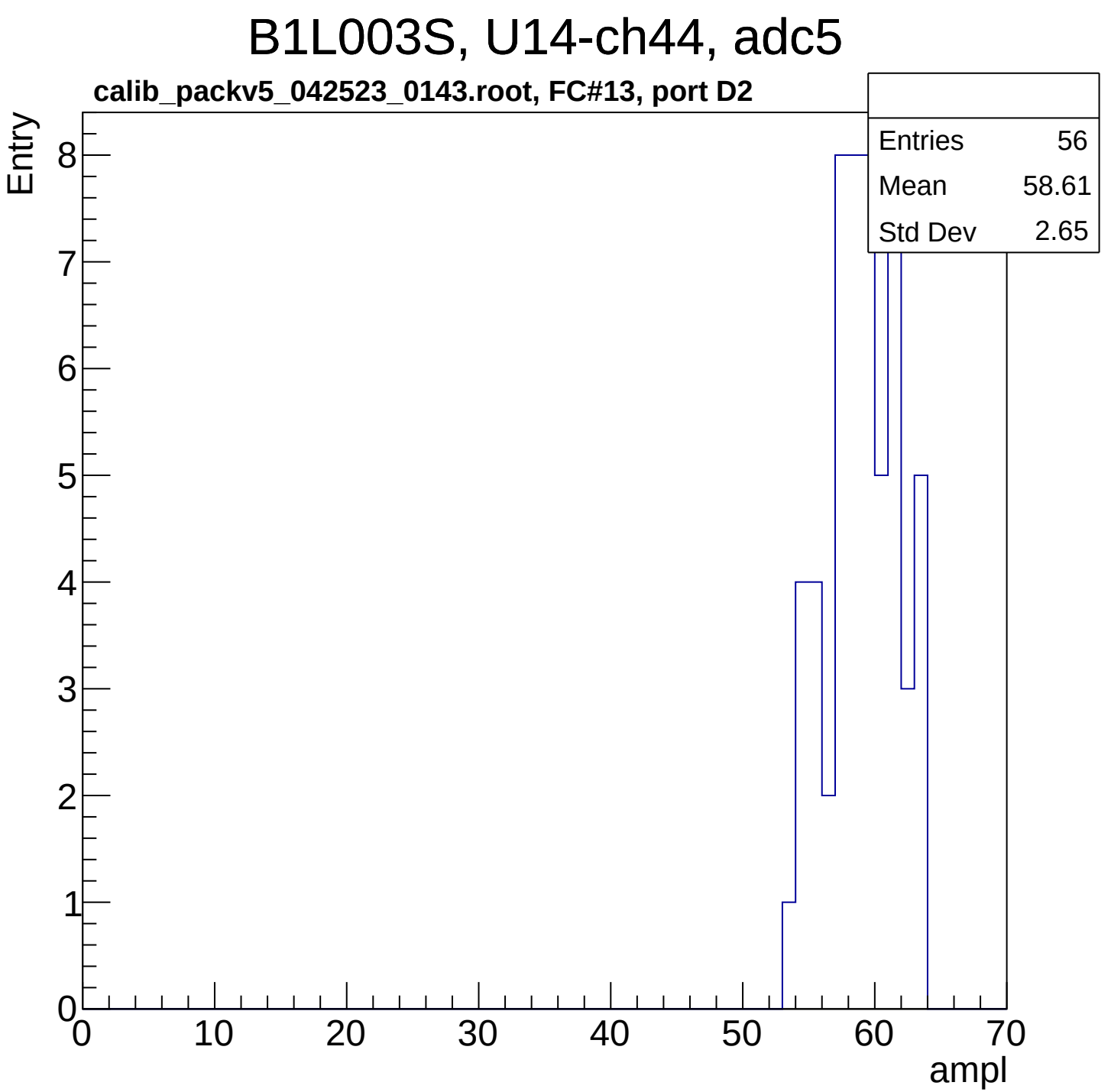

B1L003S, U14-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.88
Std Dev 4.743
Gaus mean : 29.2048
Gaus Width: 3.5996
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

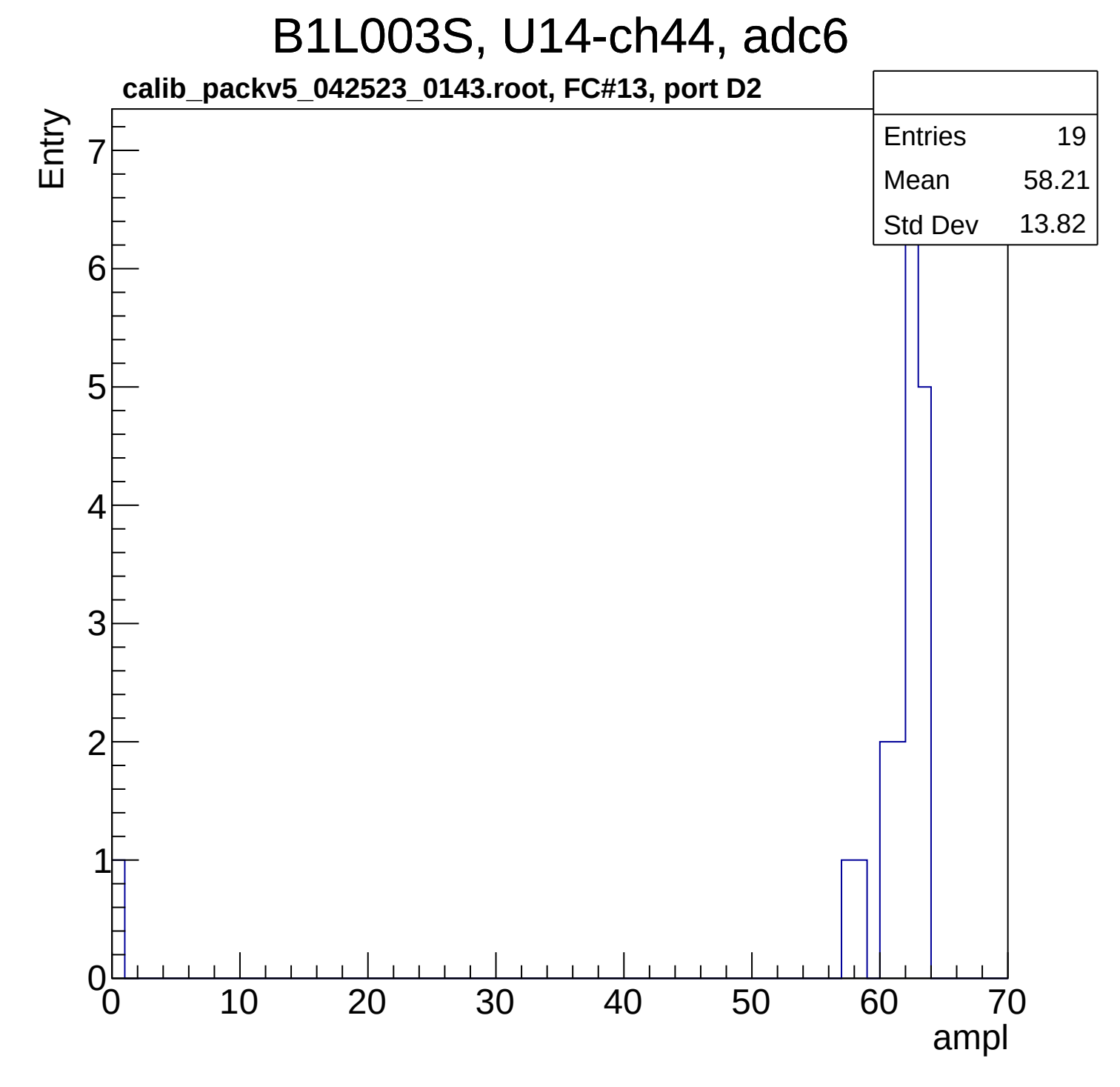

B1L003S, U14-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.46
Std Dev 2.676
Gaus mean : 35.8777
Gaus Width: 3.0271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

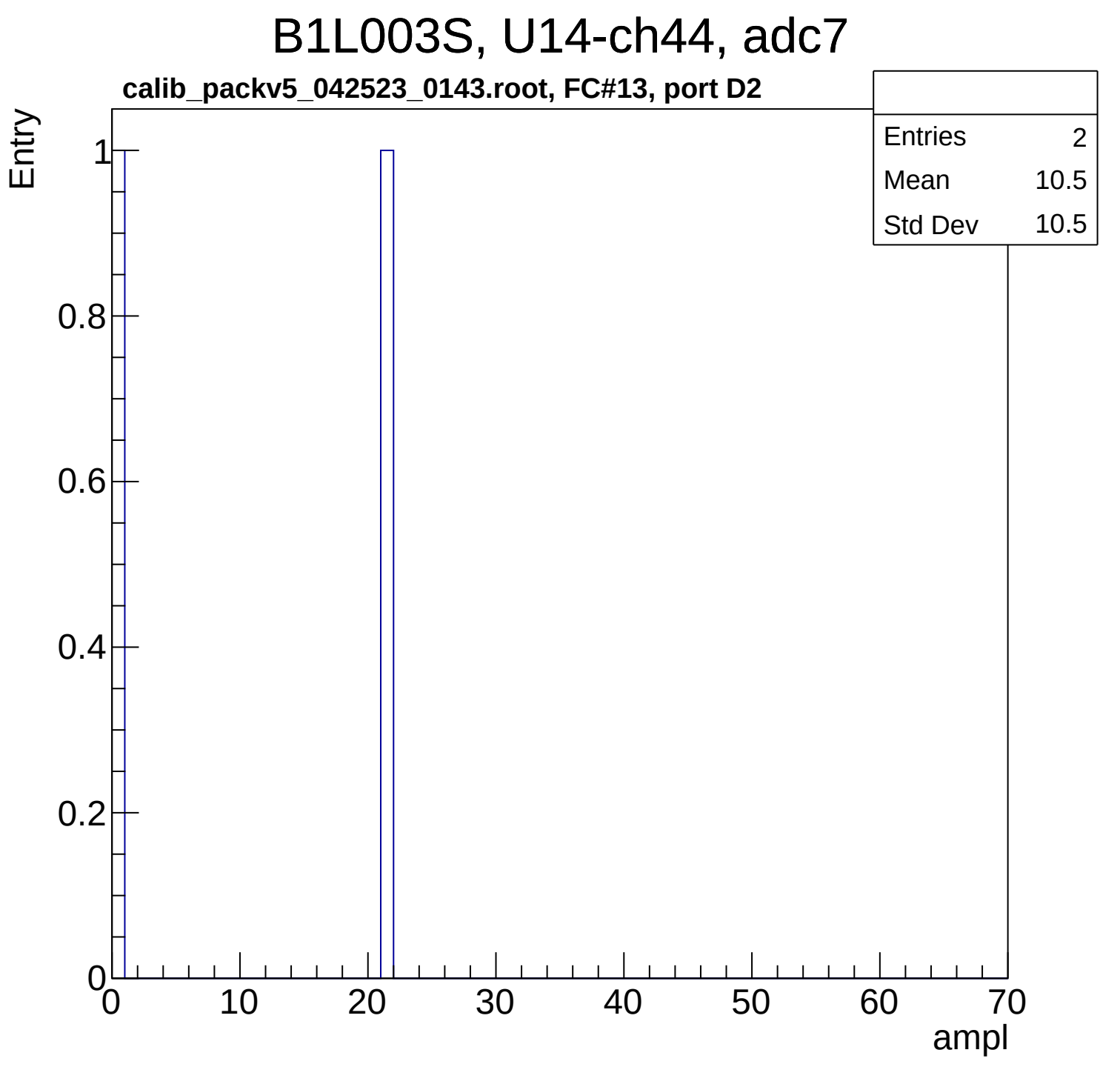

B1L003S, U14-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.1
Std Dev 3.187
Gaus mean : 42.6637
Gaus Width: 3.0544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

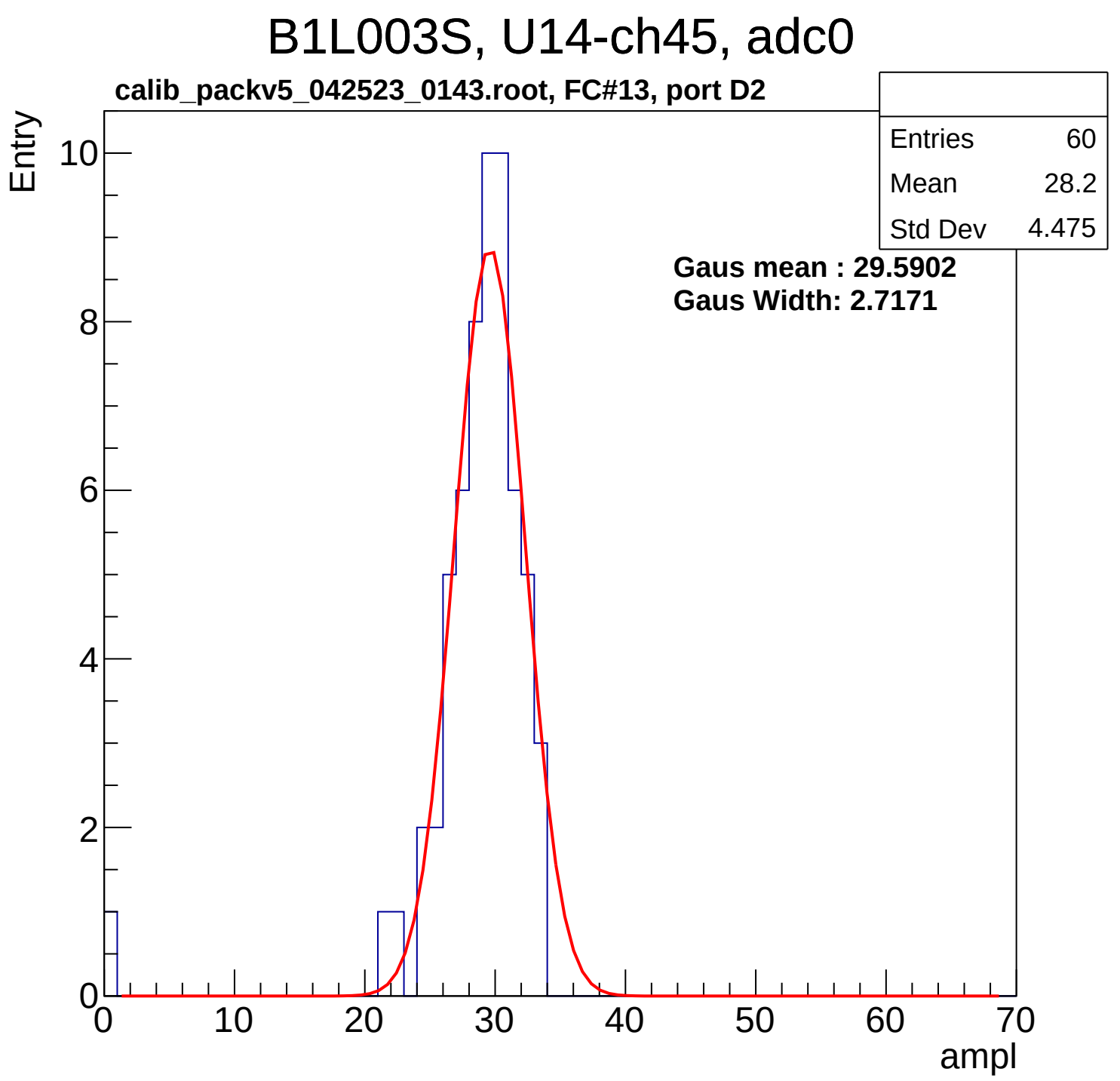

B1L003S, U14-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.95
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

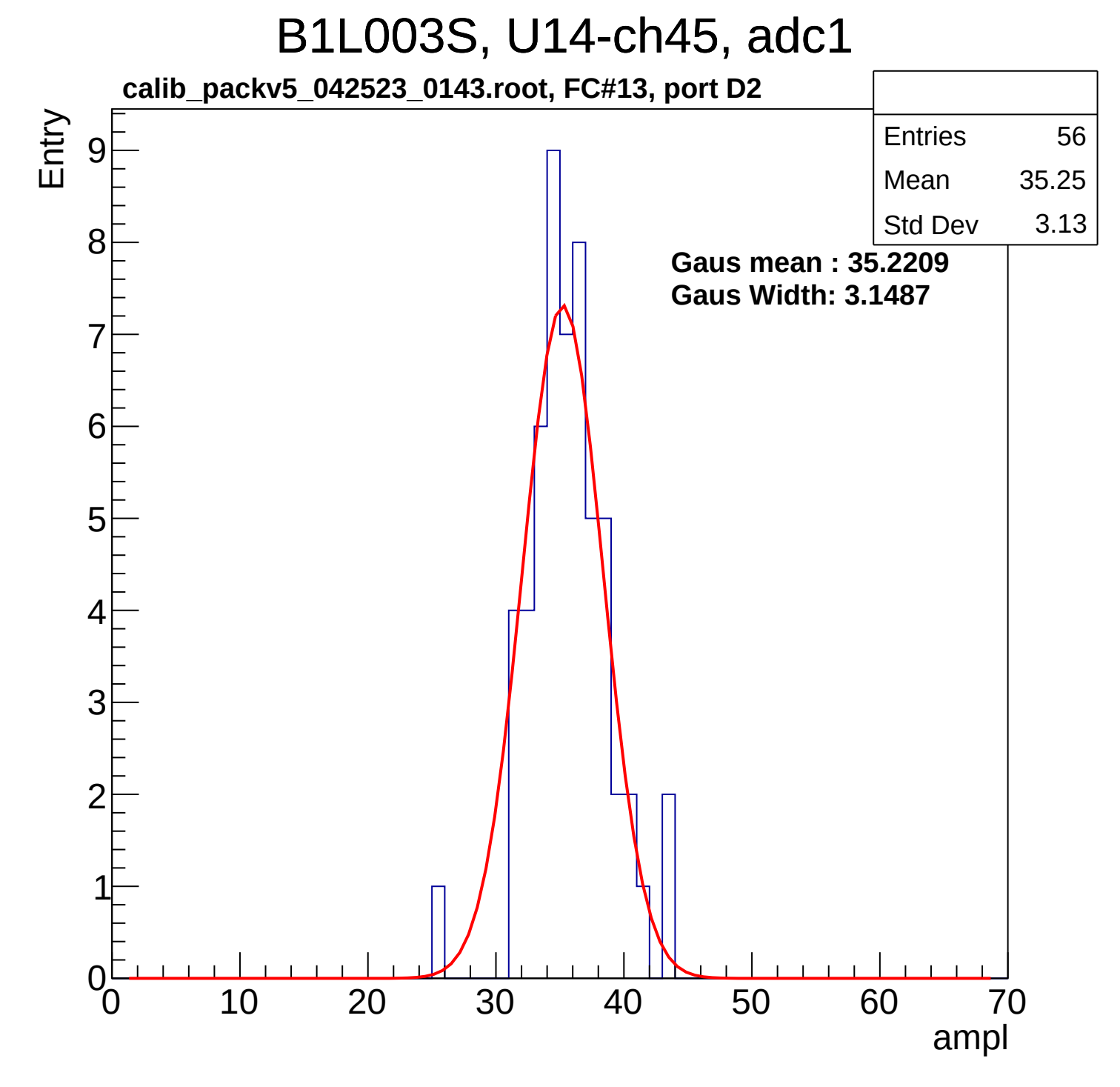

B1L003S, U14-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 53.95
Std Dev 3.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

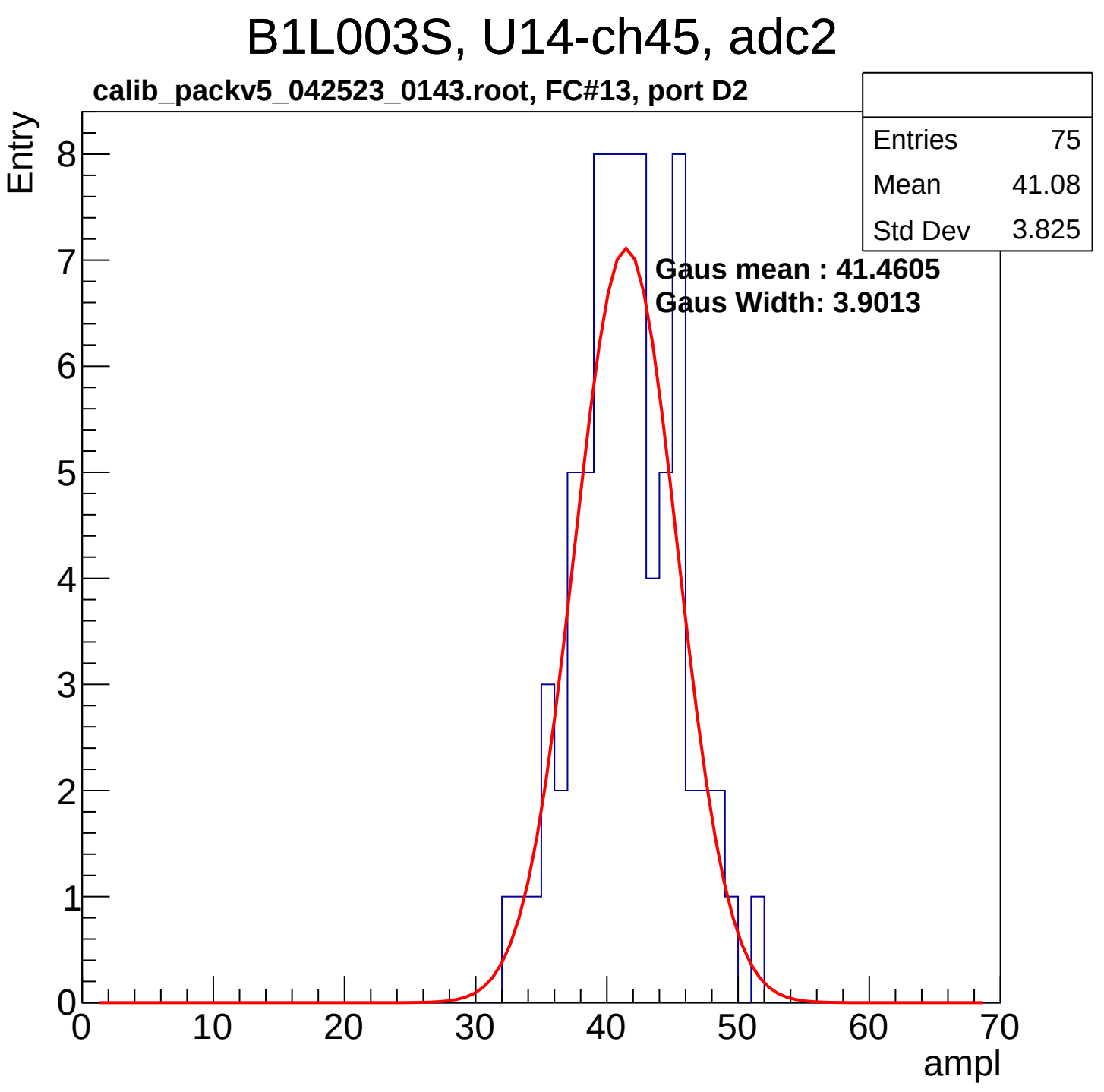

B1L003S, U14-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.61
Std Dev 2.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

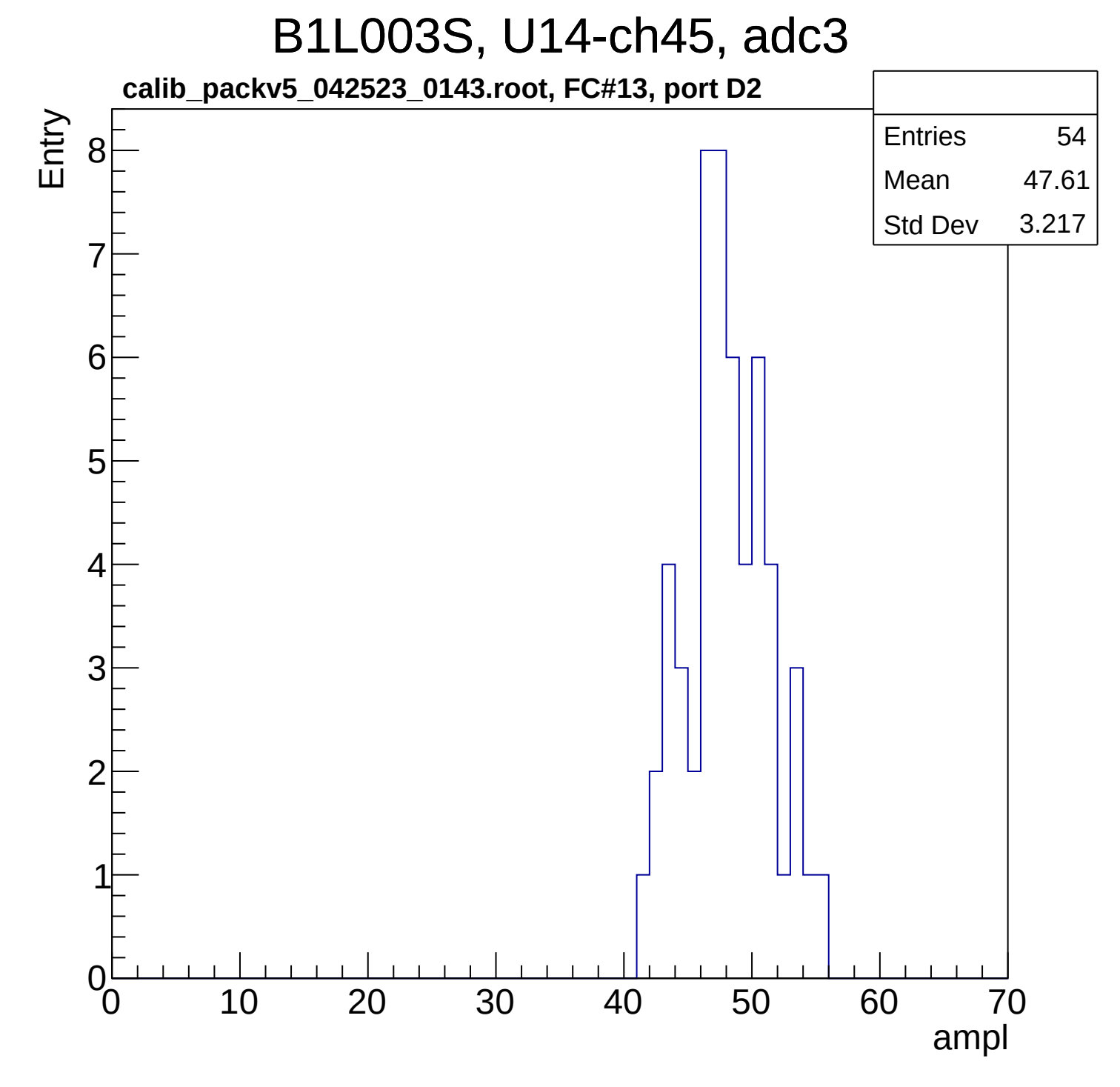

B1L003S, U14-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.21
Std Dev 13.82
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

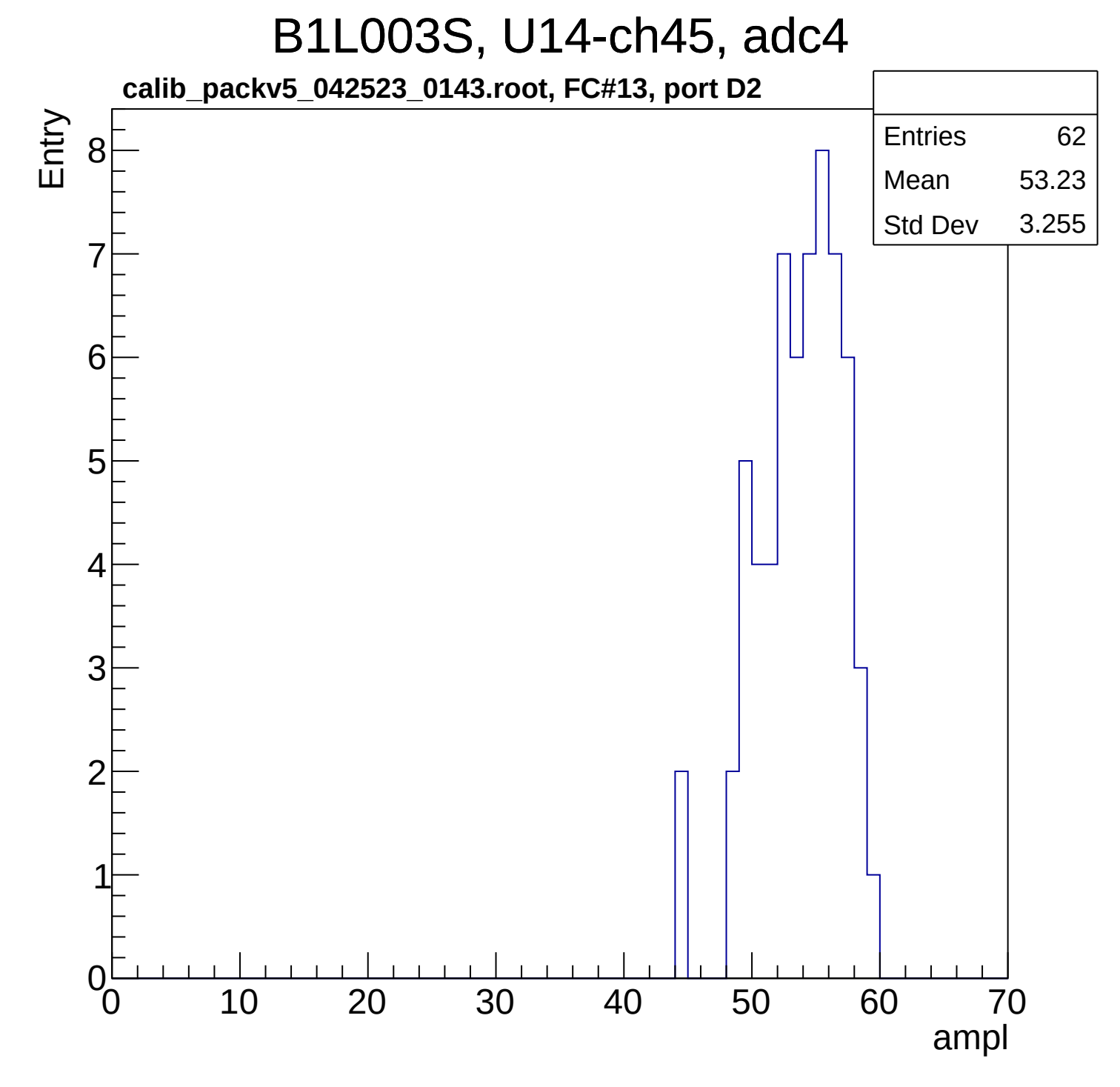

B1L003S, U14-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

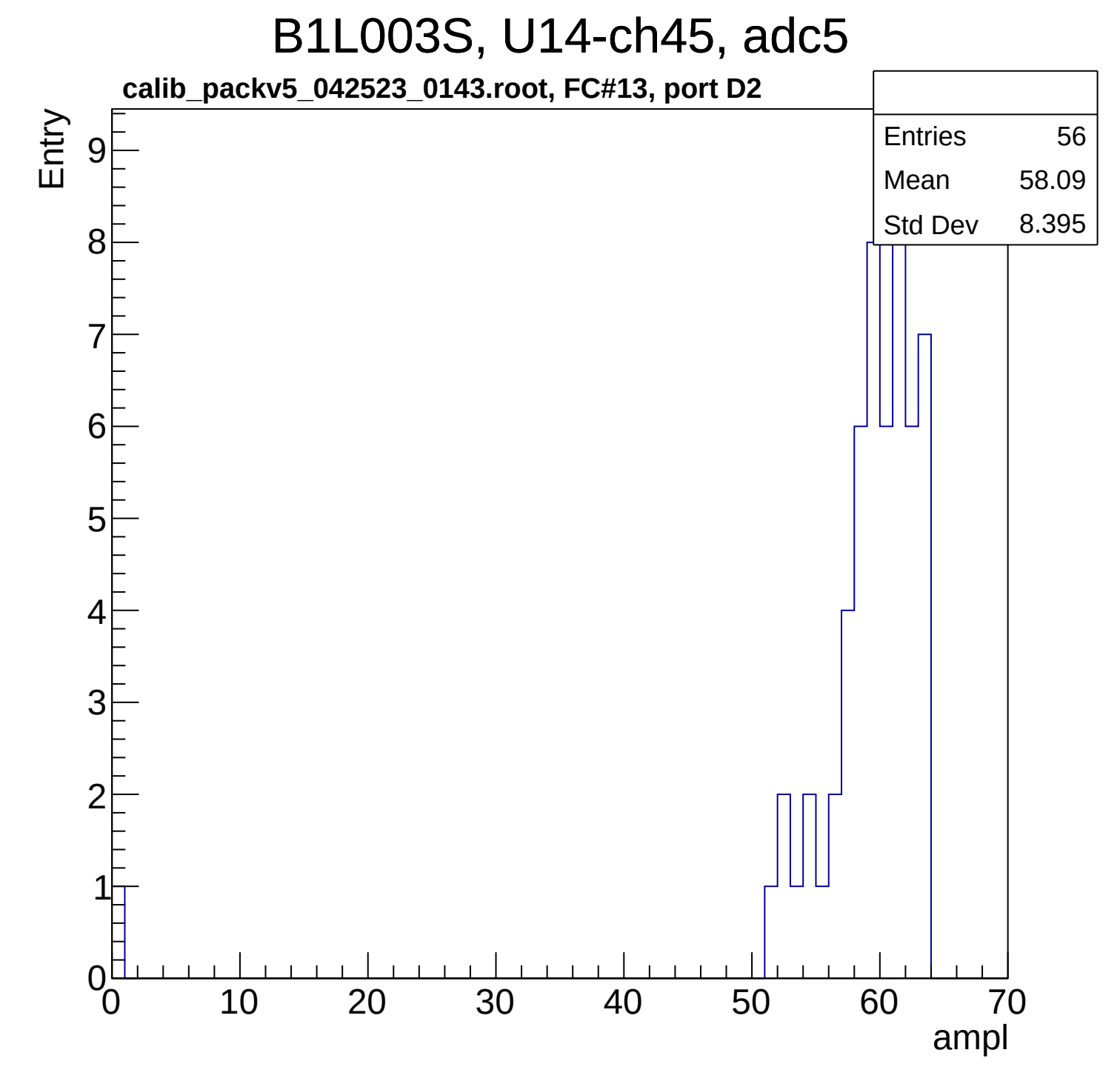

B1L003S, U14-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.2
Std Dev 4.475
Gaus mean : 29.5902
Gaus Width: 2.7171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

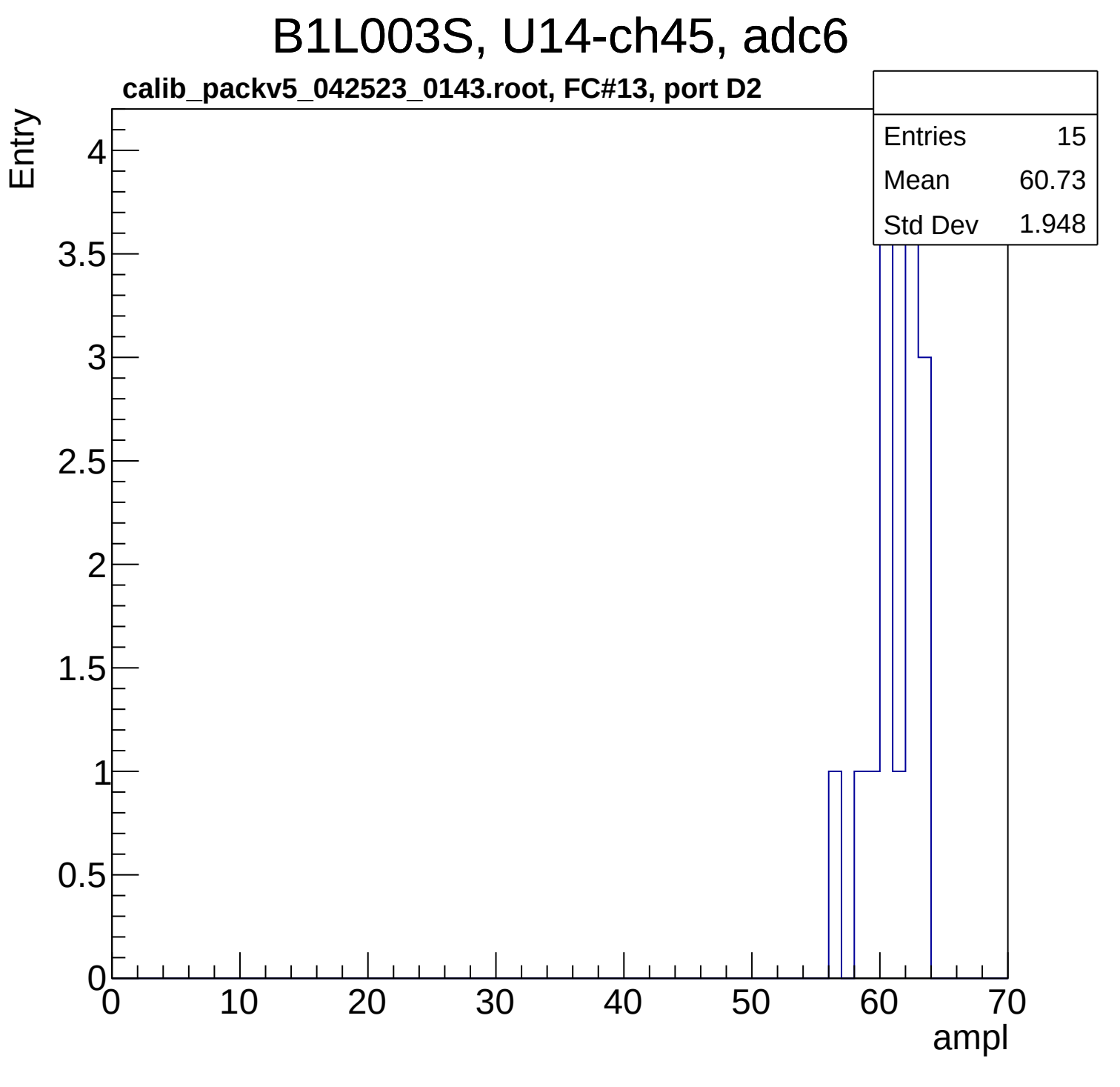

B1L003S, U14-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 35.25
Std Dev 3.13
Gaus mean : 35.2209
Gaus Width: 3.1487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

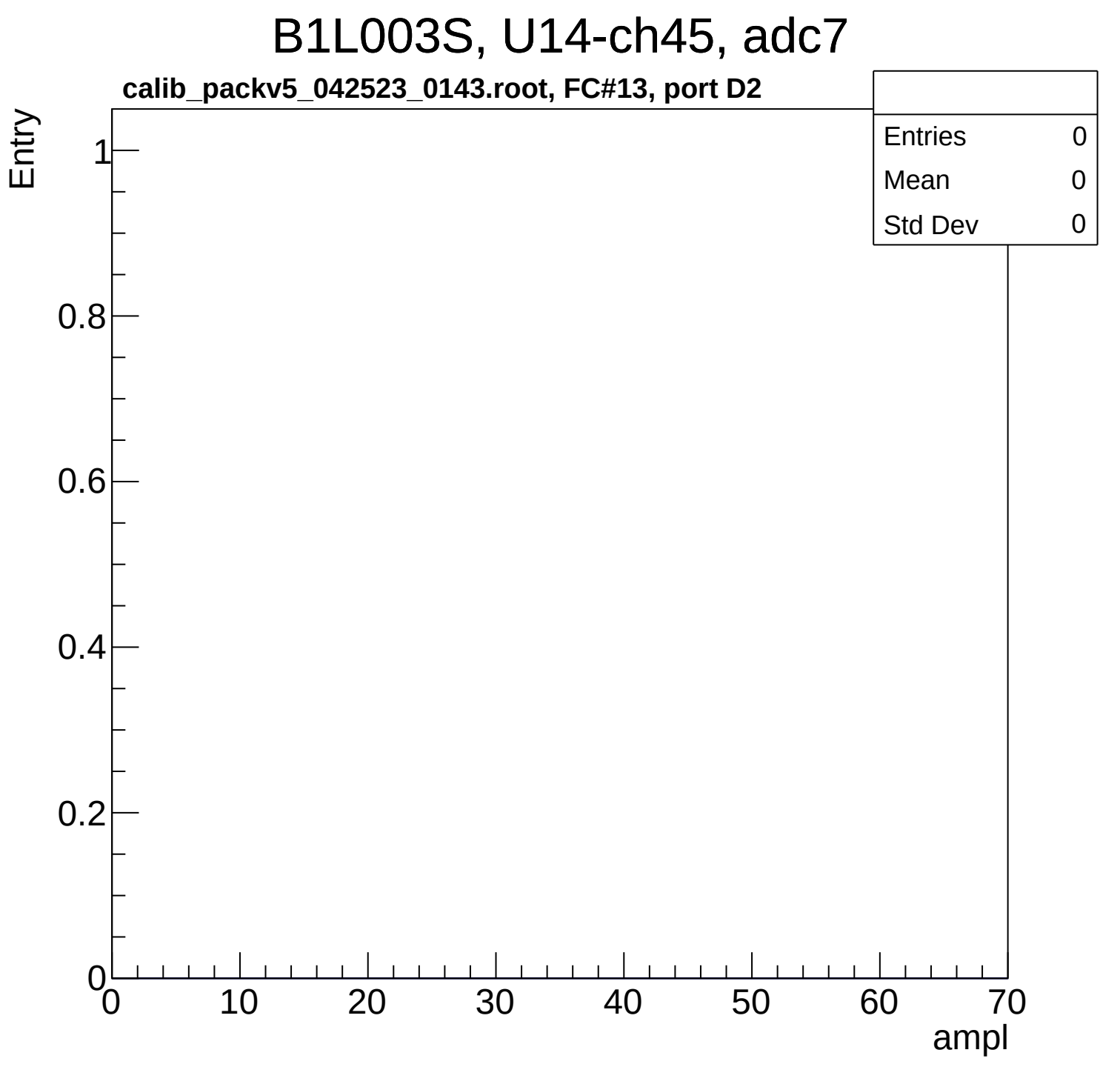

B1L003S, U14-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 41.08
Std Dev 3.825
Gaus mean : 41.4605
Gaus Width: 3.9013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

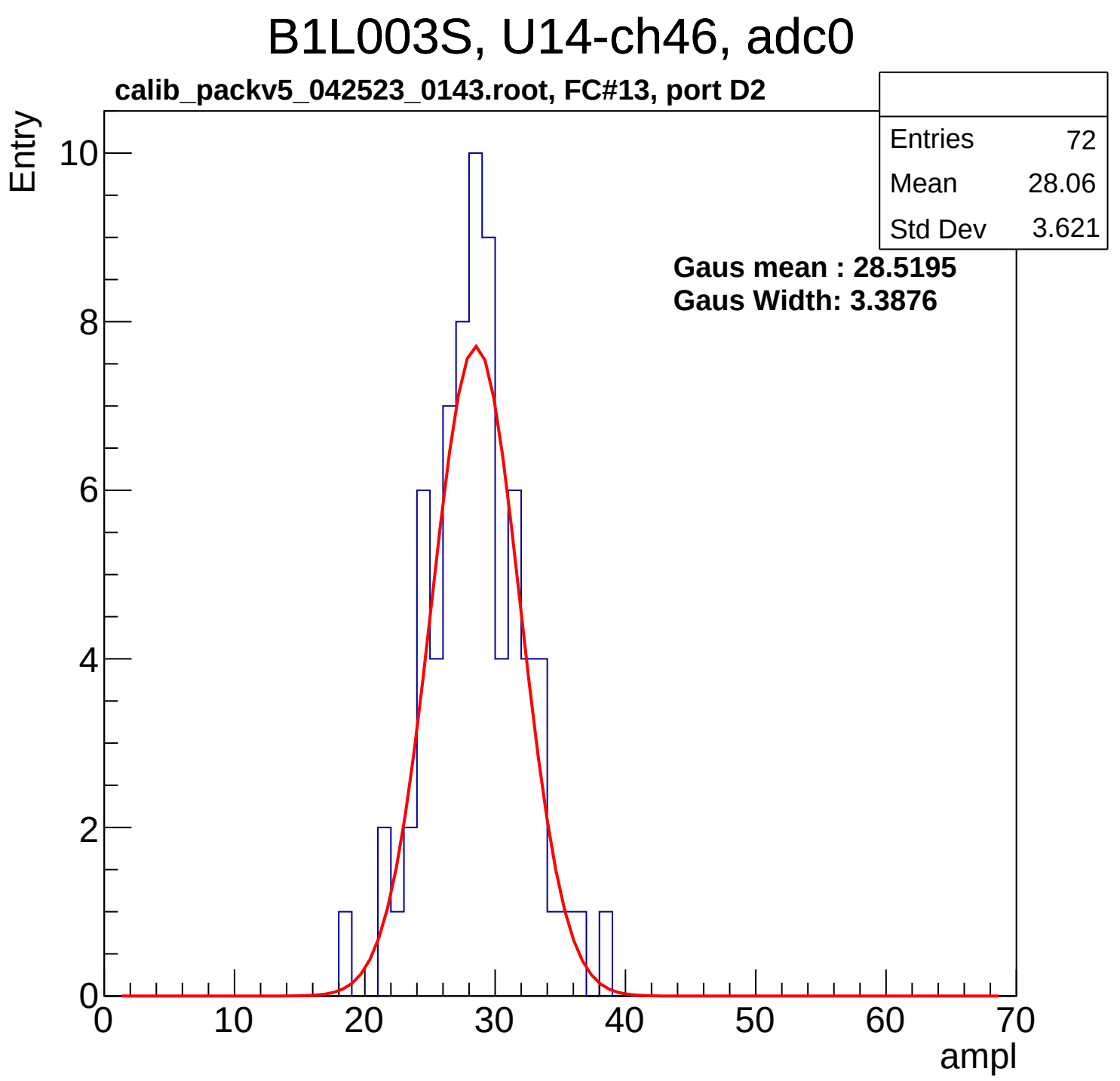

B1L003S, U14-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 47.61
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

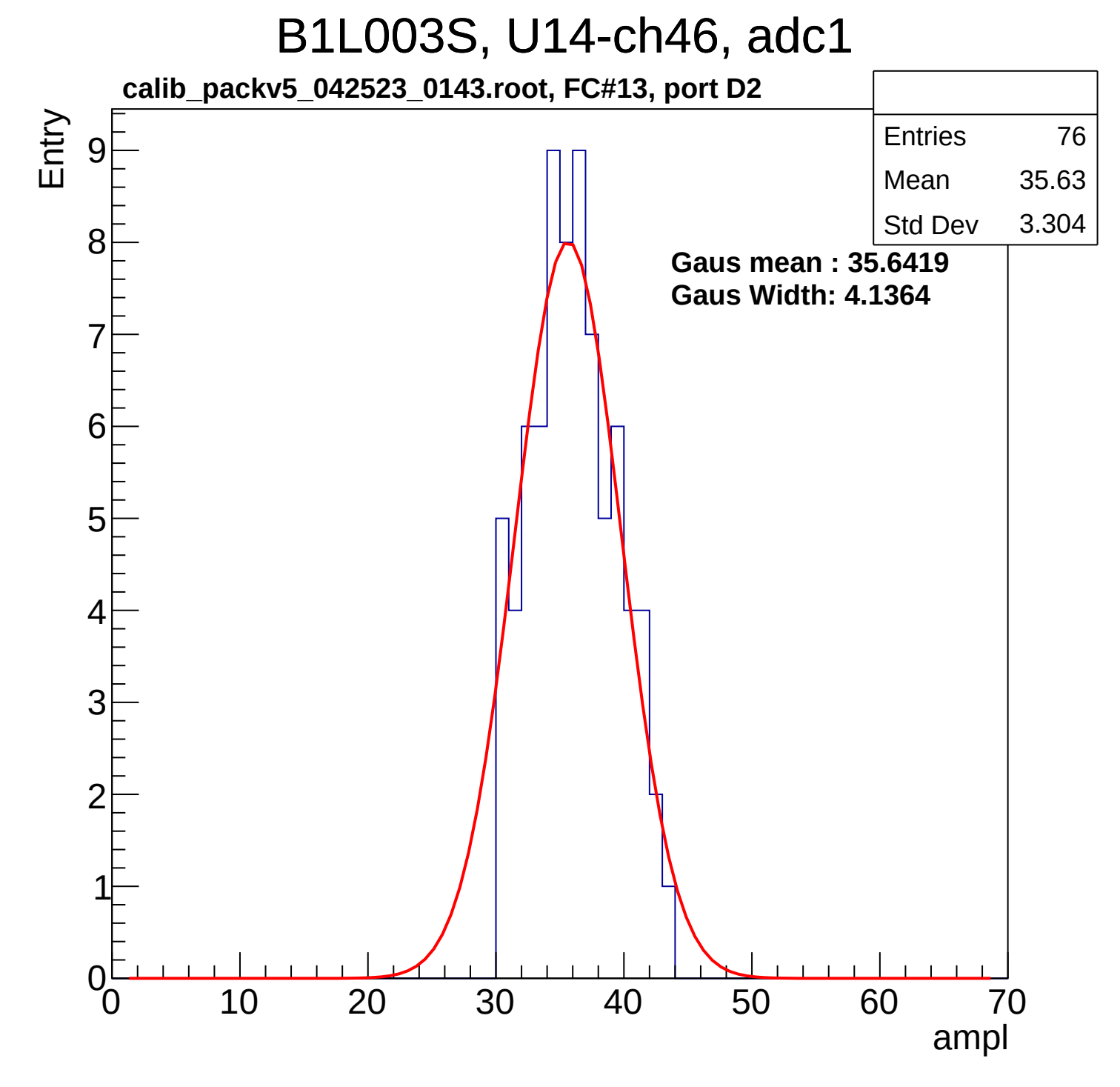

B1L003S, U14-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 53.23
Std Dev 3.255
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

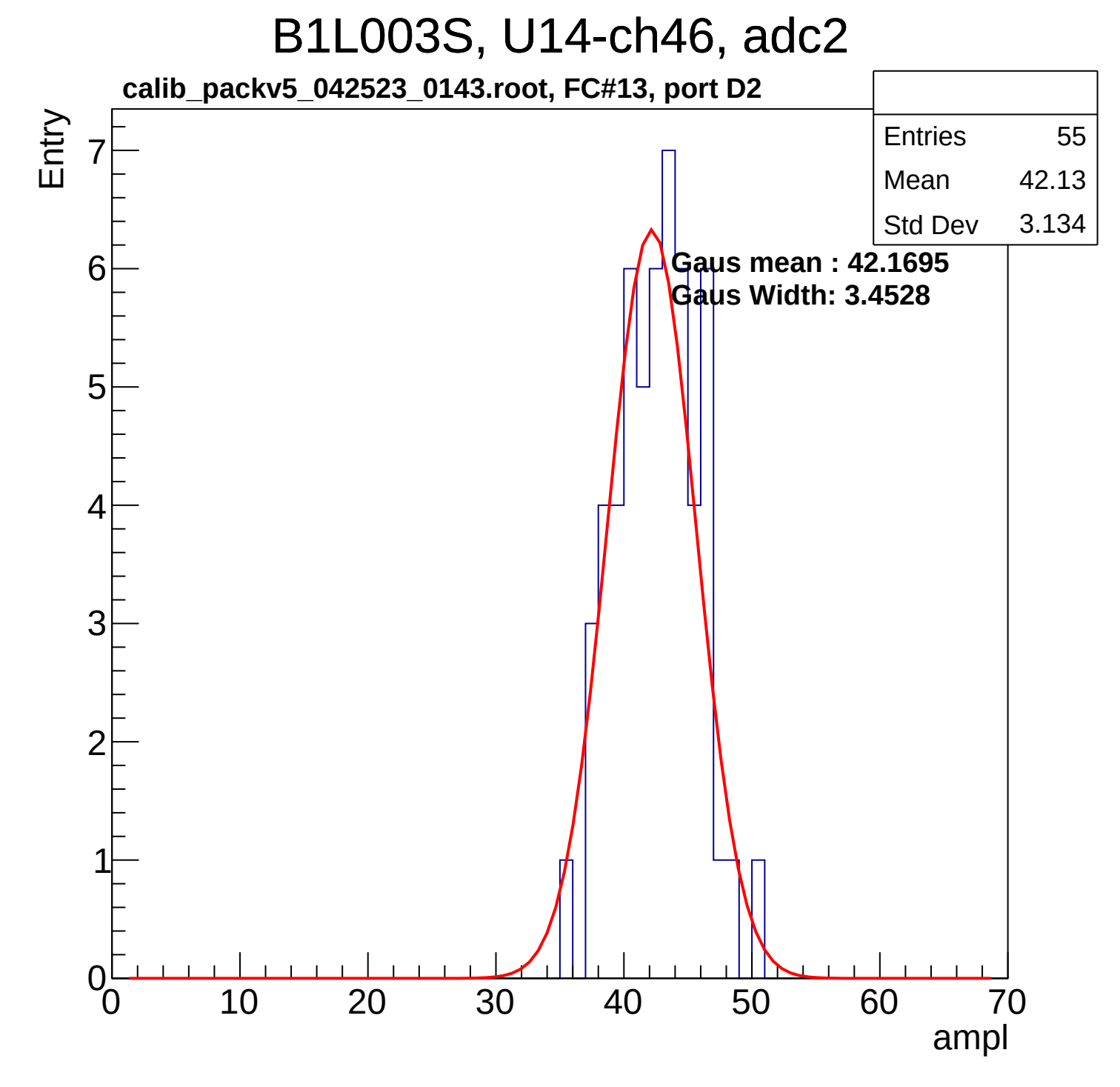

B1L003S, U14-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.09
Std Dev 8.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

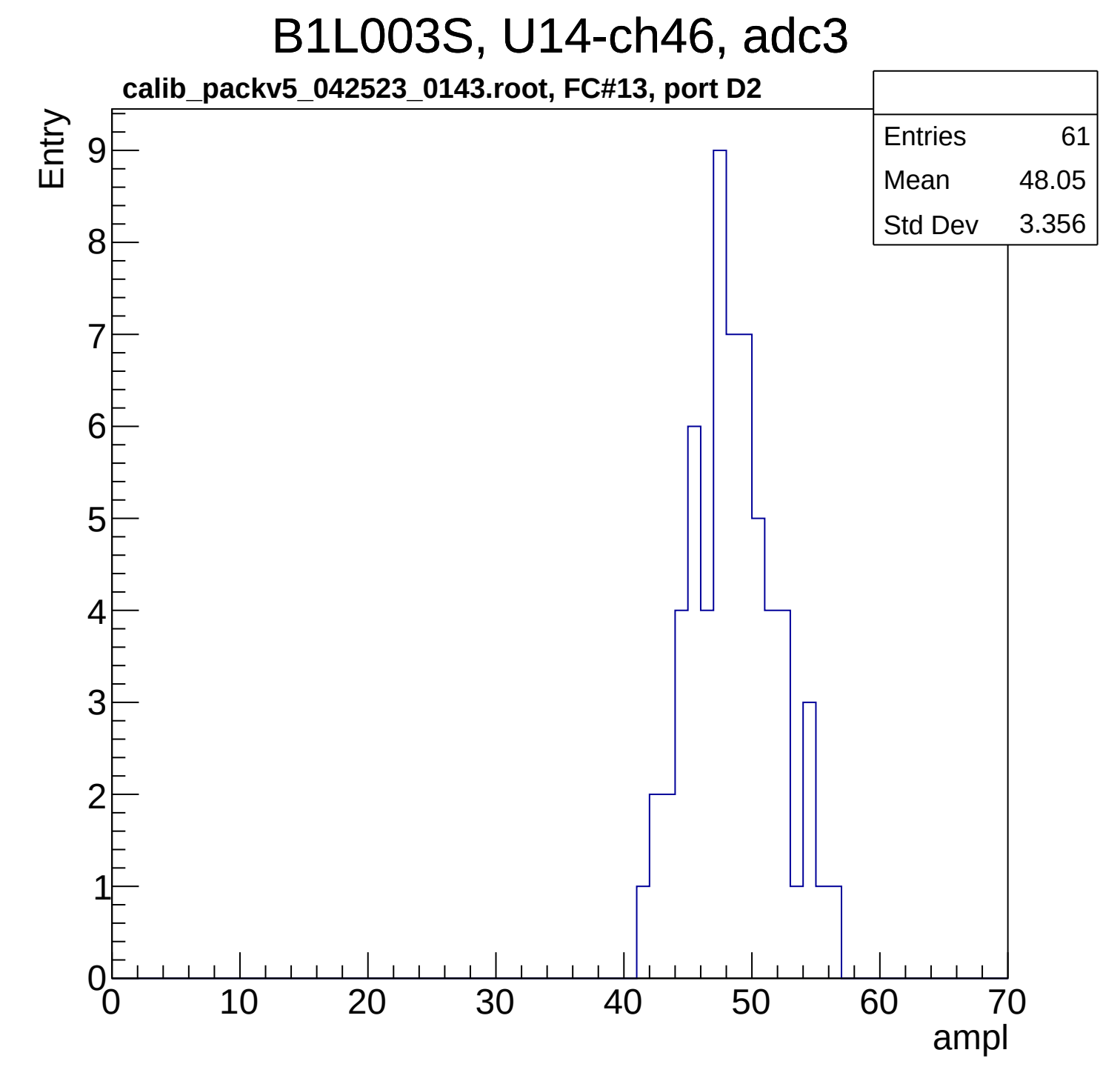

B1L003S, U14-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 60.73
Std Dev 1.948
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

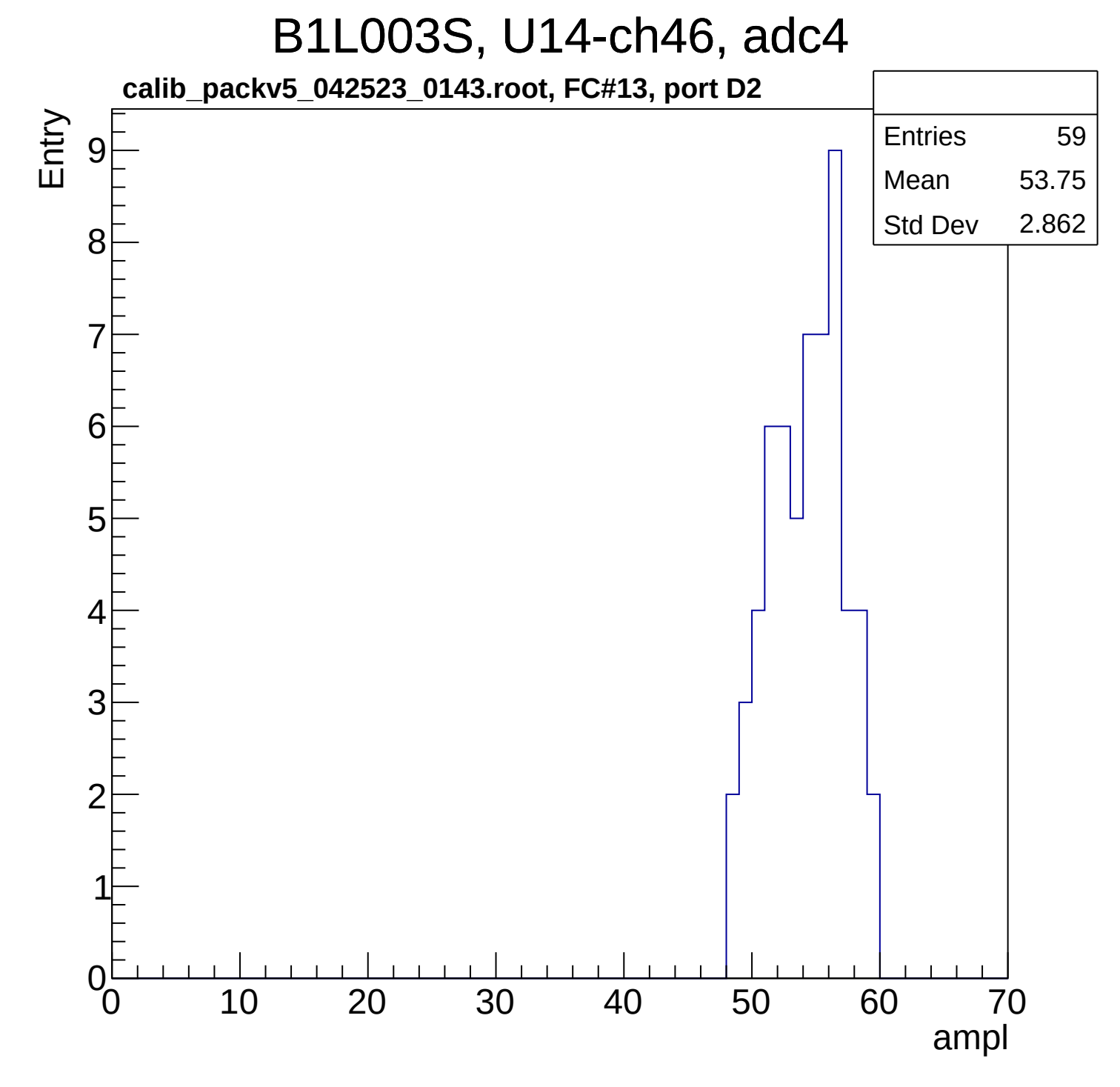

B1L003S, U14-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

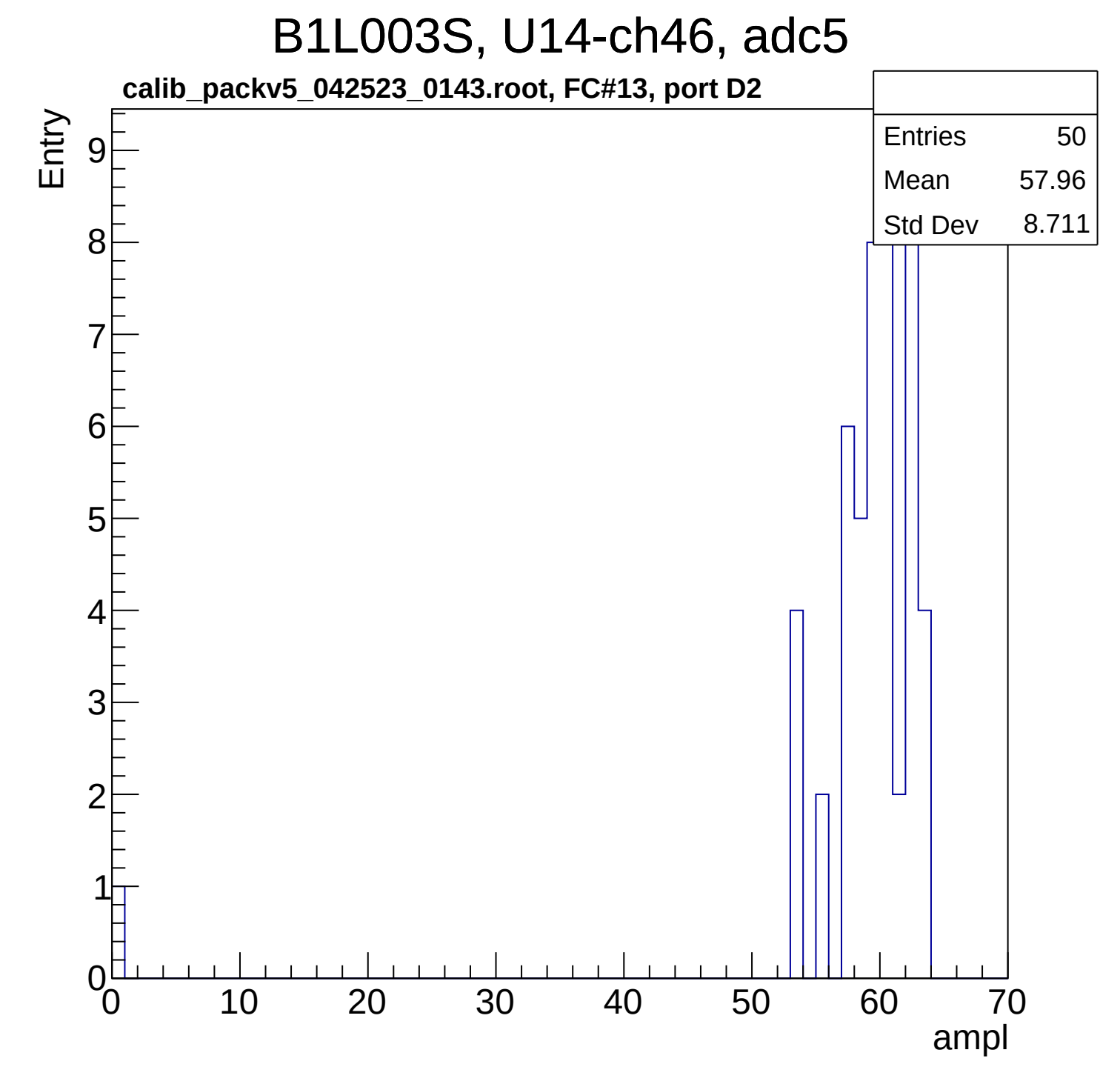

B1L003S, U14-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.06
Std Dev 3.621
Gaus mean : 28.5195
Gaus Width: 3.3876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

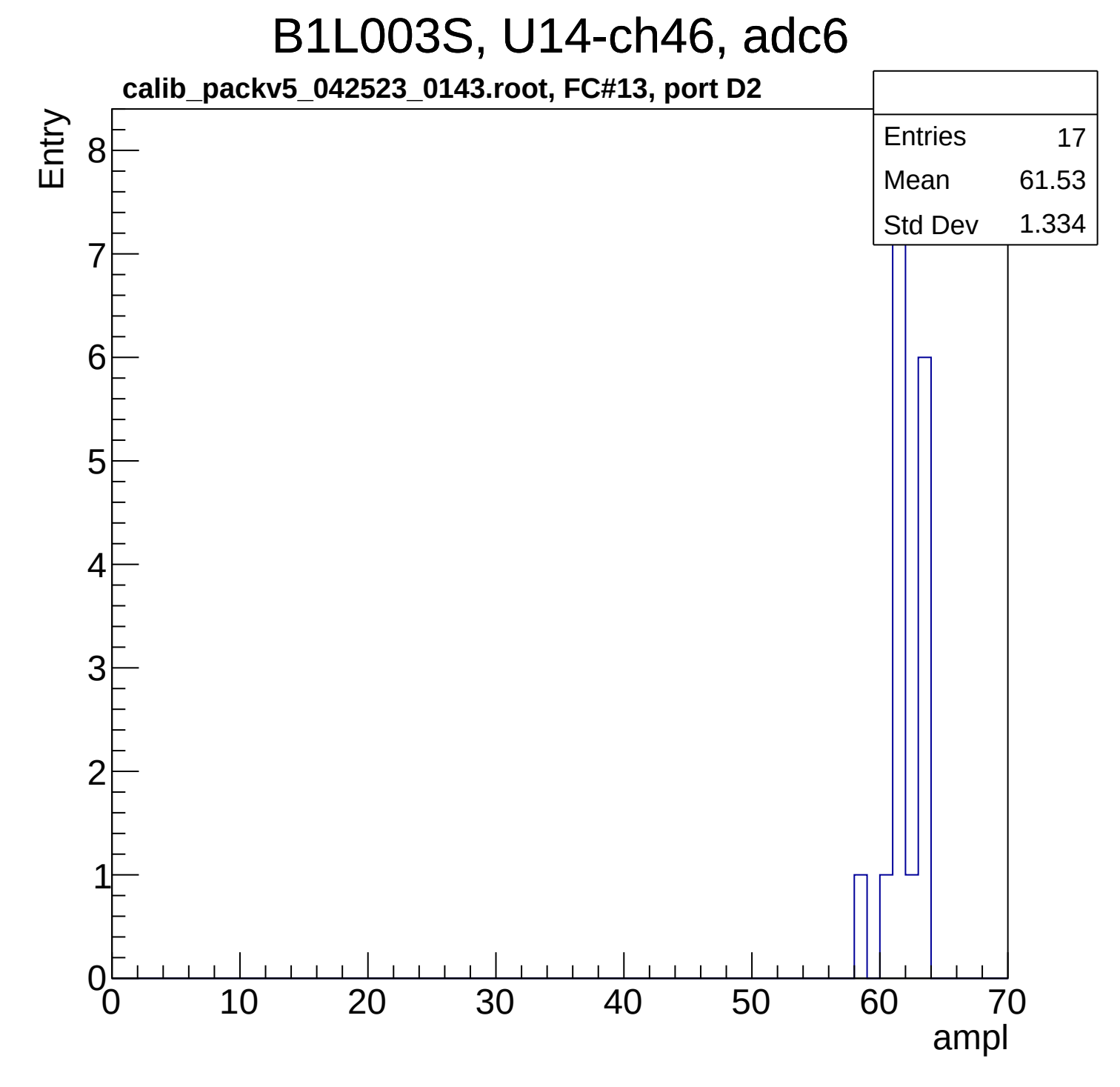

B1L003S, U14-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.63
Std Dev 3.304
Gaus mean : 35.6419
Gaus Width: 4.1364
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

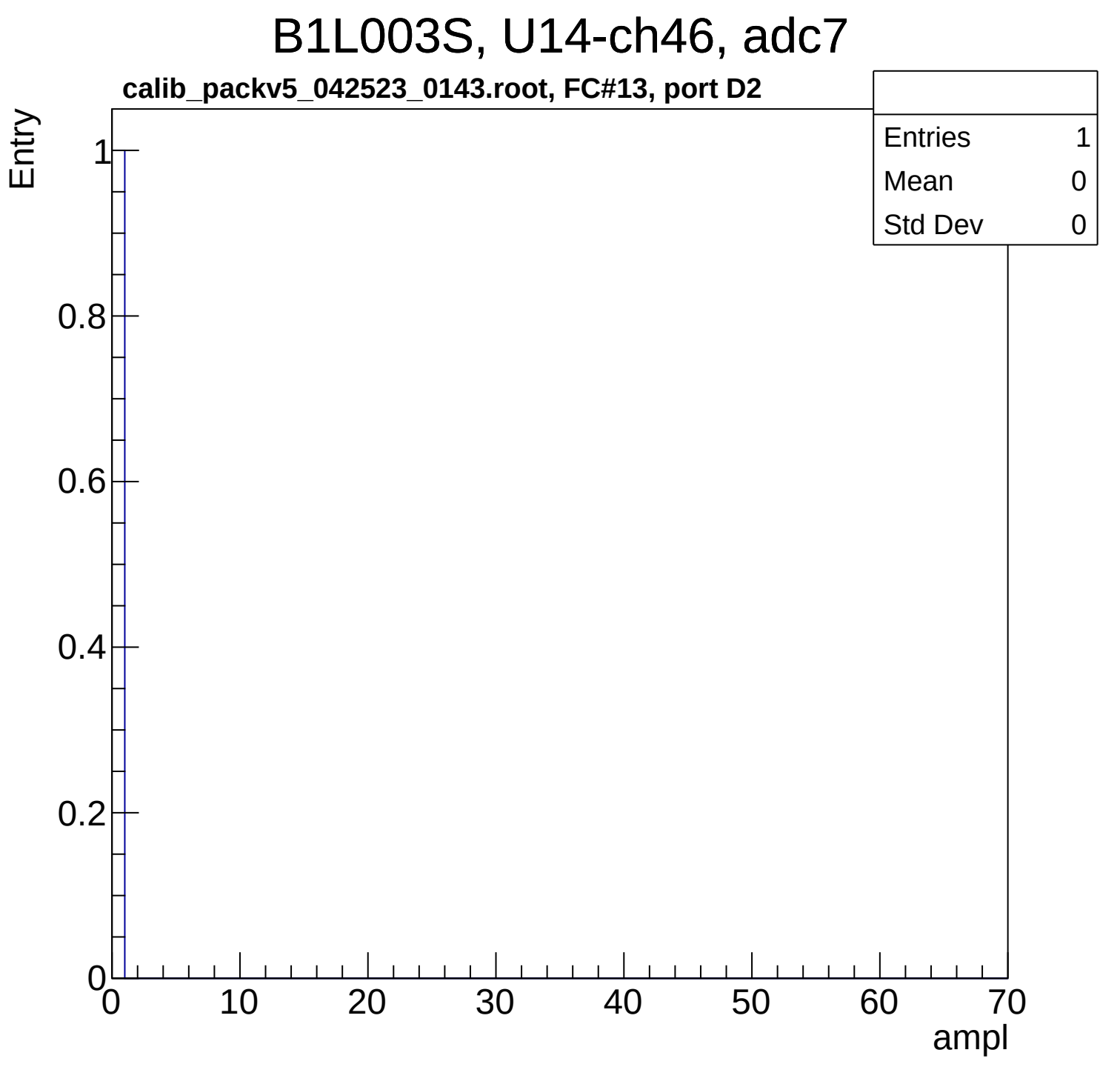

B1L003S, U14-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.13
Std Dev 3.134
Gaus mean : 42.1695
Gaus Width: 3.4528
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

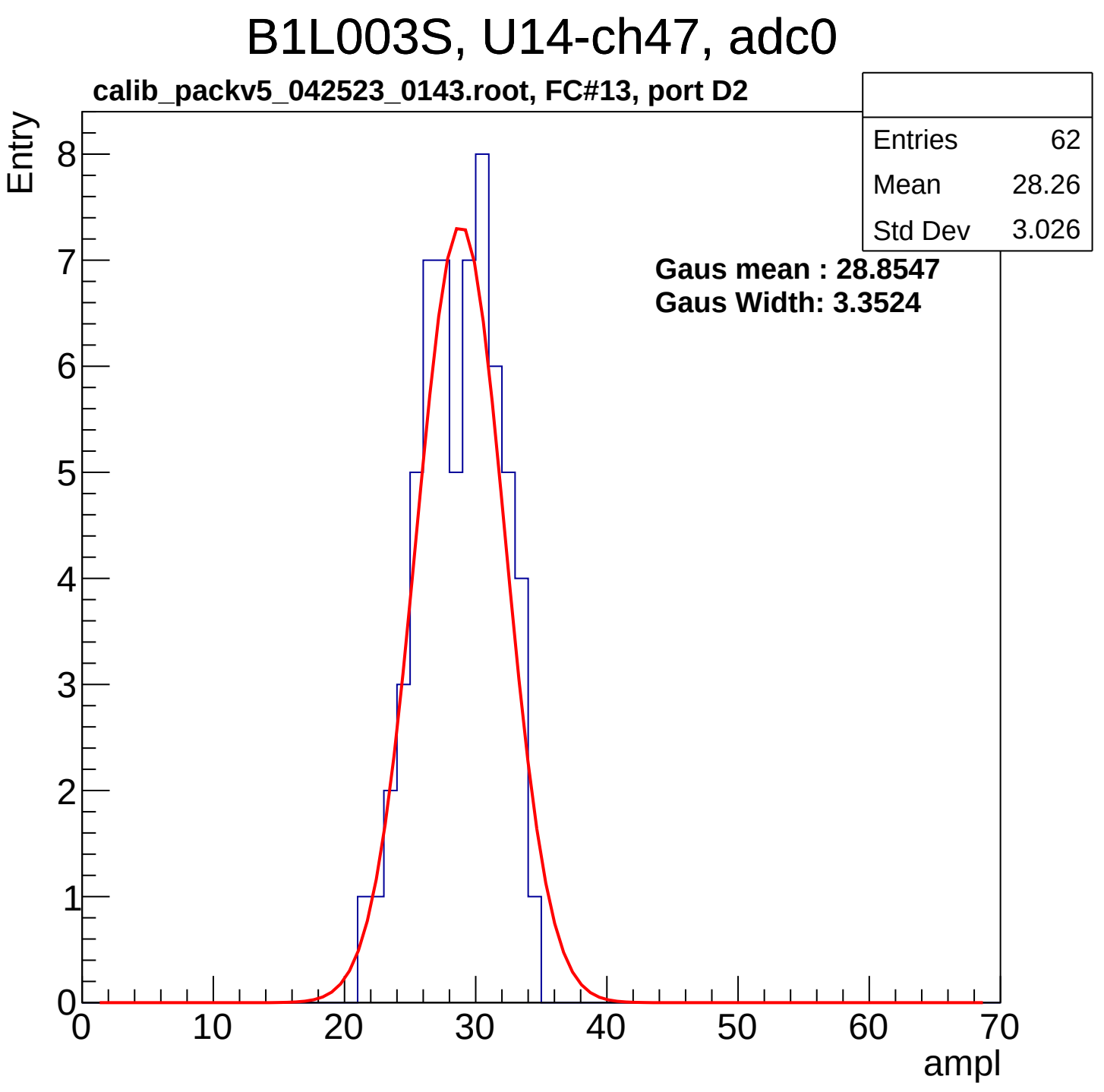

B1L003S, U14-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.05
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

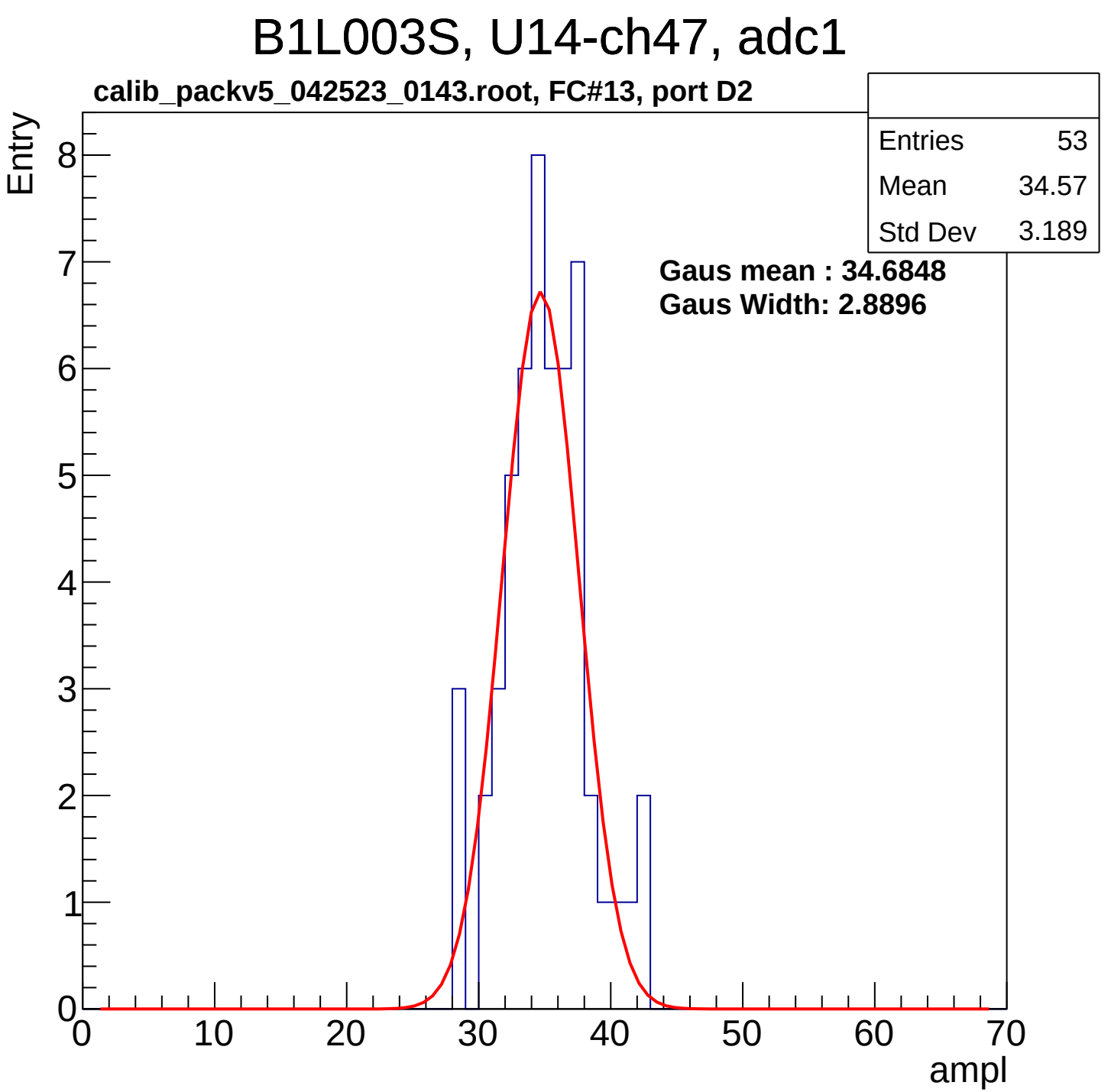

B1L003S, U14-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 53.75
Std Dev 2.862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

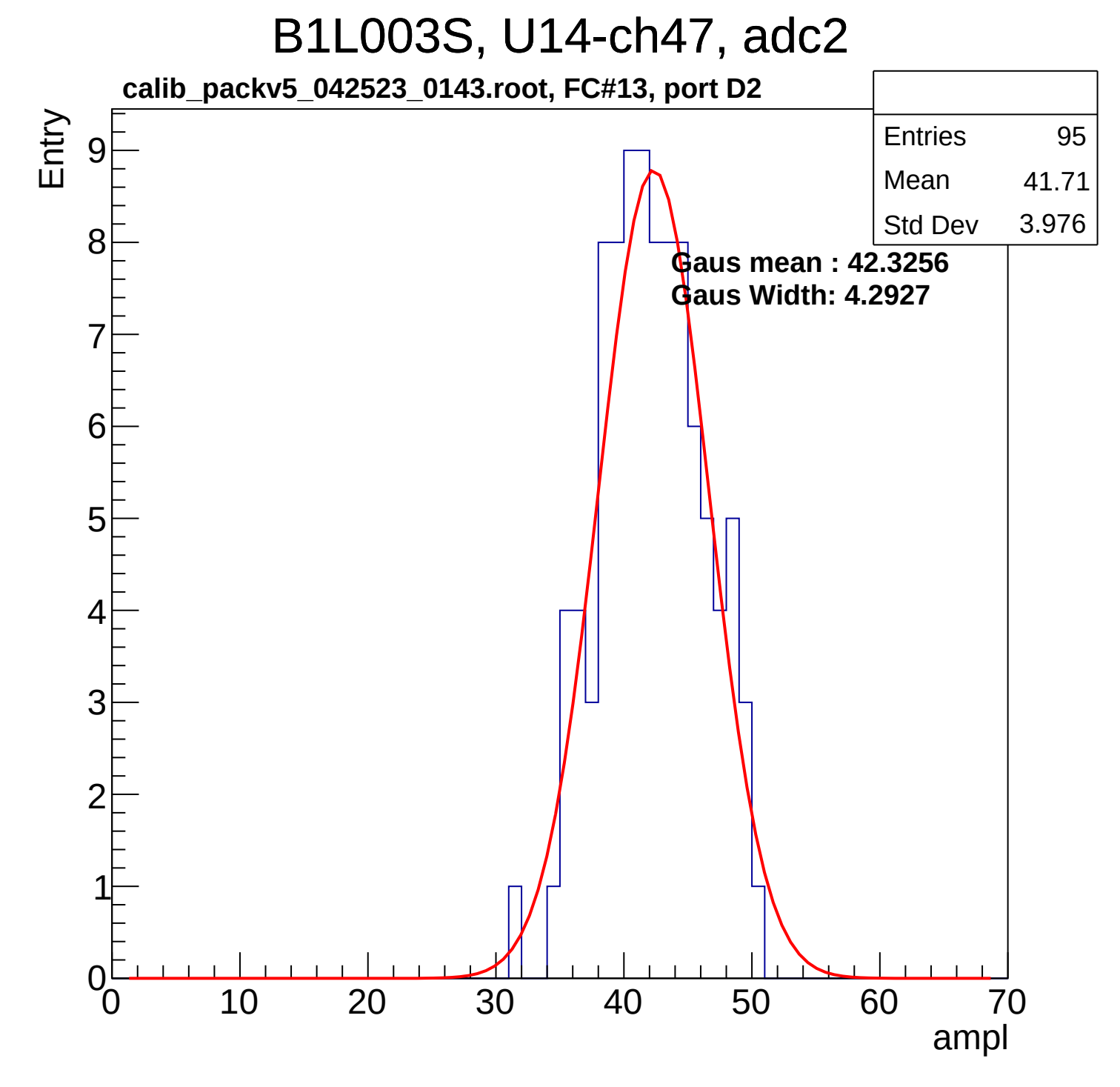

B1L003S, U14-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 57.96
Std Dev 8.711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

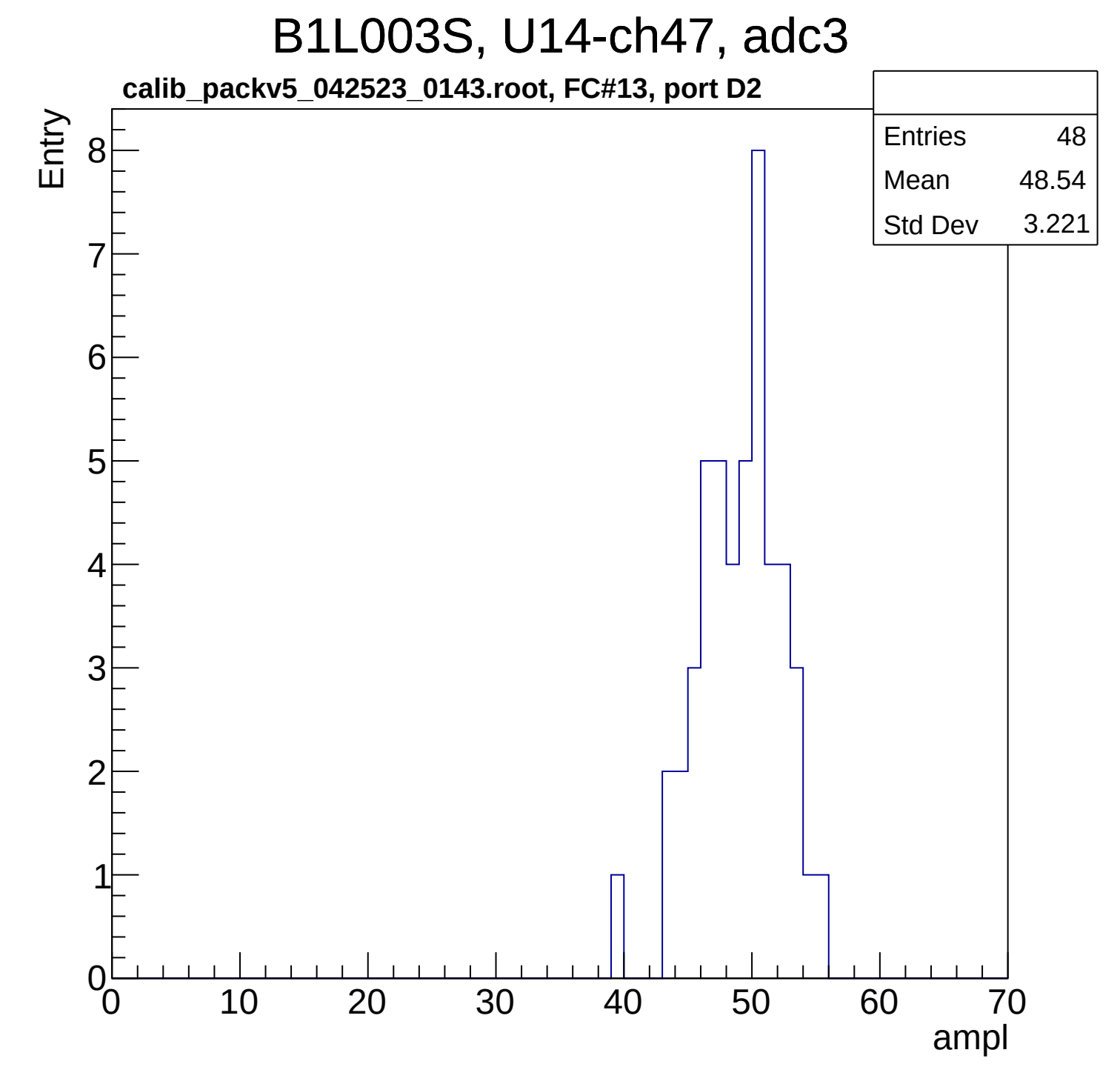

B1L003S, U14-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 61.53
Std Dev 1.334
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

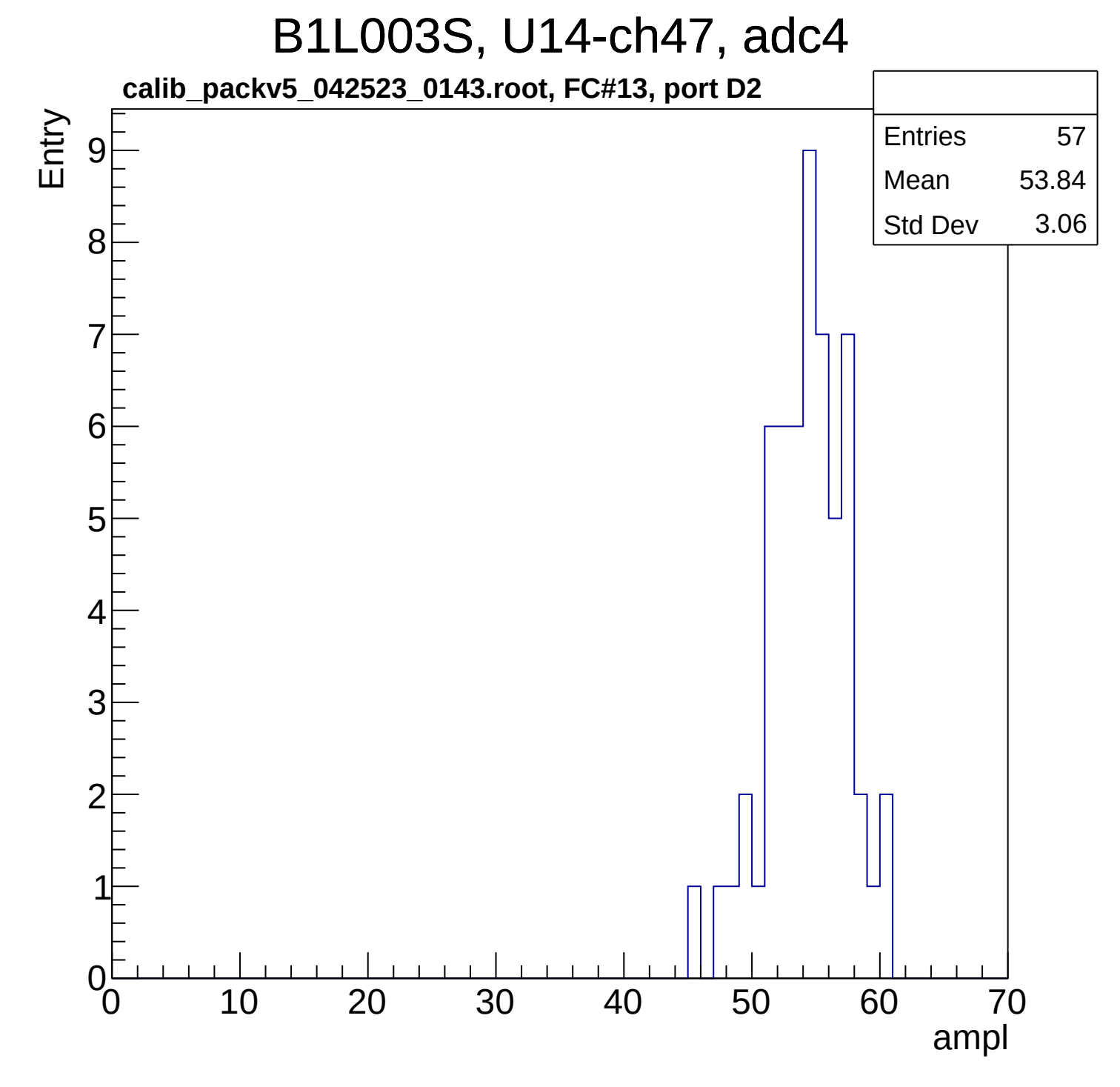

B1L003S, U14-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

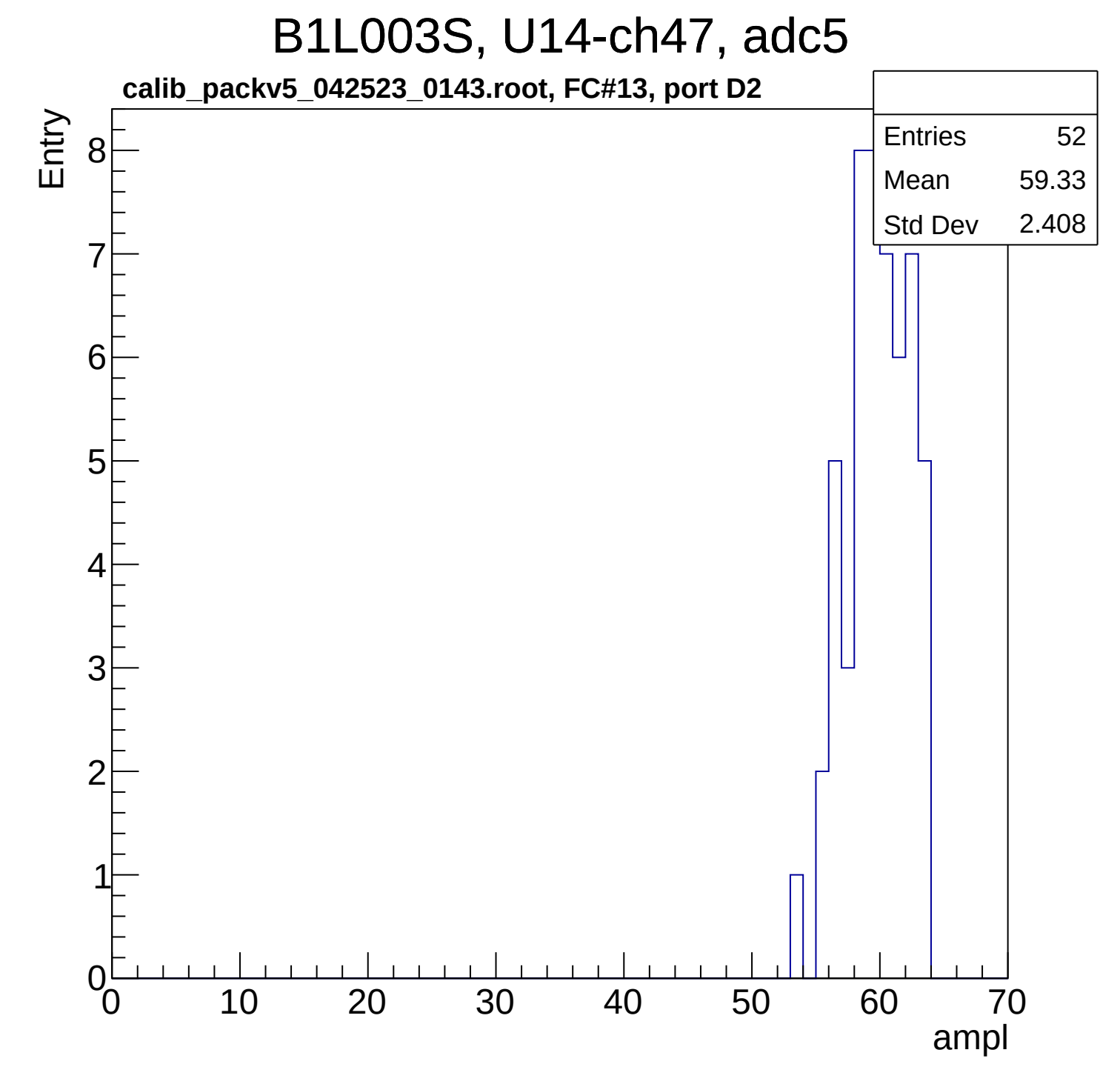

B1L003S, U14-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.26
Std Dev 3.026
Gaus mean : 28.8547
Gaus Width: 3.3524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

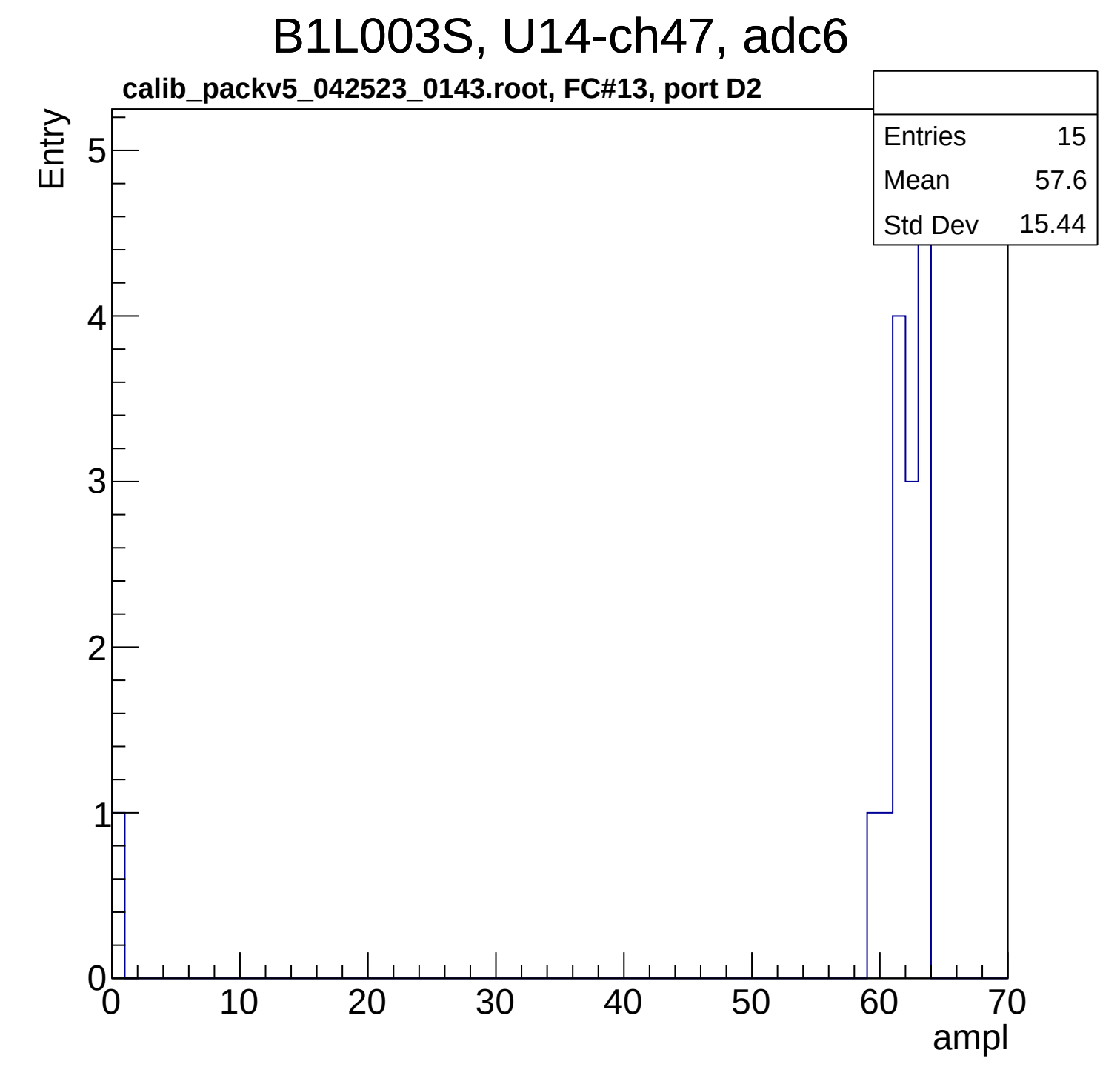

B1L003S, U14-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 34.57
Std Dev 3.189
Gaus mean : 34.6848
Gaus Width: 2.8896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

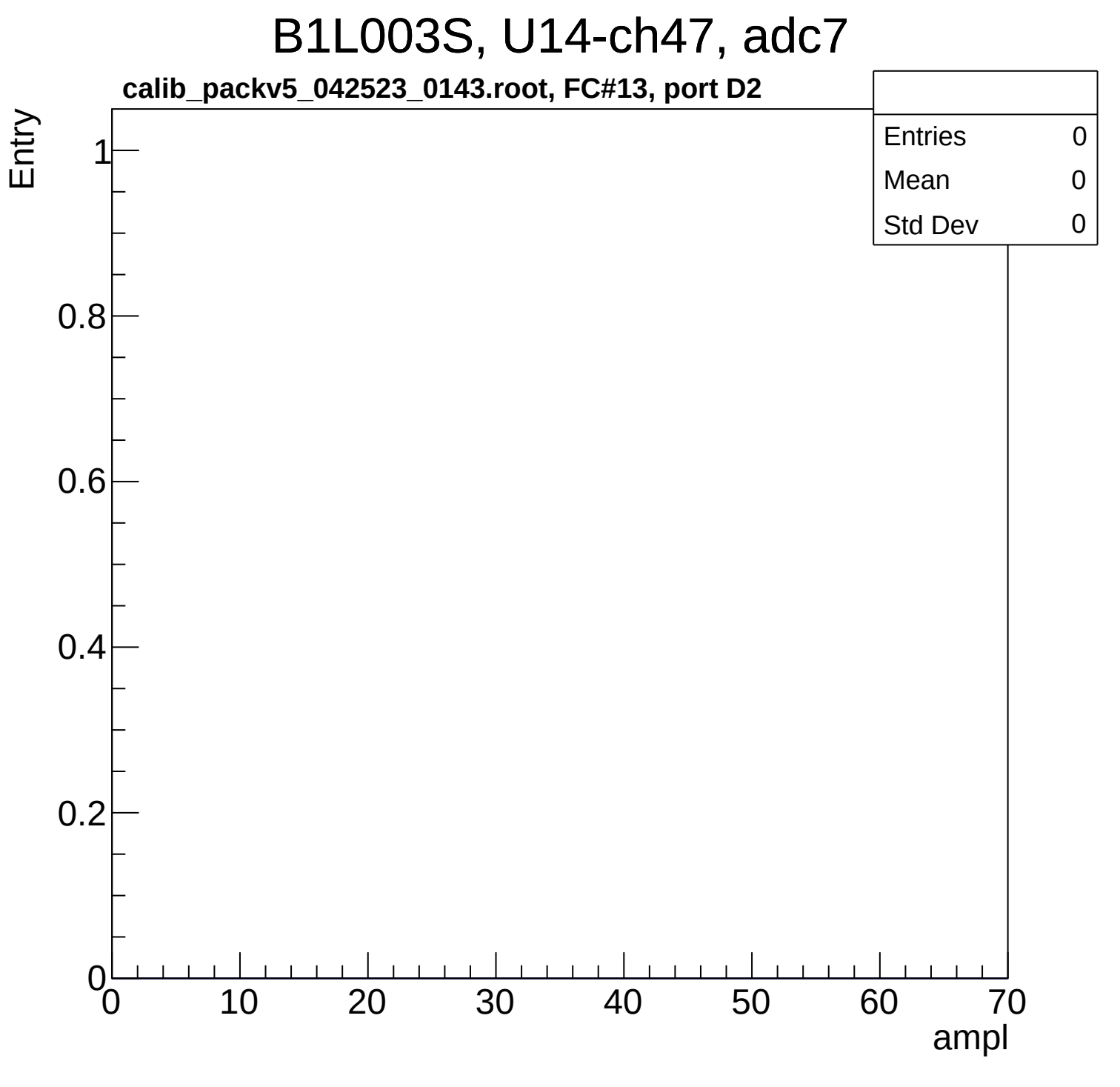

B1L003S, U14-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 95
Mean 41.71
Std Dev 3.976
Gaus mean : 42.3256
Gaus Width: 4.2927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

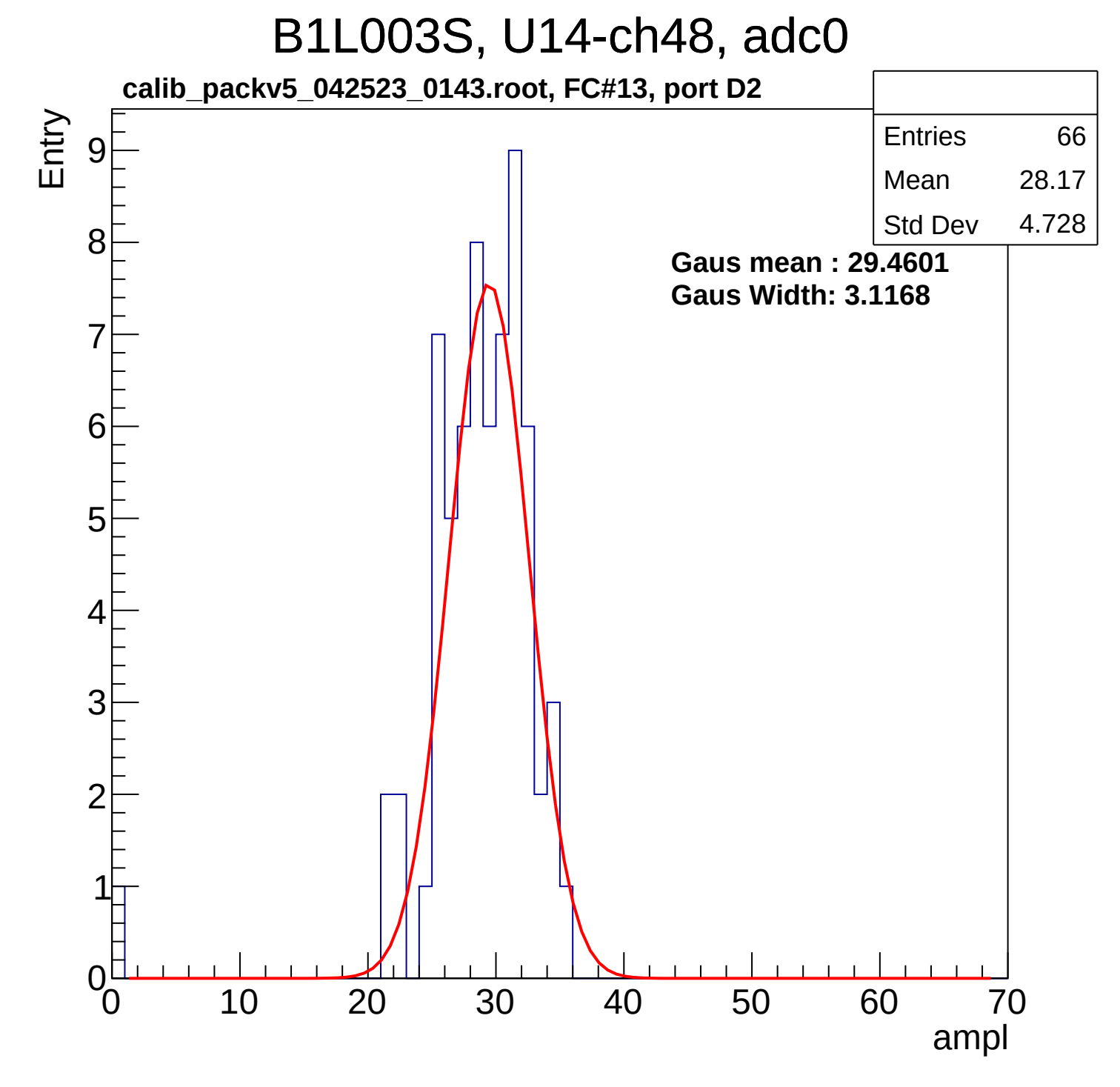

B1L003S, U14-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 48.54
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

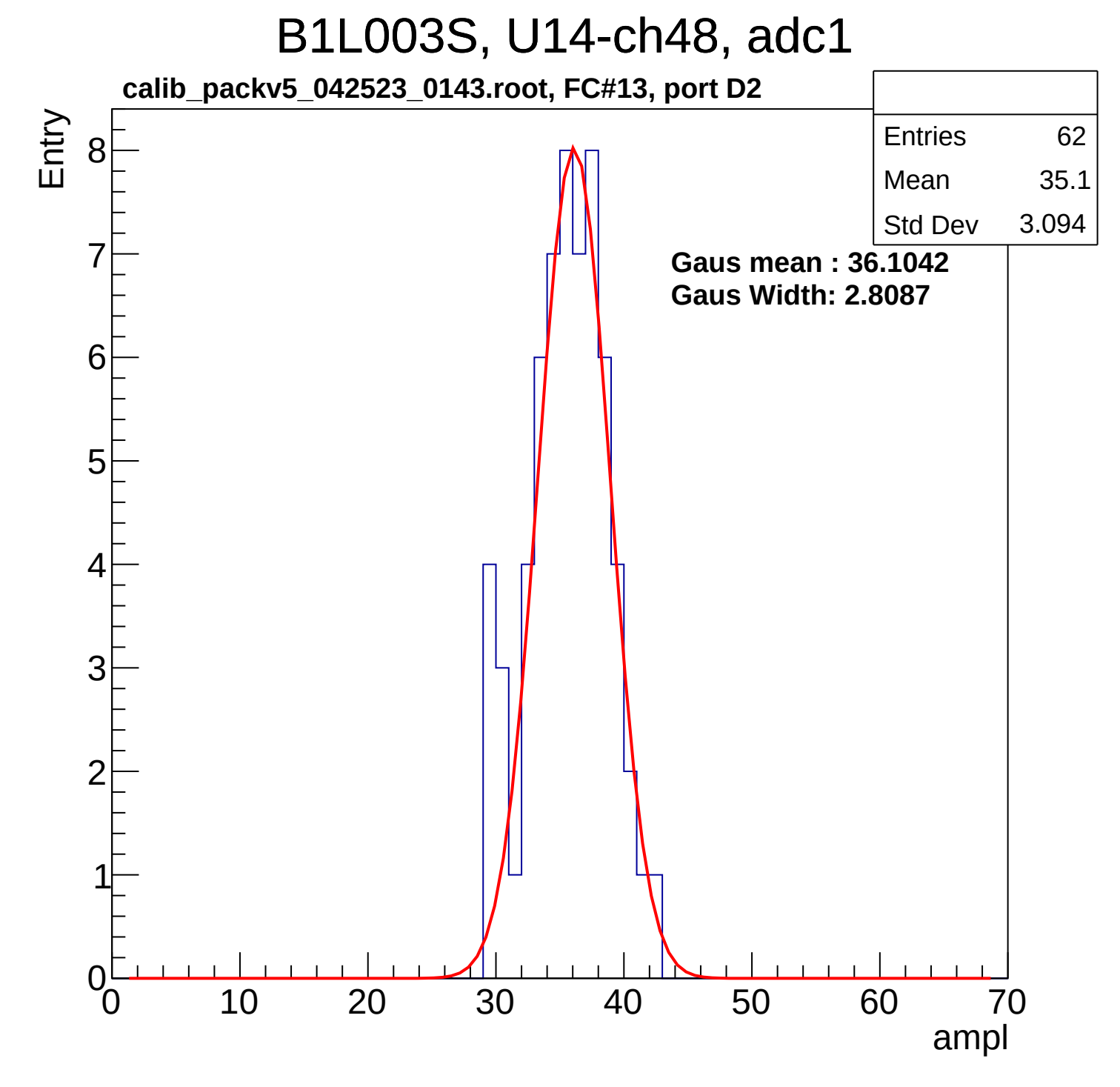

B1L003S, U14-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.84
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

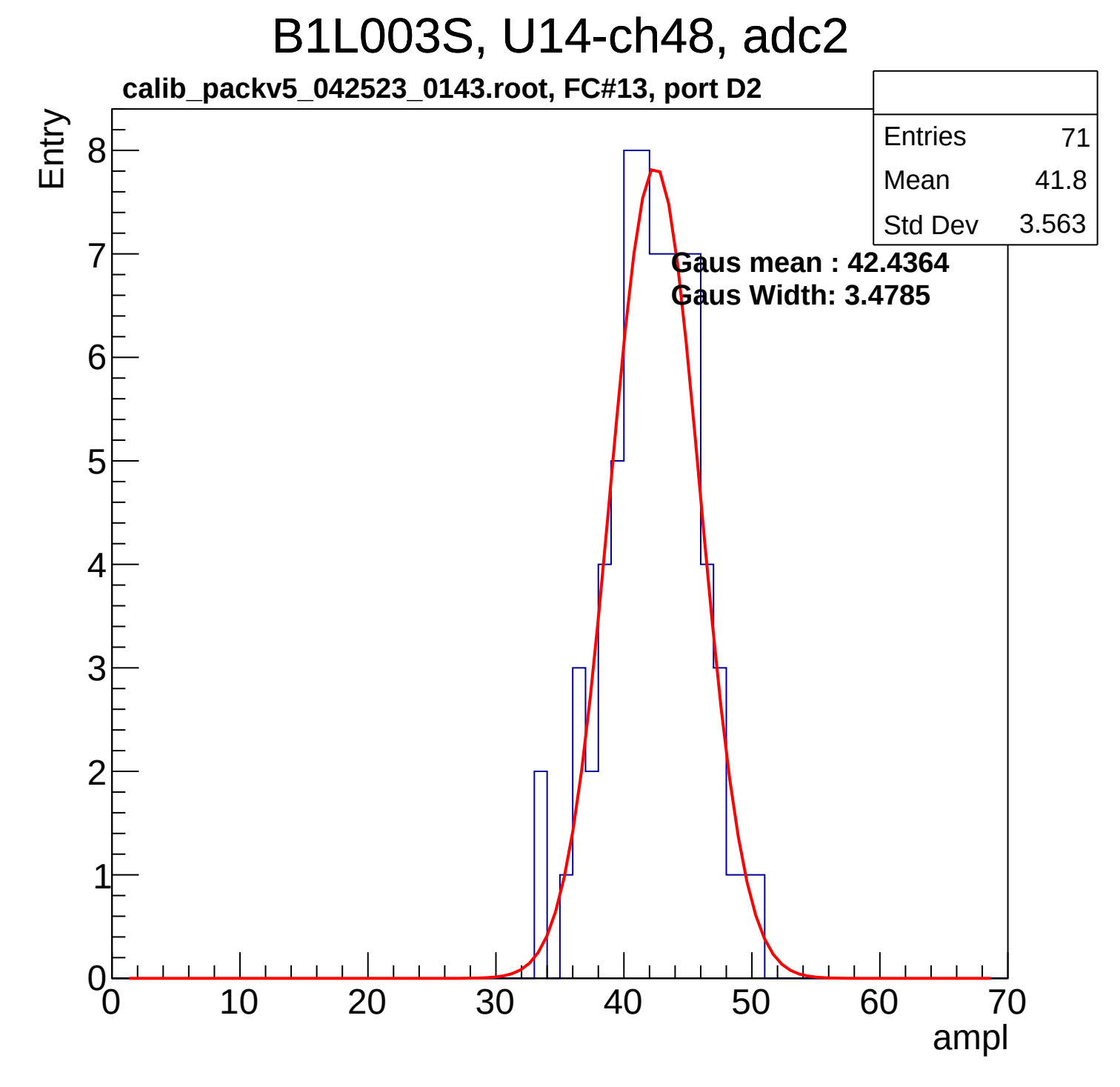

B1L003S, U14-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.33
Std Dev 2.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

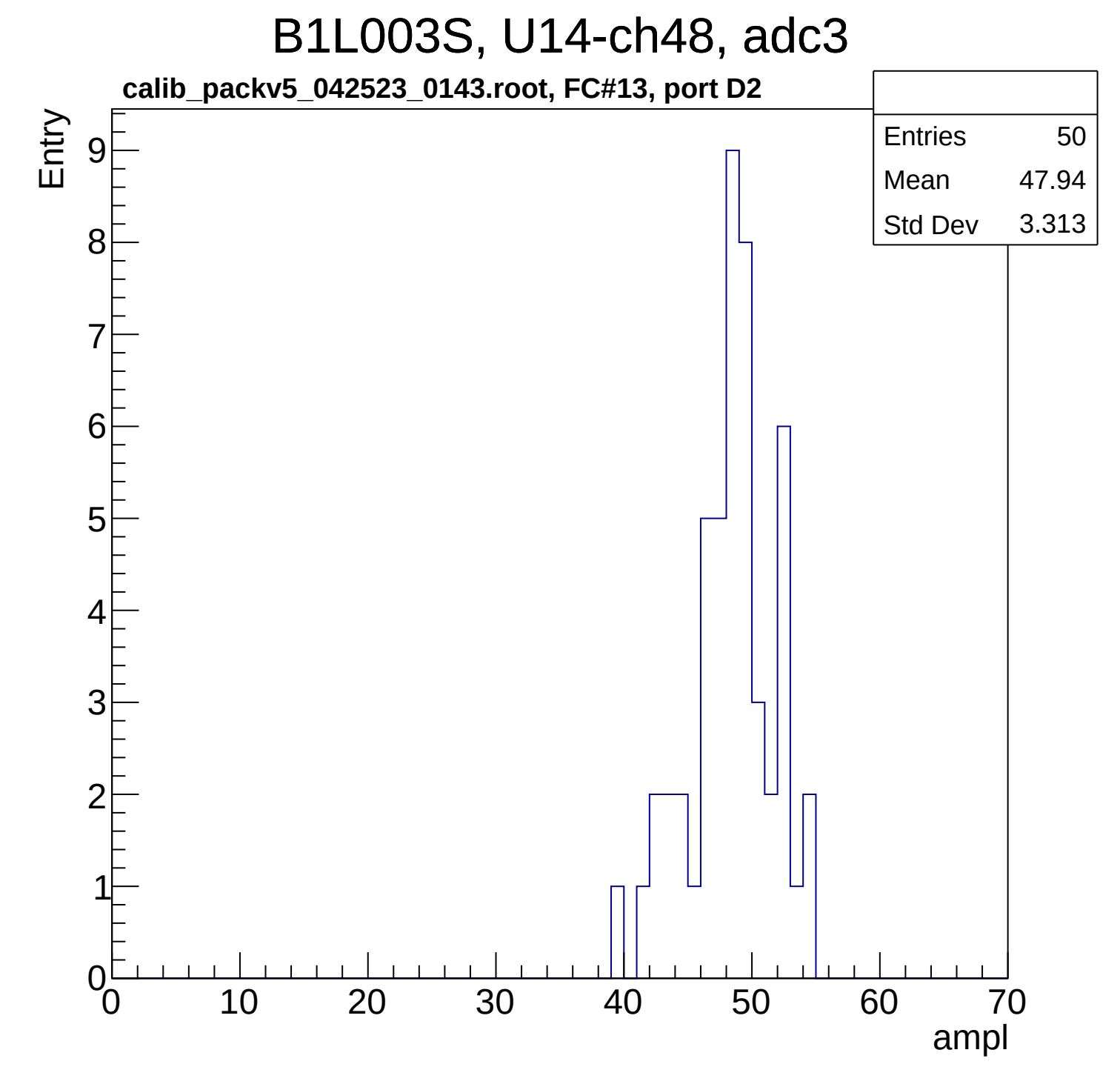

B1L003S, U14-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.6
Std Dev 15.44
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

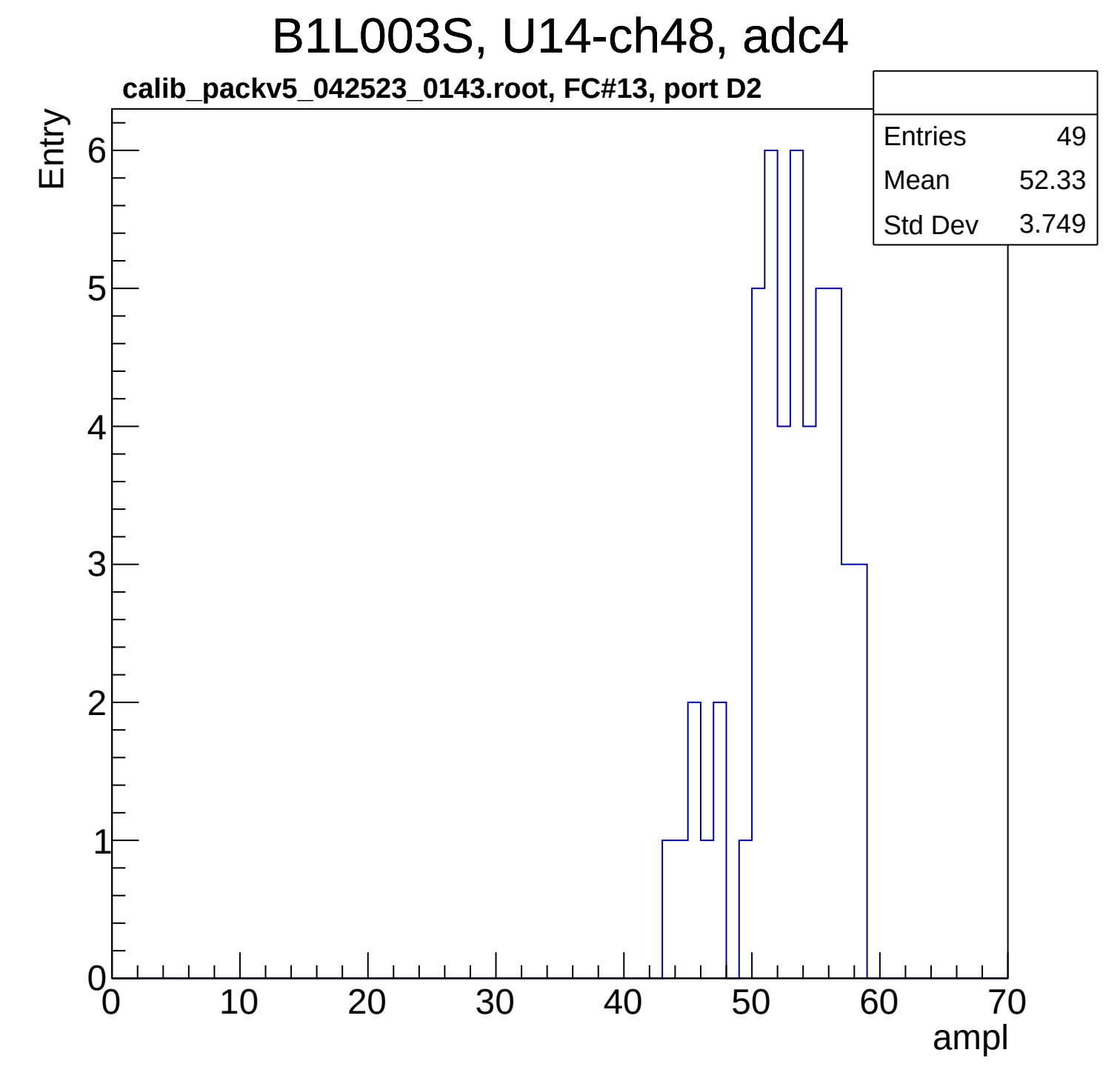

B1L003S, U14-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

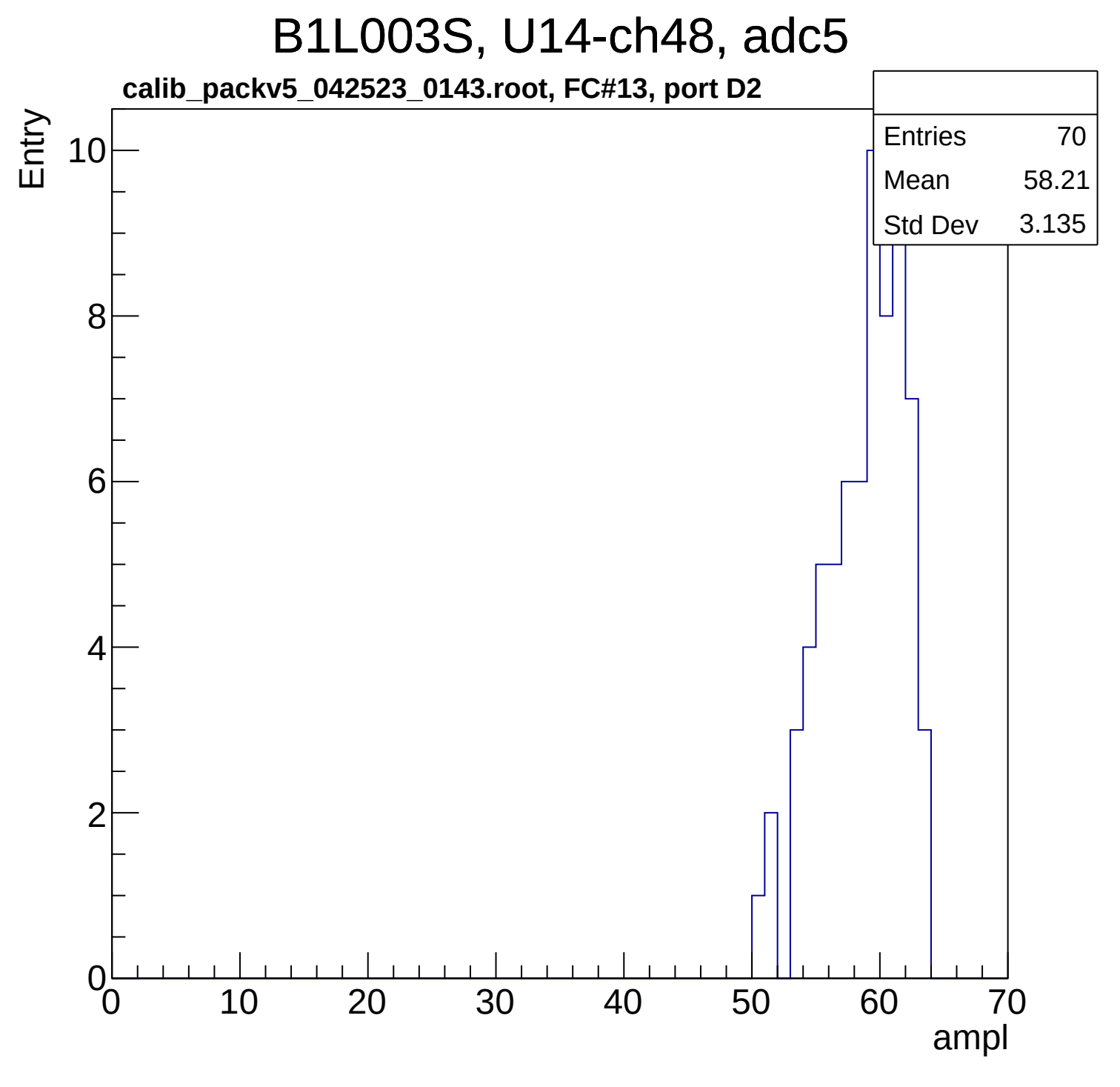

B1L003S, U14-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.17
Std Dev 4.728
Gaus mean : 29.4601
Gaus Width: 3.1168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

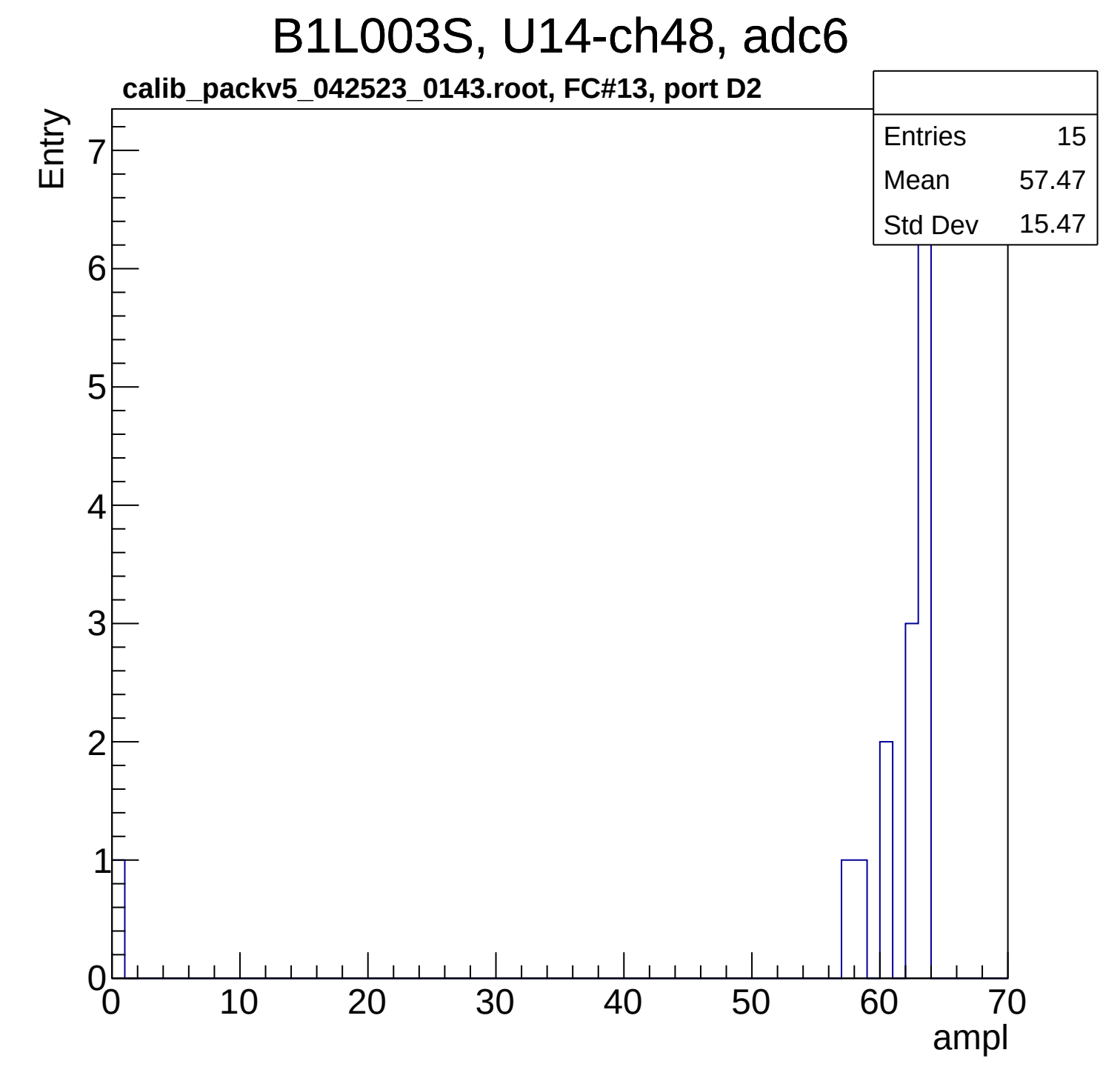

B1L003S, U14-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.1
Std Dev 3.094
Gaus mean : 36.1042
Gaus Width: 2.8087
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

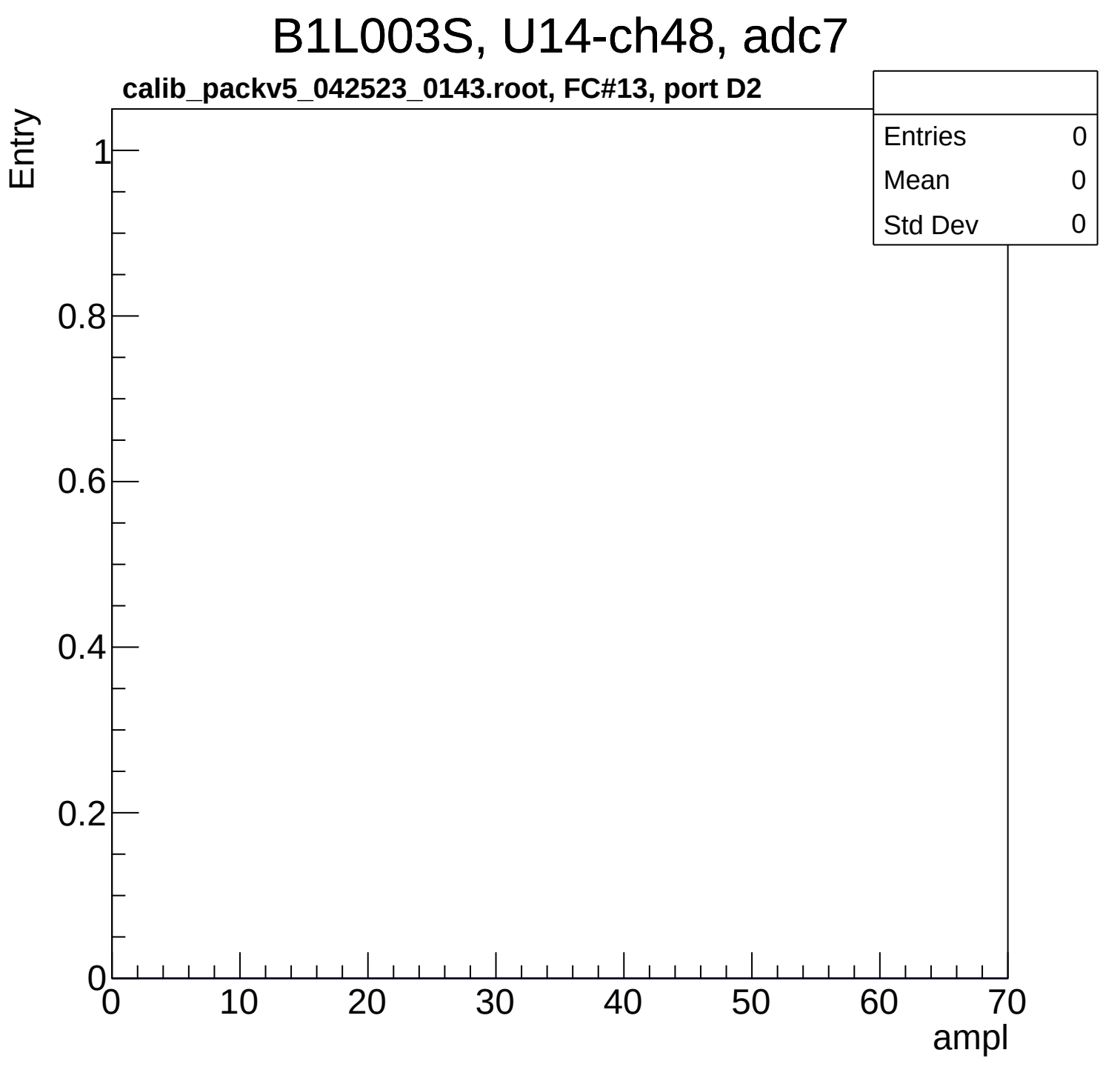

B1L003S, U14-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 41.8
Std Dev 3.563
Gaus mean : 42.4364
Gaus Width: 3.4785
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

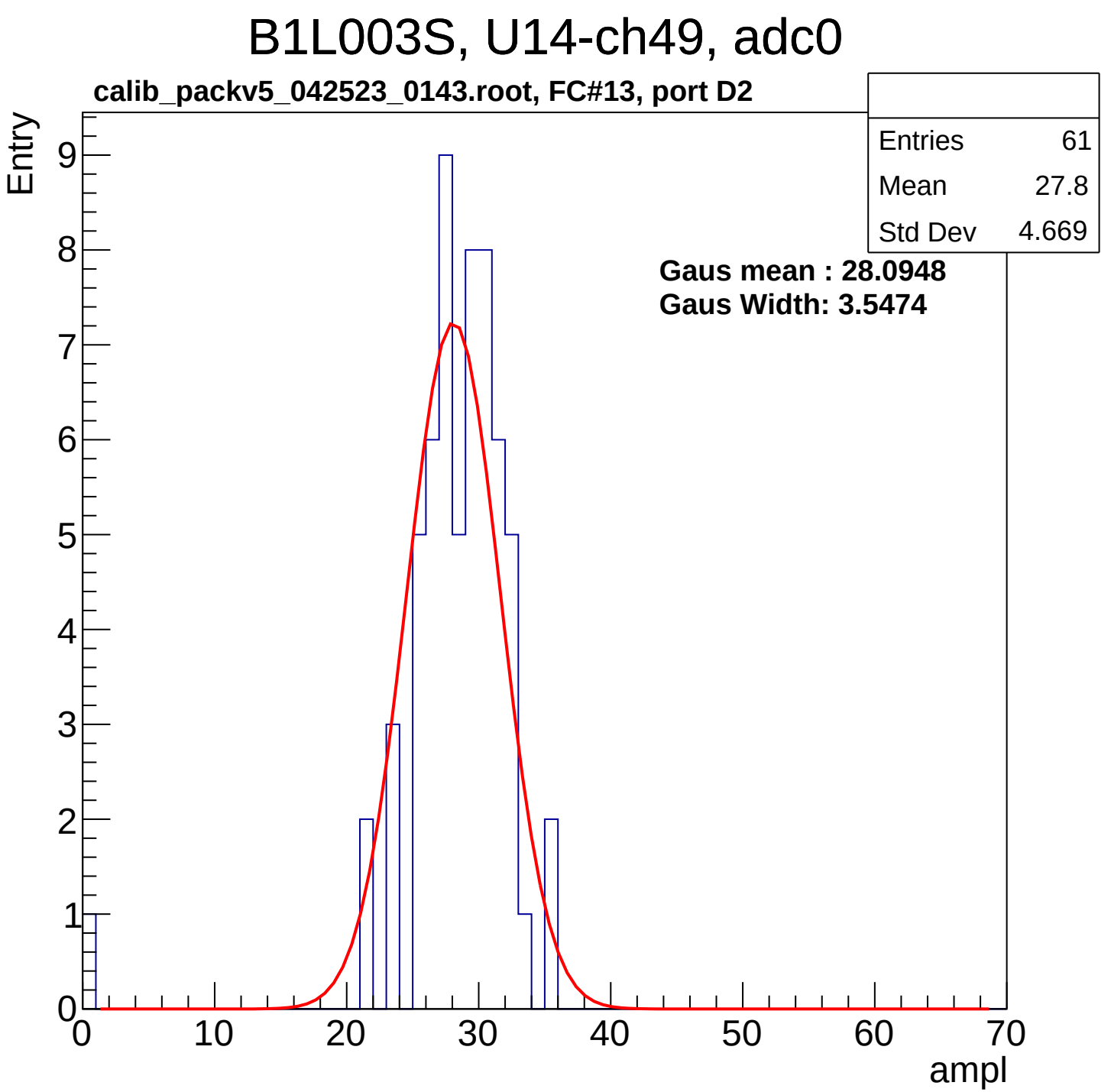

B1L003S, U14-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 47.94
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

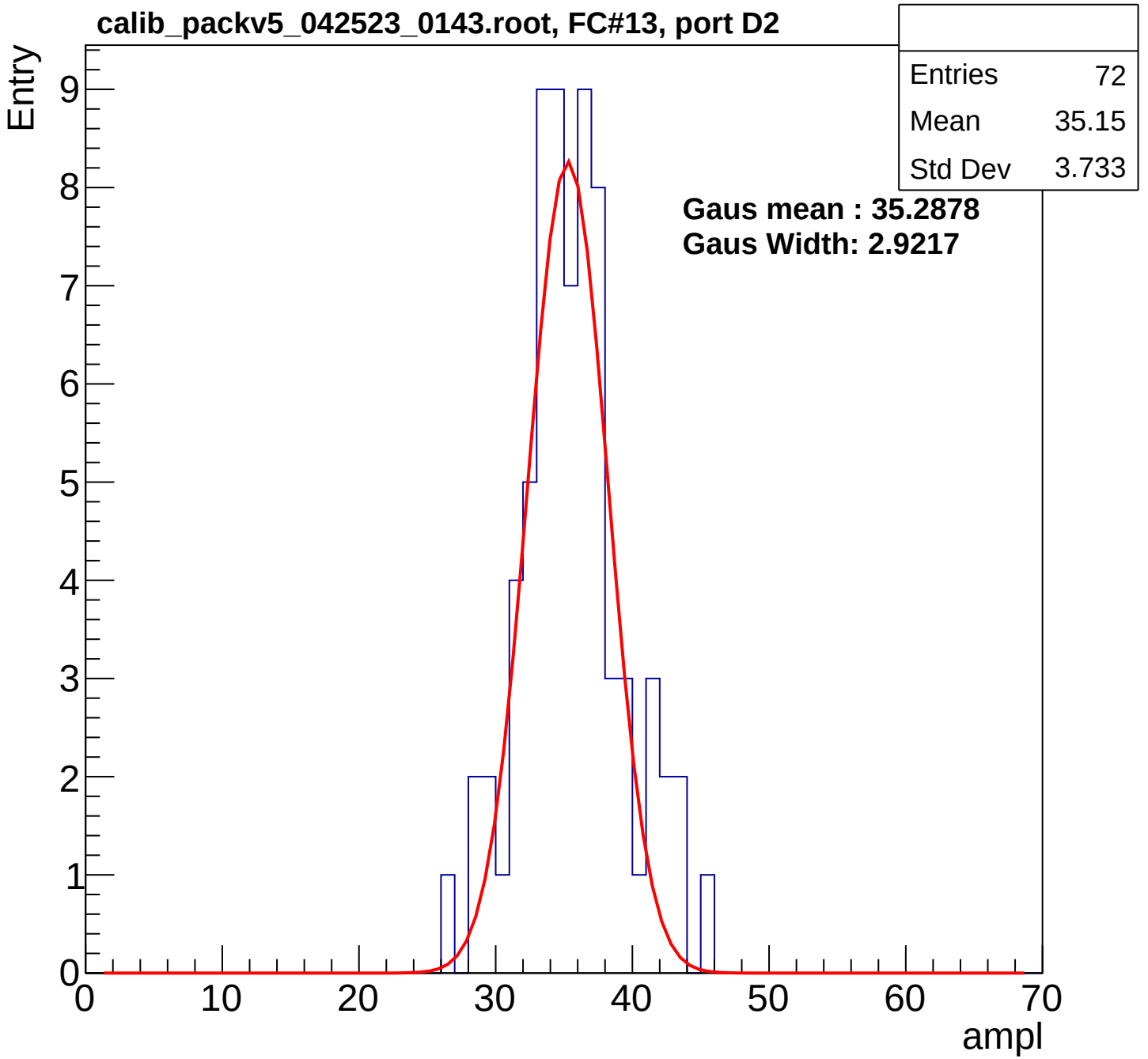

B1L003S, U14-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 52.33
Std Dev 3.749
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

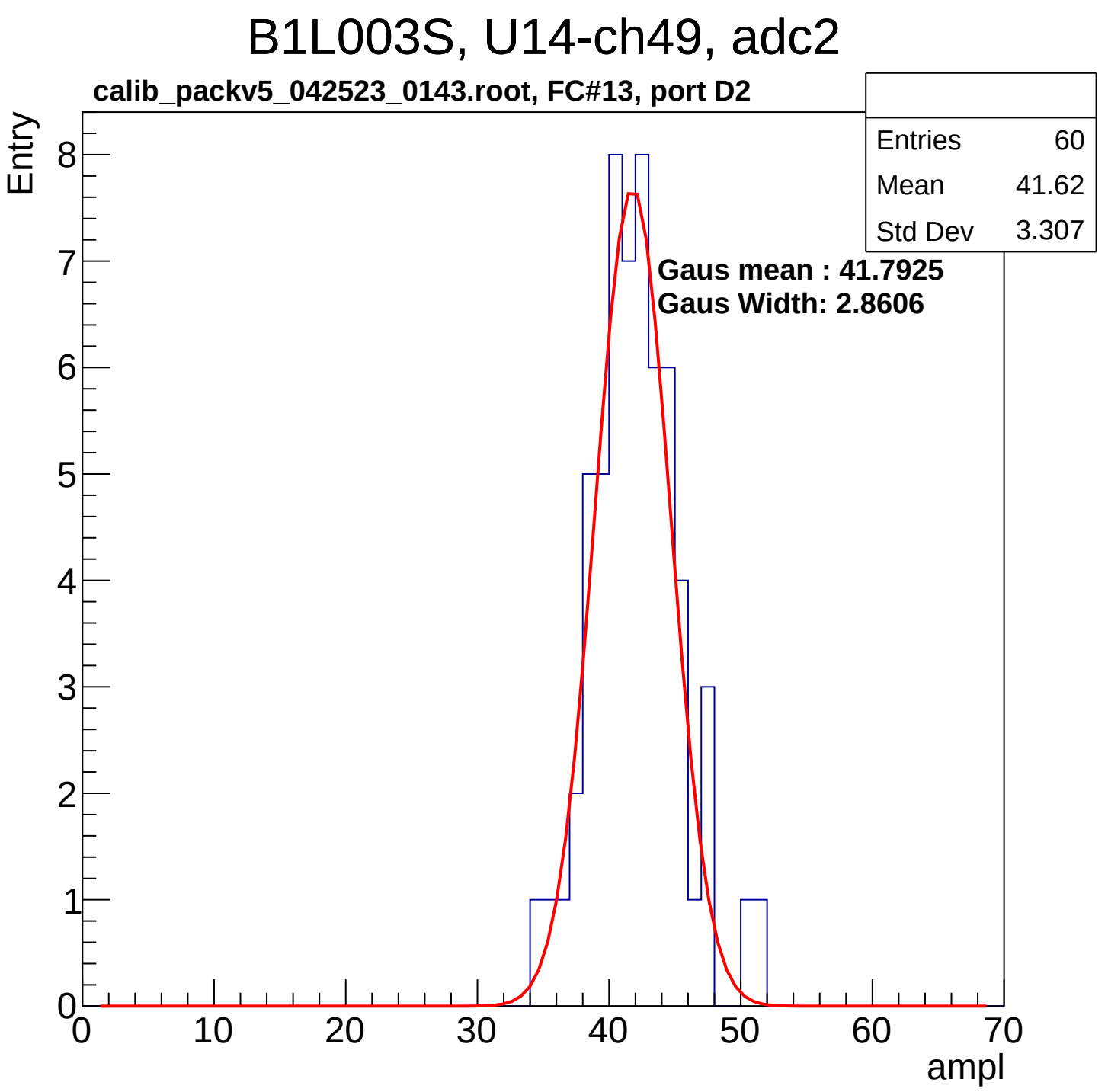

B1L003S, U14-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 58.21
Std Dev 3.135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

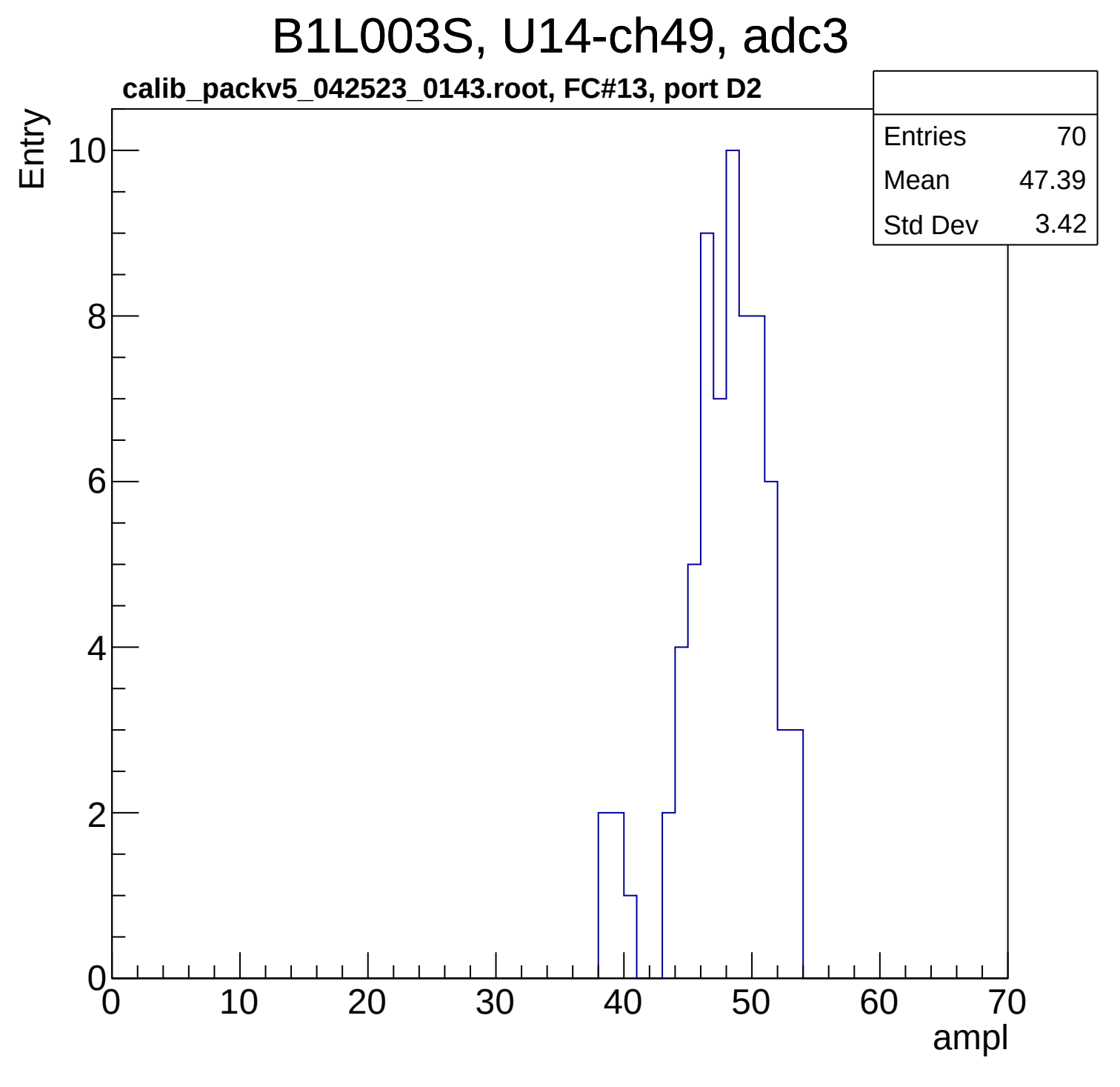

B1L003S, U14-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.47
Std Dev 15.47
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

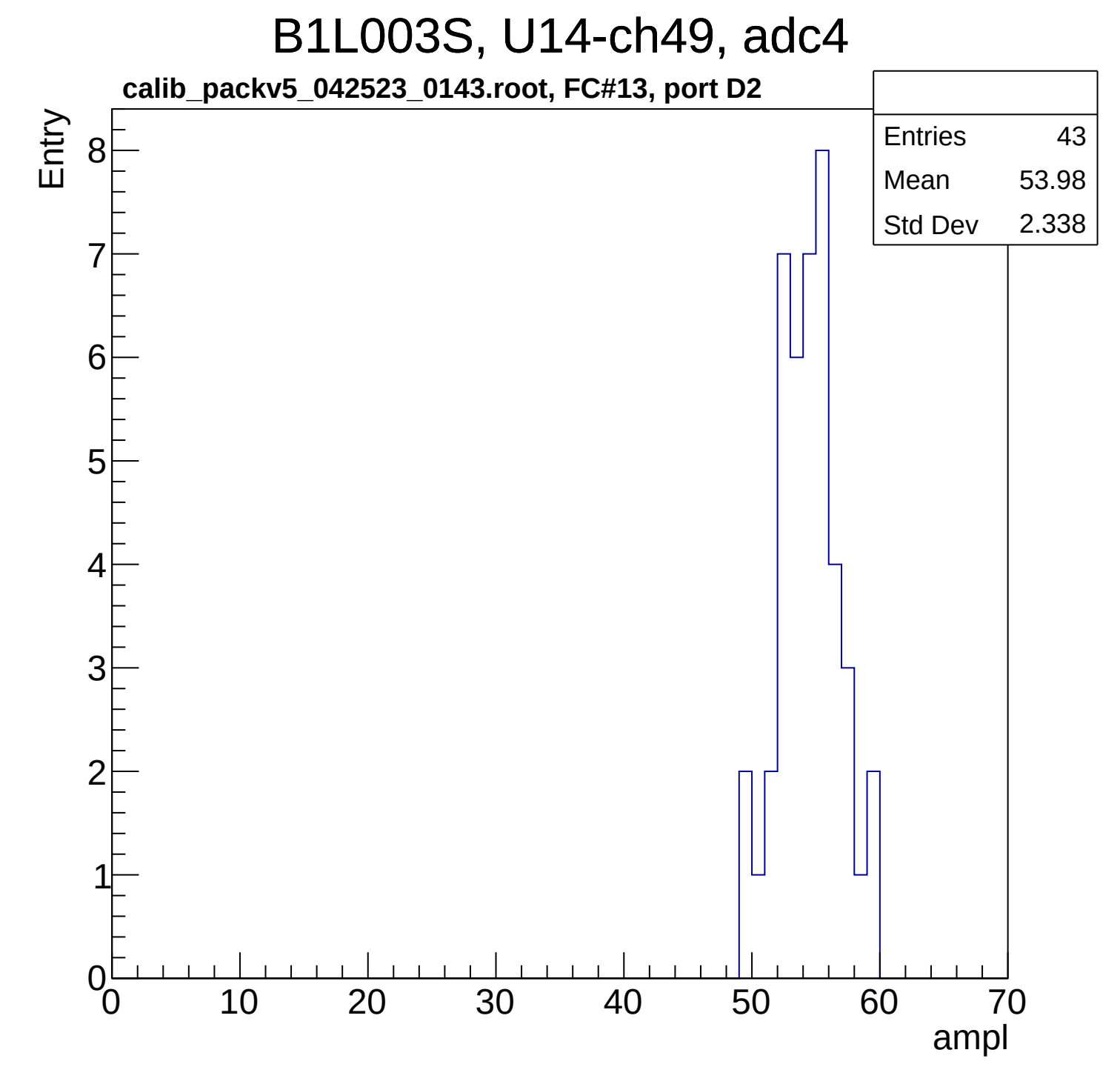

B1L003S, U14-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

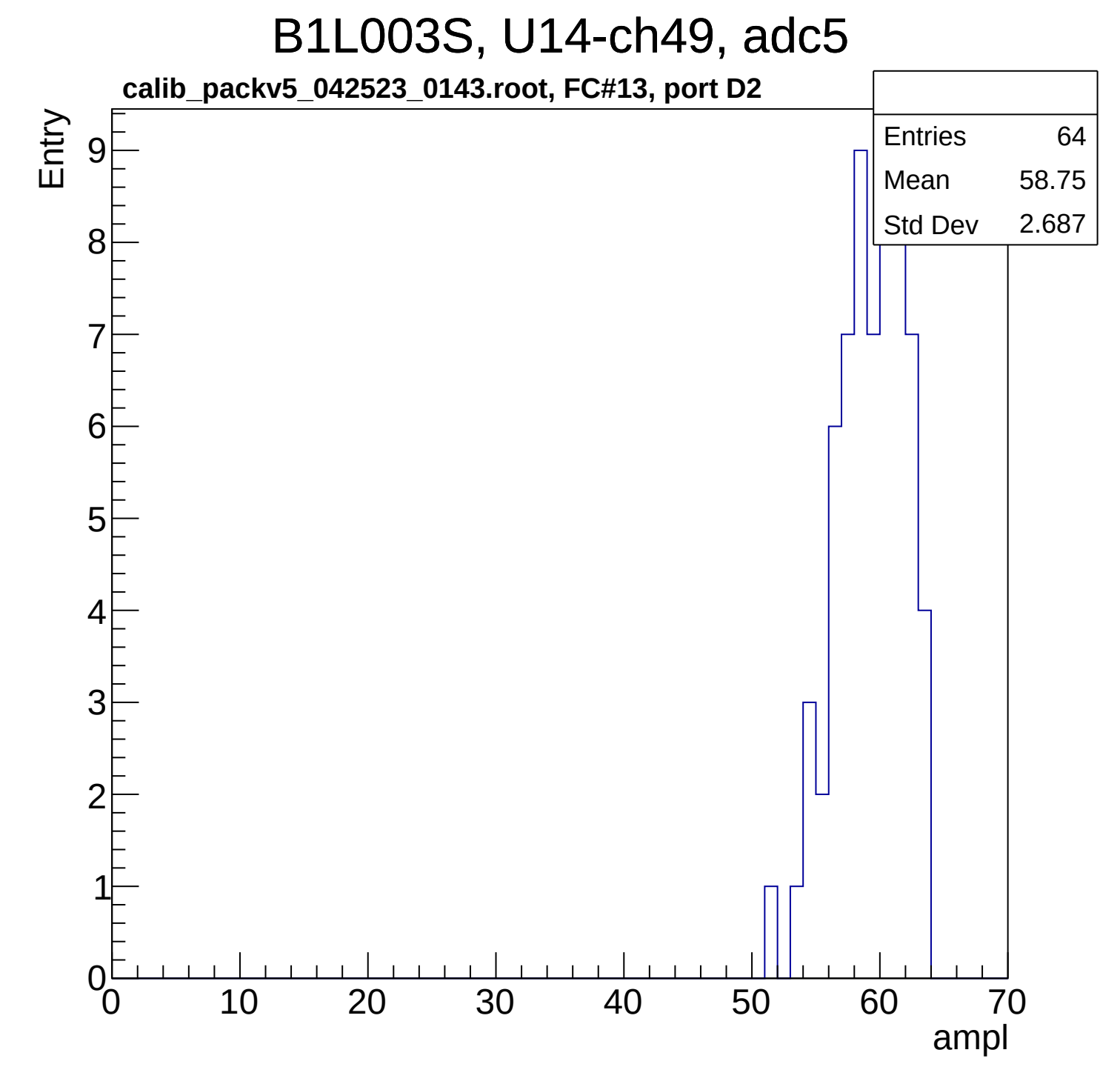

B1L003S, U14-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.8
Std Dev 4.669
Gaus mean : 28.0948
Gaus Width: 3.5474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

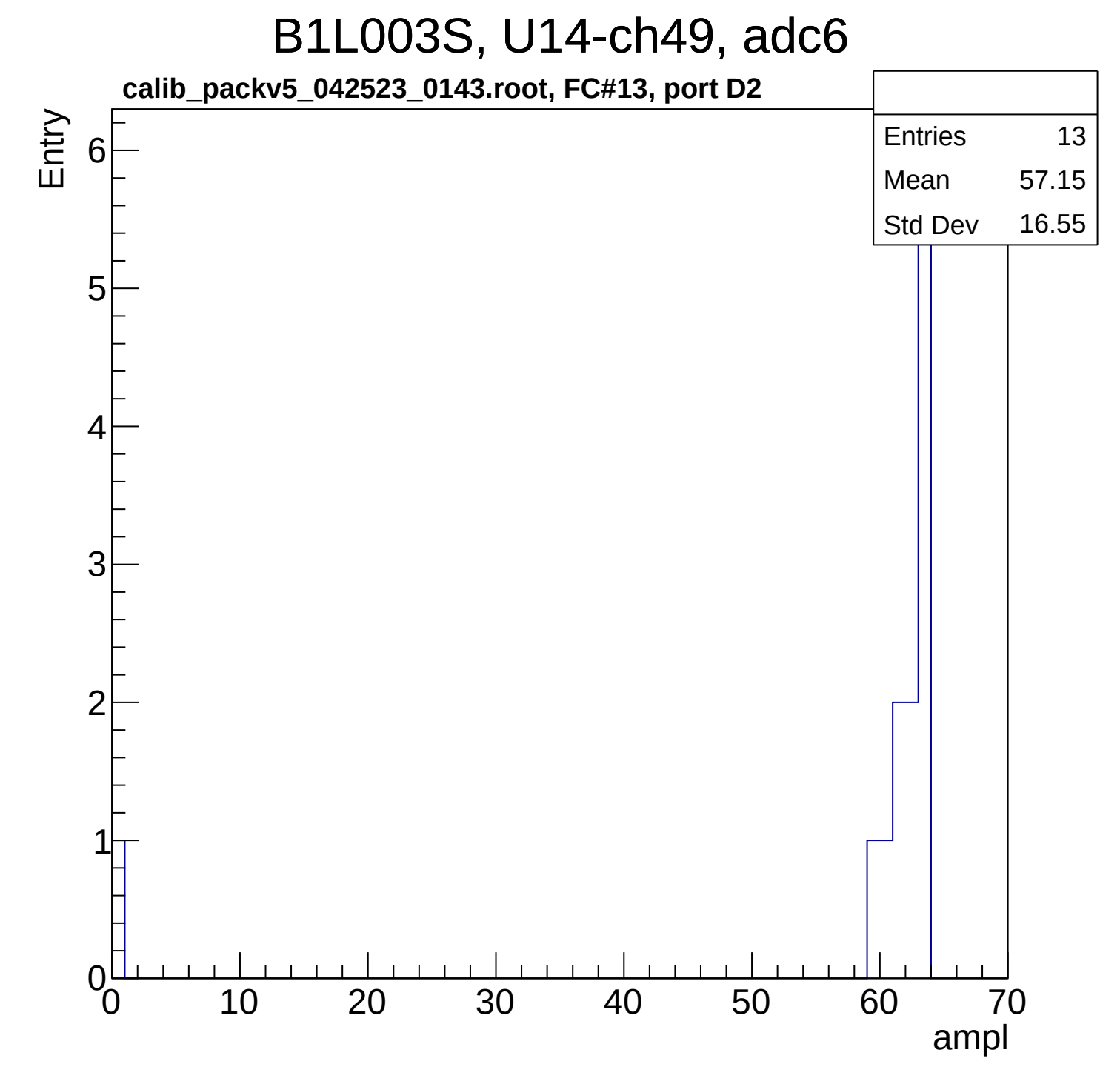

B1L003S, U14-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 35.15
Std Dev 3.733
Gaus mean : 35.2878
Gaus Width: 2.9217
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

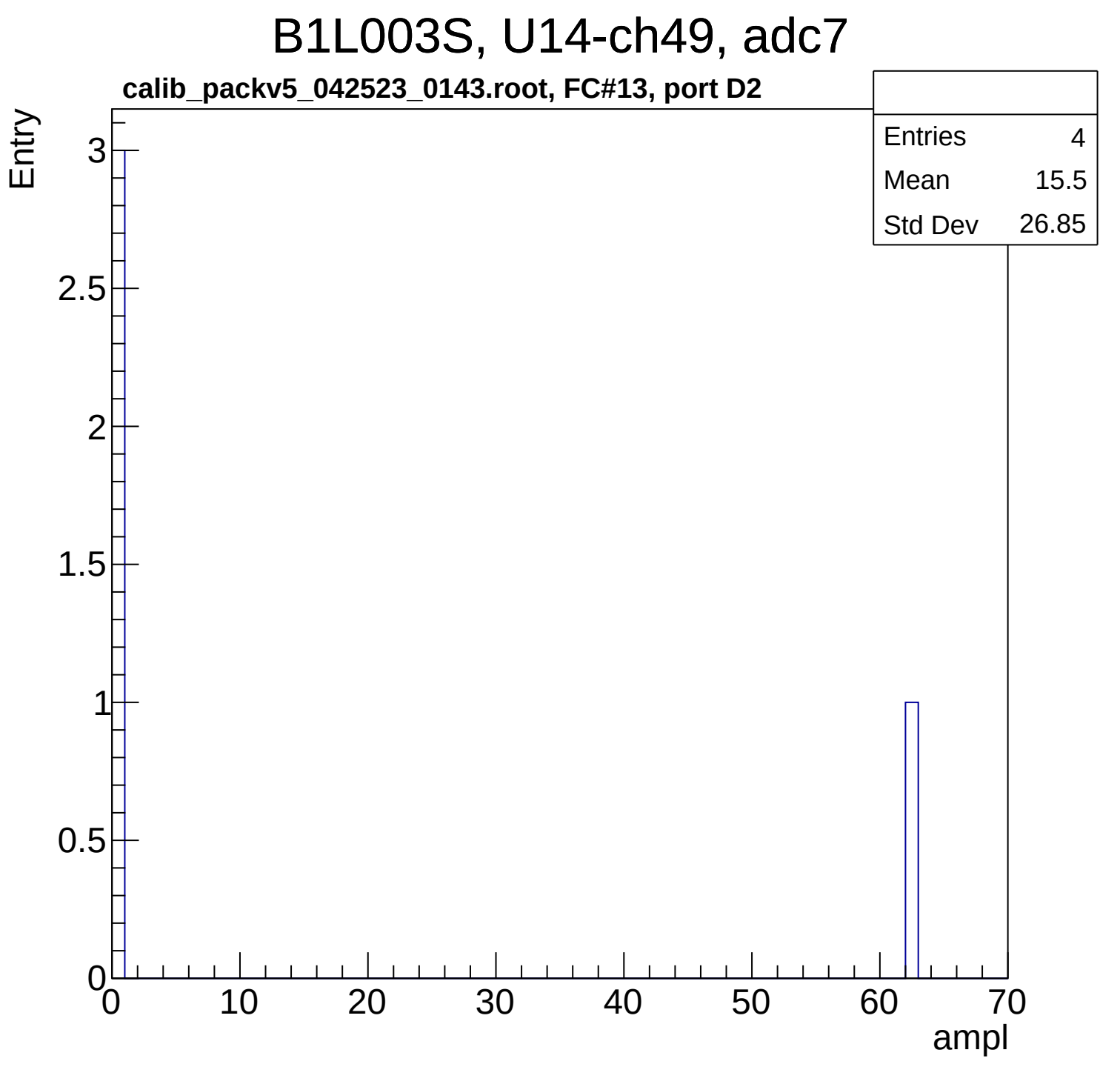

B1L003S, U14-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.62
Std Dev 3.307
Gaus mean : 41.7925
Gaus Width: 2.8606
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

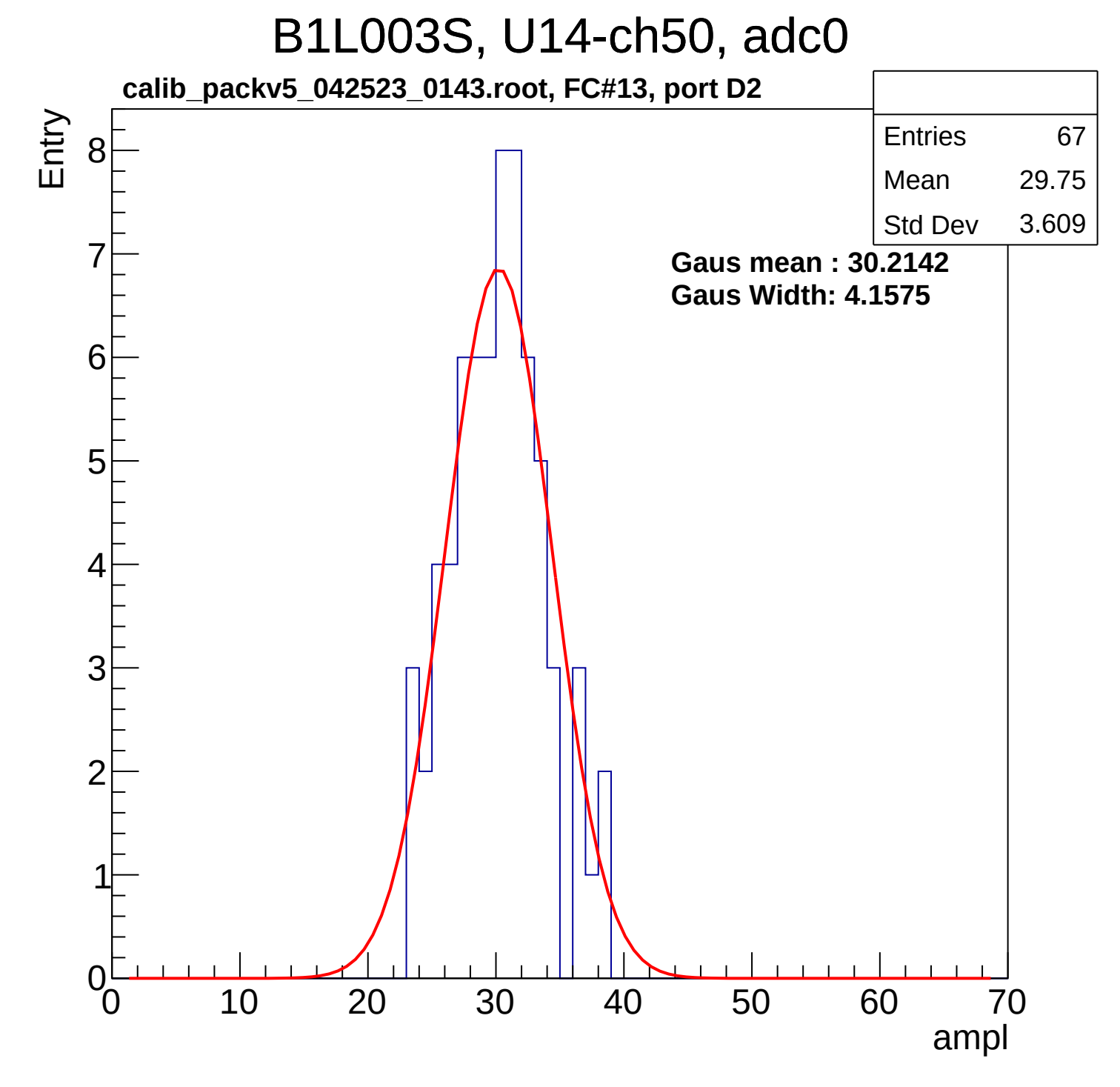

B1L003S, U14-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.39
Std Dev 3.42
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

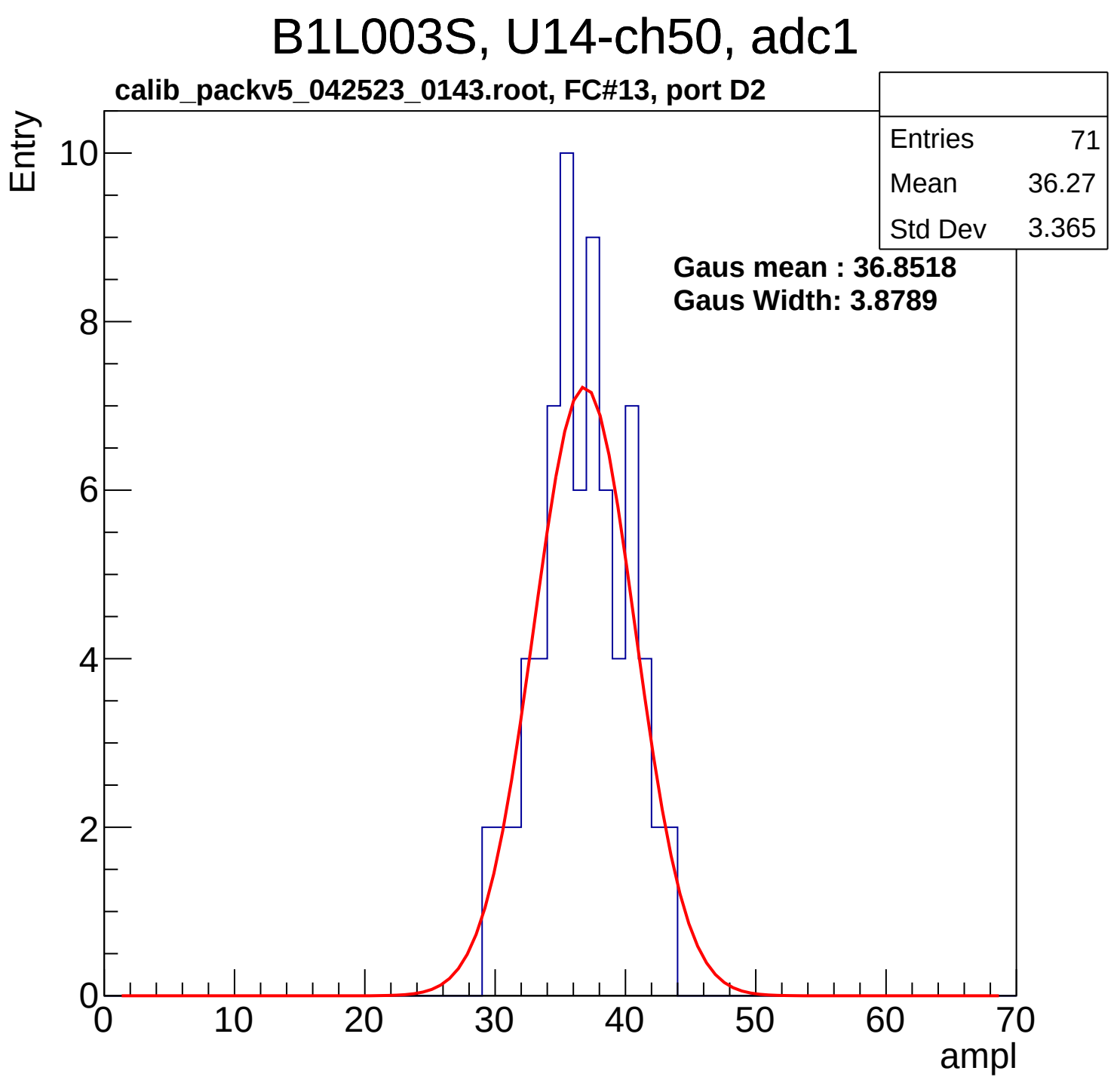

B1L003S, U14-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 53.98
Std Dev 2.338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

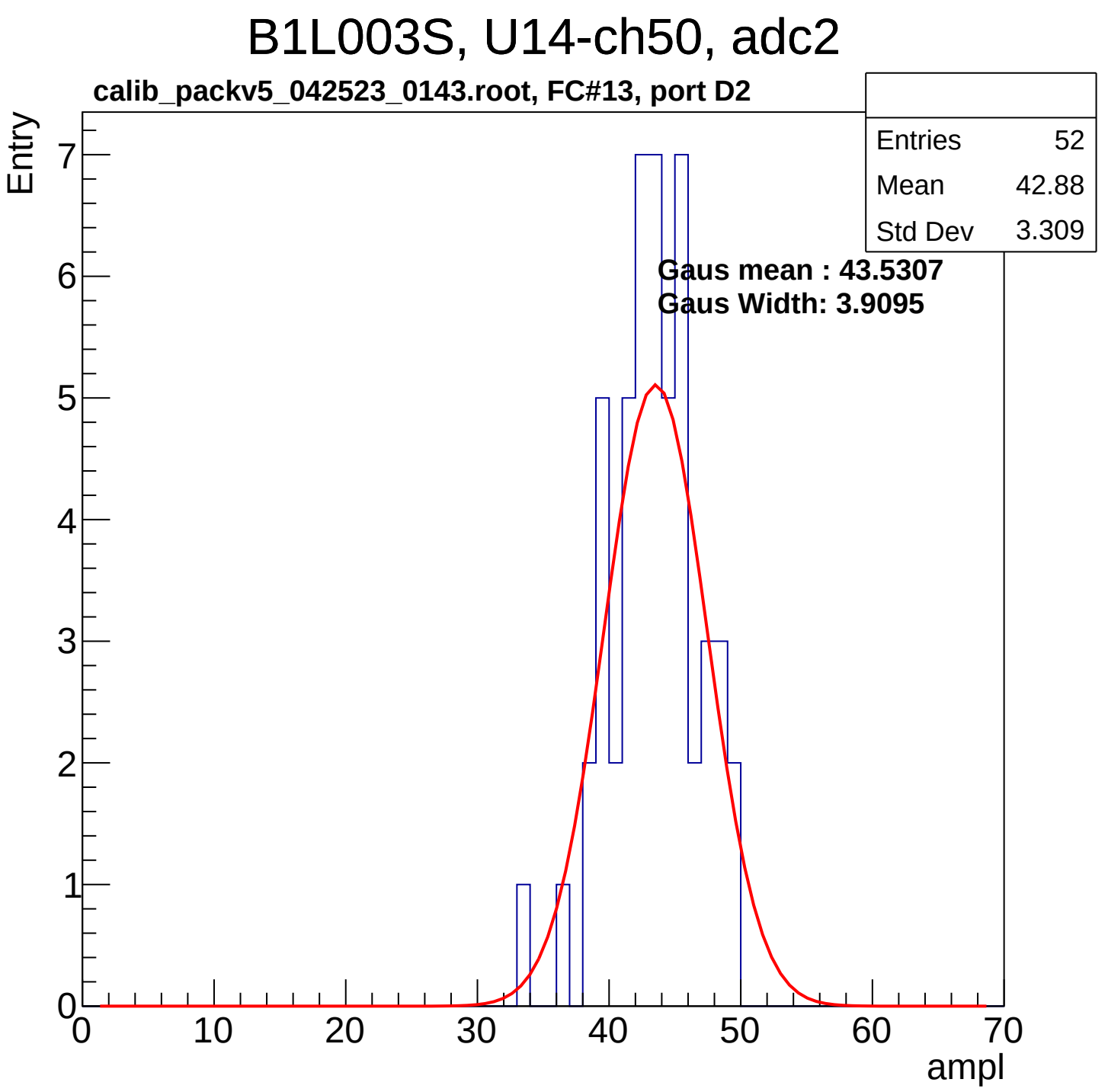

B1L003S, U14-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 58.75
Std Dev 2.687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

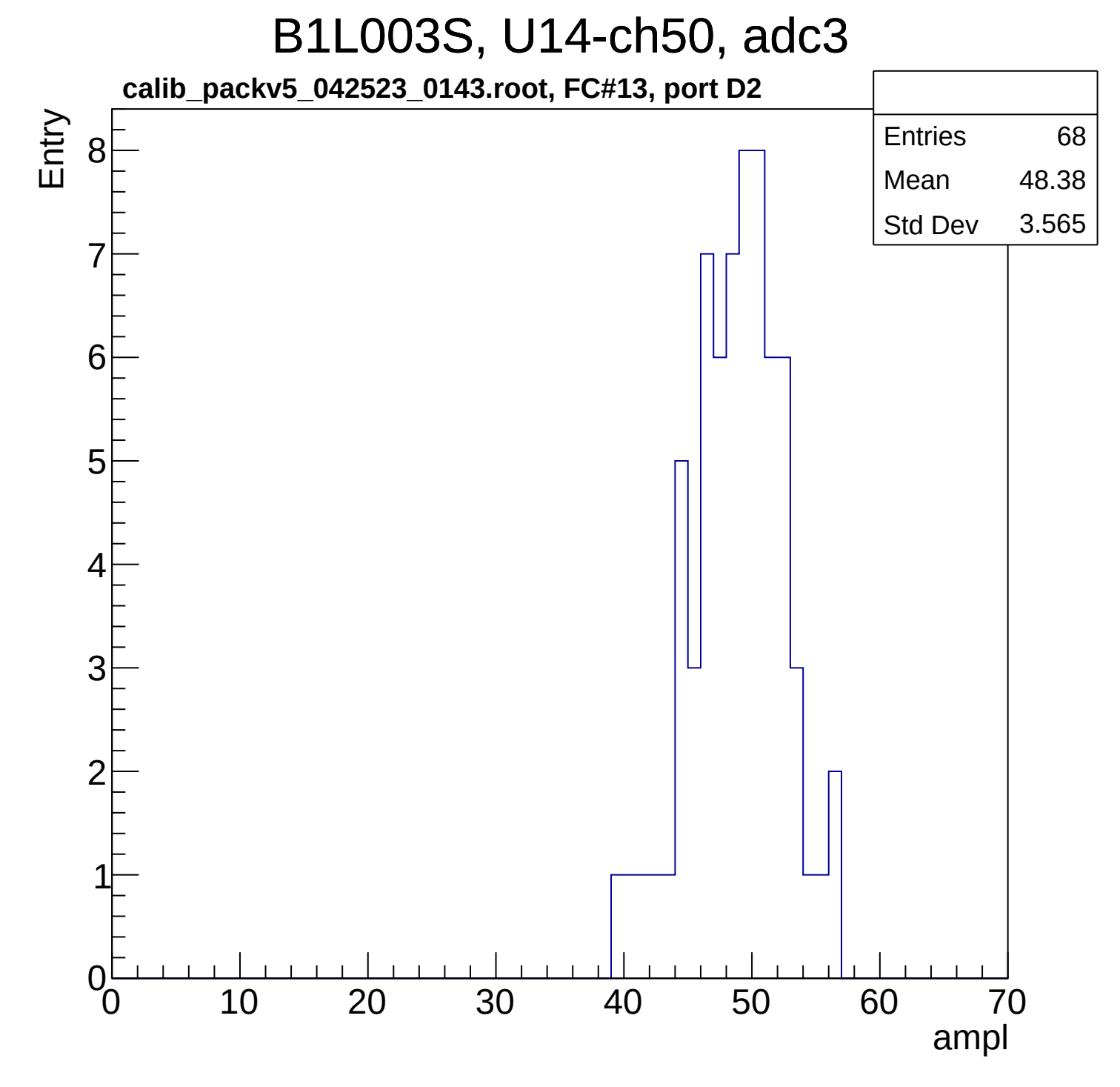

B1L003S, U14-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 57.15
Std Dev 16.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

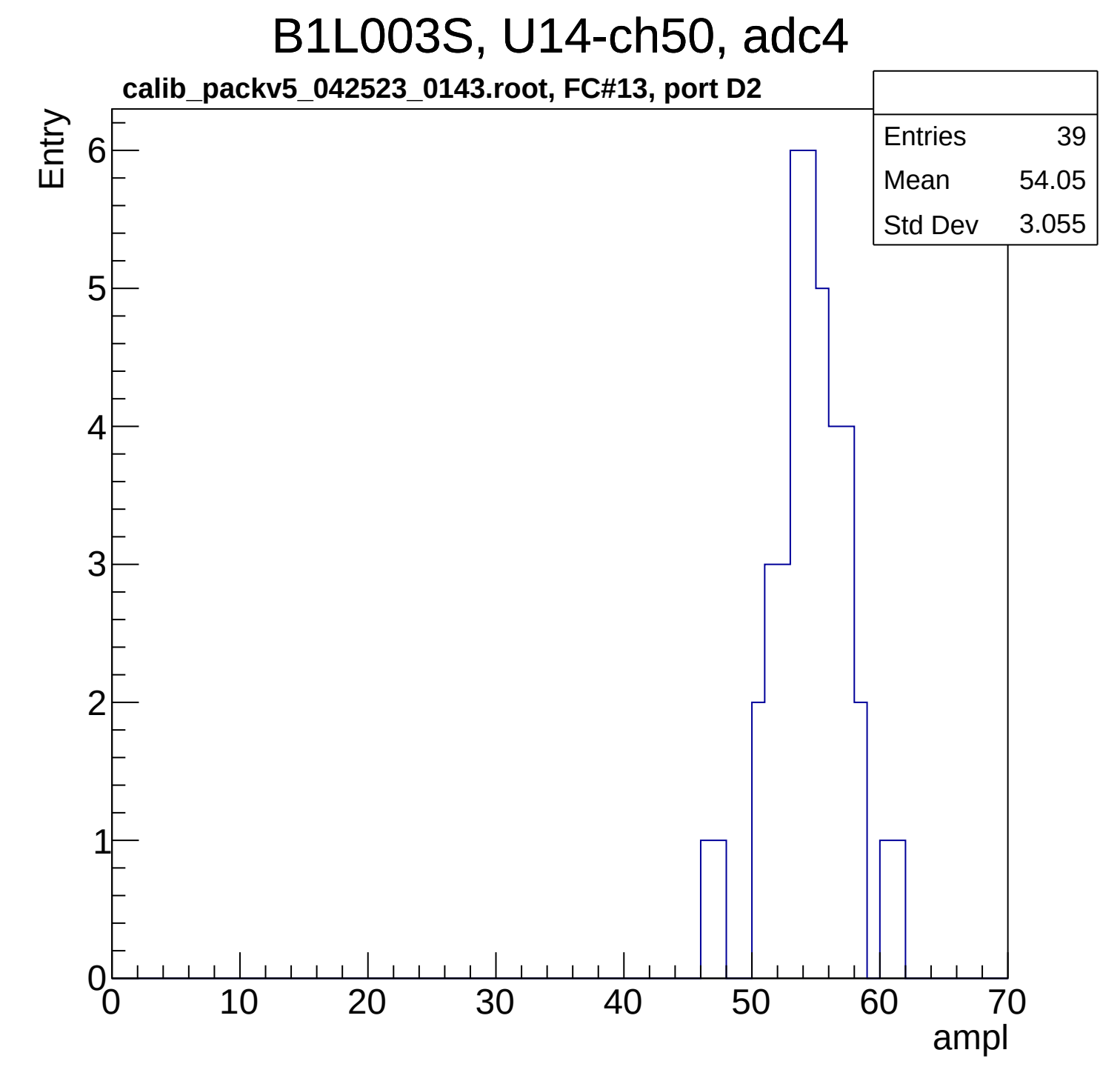

B1L003S, U14-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

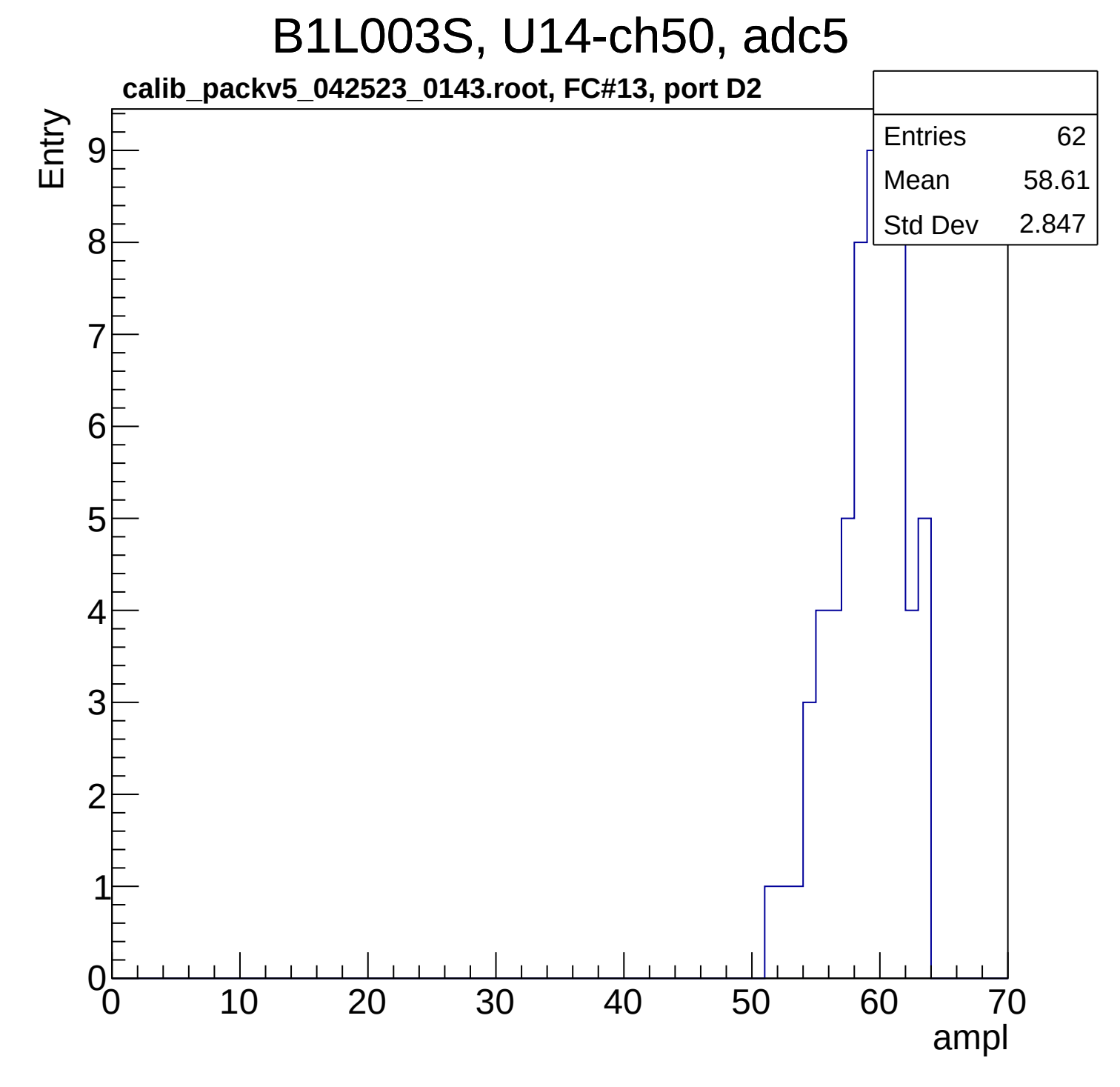

B1L003S, U14-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.75
Std Dev 3.609
Gaus mean : 30.2142
Gaus Width: 4.1575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

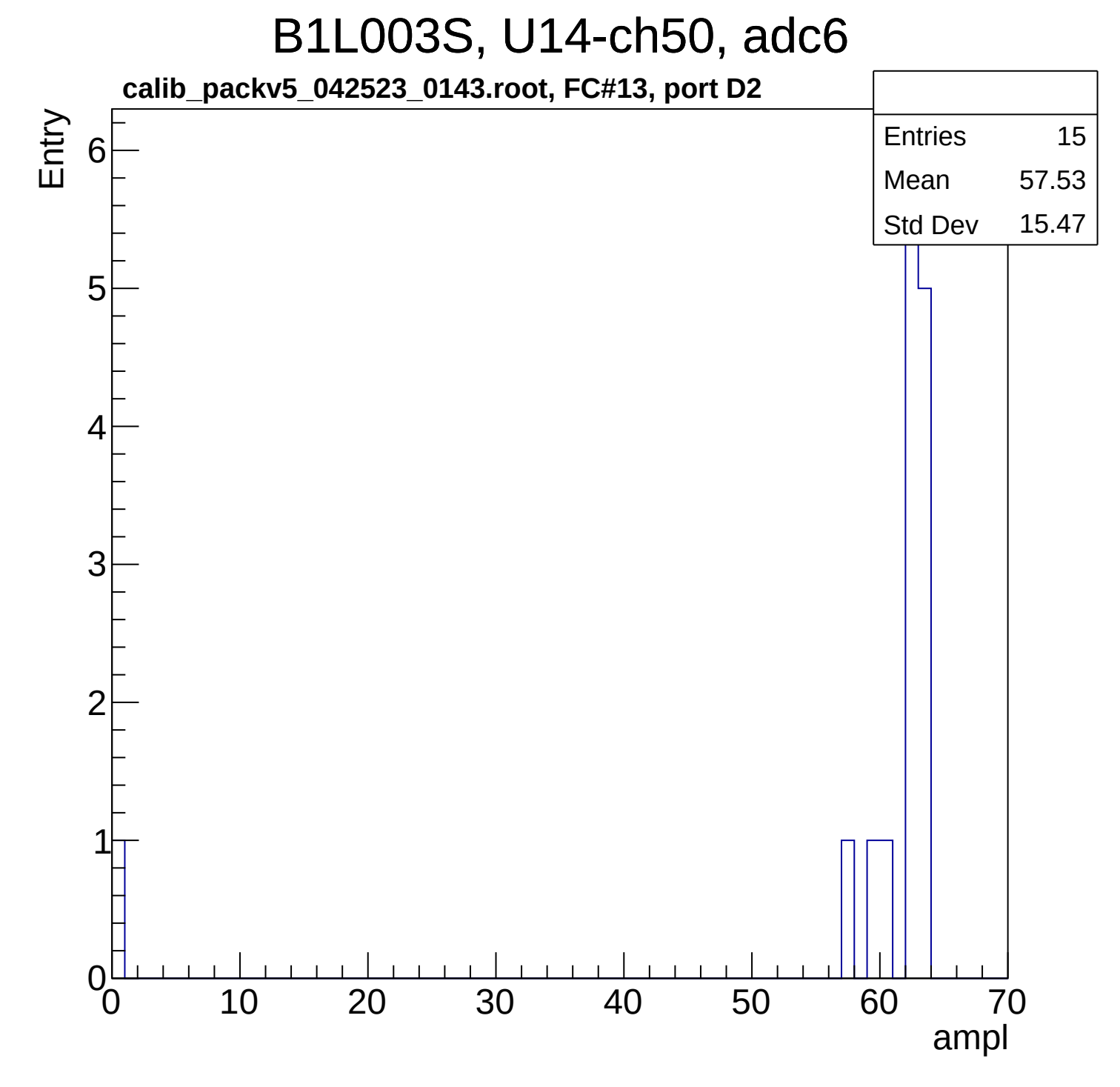

B1L003S, U14-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.27
Std Dev 3.365
Gaus mean : 36.8518
Gaus Width: 3.8789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

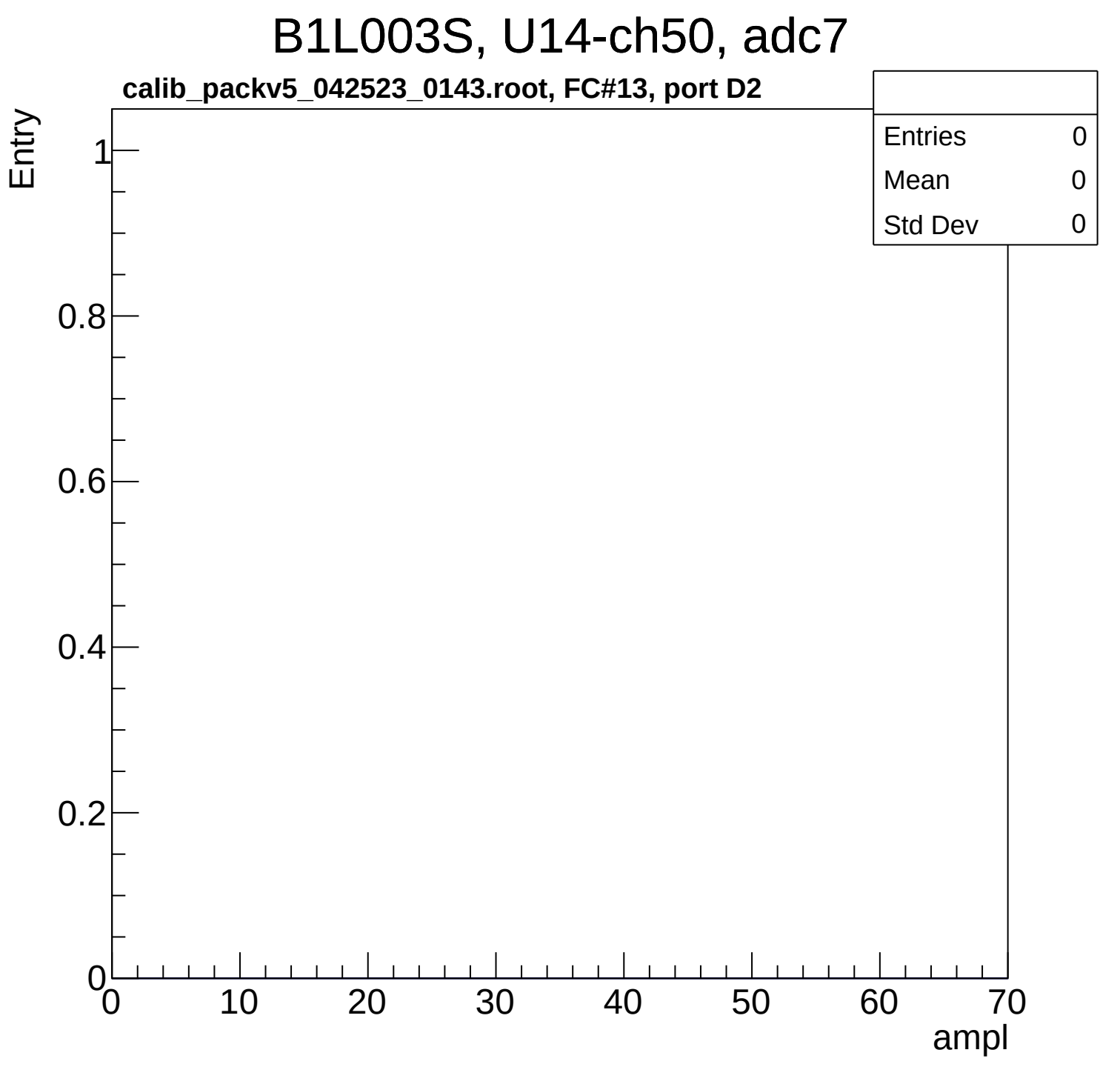

B1L003S, U14-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 42.88
Std Dev 3.309
Gaus mean : 43.5307
Gaus Width: 3.9095
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

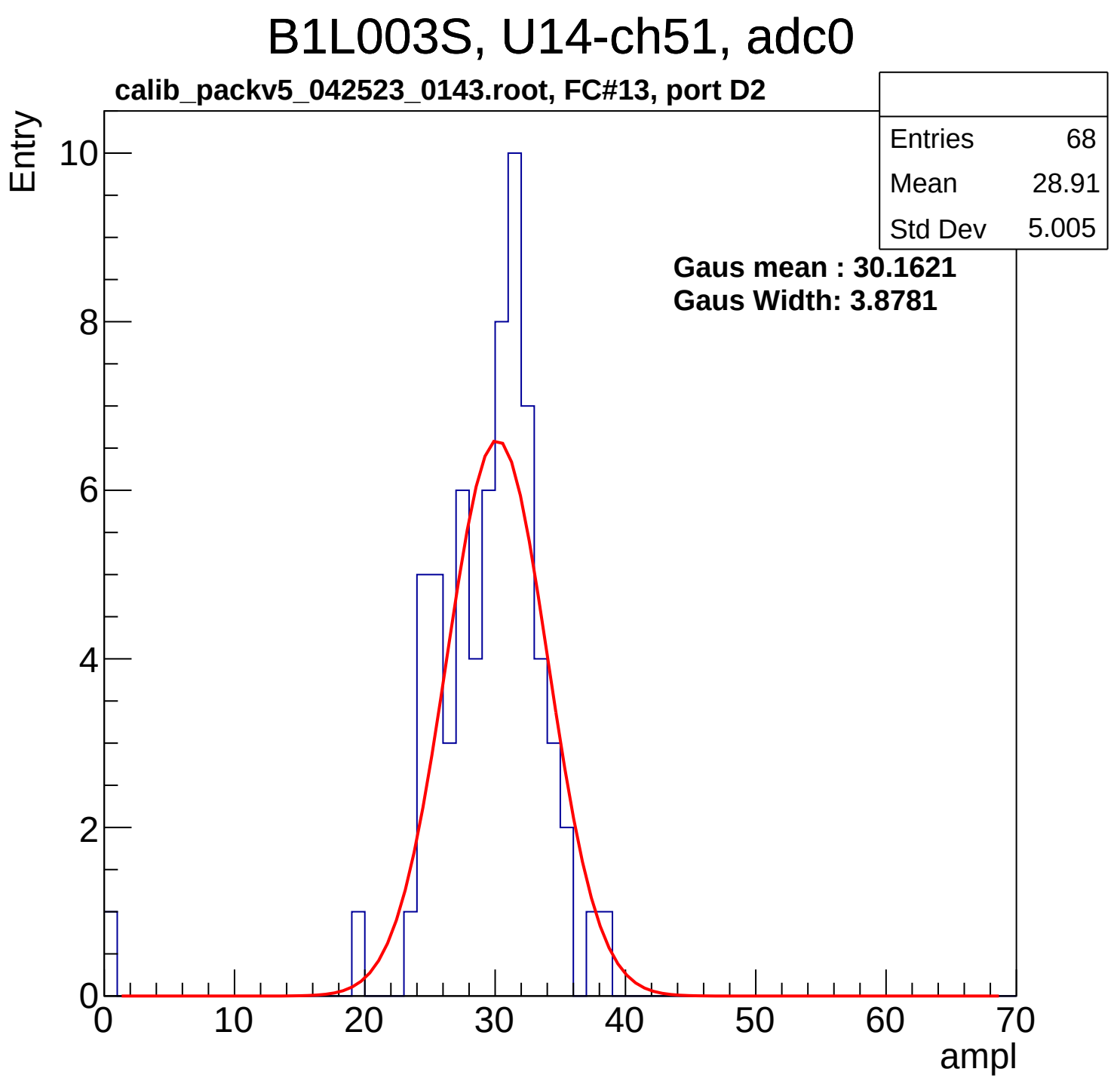

B1L003S, U14-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.38
Std Dev 3.565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

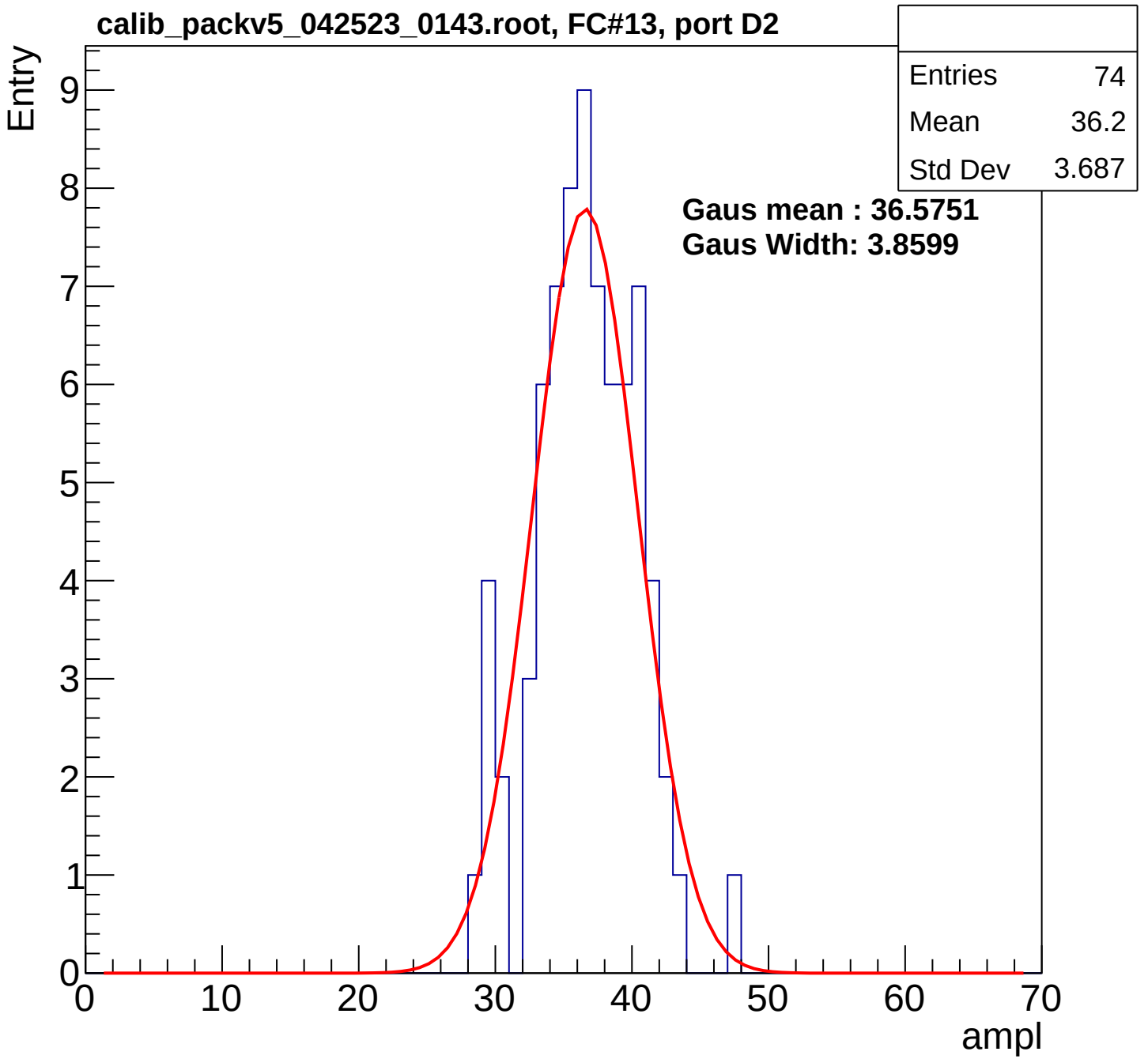

B1L003S, U14-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 54.05
Std Dev 3.055
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

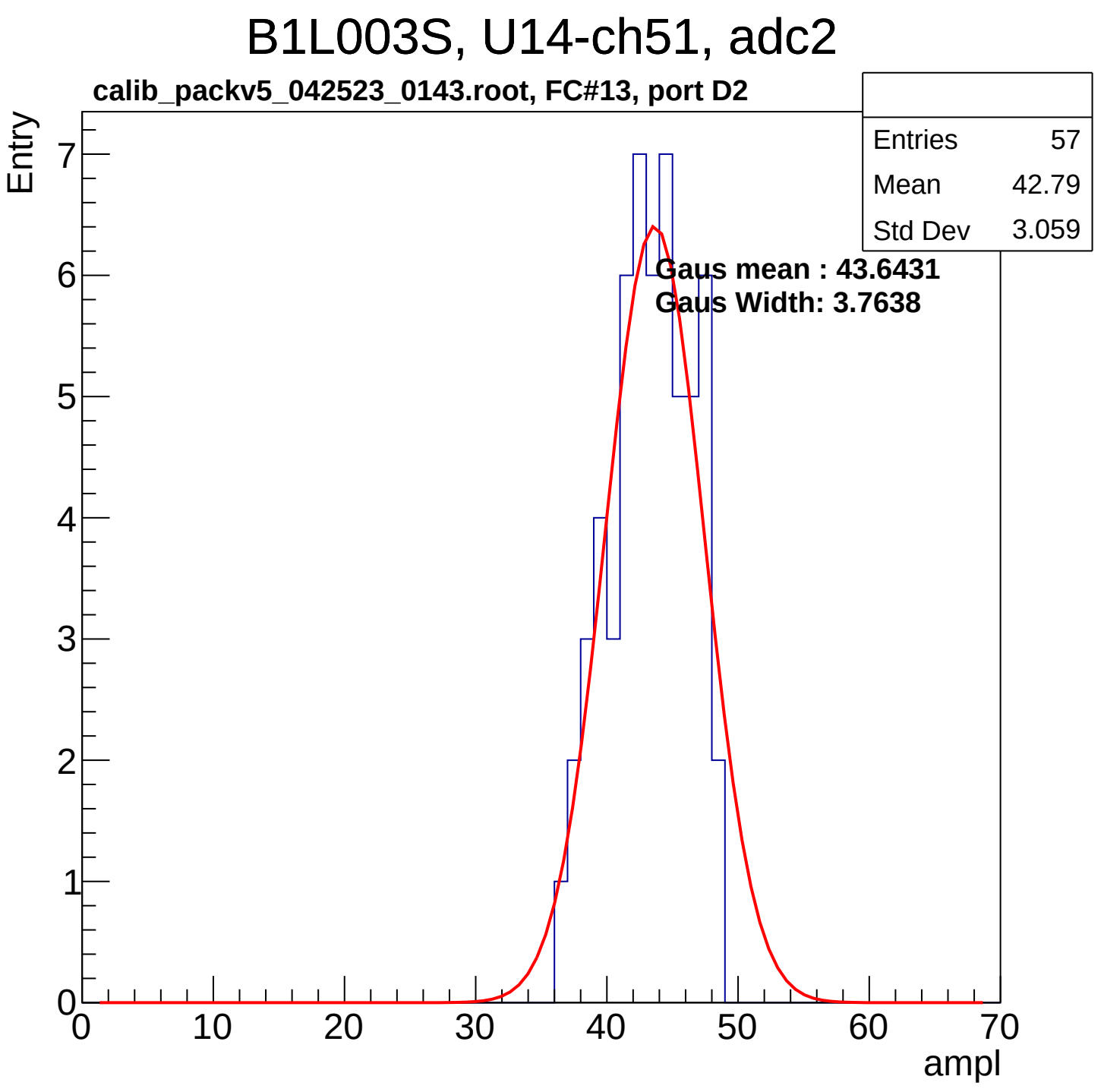

B1L003S, U14-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.61
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

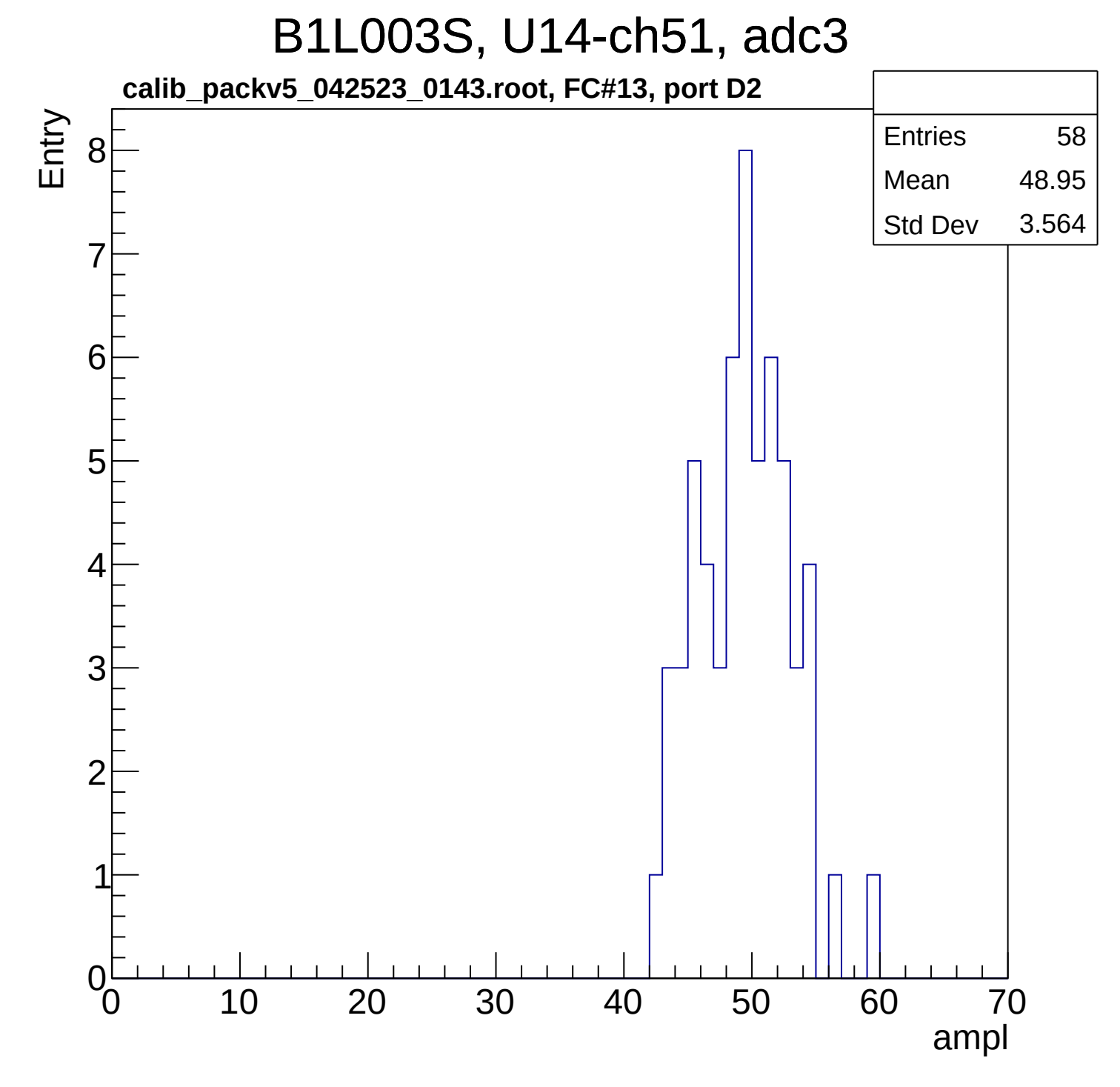

B1L003S, U14-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.53
Std Dev 15.47
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

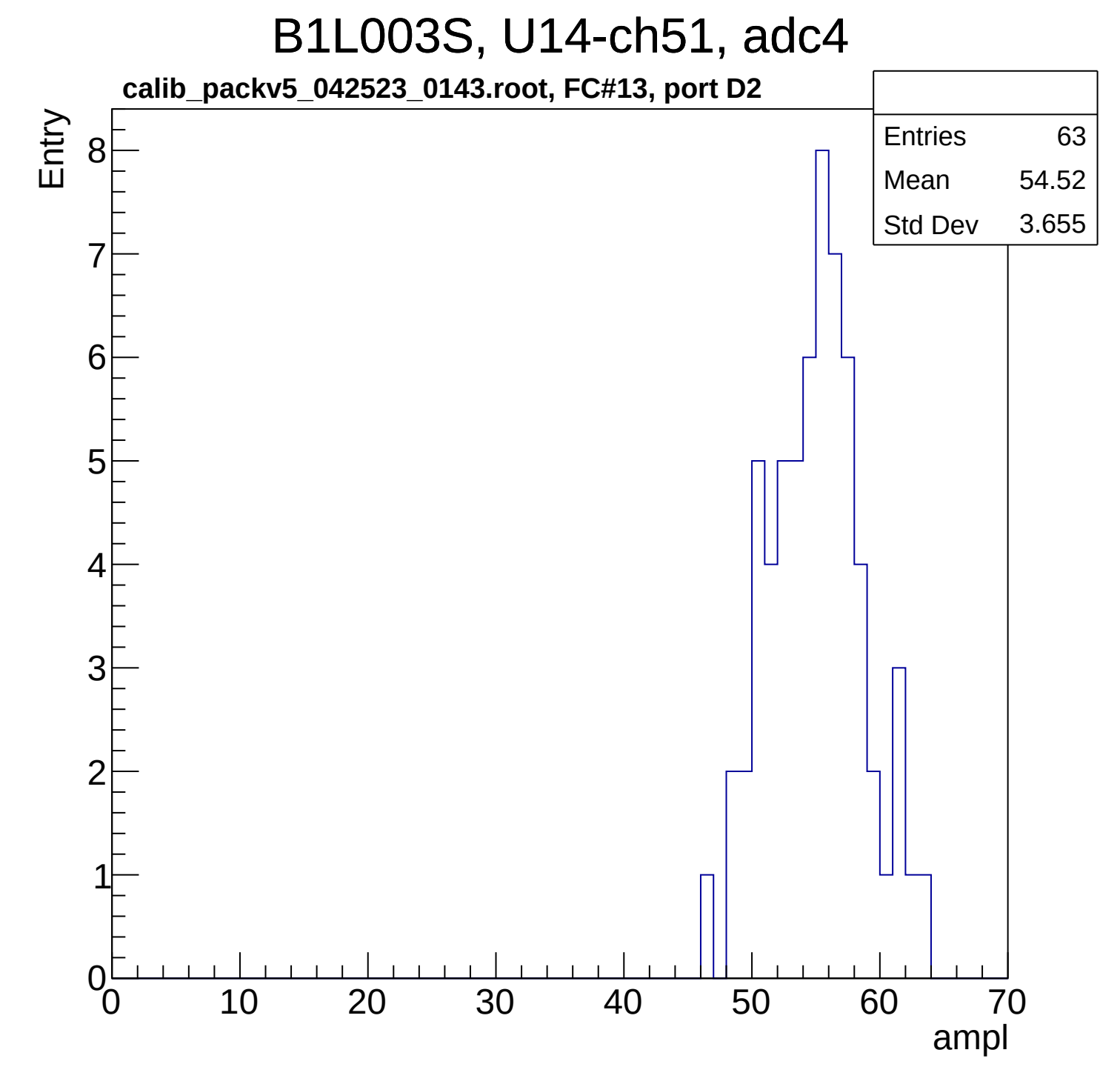

B1L003S, U14-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

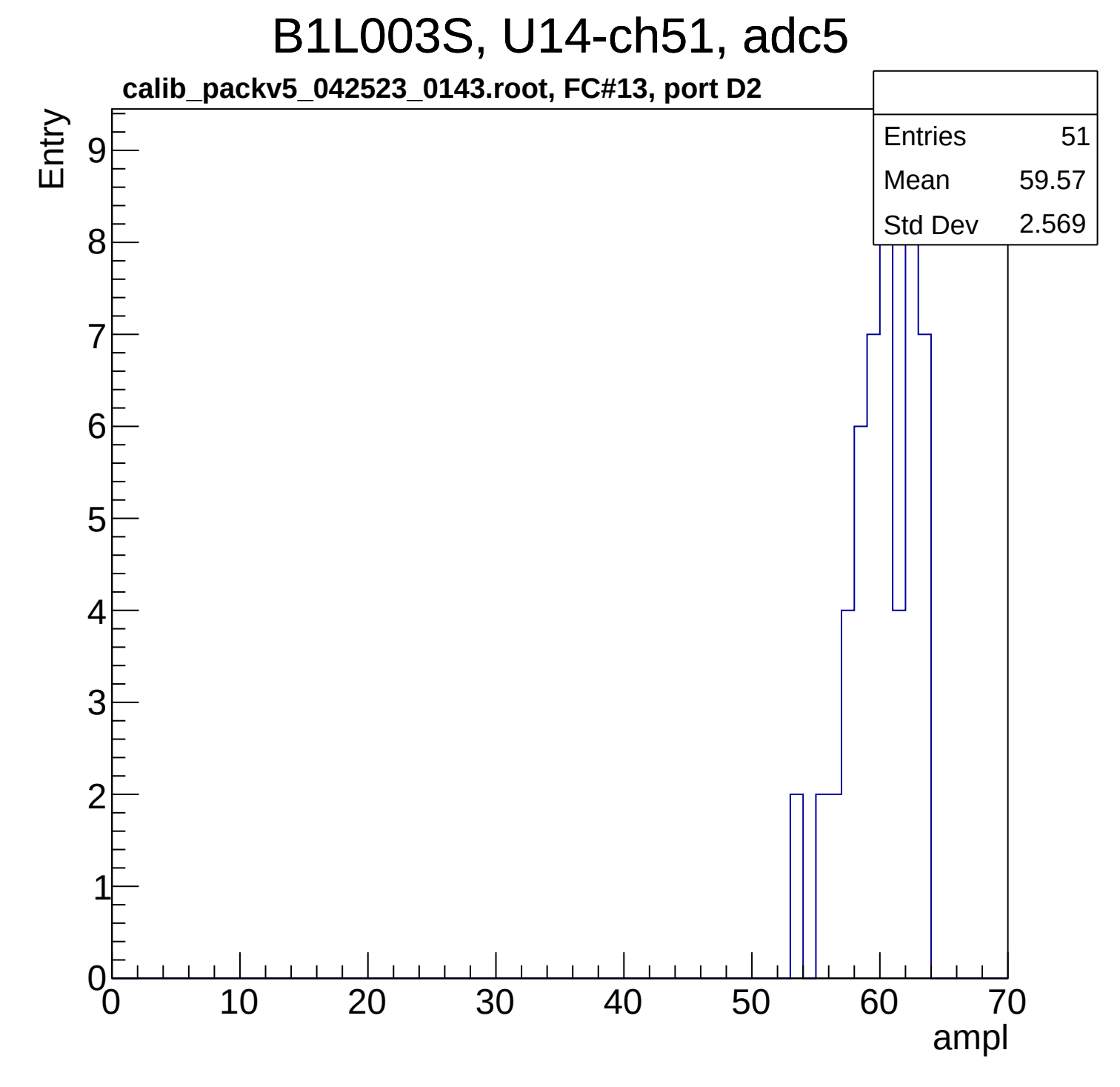

B1L003S, U14-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.91
Std Dev 5.005
Gaus mean : 30.1621
Gaus Width: 3.8781
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

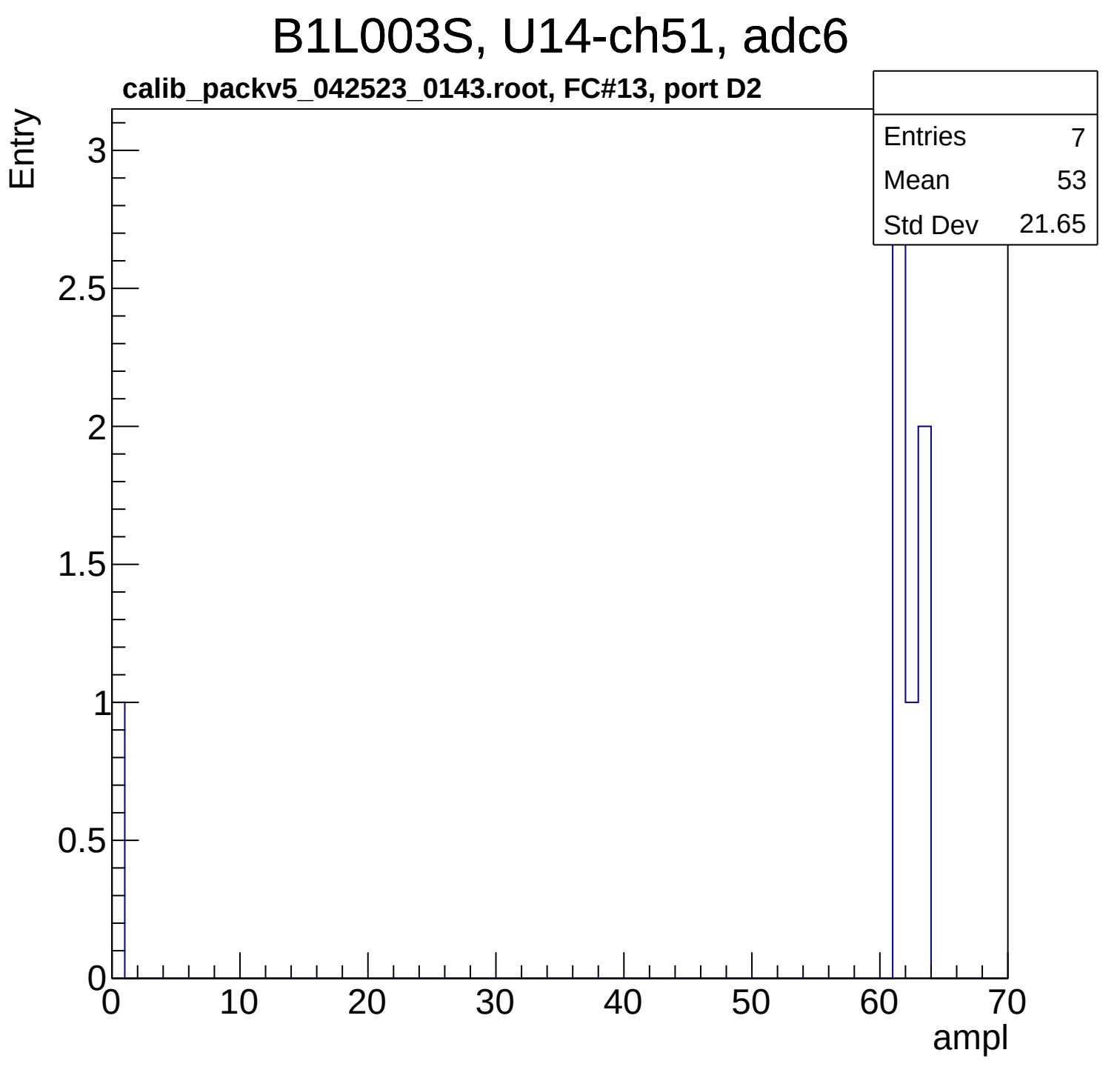

B1L003S, U14-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.2
Std Dev 3.687
Gaus mean : 36.5751
Gaus Width: 3.8599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

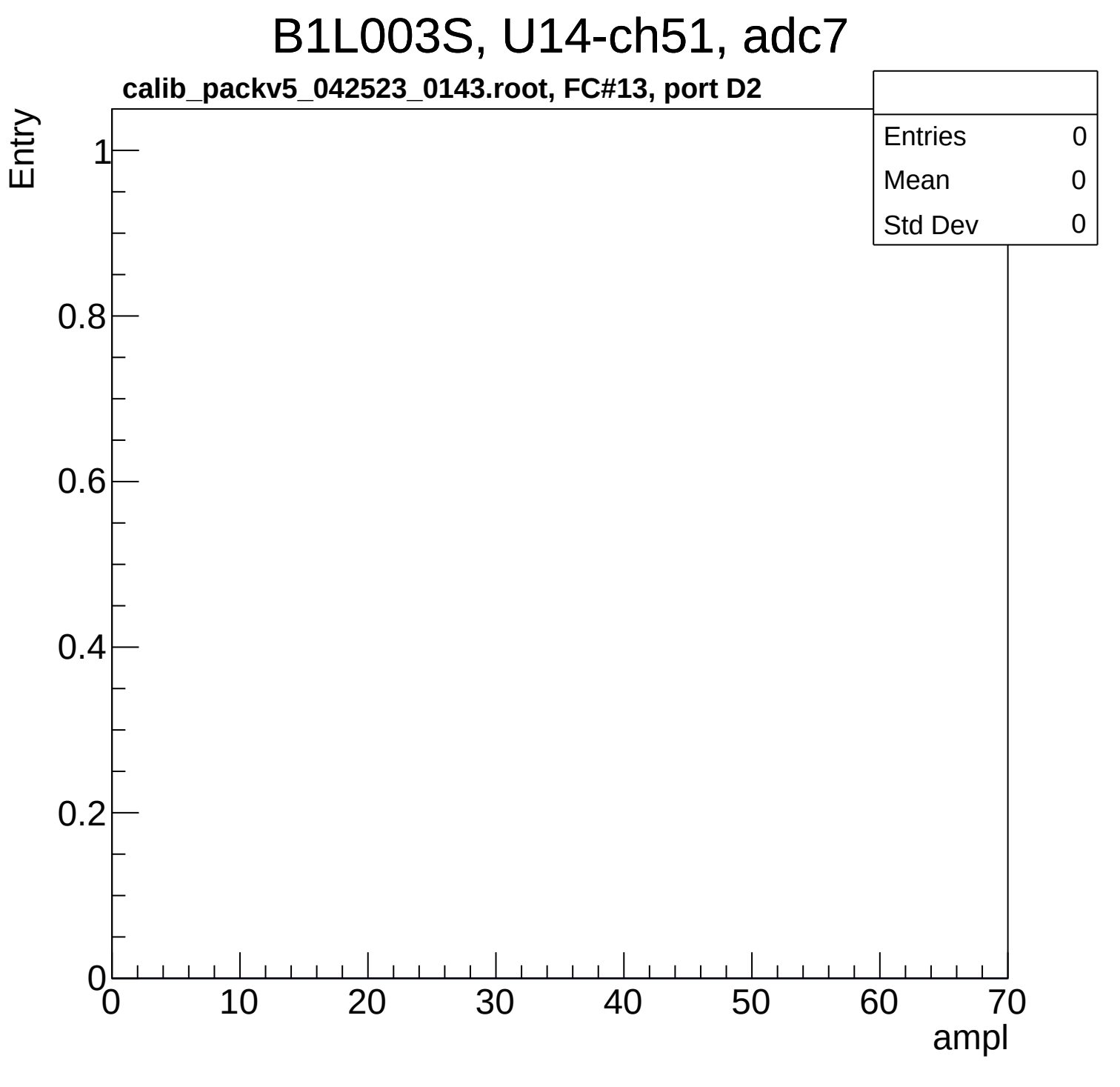

B1L003S, U14-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.79
Std Dev 3.059
Gaus mean : 43.6431
Gaus Width: 3.7638
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

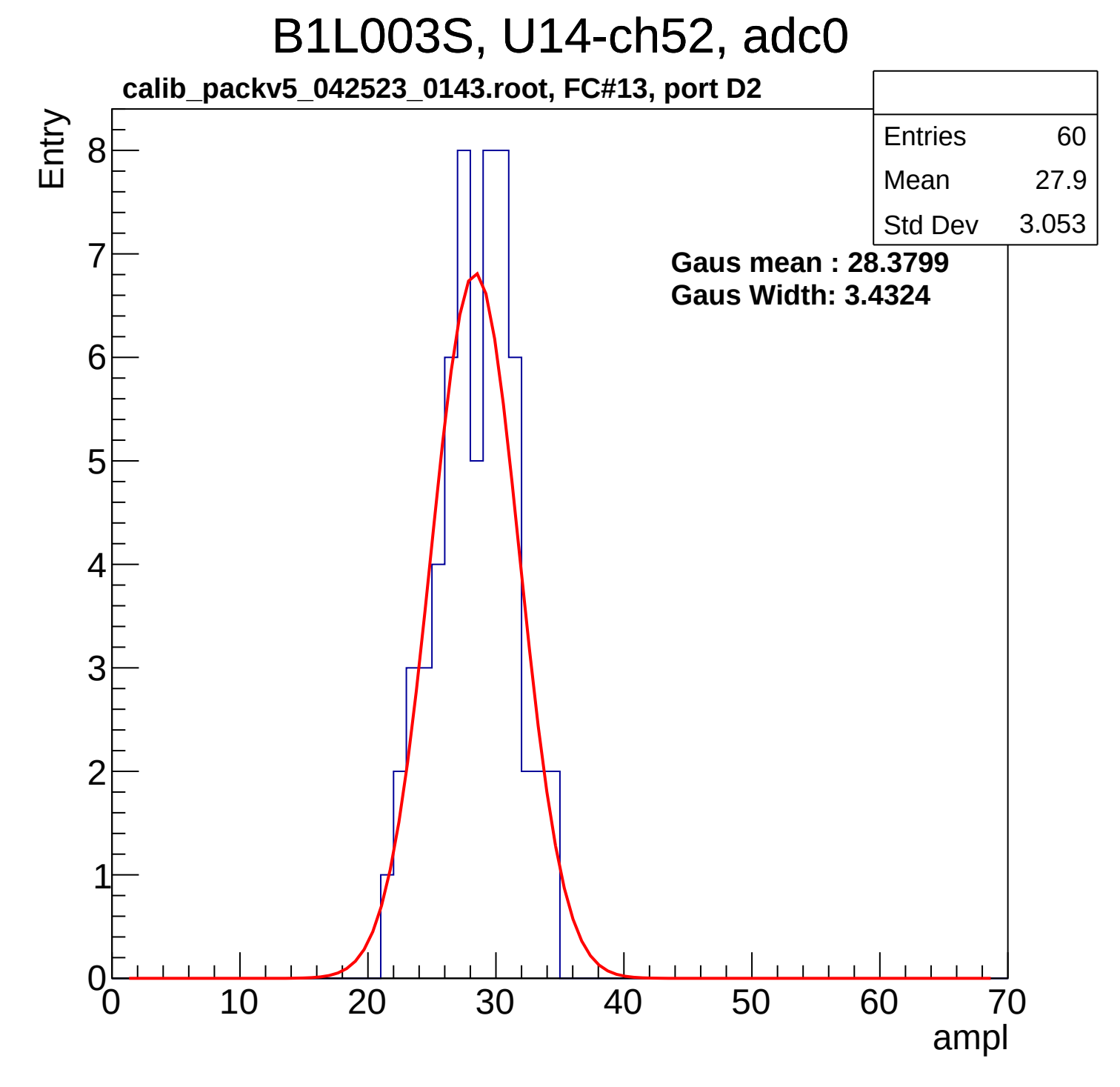

B1L003S, U14-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.95
Std Dev 3.564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

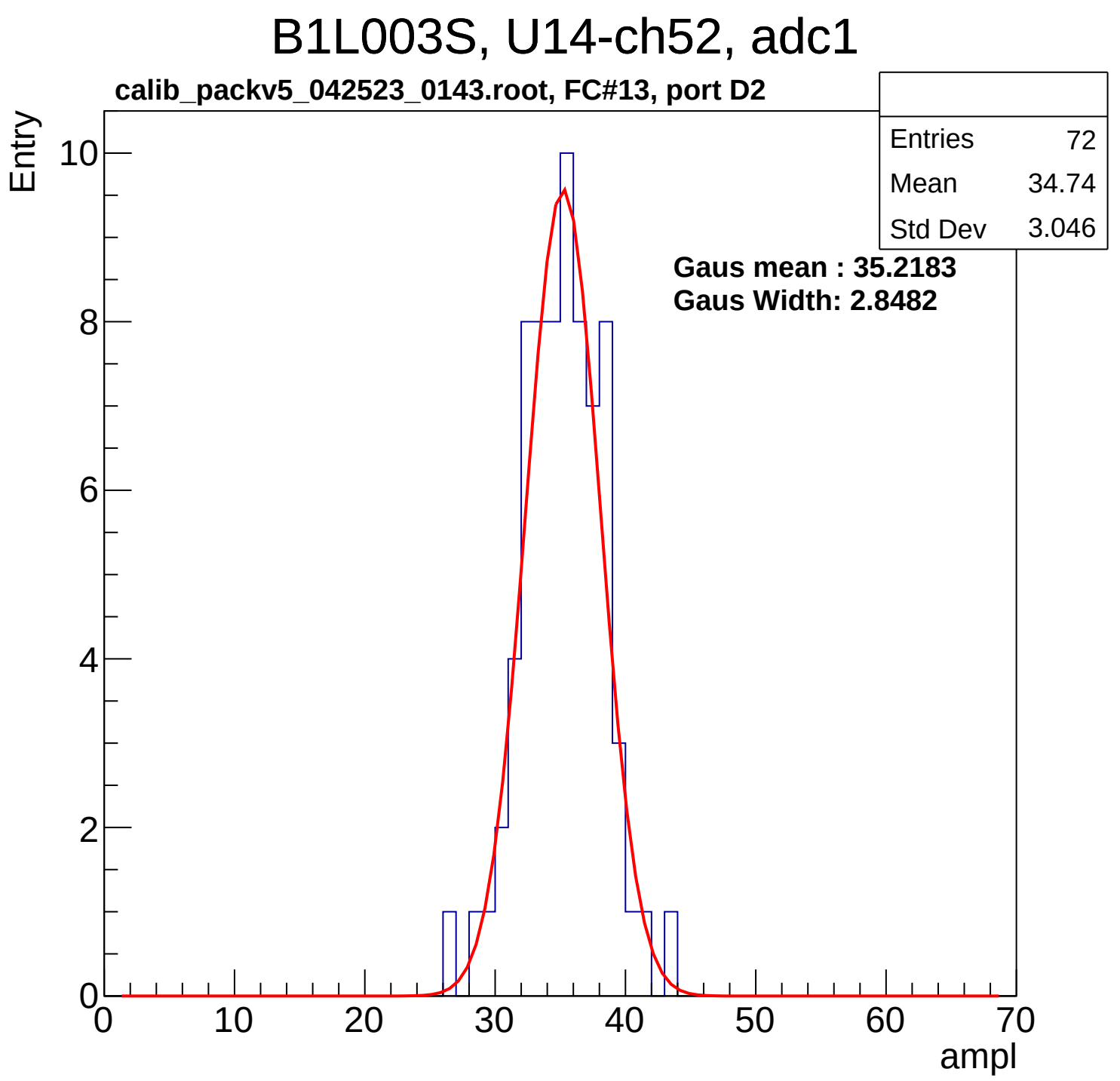

B1L003S, U14-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.52
Std Dev 3.655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

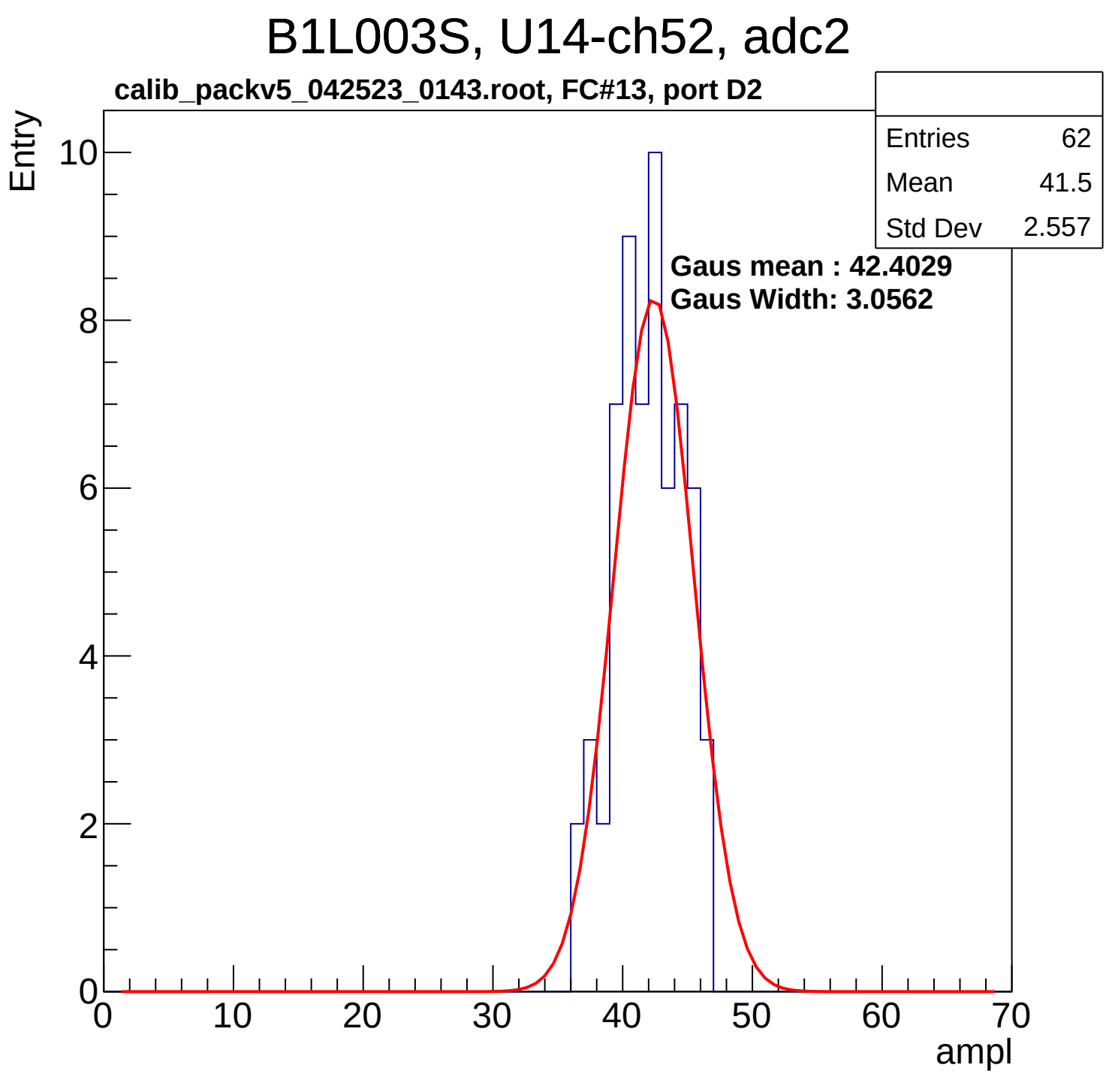

B1L003S, U14-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.57
Std Dev 2.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

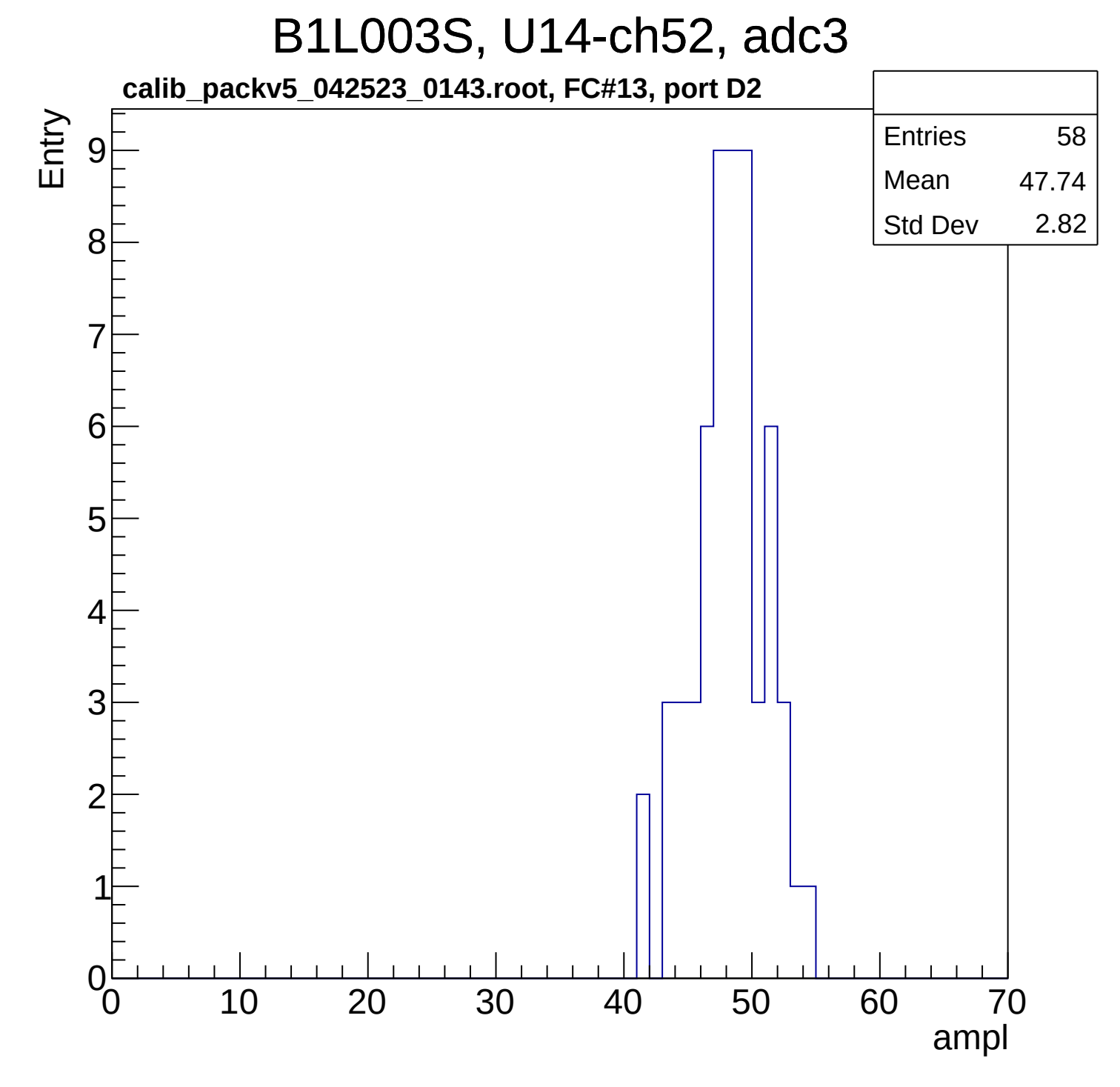

B1L003S, U14-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53
Std Dev 21.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

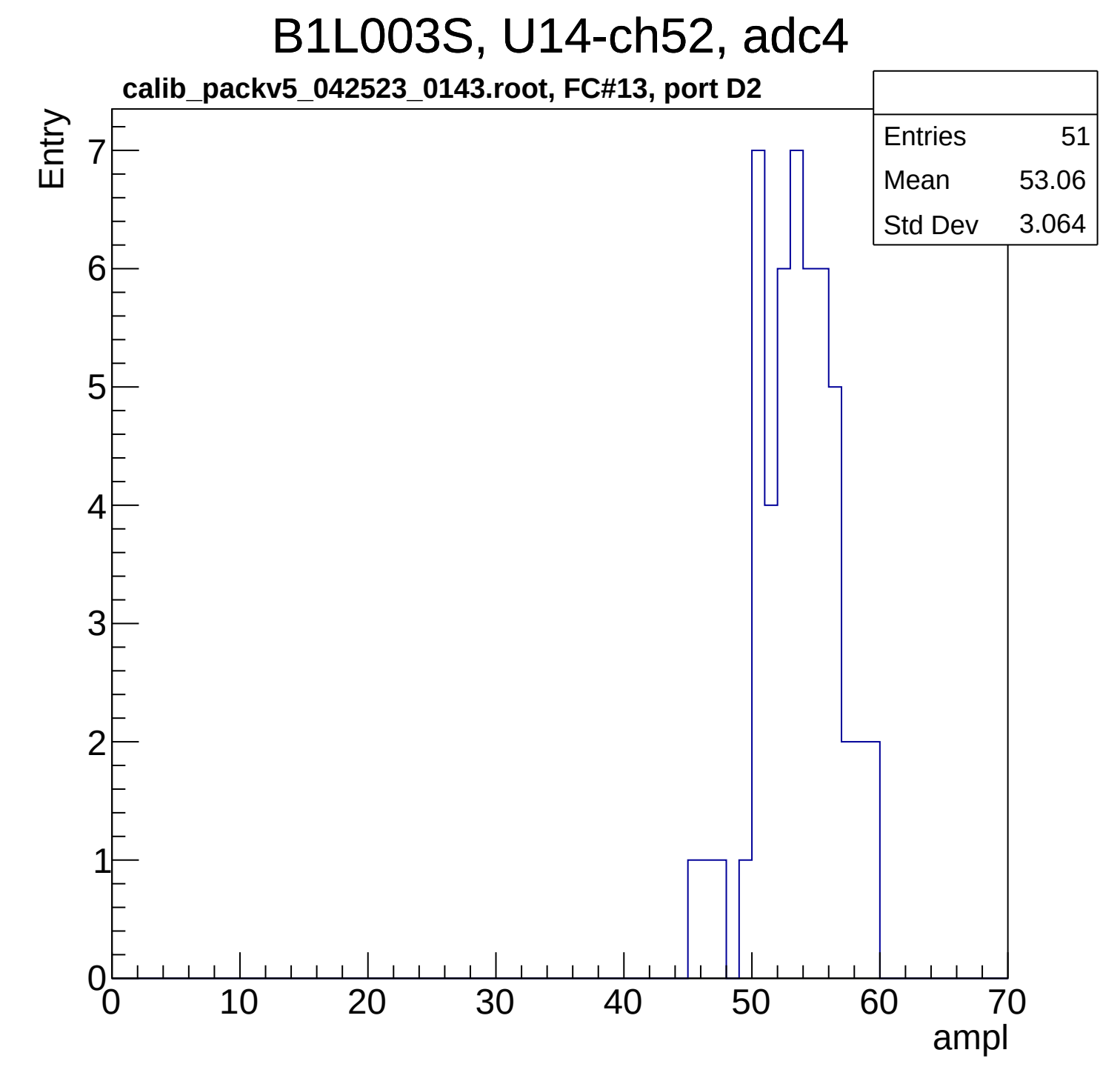

B1L003S, U14-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

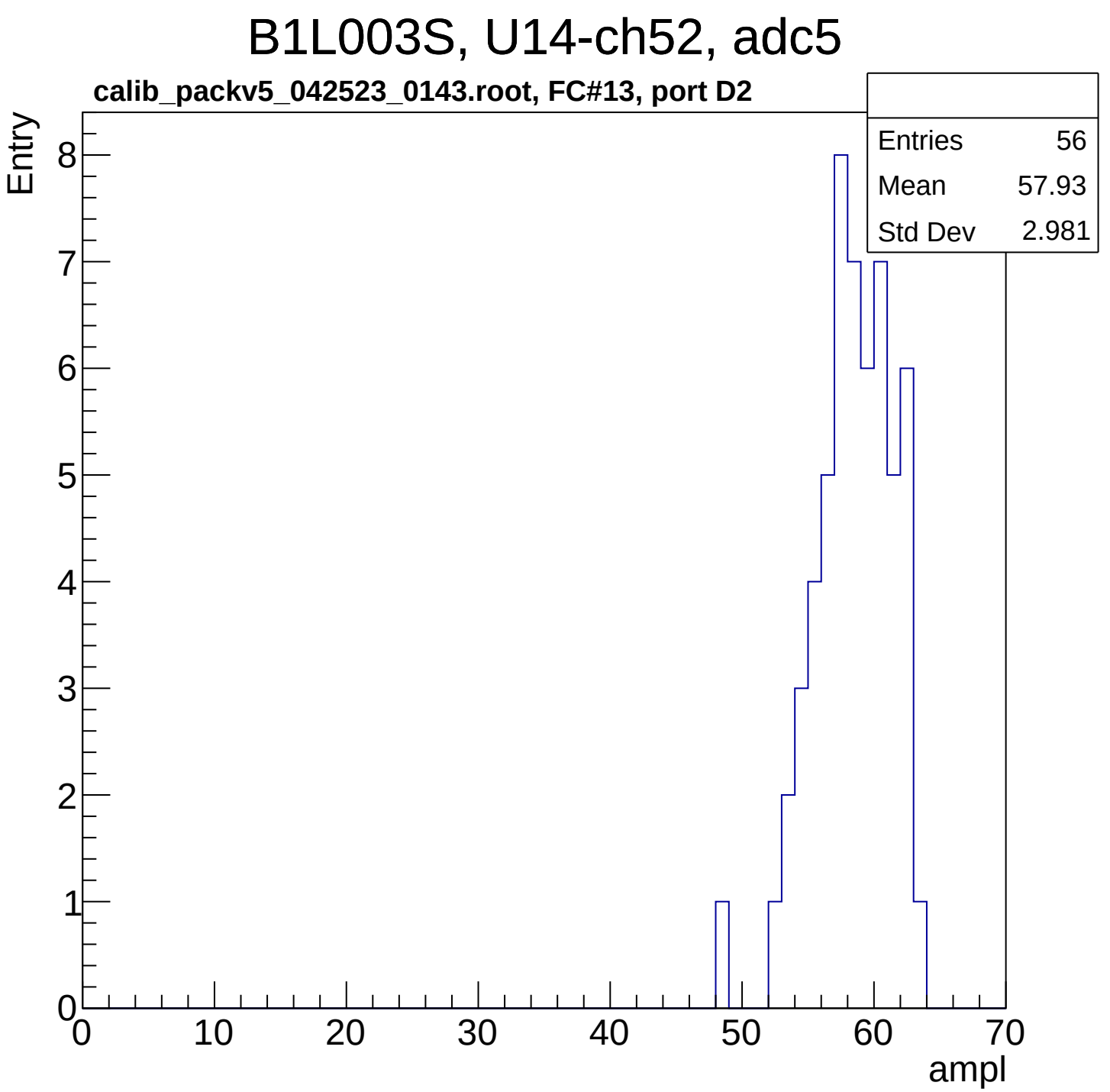

B1L003S, U14-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 27.9
Std Dev 3.053
Gaus mean : 28.3799
Gaus Width: 3.4324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

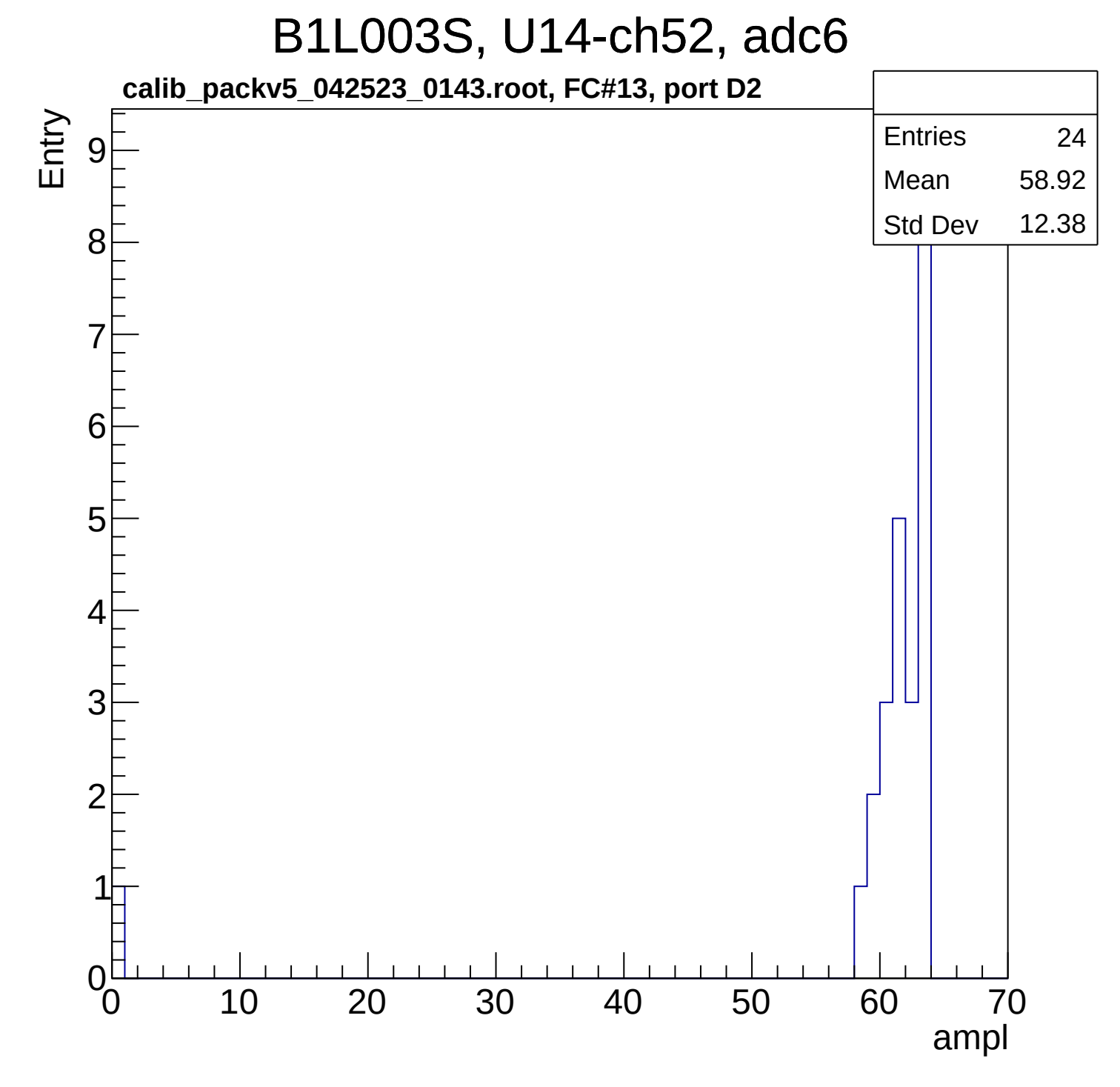

B1L003S, U14-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.74
Std Dev 3.046
Gaus mean : 35.2183
Gaus Width: 2.8482
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

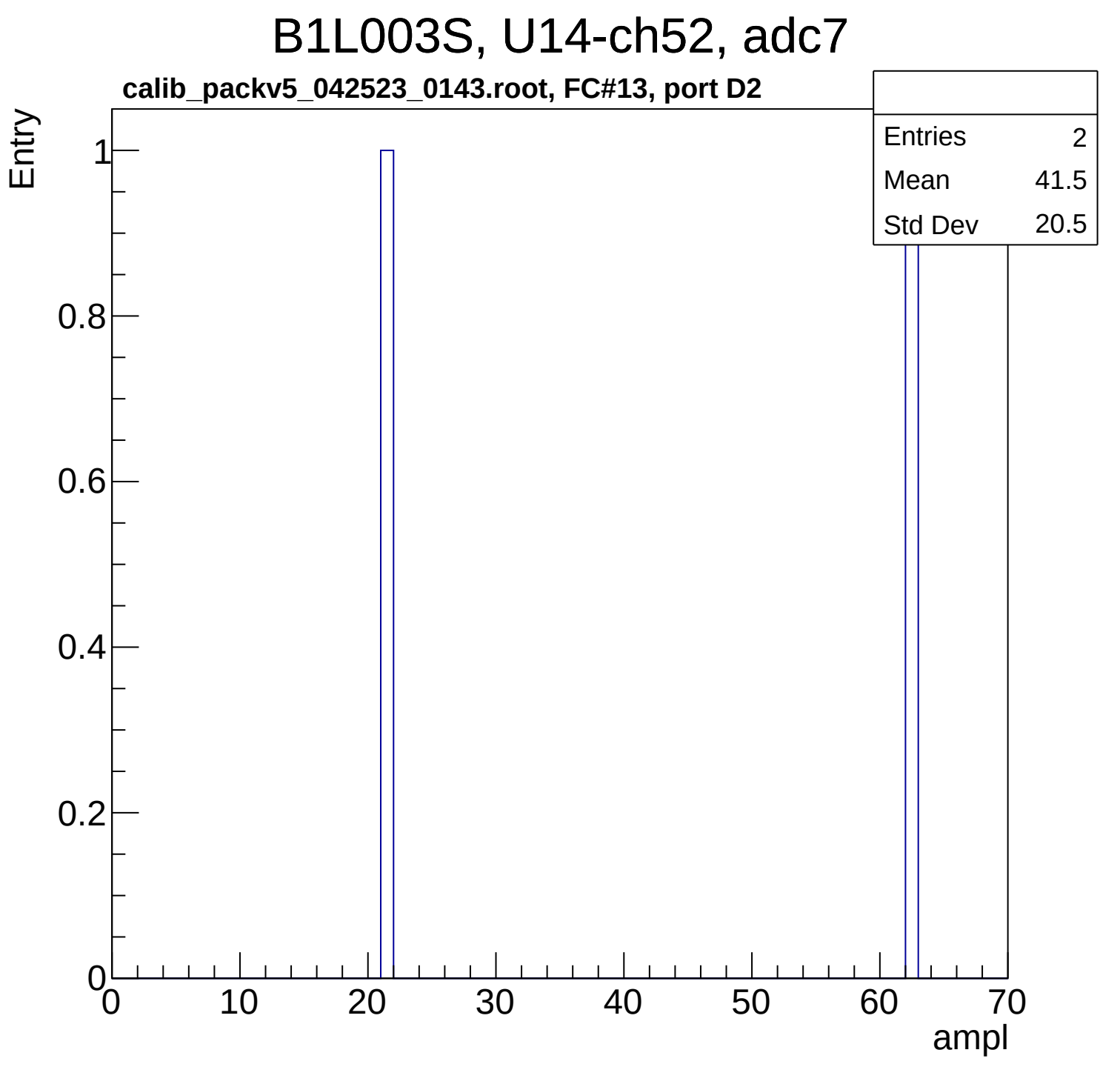

B1L003S, U14-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 41.5
Std Dev 2.557
Gaus mean : 42.4029
Gaus Width: 3.0562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

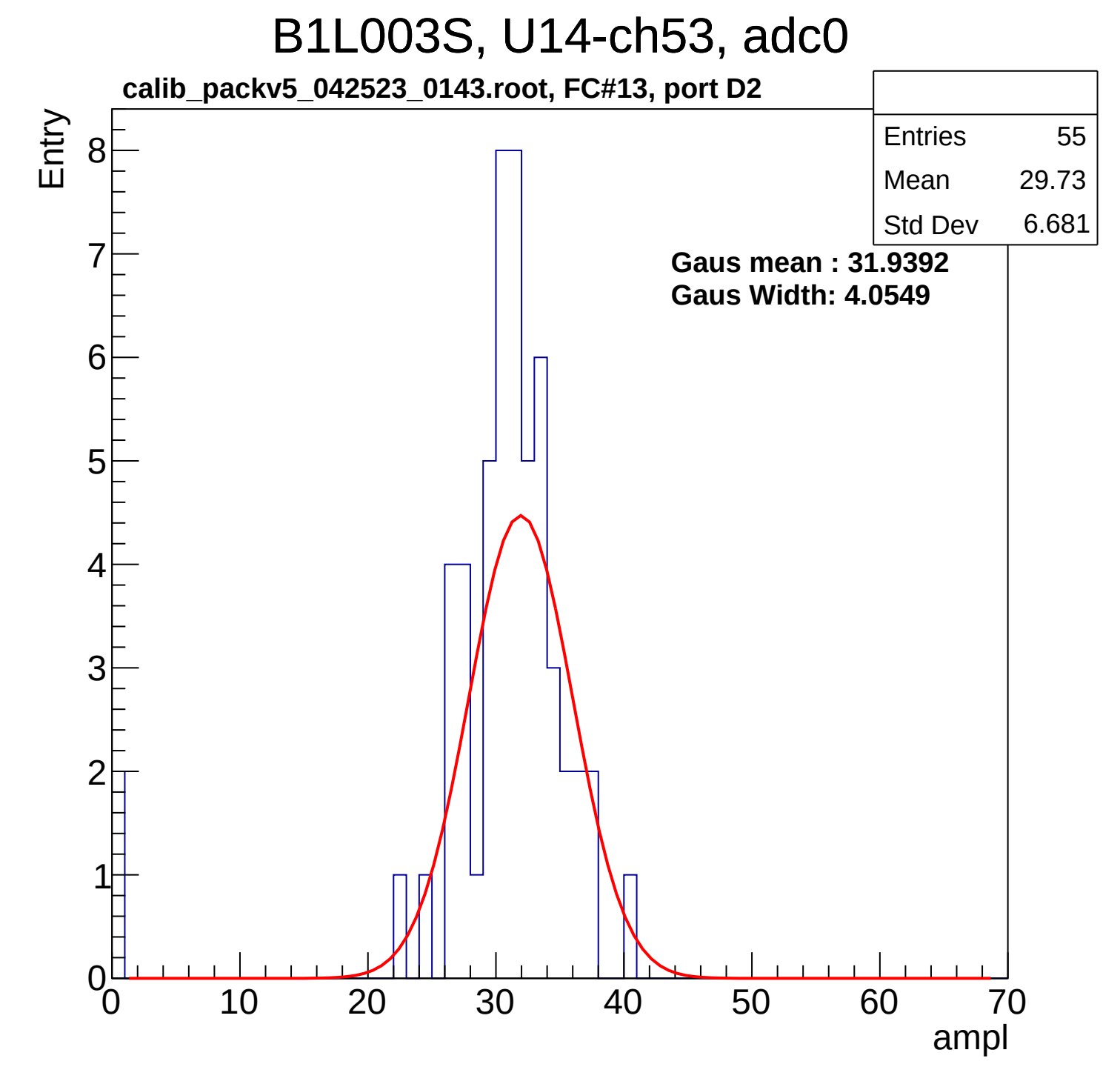

B1L003S, U14-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 47.74
Std Dev 2.82
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

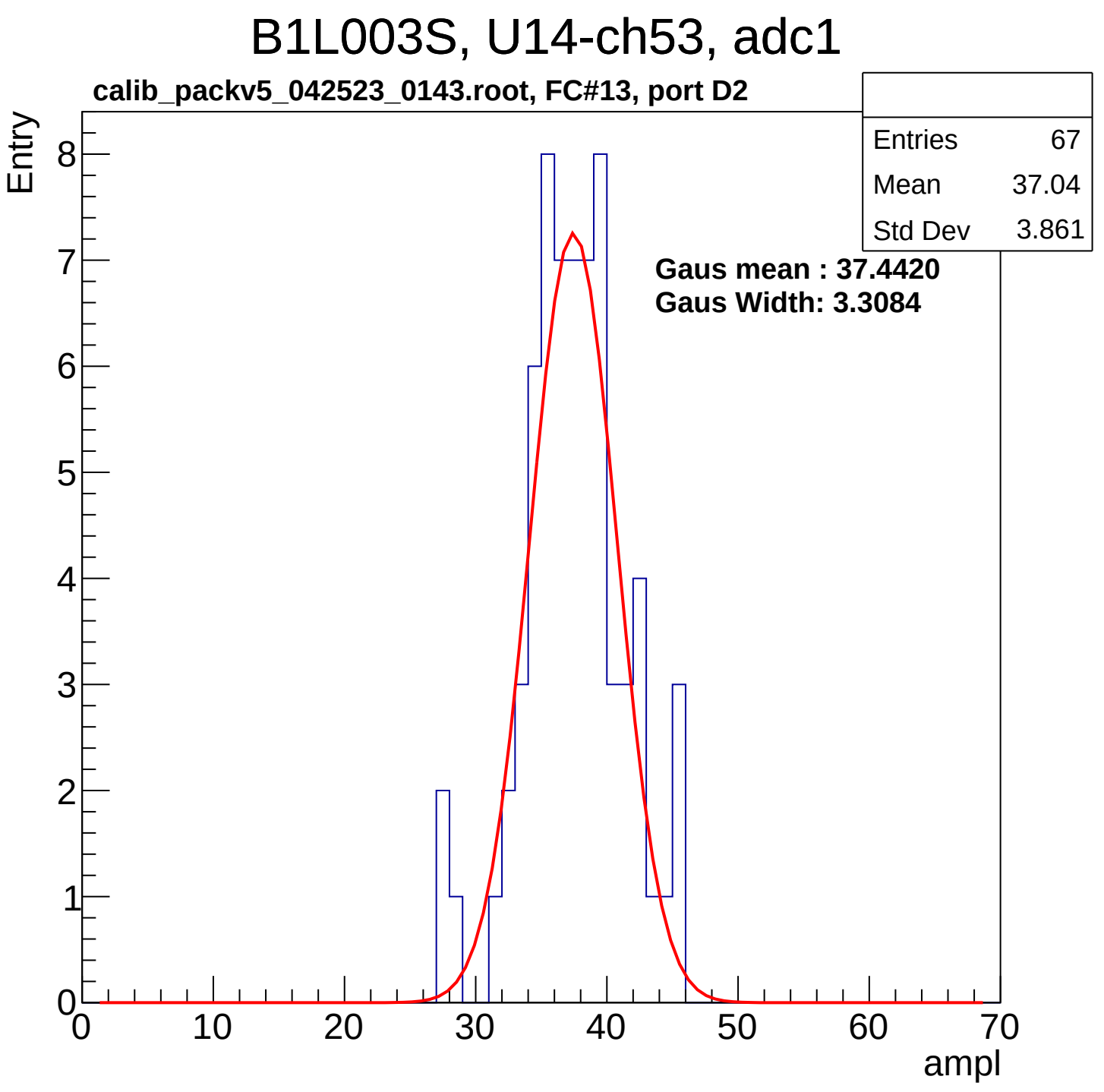

B1L003S, U14-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.06
Std Dev 3.064
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

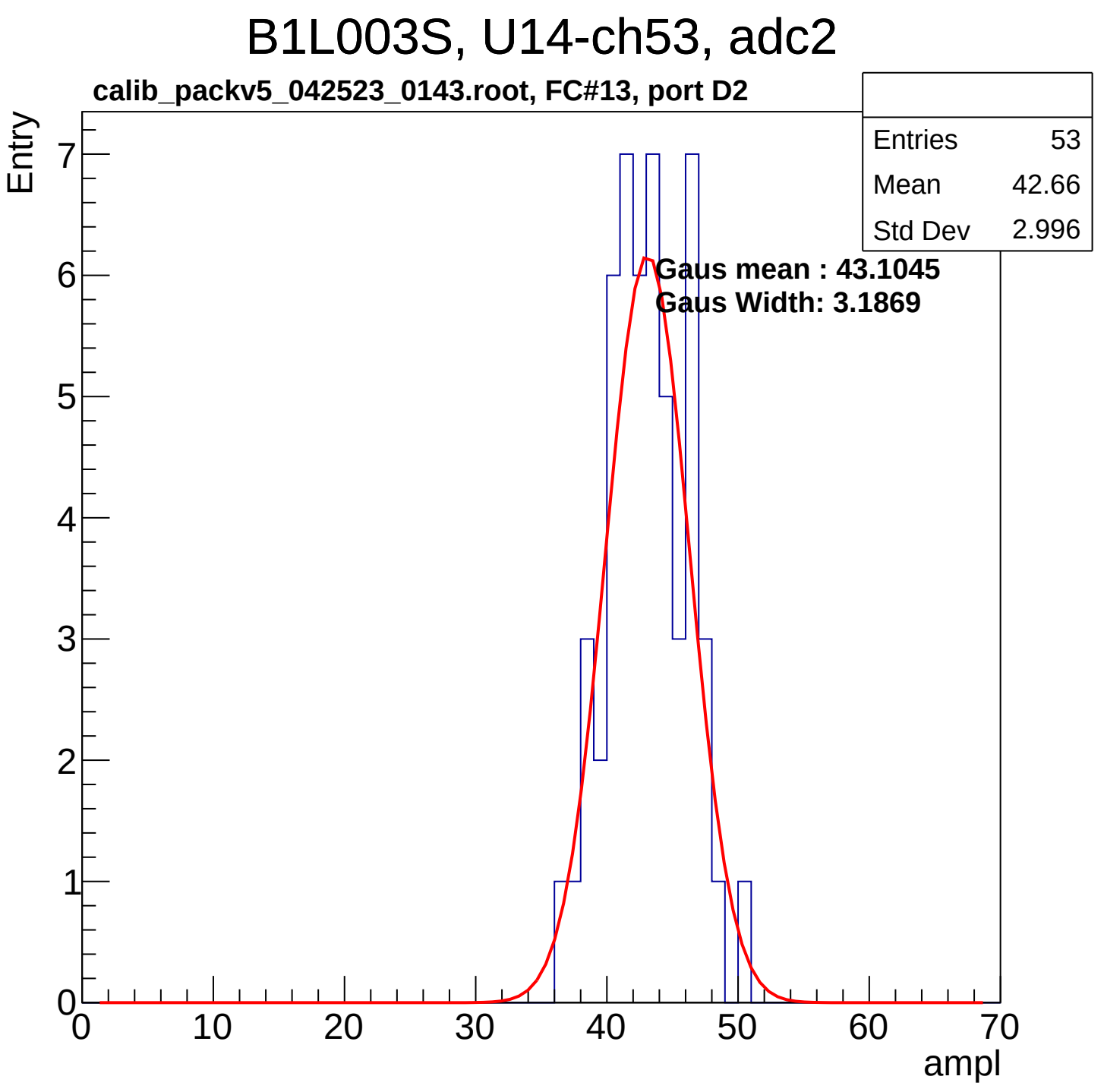

B1L003S, U14-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 57.93
Std Dev 2.981
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

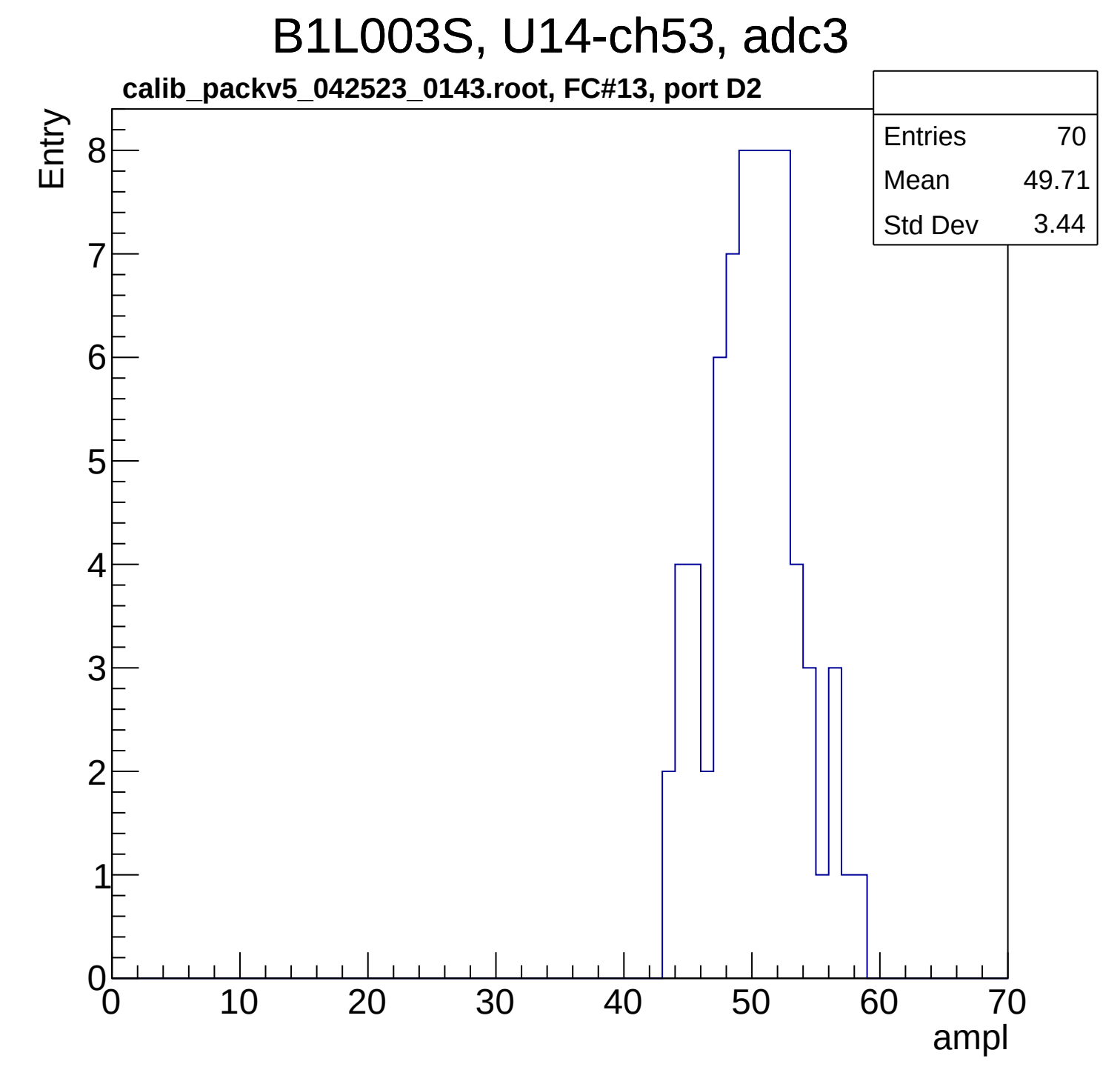

B1L003S, U14-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.92
Std Dev 12.38
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

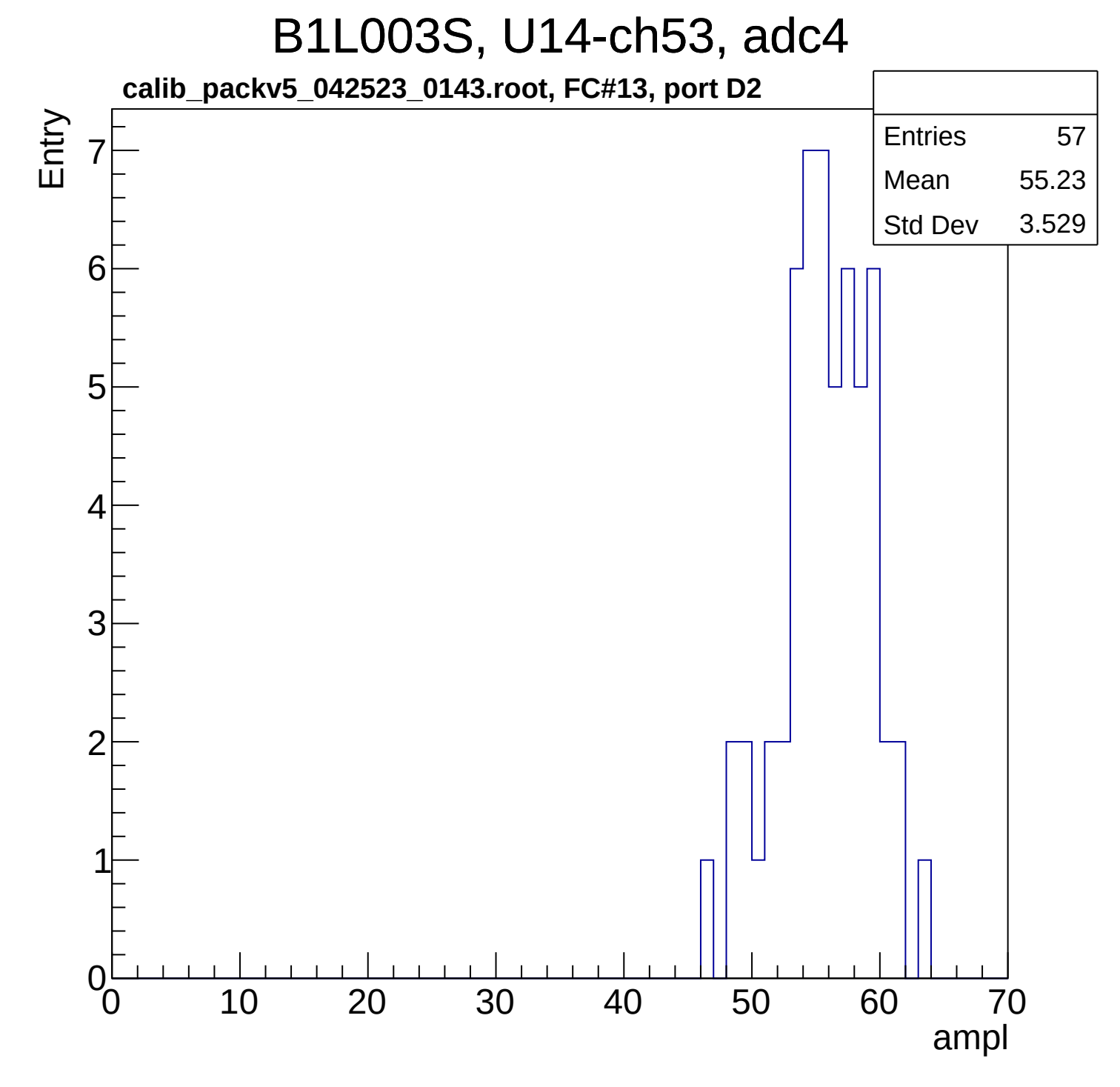

B1L003S, U14-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 41.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

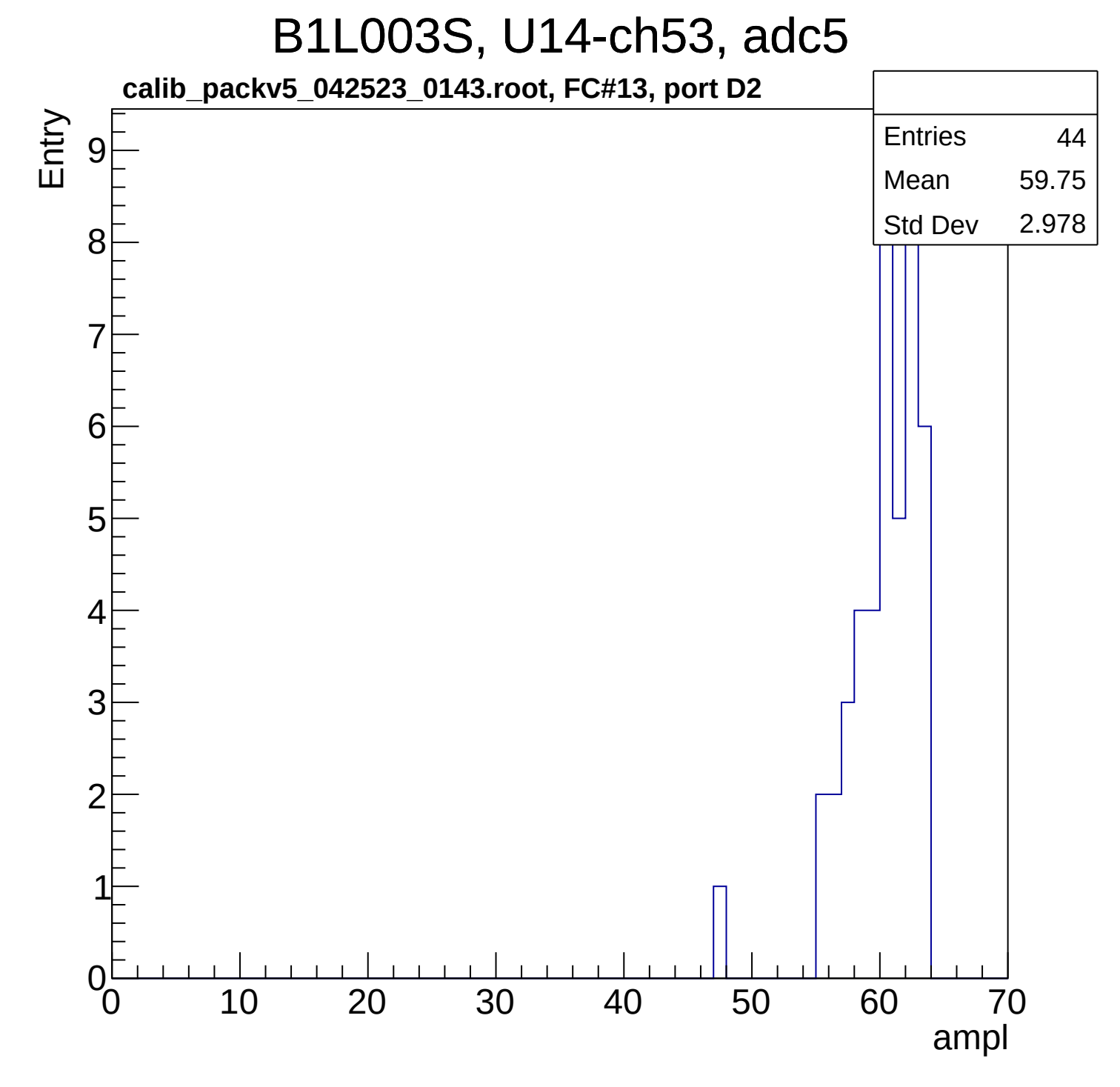

B1L003S, U14-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.73
Std Dev 6.681
Gaus mean : 31.9392
Gaus Width: 4.0549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

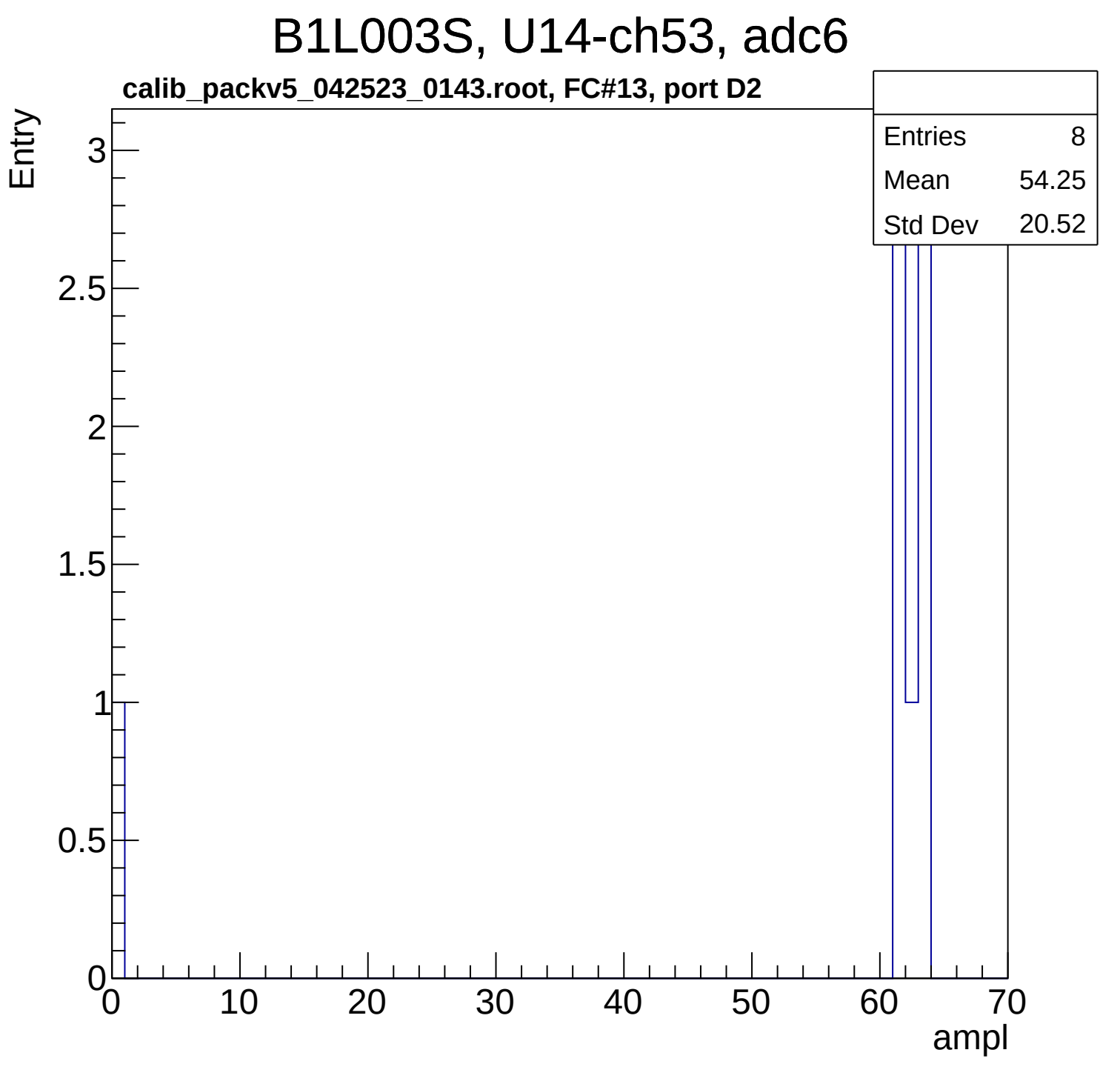

B1L003S, U14-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.04
Std Dev 3.861
Gaus mean : 37.4420
Gaus Width: 3.3084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

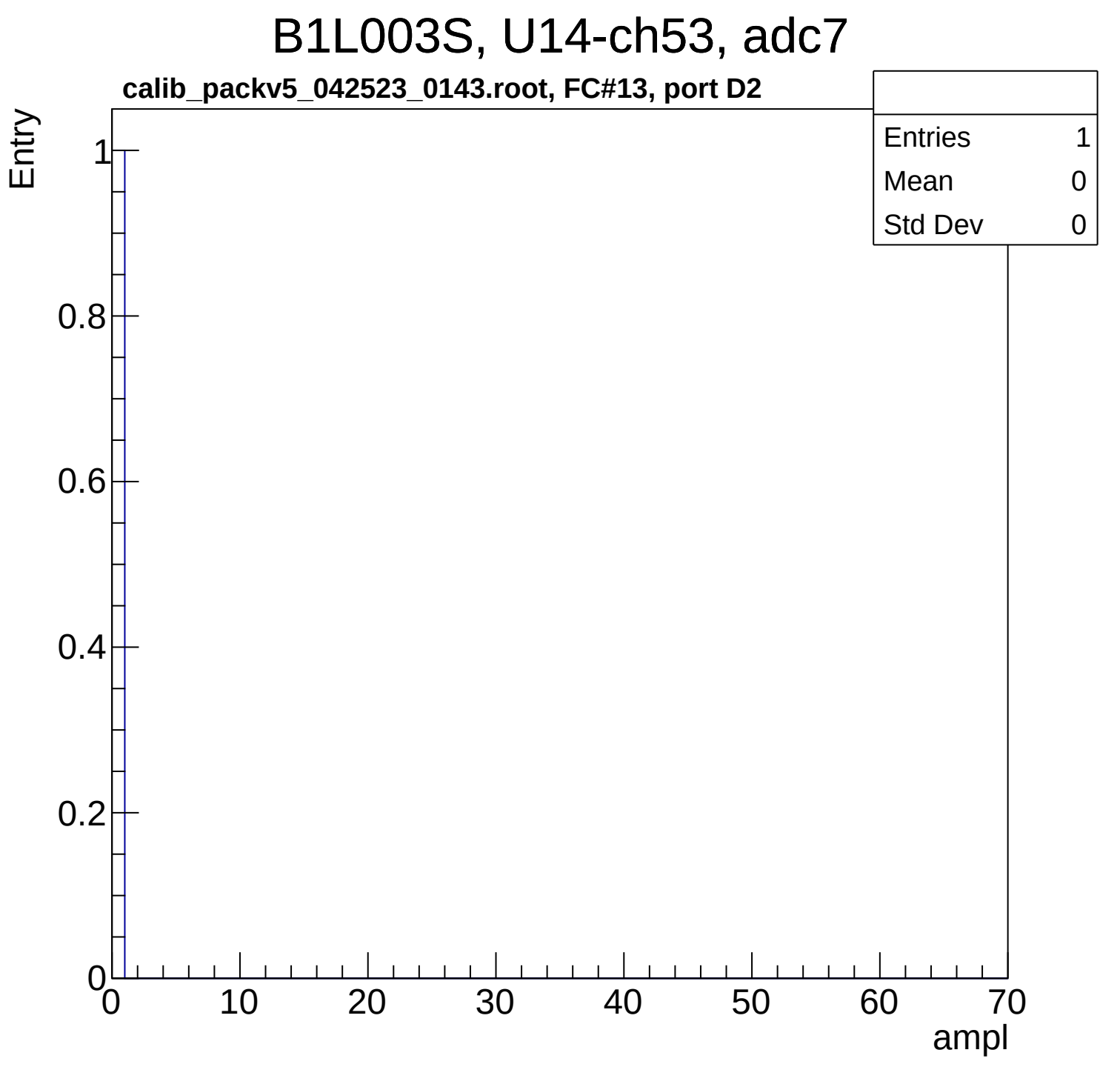

B1L003S, U14-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.66
Std Dev 2.996
Gaus mean : 43.1045
Gaus Width: 3.1869
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

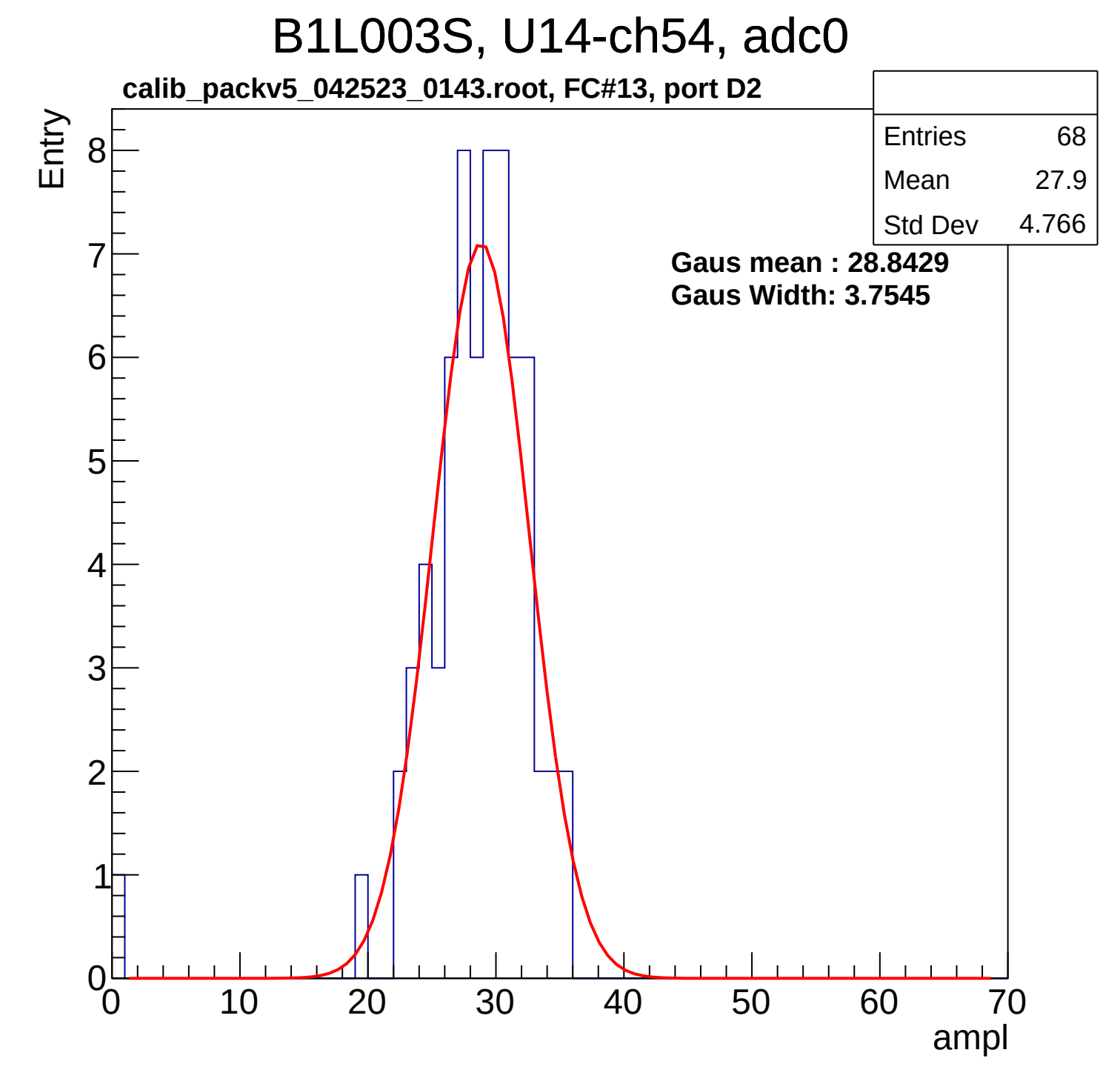

B1L003S, U14-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.71
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

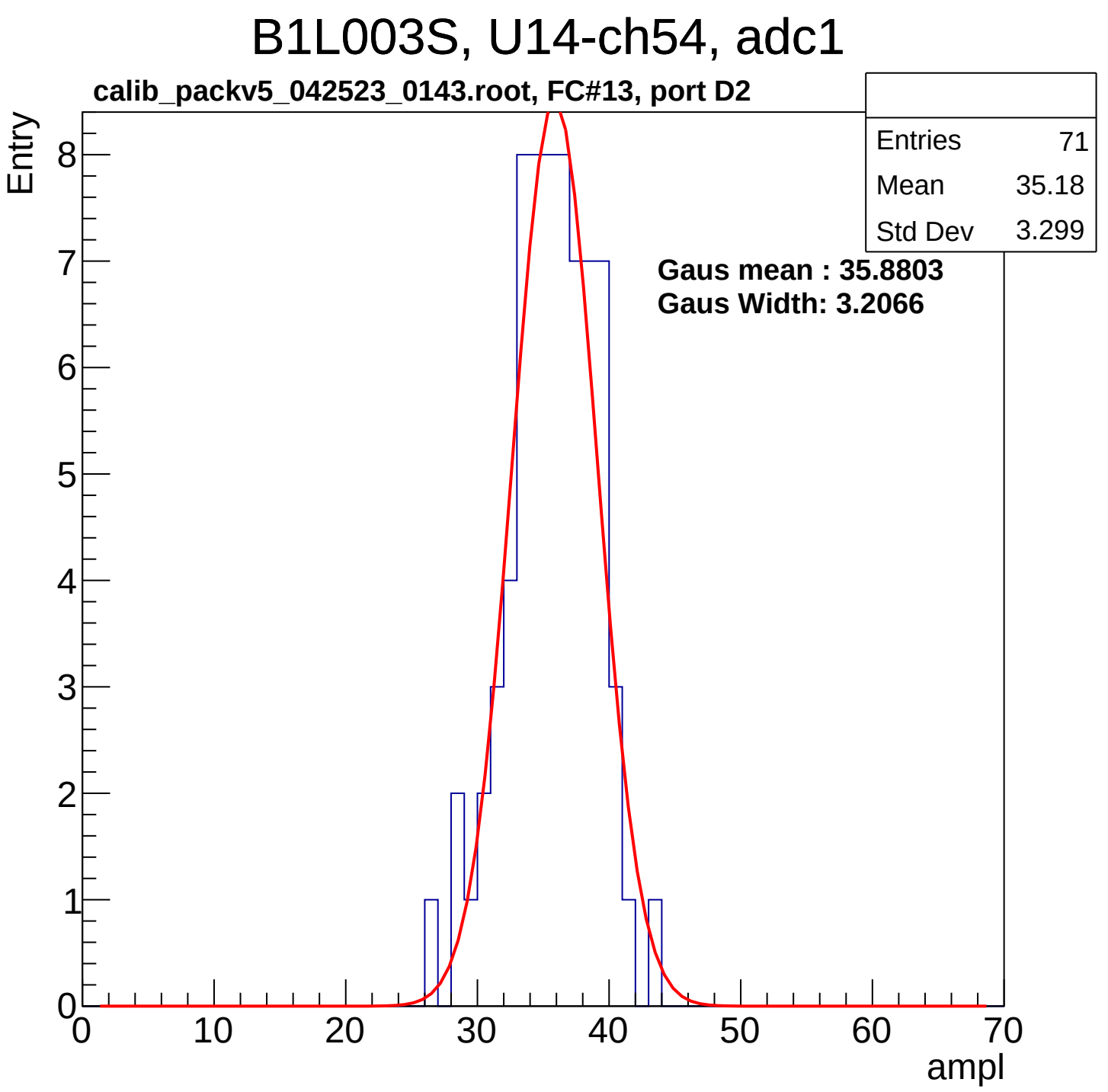

B1L003S, U14-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.23
Std Dev 3.529
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

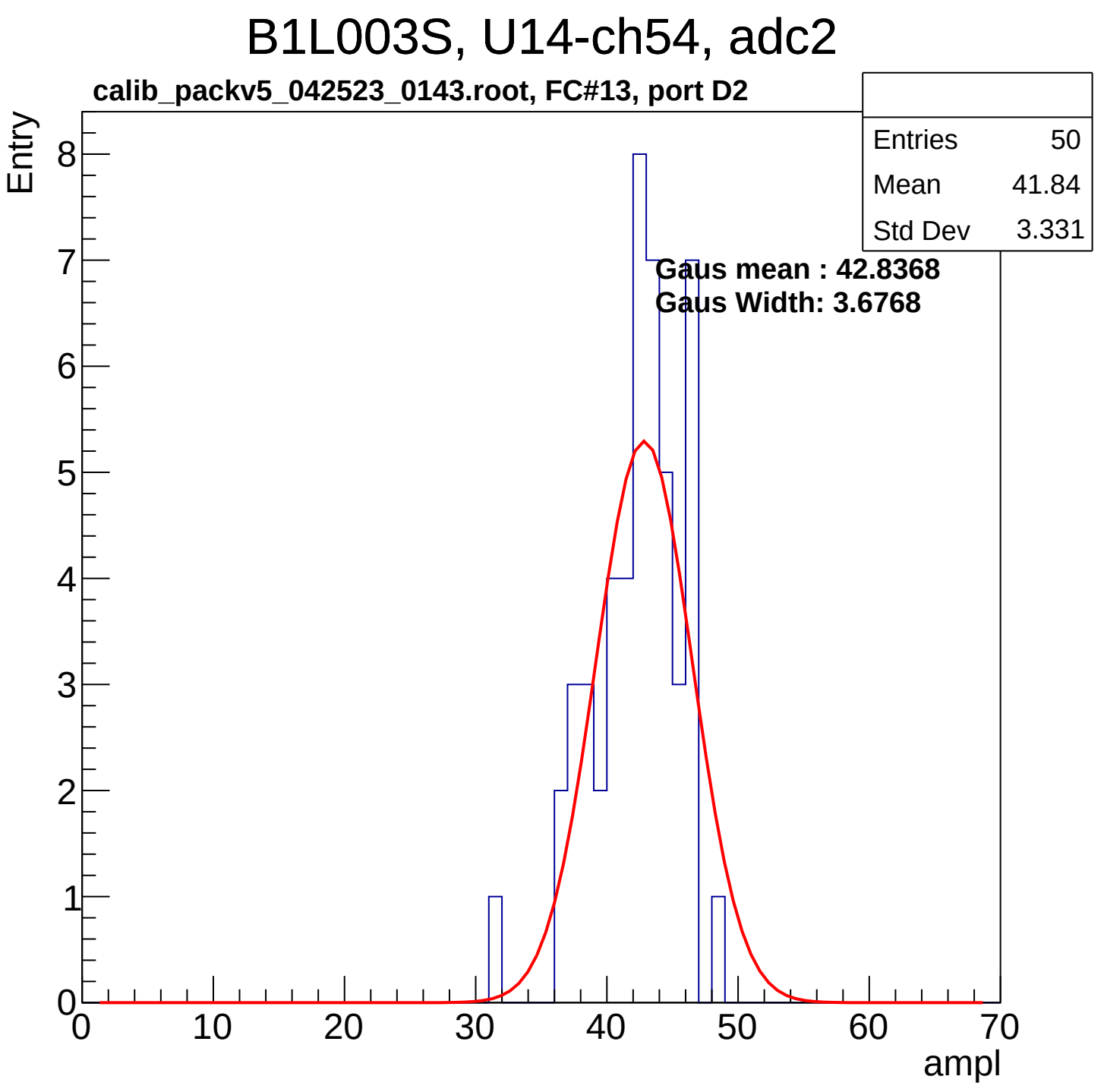

B1L003S, U14-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.75
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

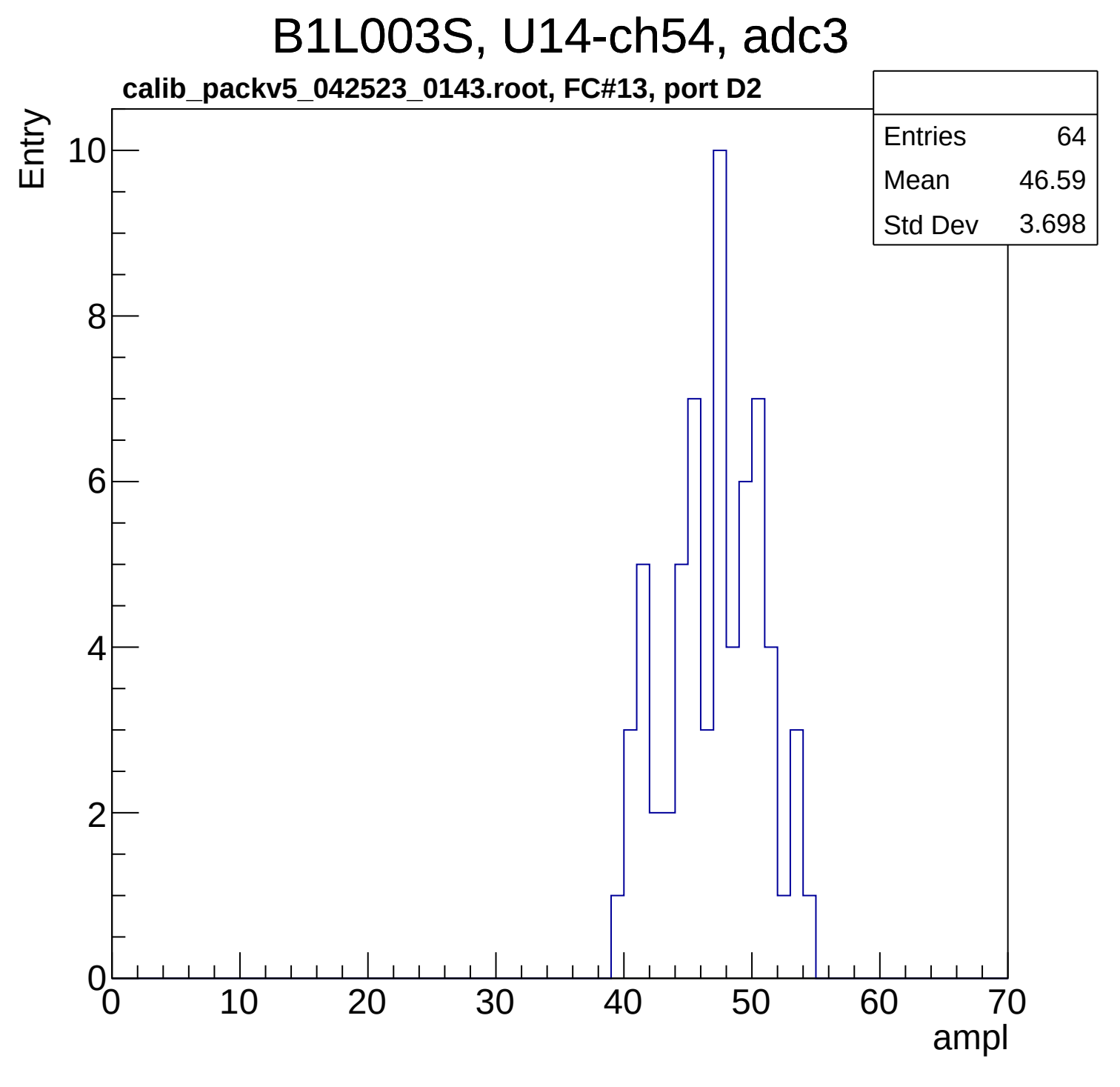

B1L003S, U14-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.25
Std Dev 20.52
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

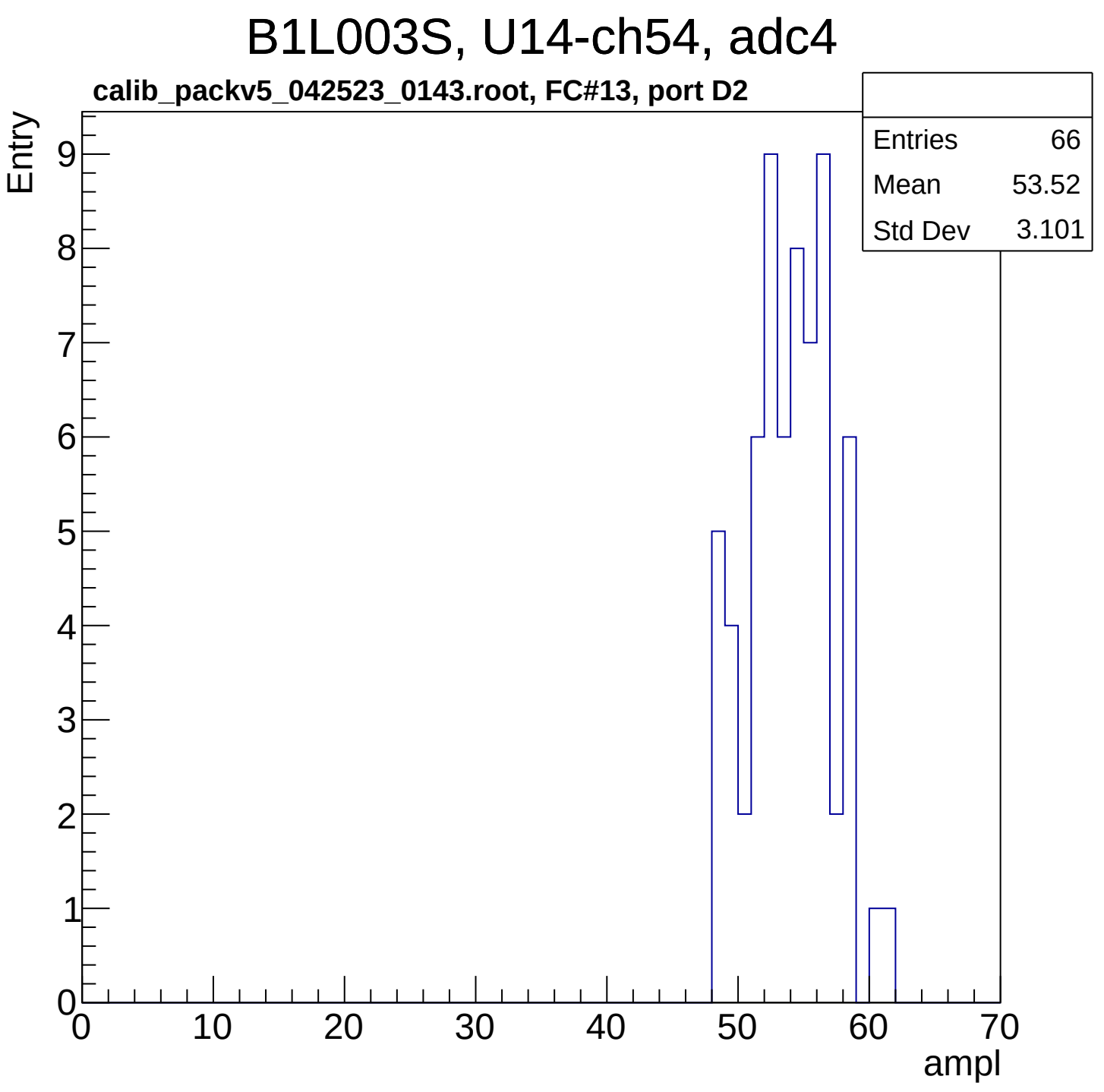

B1L003S, U14-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

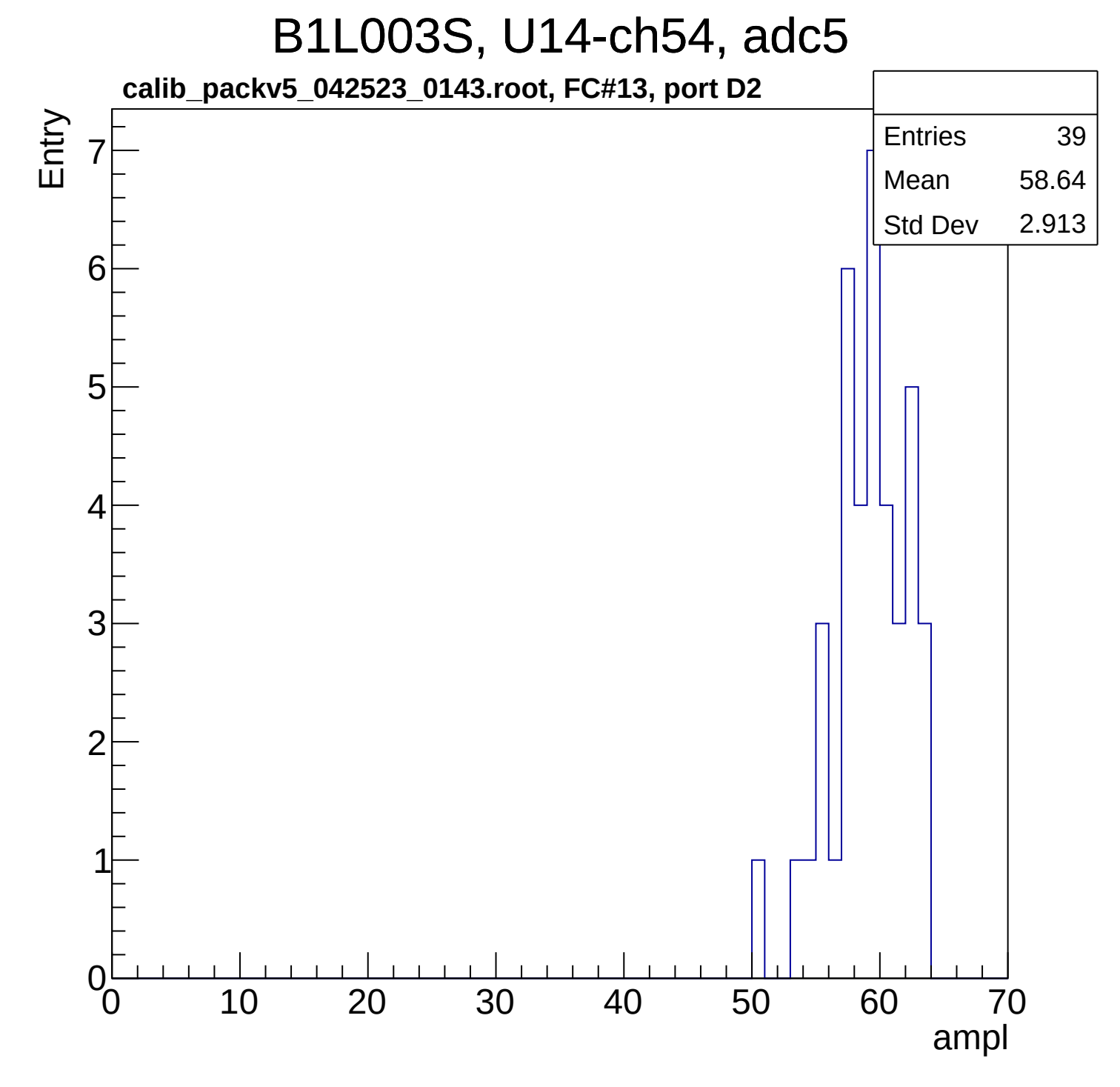

B1L003S, U14-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 27.9
Std Dev 4.766
Gaus mean : 28.8429
Gaus Width: 3.7545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

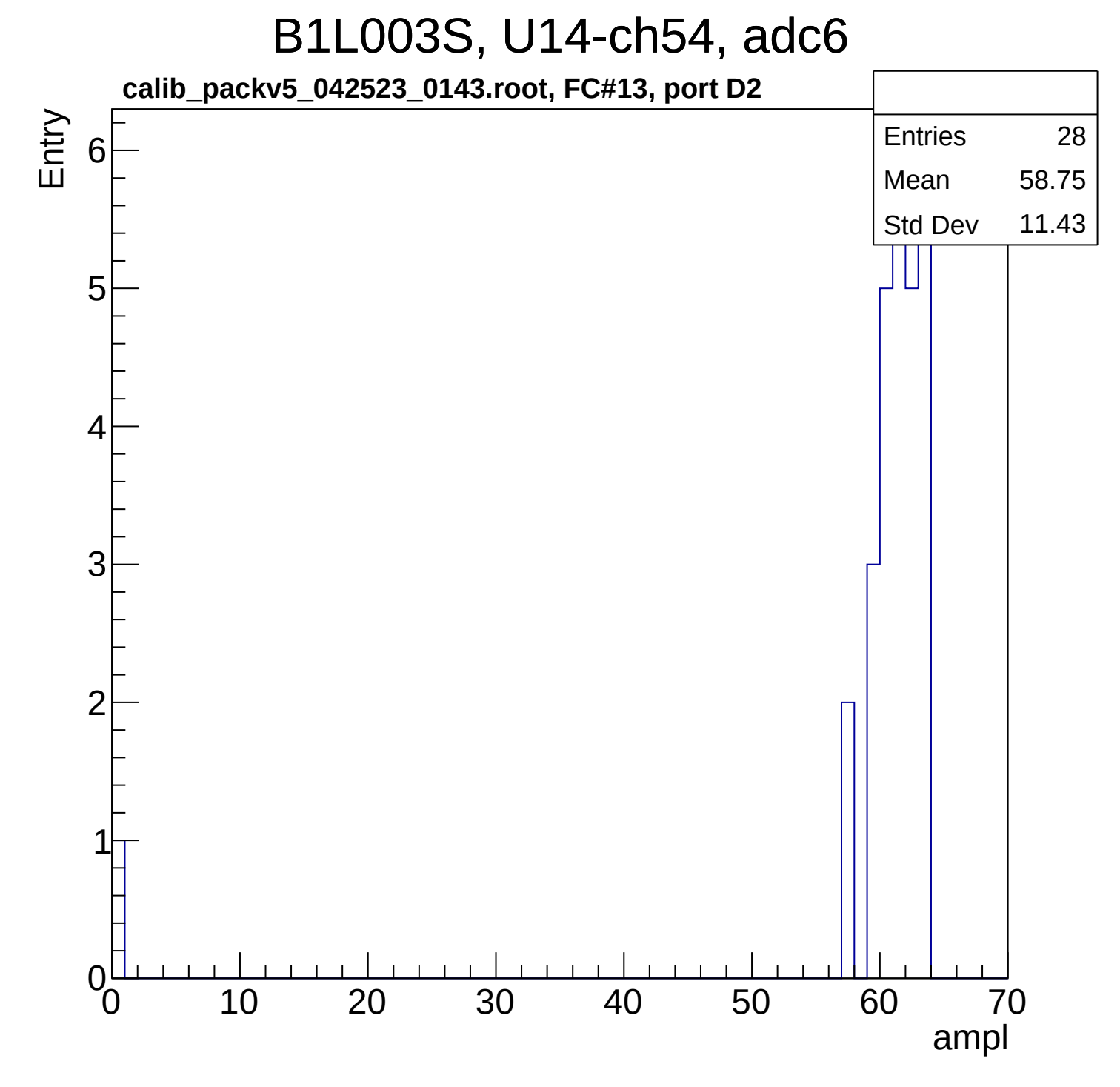

B1L003S, U14-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.18
Std Dev 3.299
Gaus mean : 35.8803
Gaus Width: 3.2066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

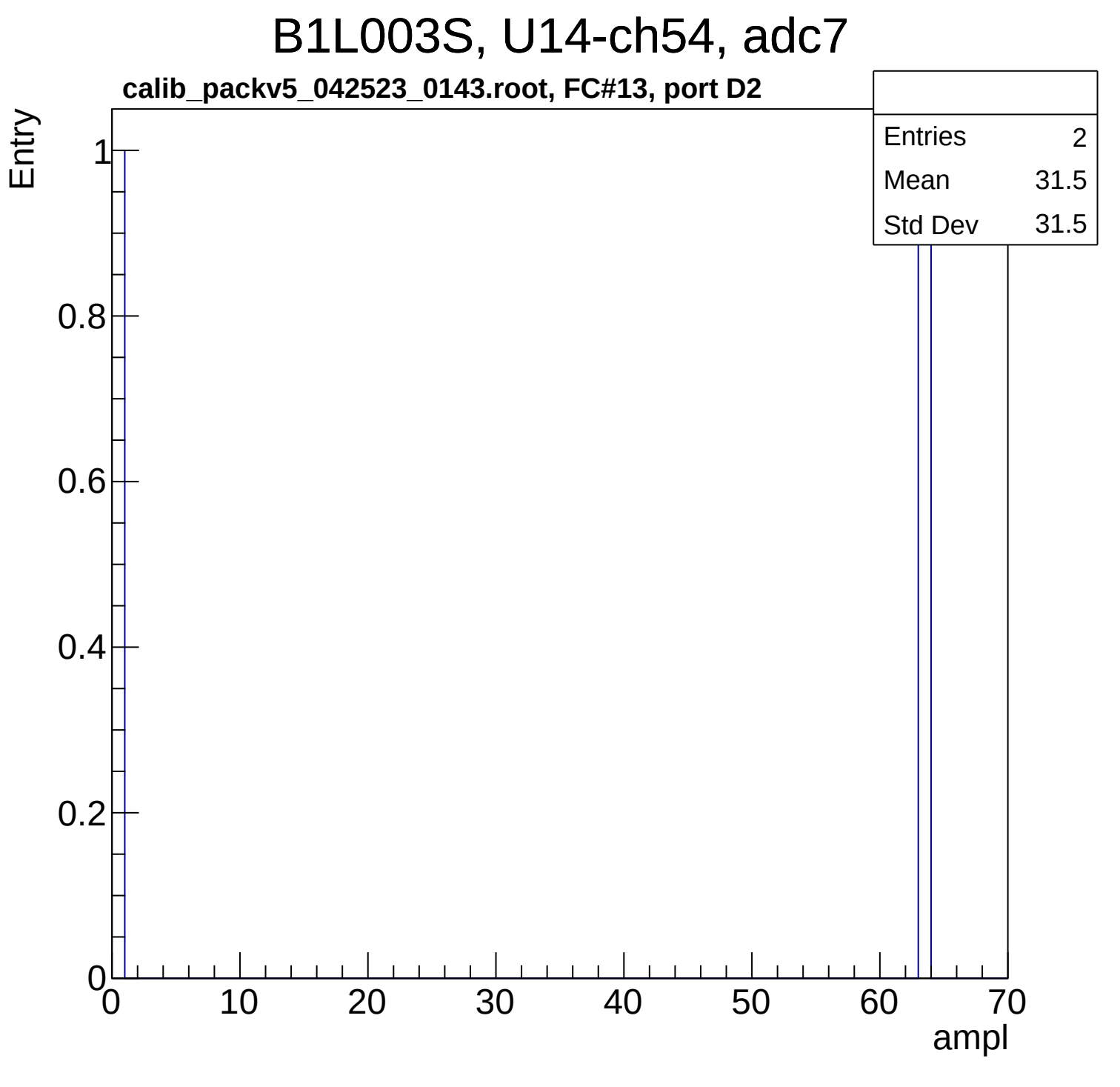

B1L003S, U14-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.84
Std Dev 3.331
Gaus mean : 42.8368
Gaus Width: 3.6768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

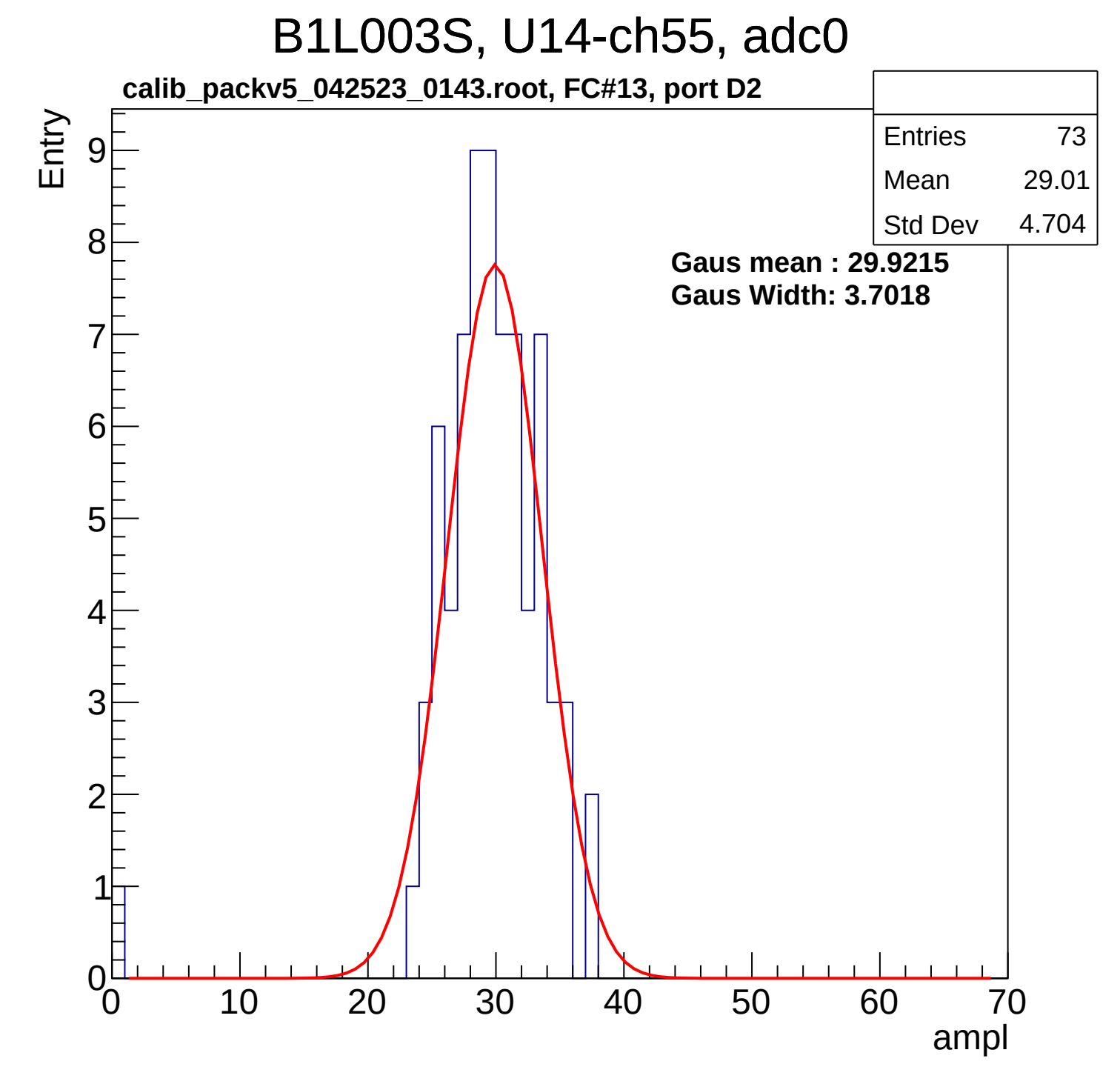

B1L003S, U14-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 46.59
Std Dev 3.698
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

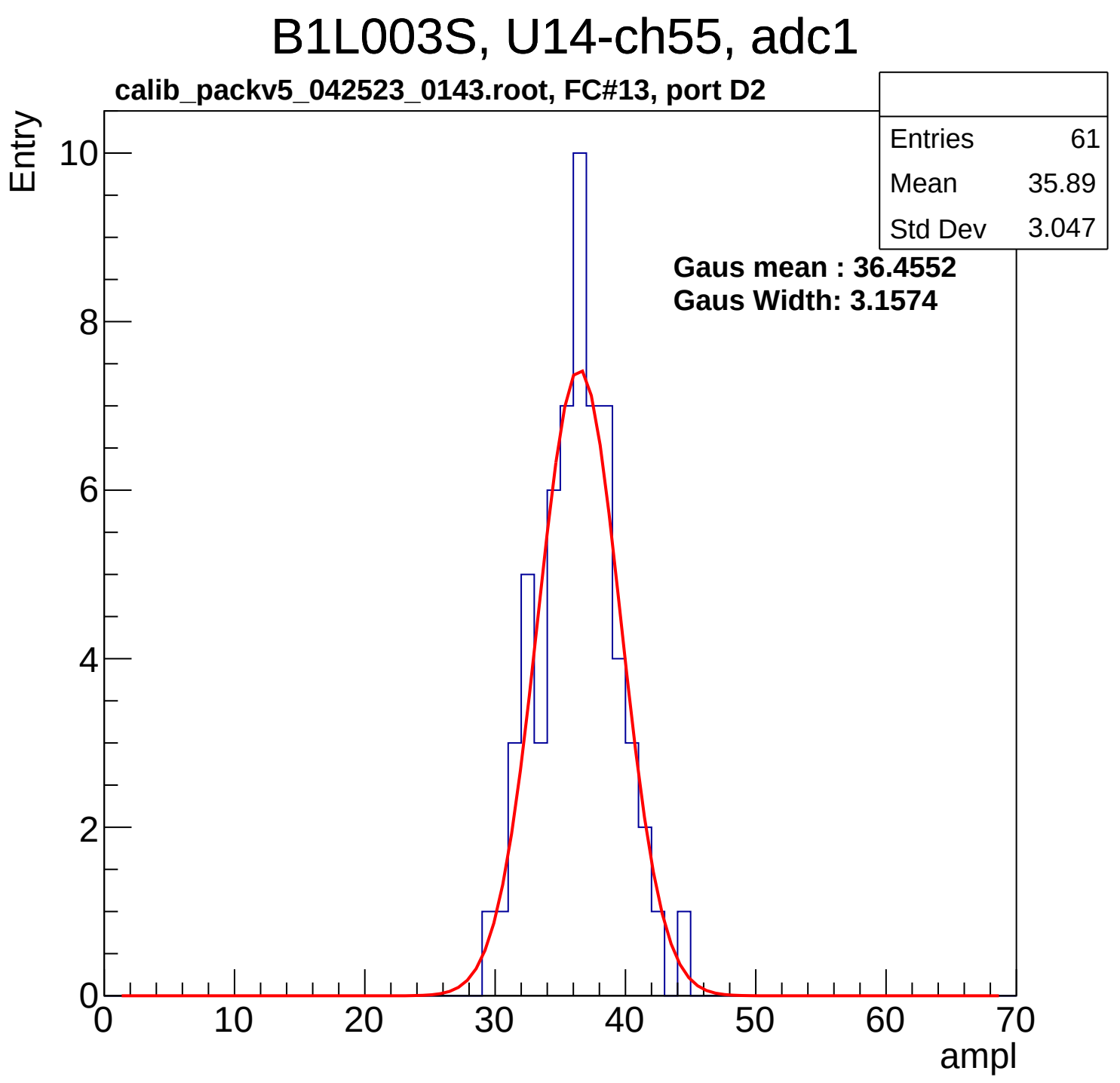

B1L003S, U14-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.52
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

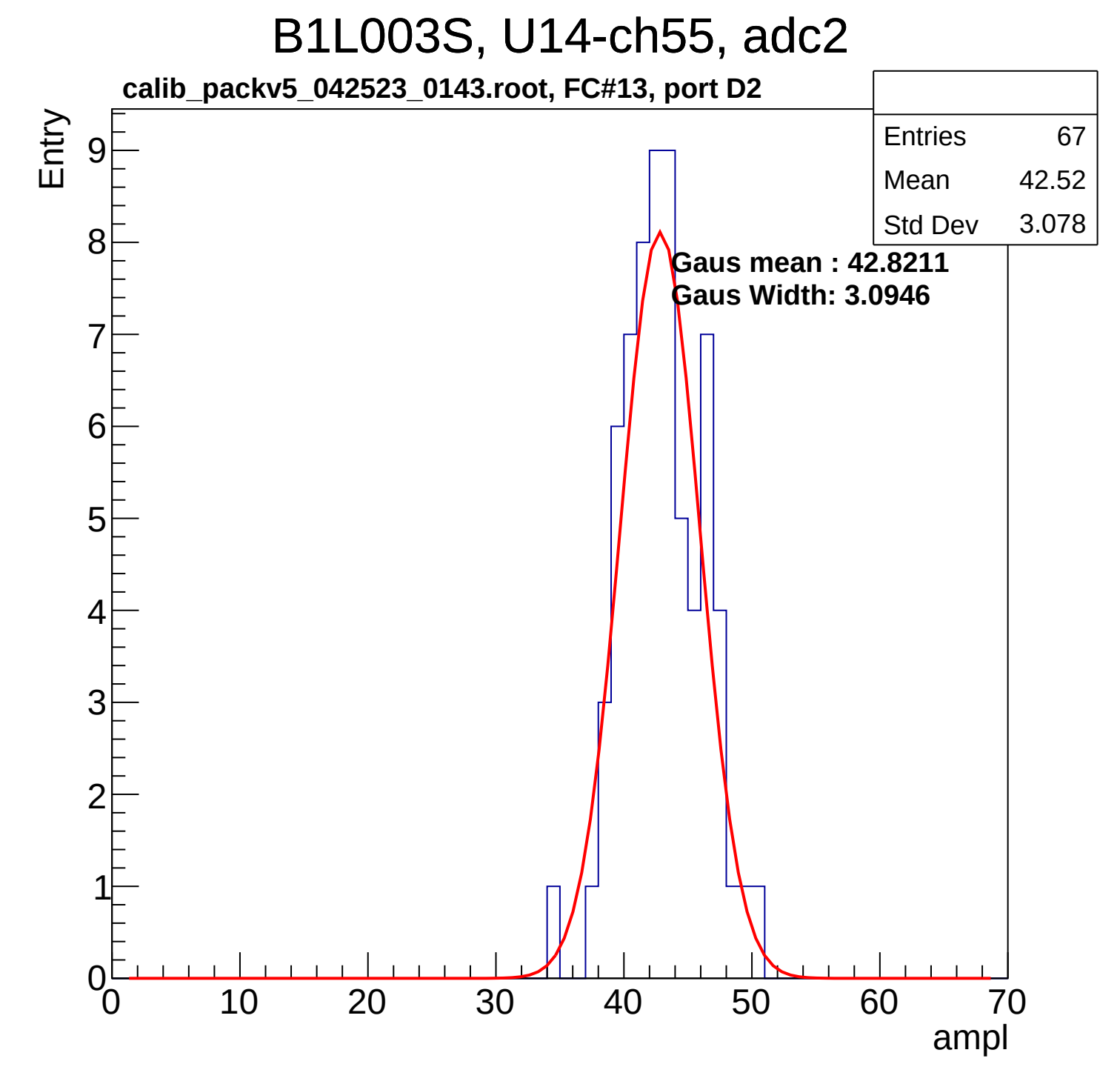

B1L003S, U14-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.64
Std Dev 2.913
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

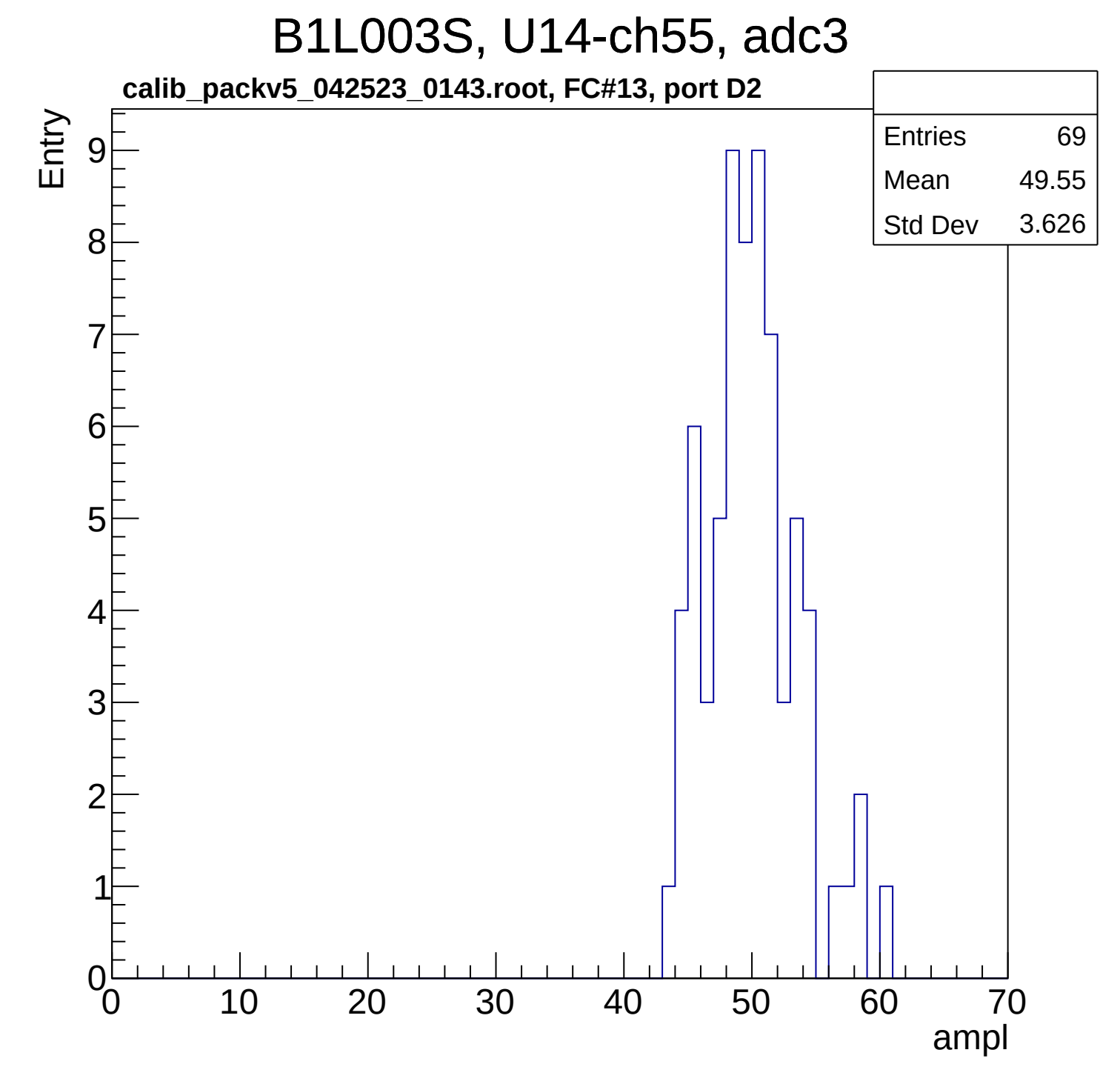

B1L003S, U14-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 28
Mean 58.75
Std Dev 11.43
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

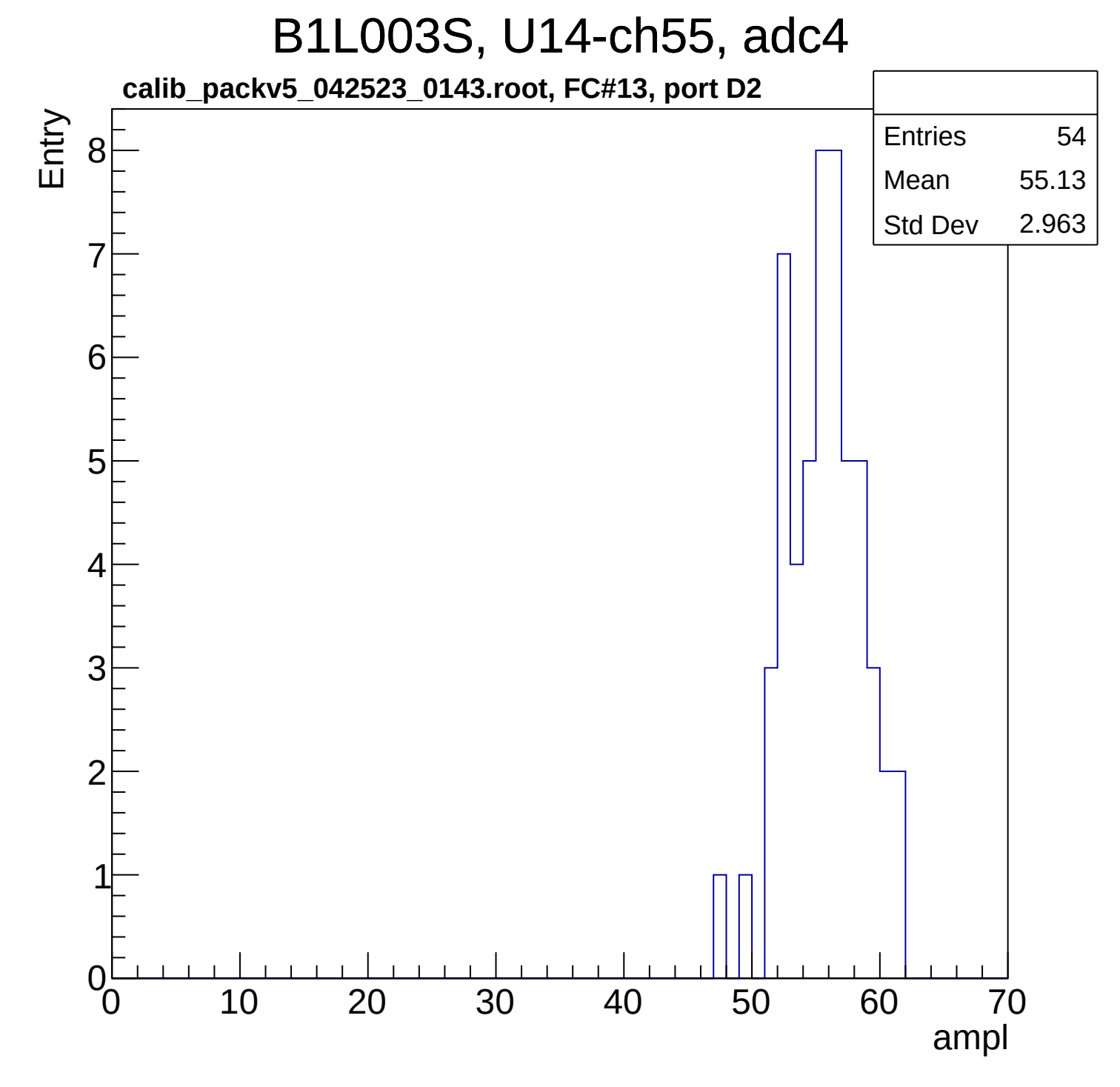

B1L003S, U14-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

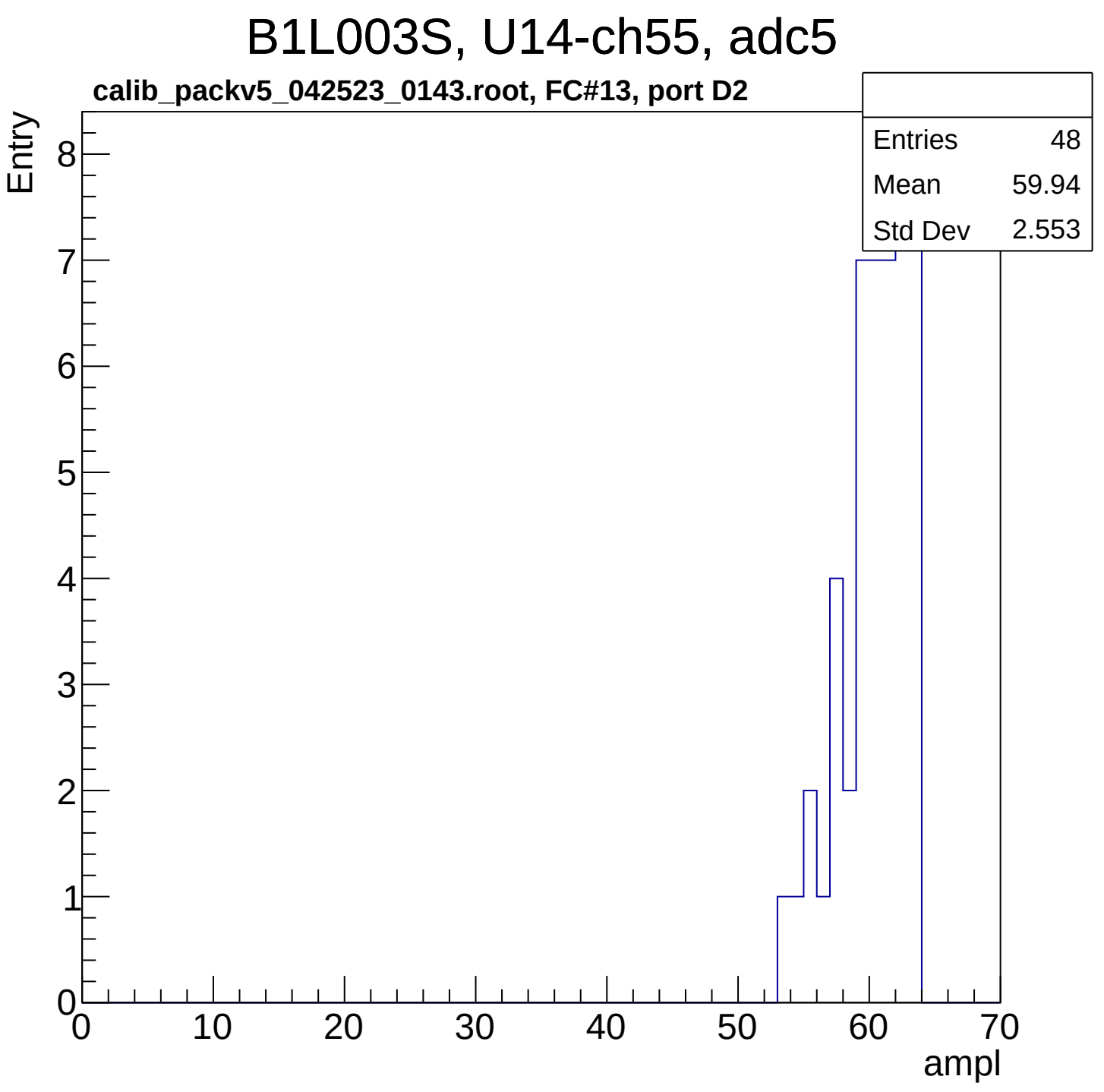

B1L003S, U14-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 29.01
Std Dev 4.704
Gaus mean : 29.9215
Gaus Width: 3.7018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

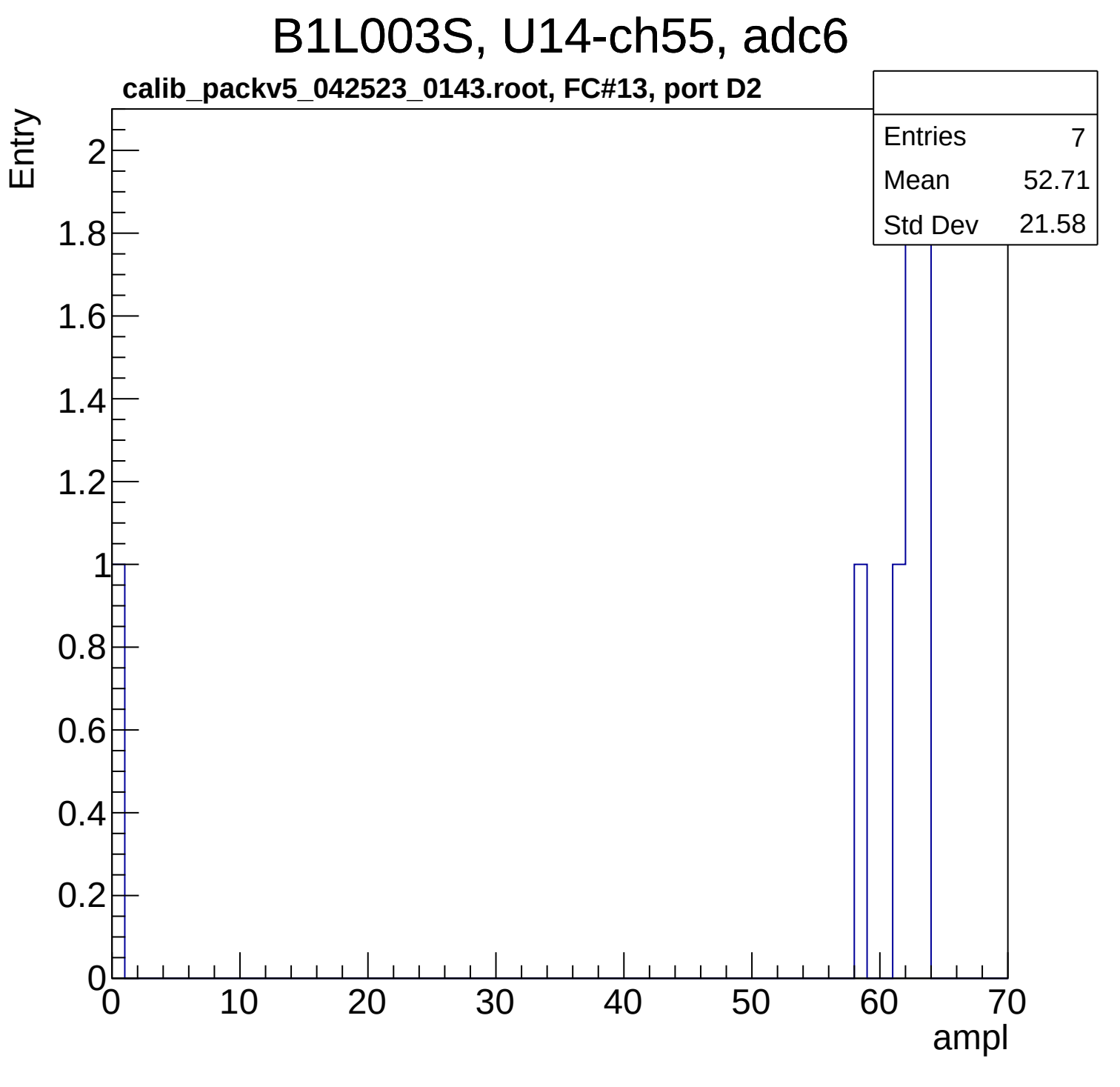

B1L003S, U14-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.89
Std Dev 3.047
Gaus mean : 36.4552
Gaus Width: 3.1574
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

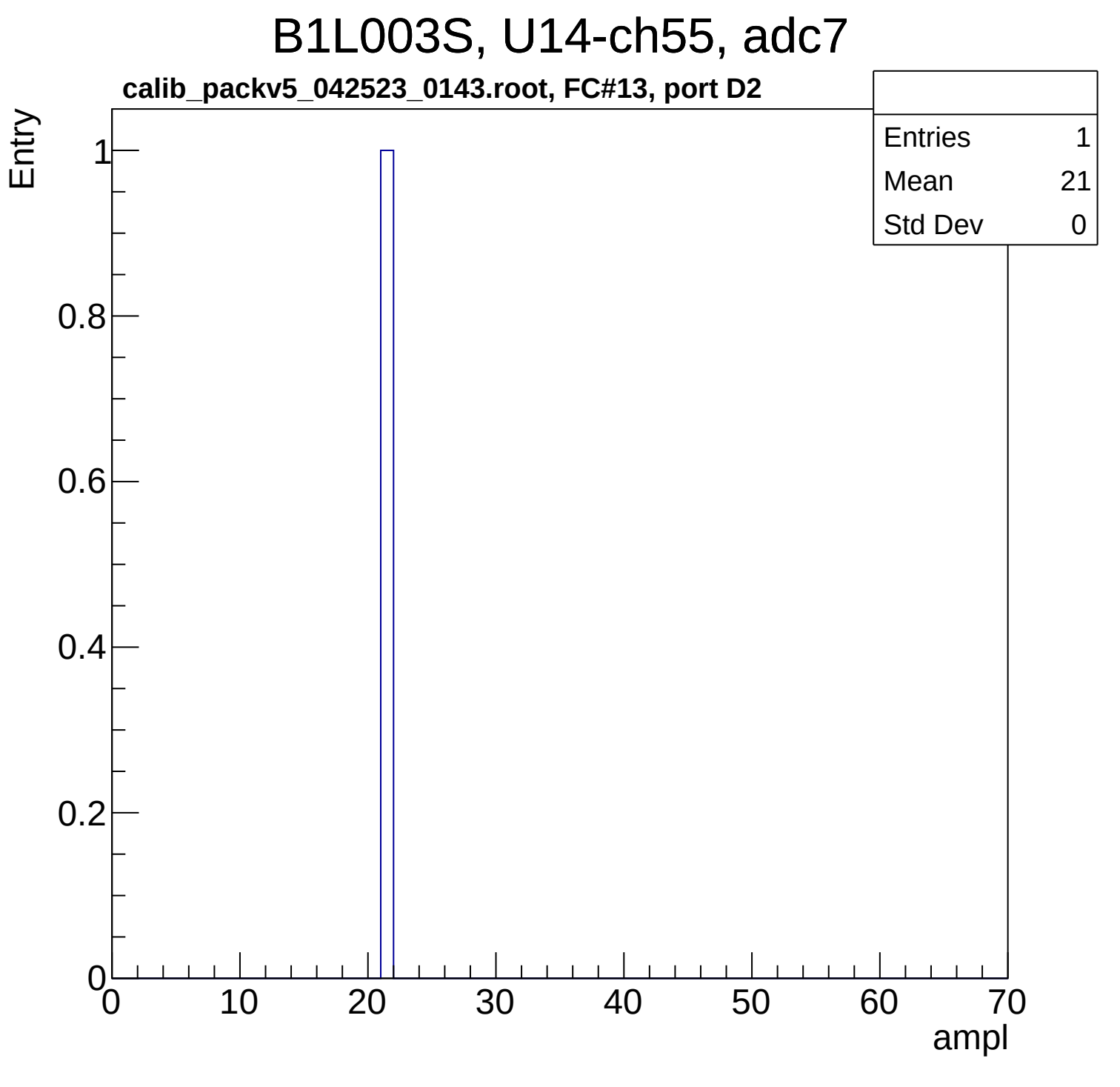

B1L003S, U14-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.52
Std Dev 3.078
Gaus mean : 42.8211
Gaus Width: 3.0946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

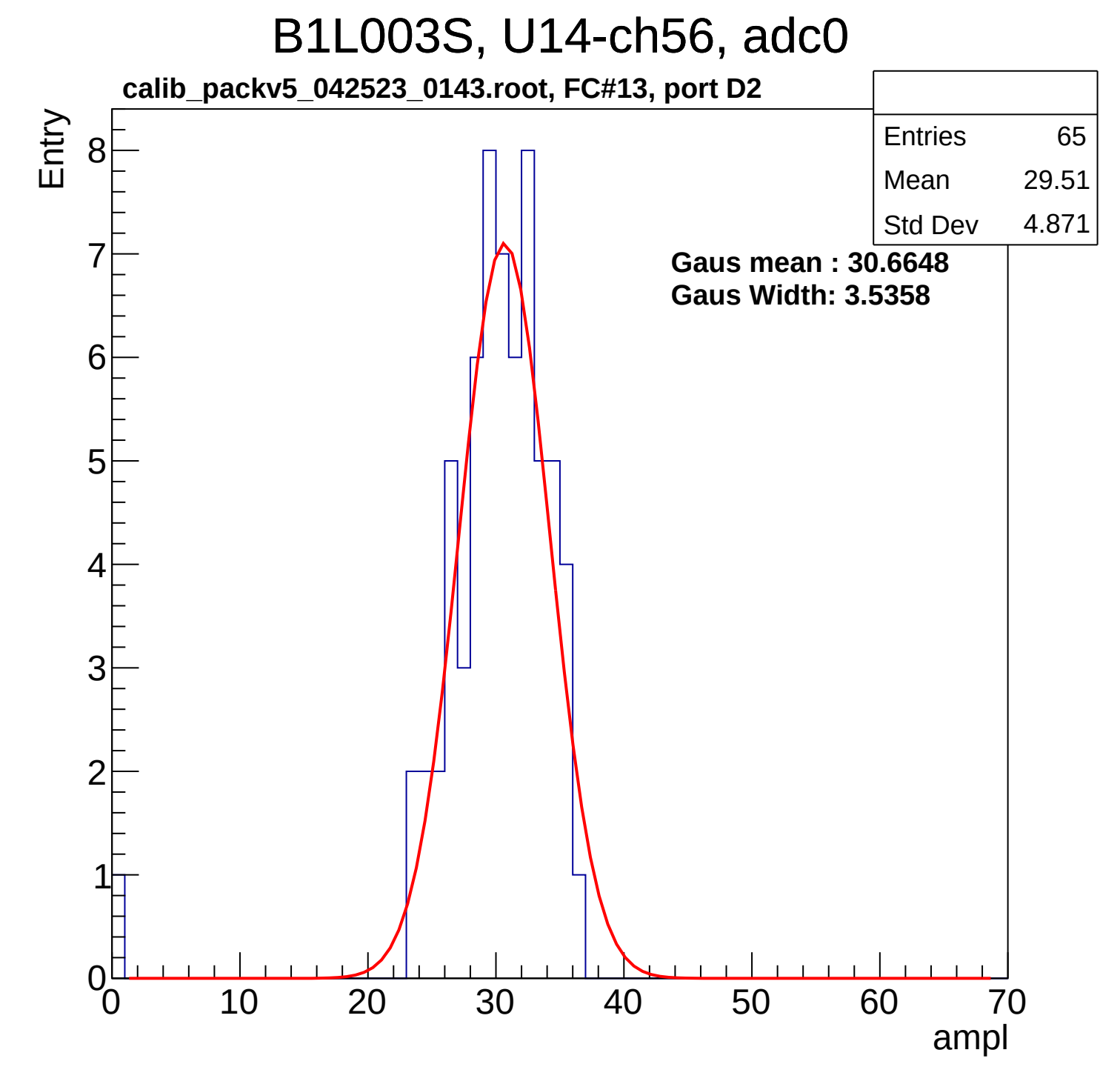

B1L003S, U14-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.55
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

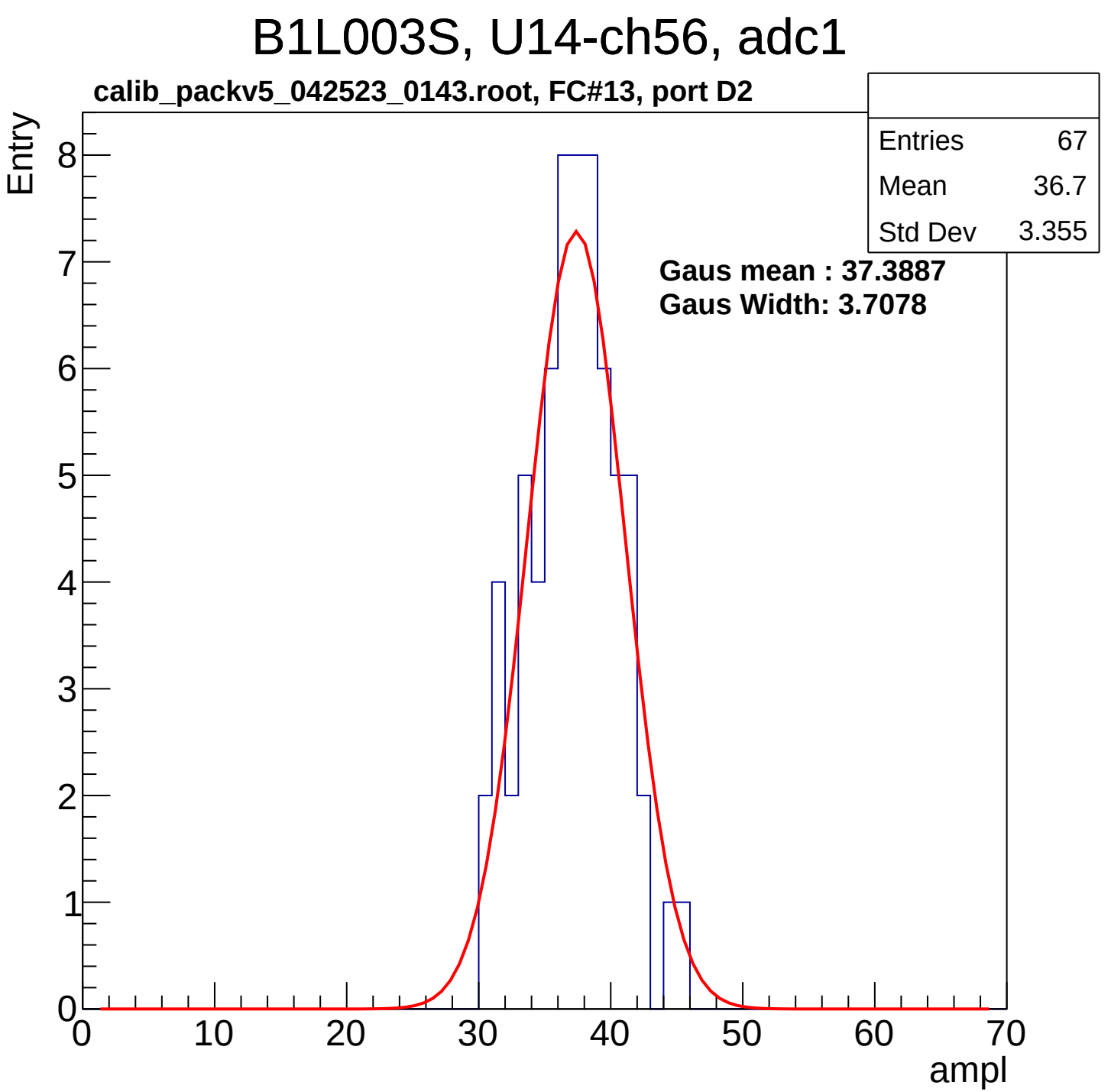

B1L003S, U14-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.13
Std Dev 2.963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

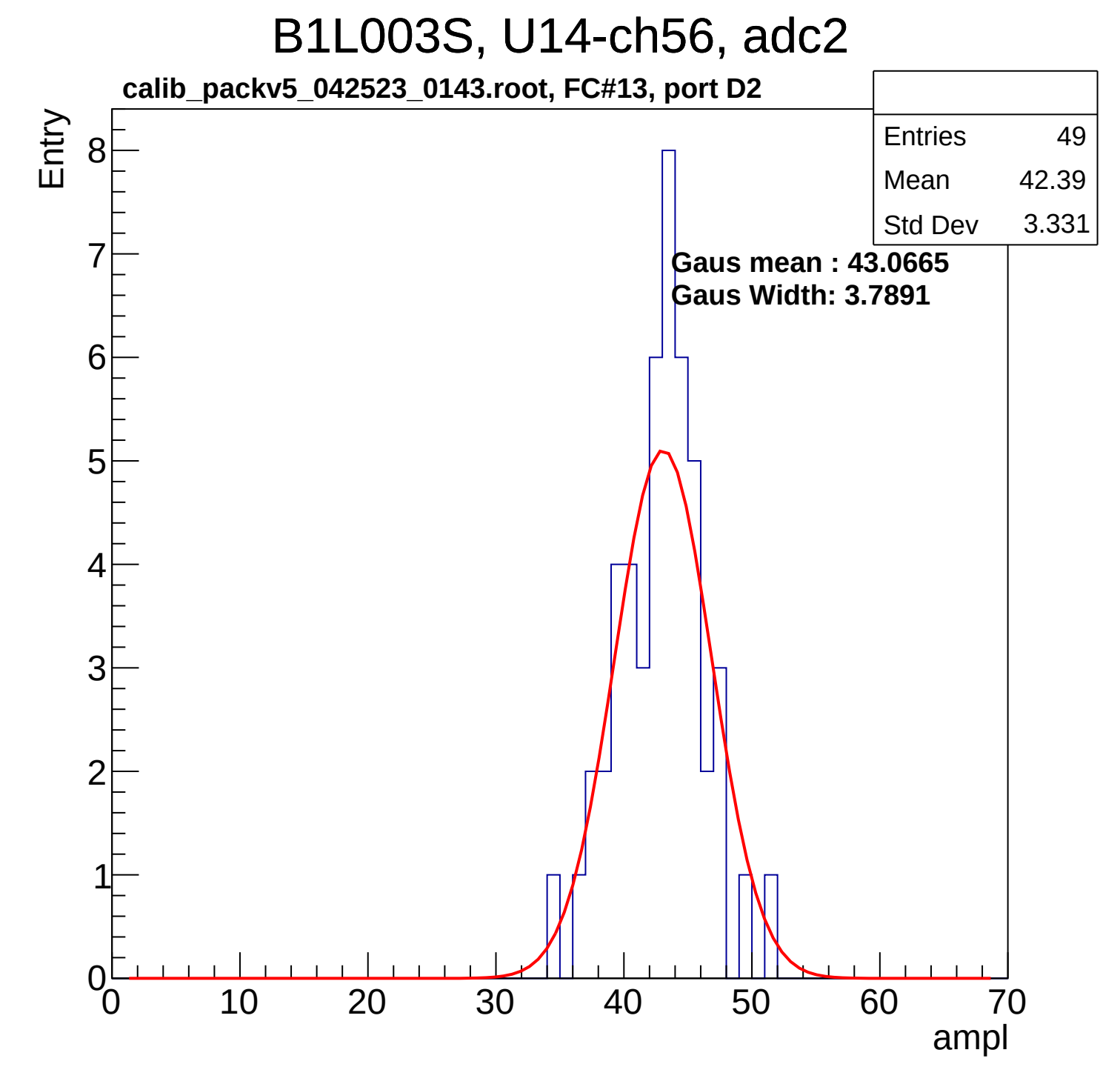

B1L003S, U14-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.94
Std Dev 2.553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

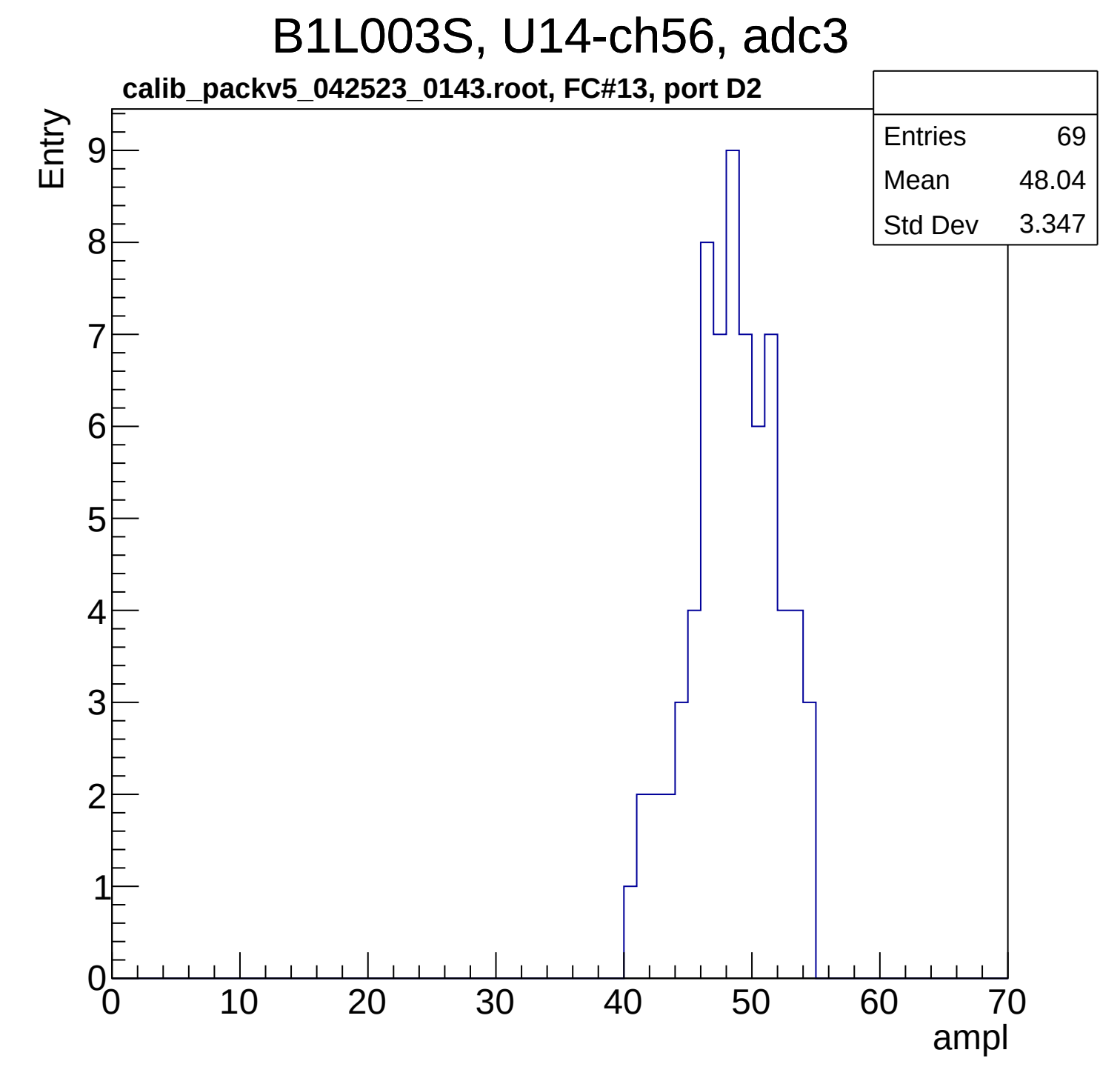

B1L003S, U14-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.71
Std Dev 21.58
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

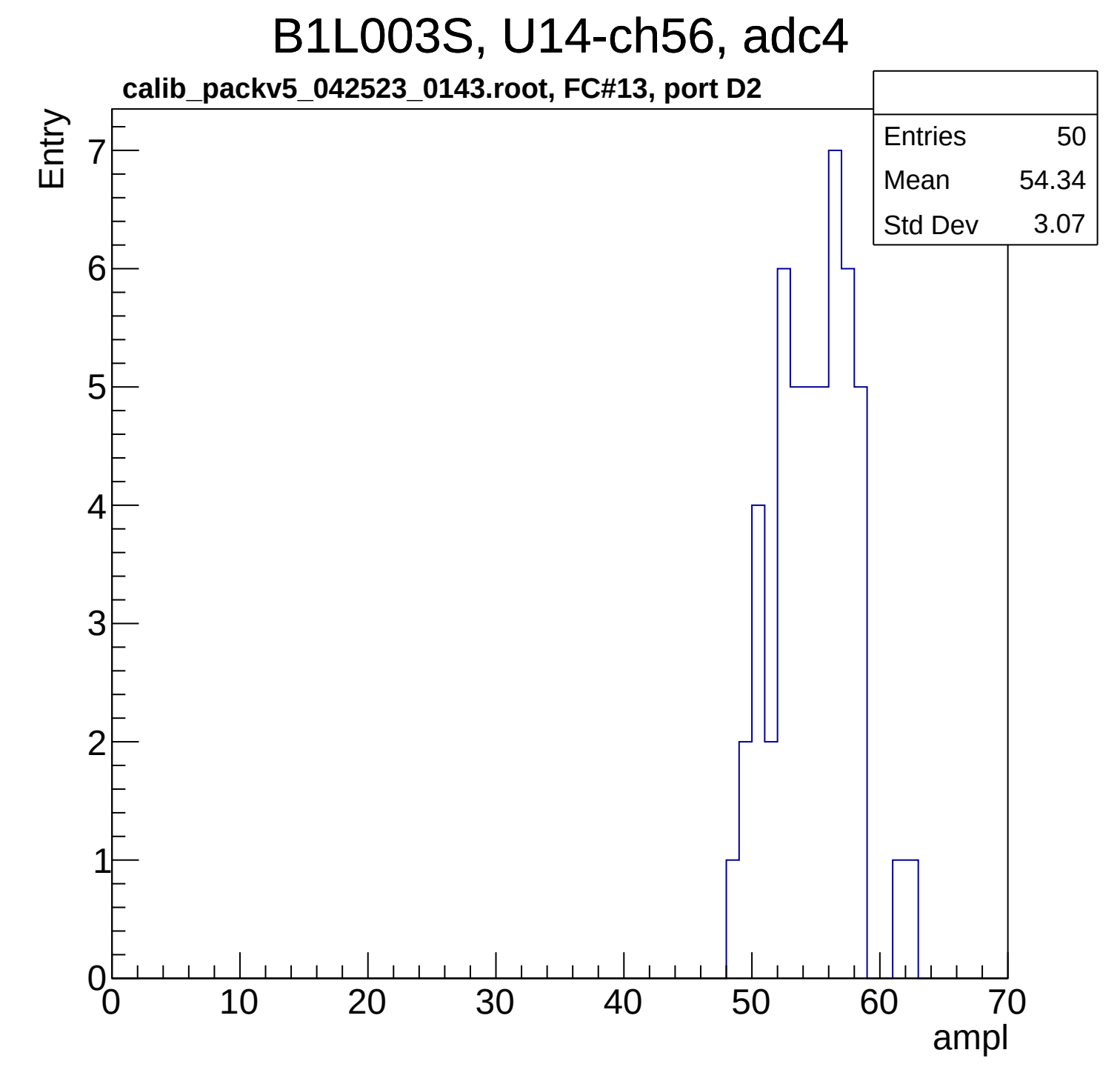

B1L003S, U14-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

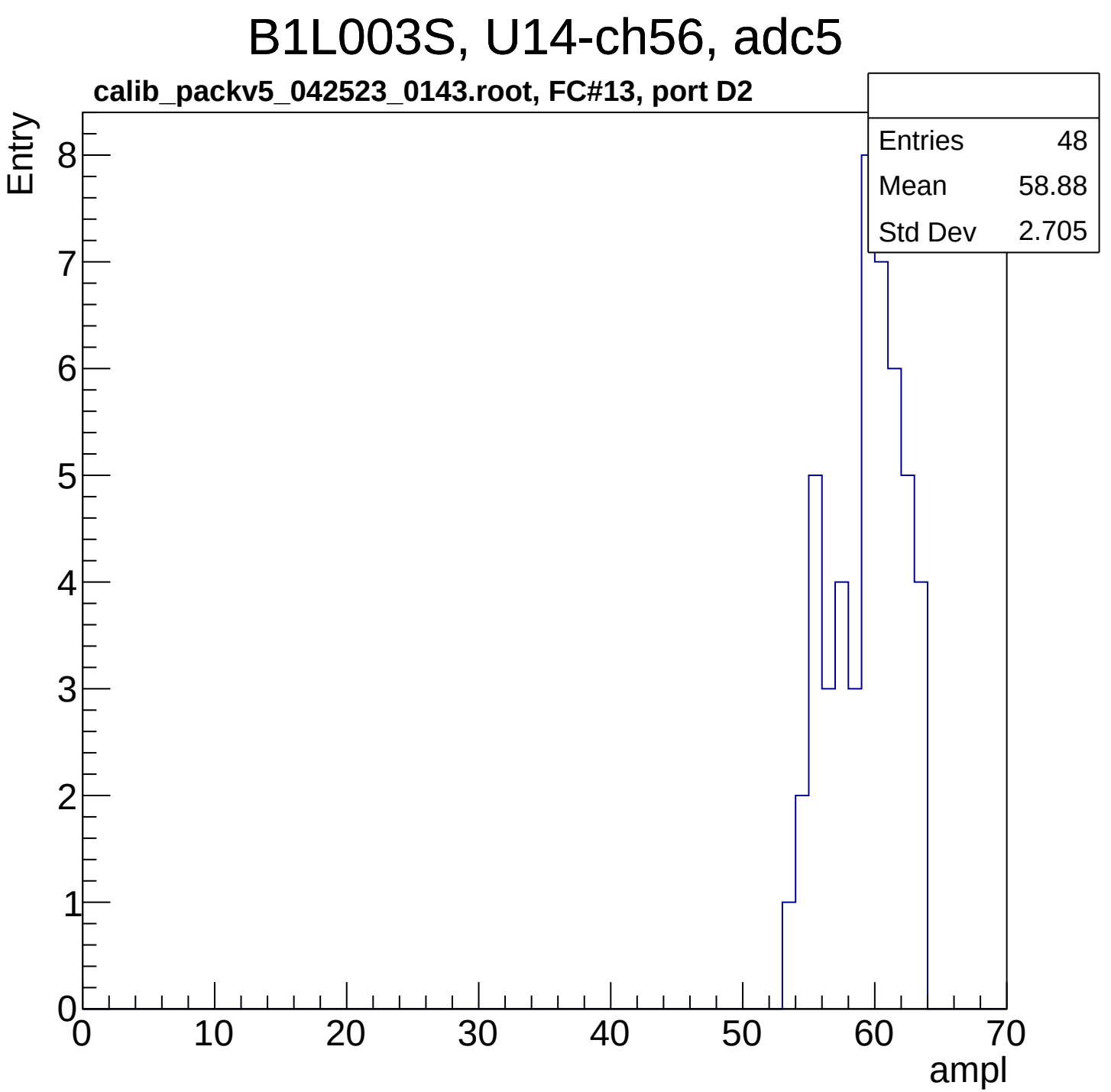

B1L003S, U14-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.51
Std Dev 4.871
Gaus mean : 30.6648
Gaus Width: 3.5358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

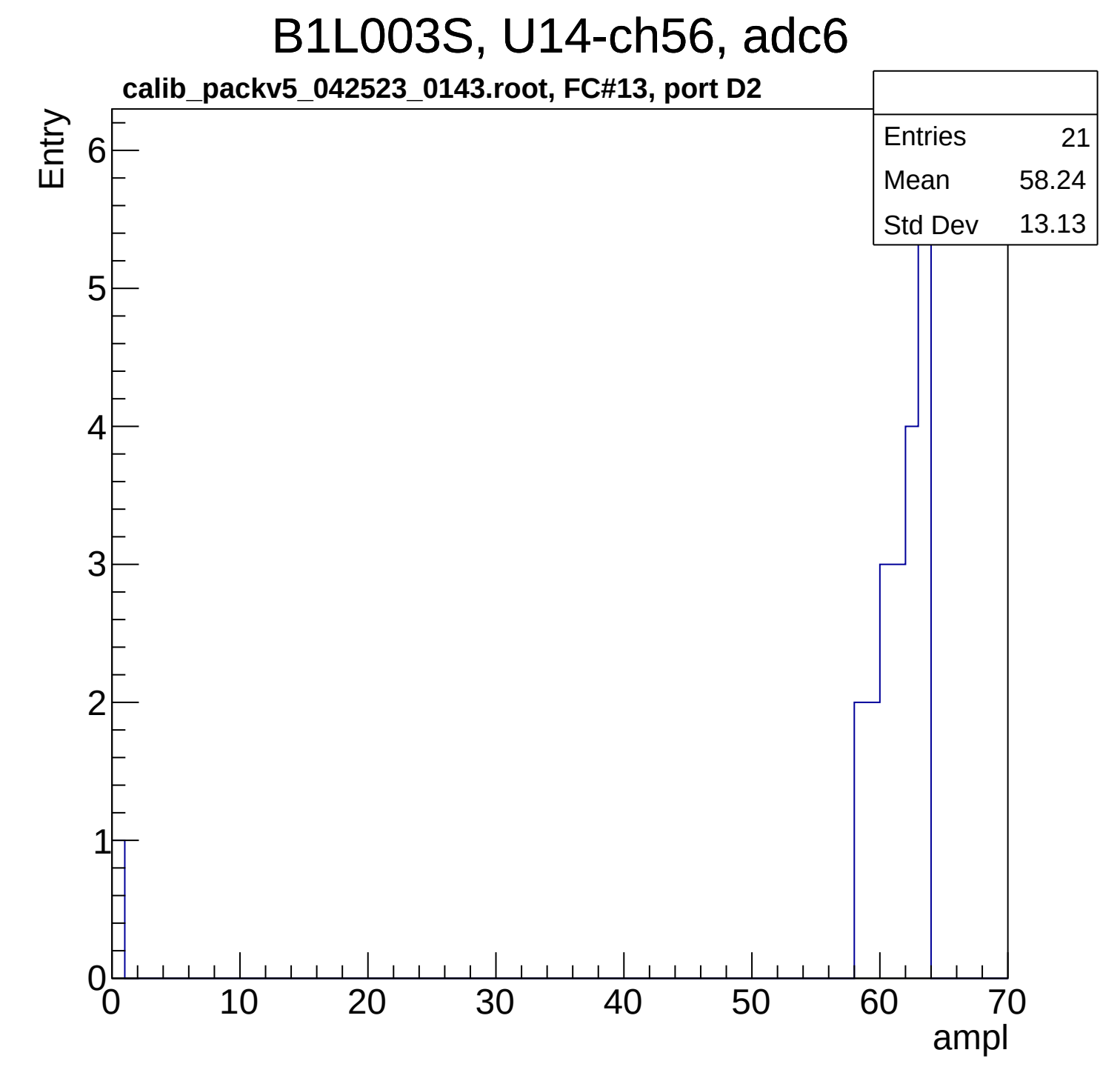

B1L003S, U14-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.7
Std Dev 3.355
Gaus mean : 37.3887
Gaus Width: 3.7078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

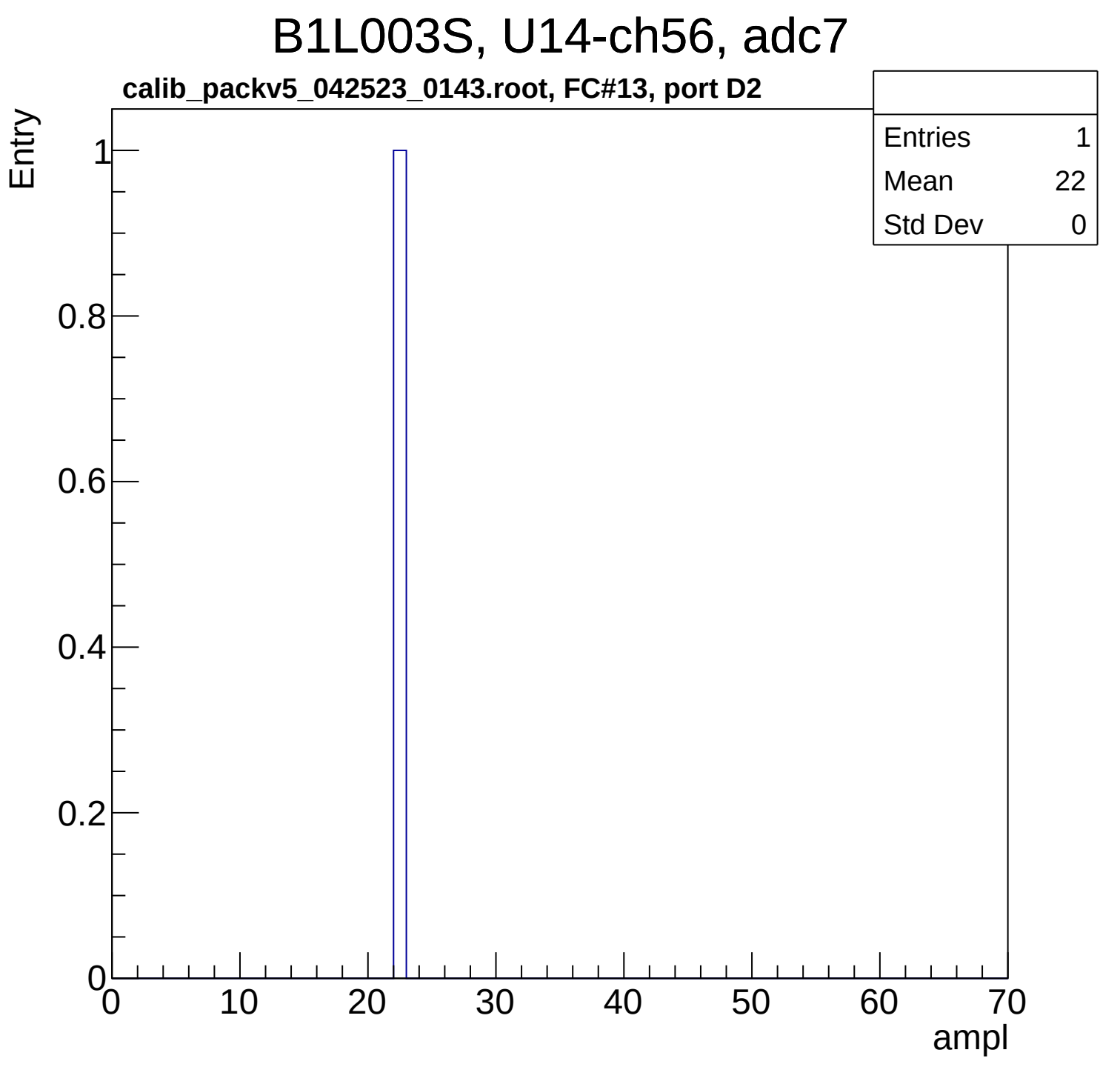

B1L003S, U14-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 42.39
Std Dev 3.331
Gaus mean : 43.0665
Gaus Width: 3.7891
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

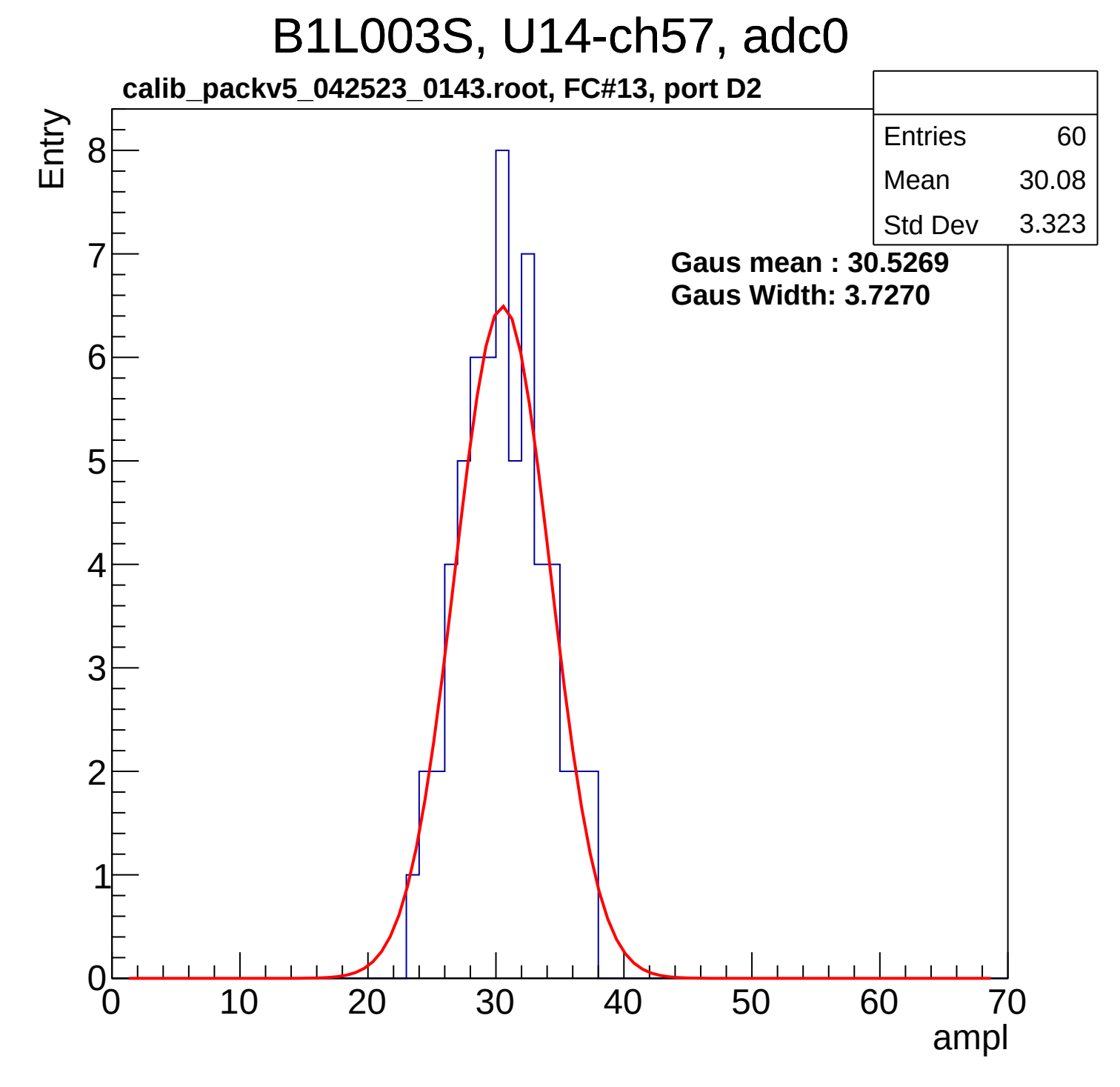

B1L003S, U14-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.04
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

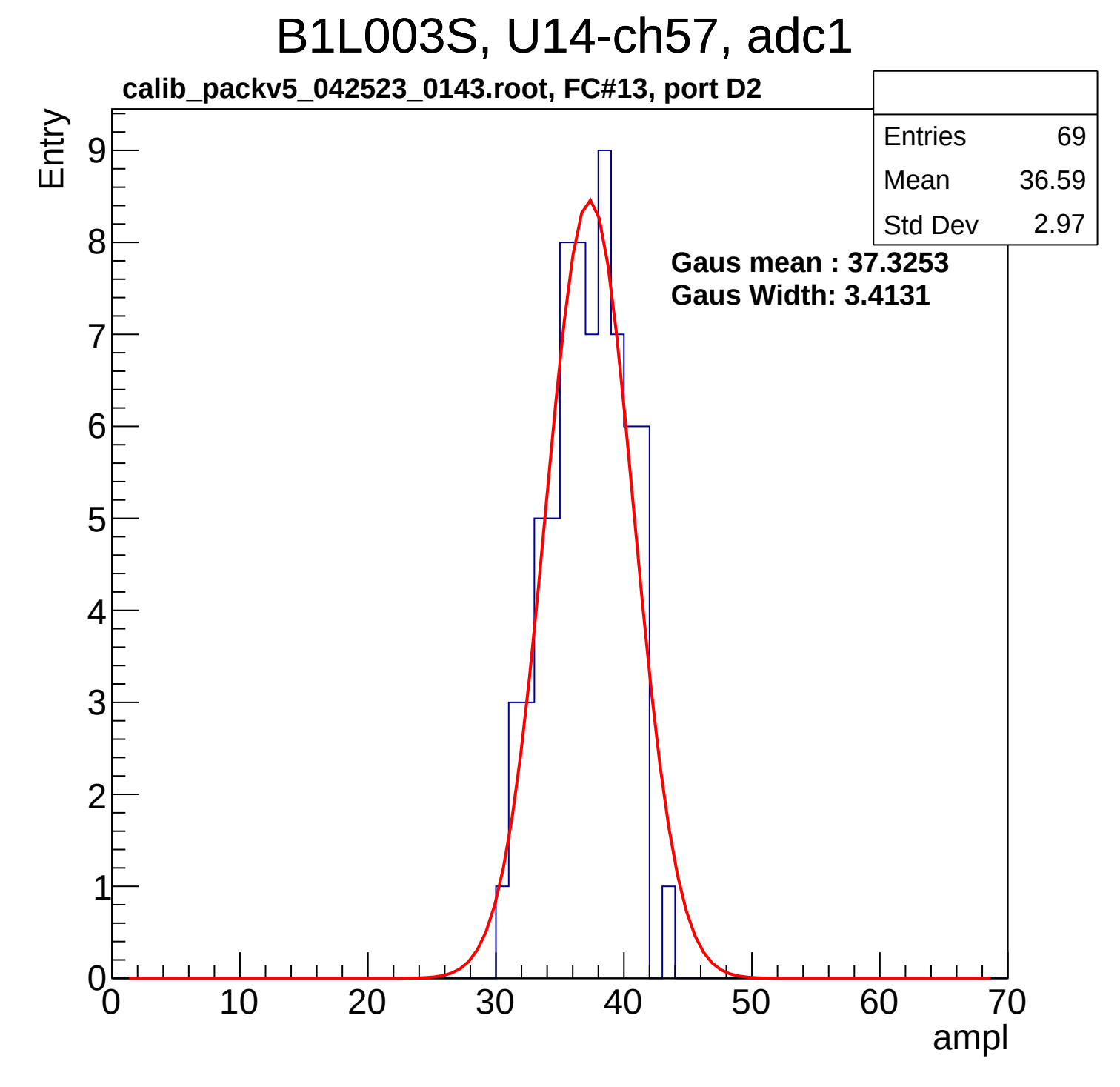

B1L003S, U14-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.34
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

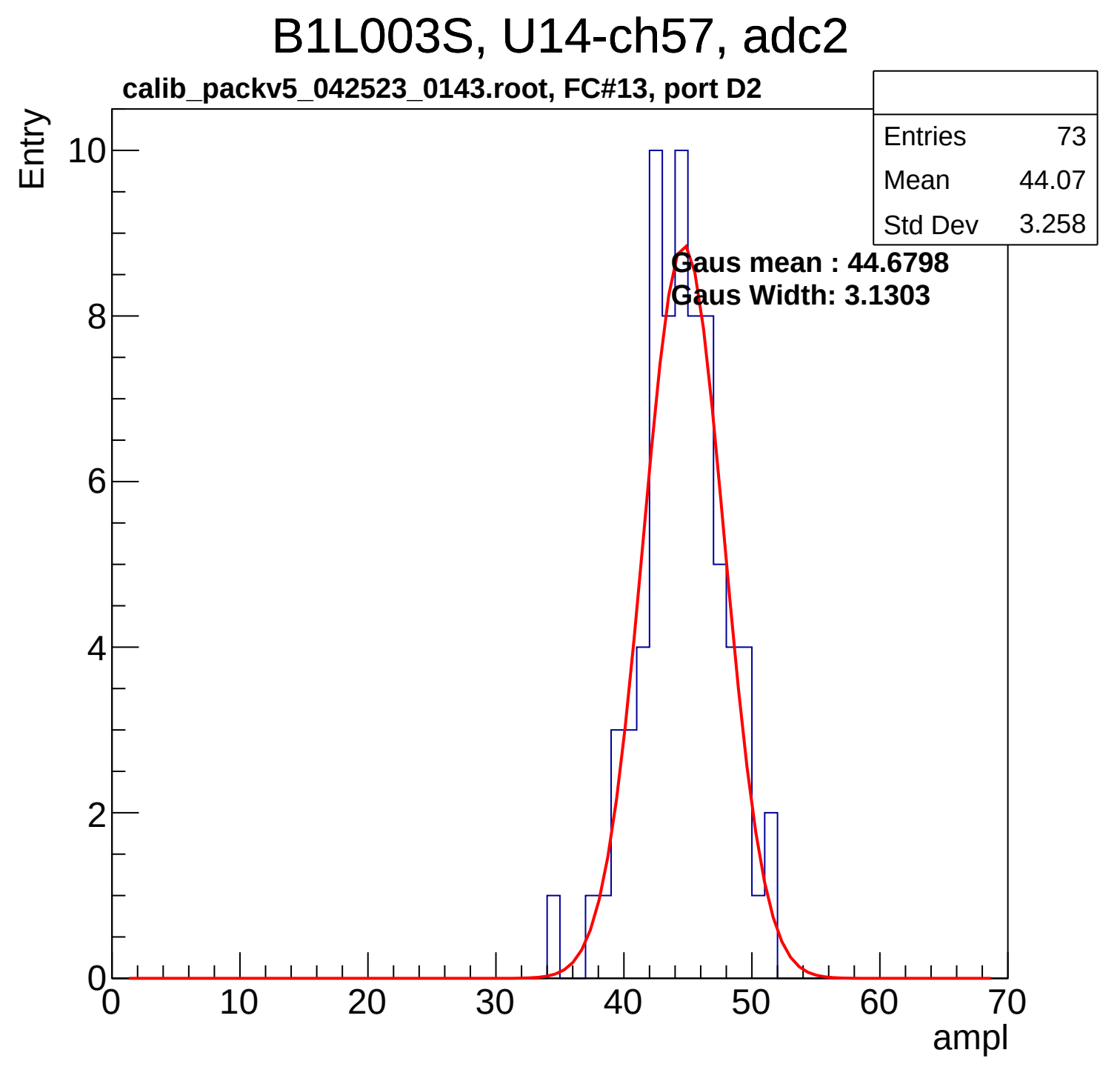

B1L003S, U14-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.88
Std Dev 2.705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

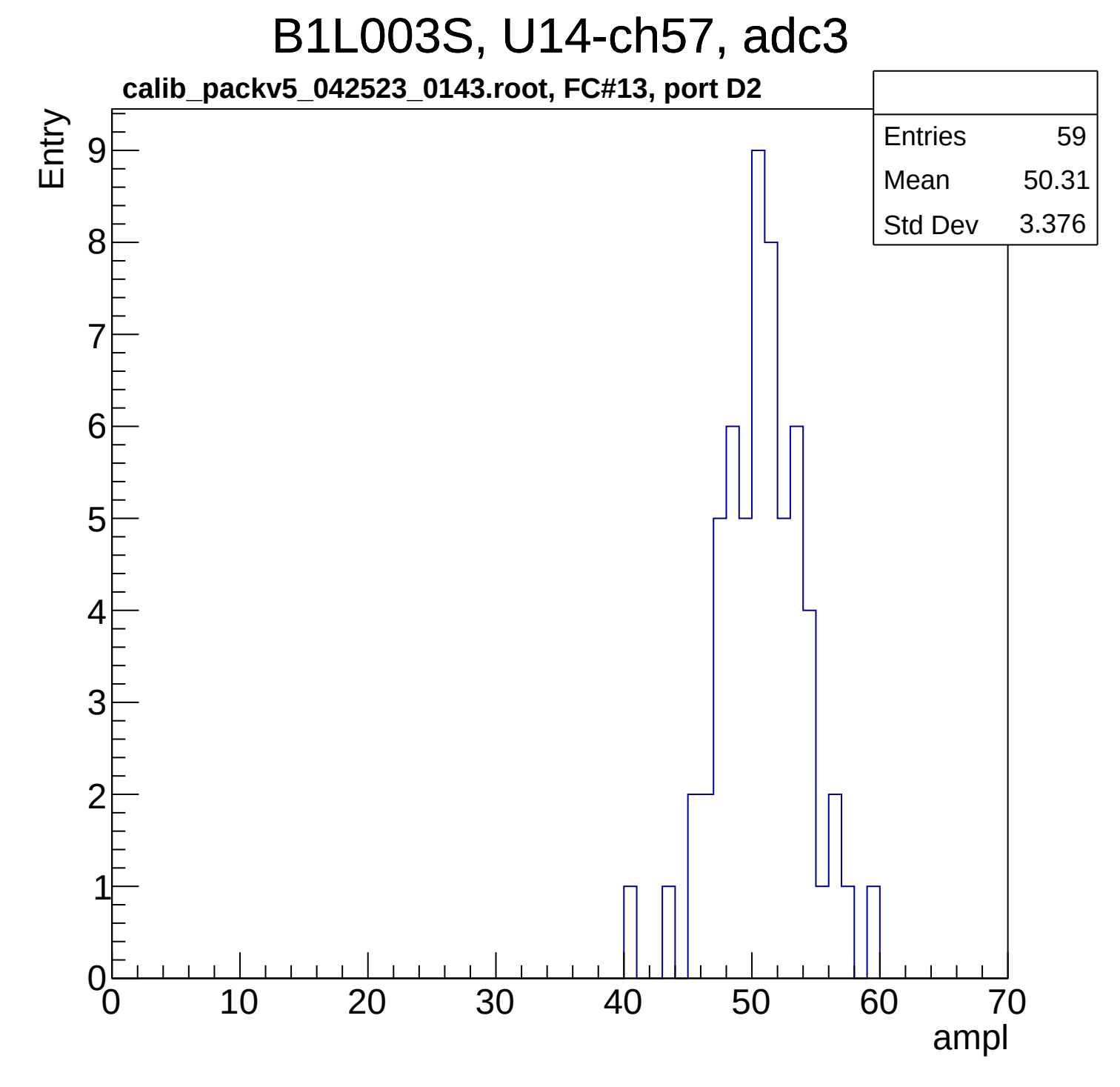

B1L003S, U14-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.24
Std Dev 13.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

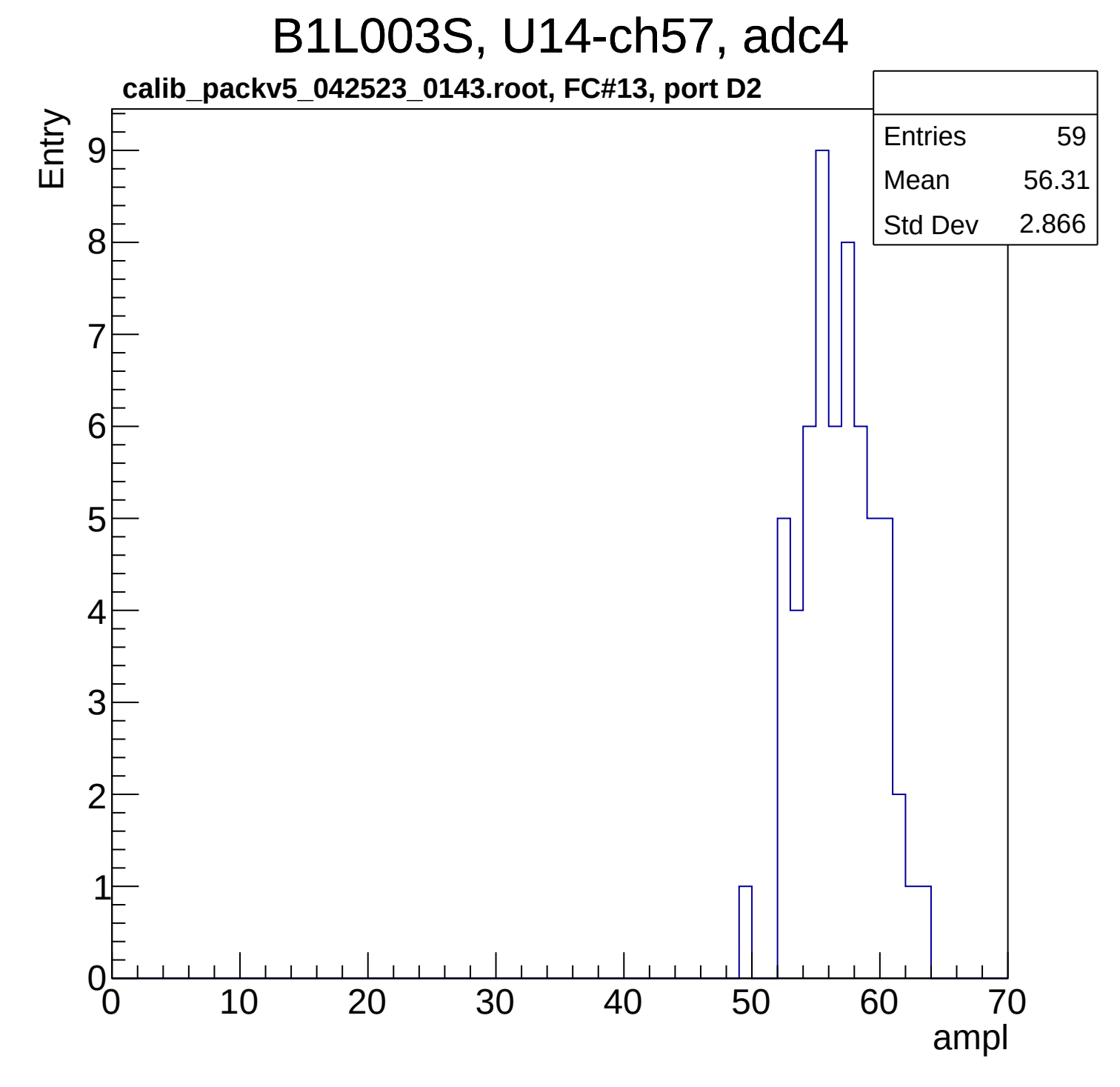

B1L003S, U14-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

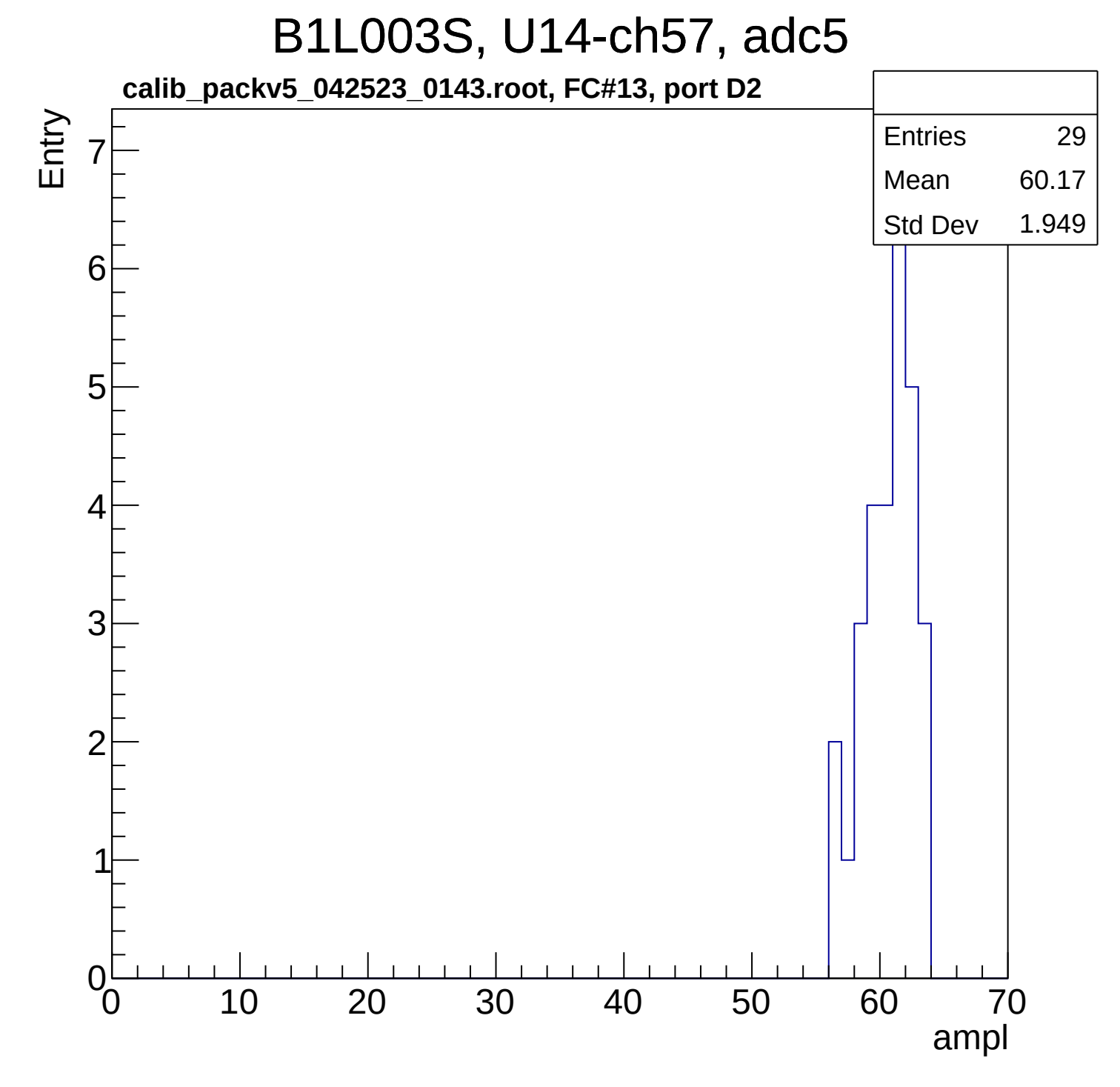

B1L003S, U14-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.08
Std Dev 3.323
Gaus mean : 30.5269
Gaus Width: 3.7270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

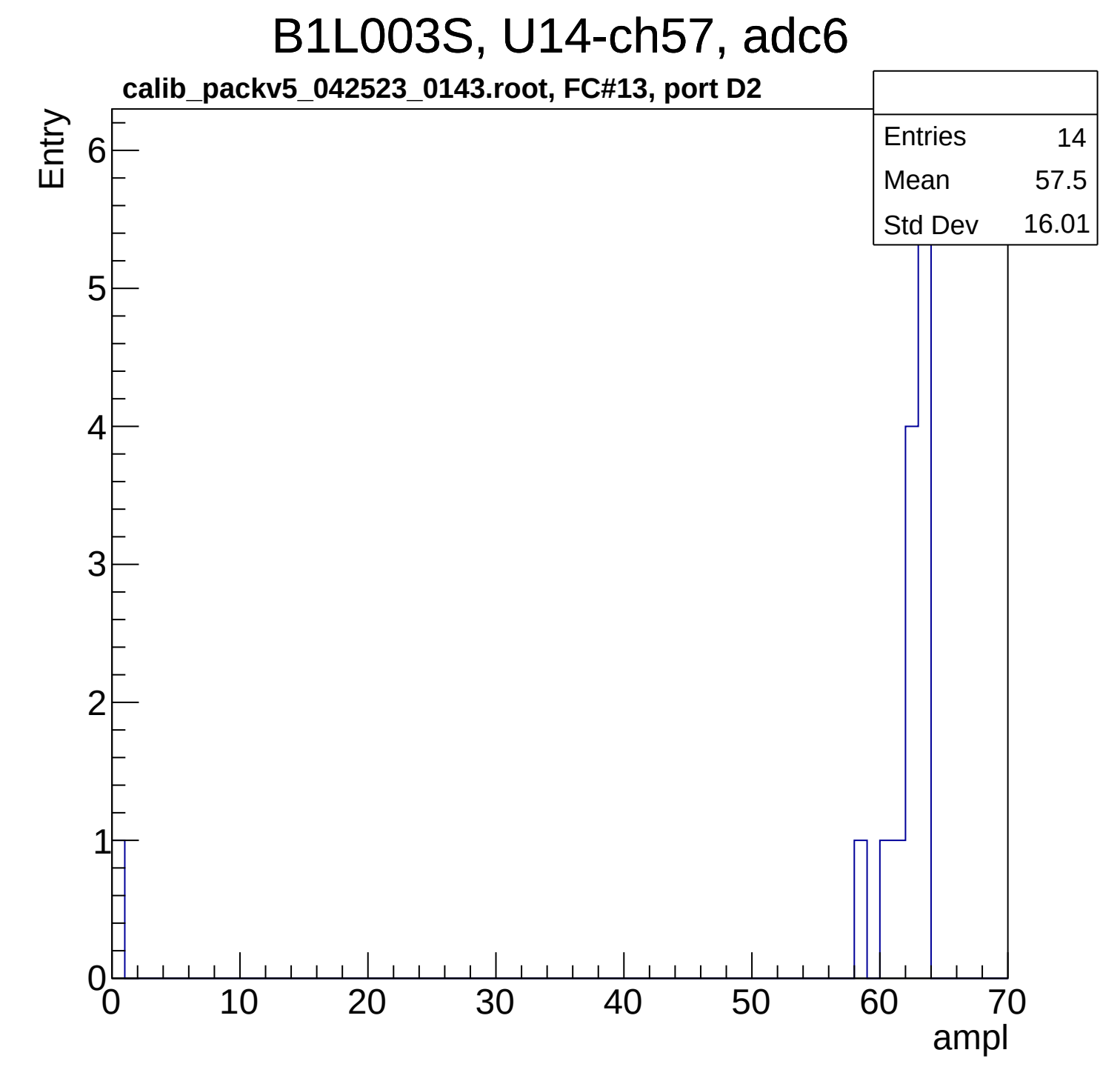

B1L003S, U14-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.59
Std Dev 2.97
Gaus mean : 37.3253
Gaus Width: 3.4131
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

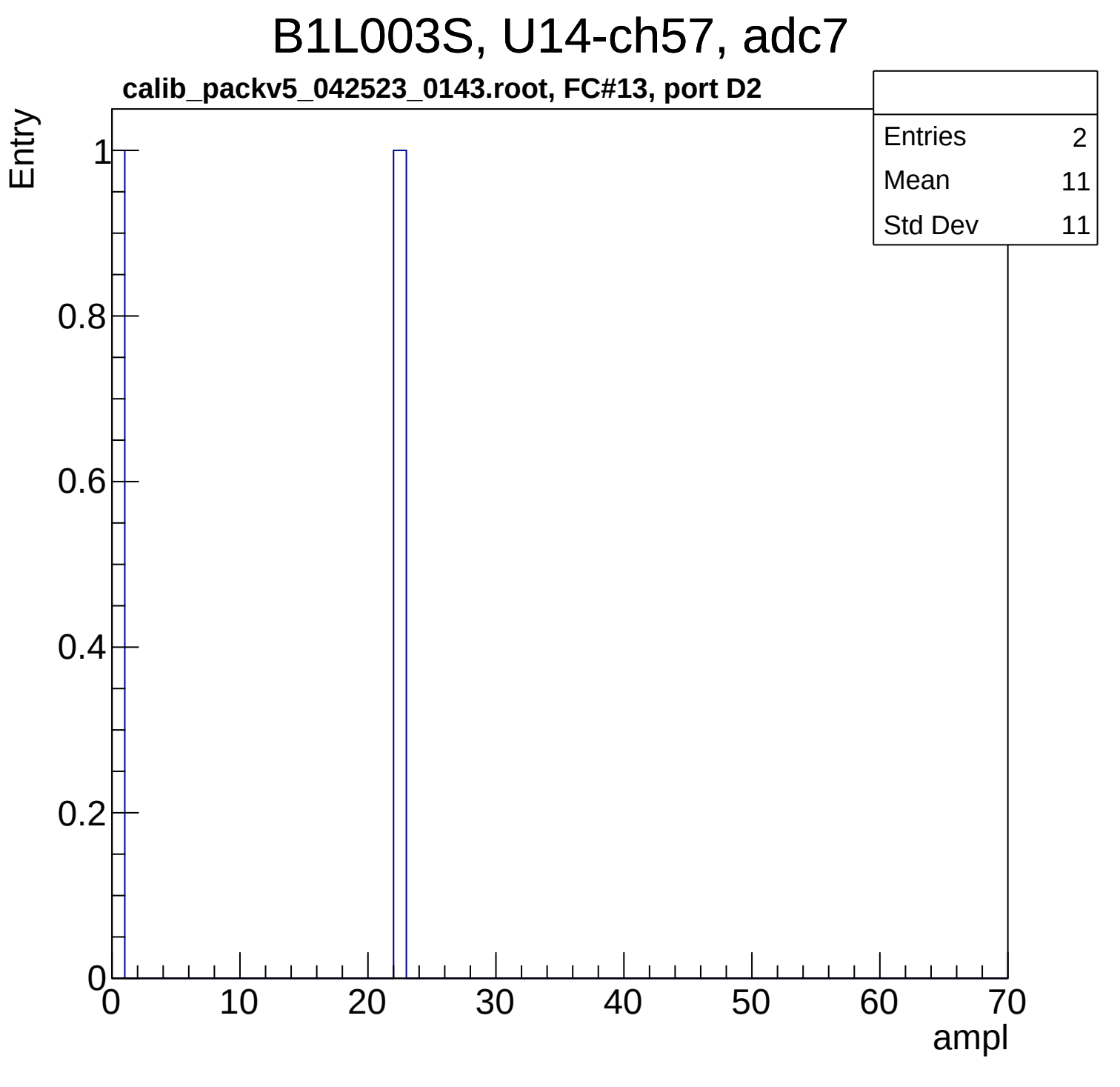

B1L003S, U14-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.07
Std Dev 3.258
Gaus mean : 44.6798
Gaus Width: 3.1303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

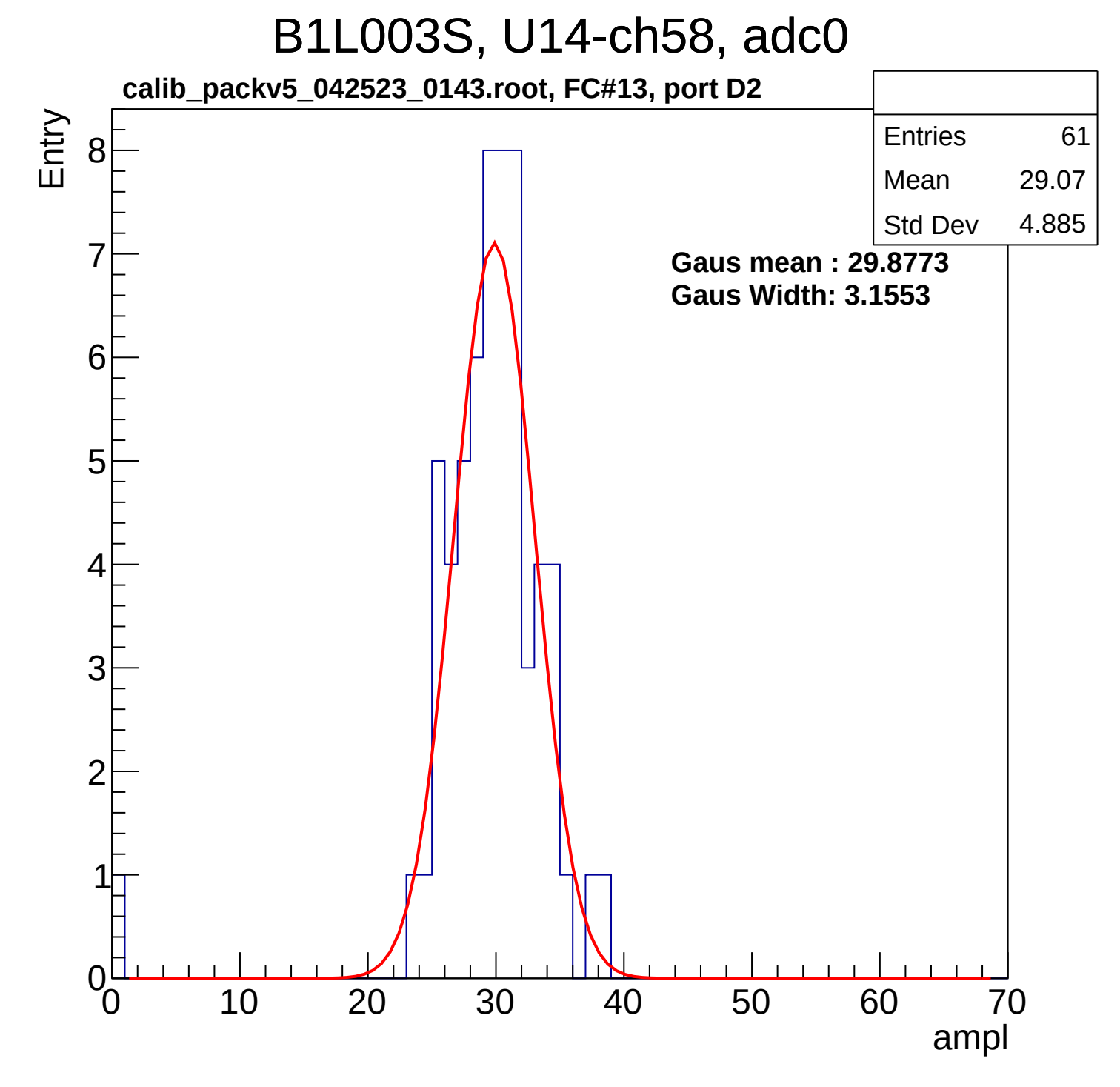

B1L003S, U14-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 50.31
Std Dev 3.376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

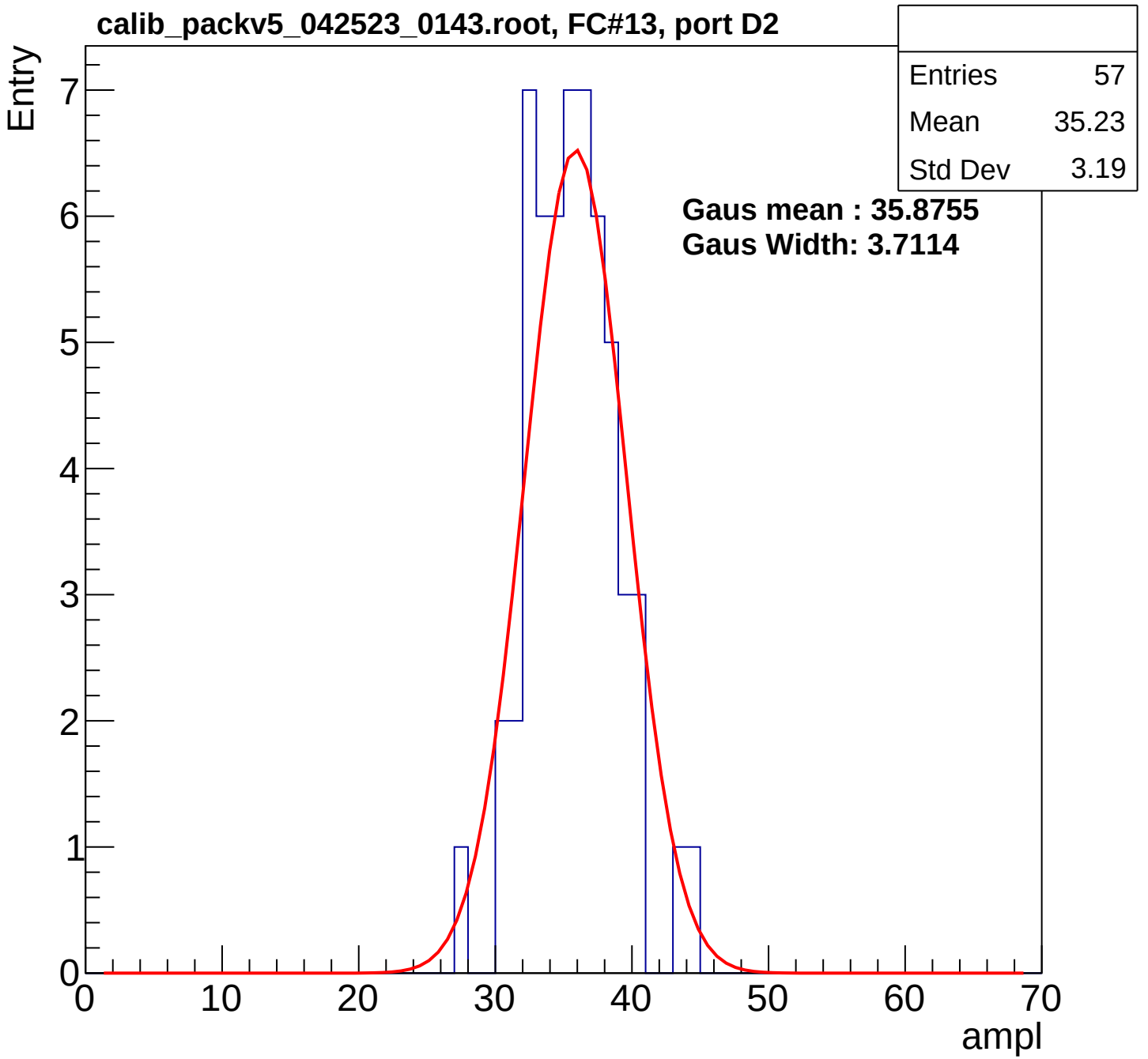

B1L003S, U14-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 56.31
Std Dev 2.866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

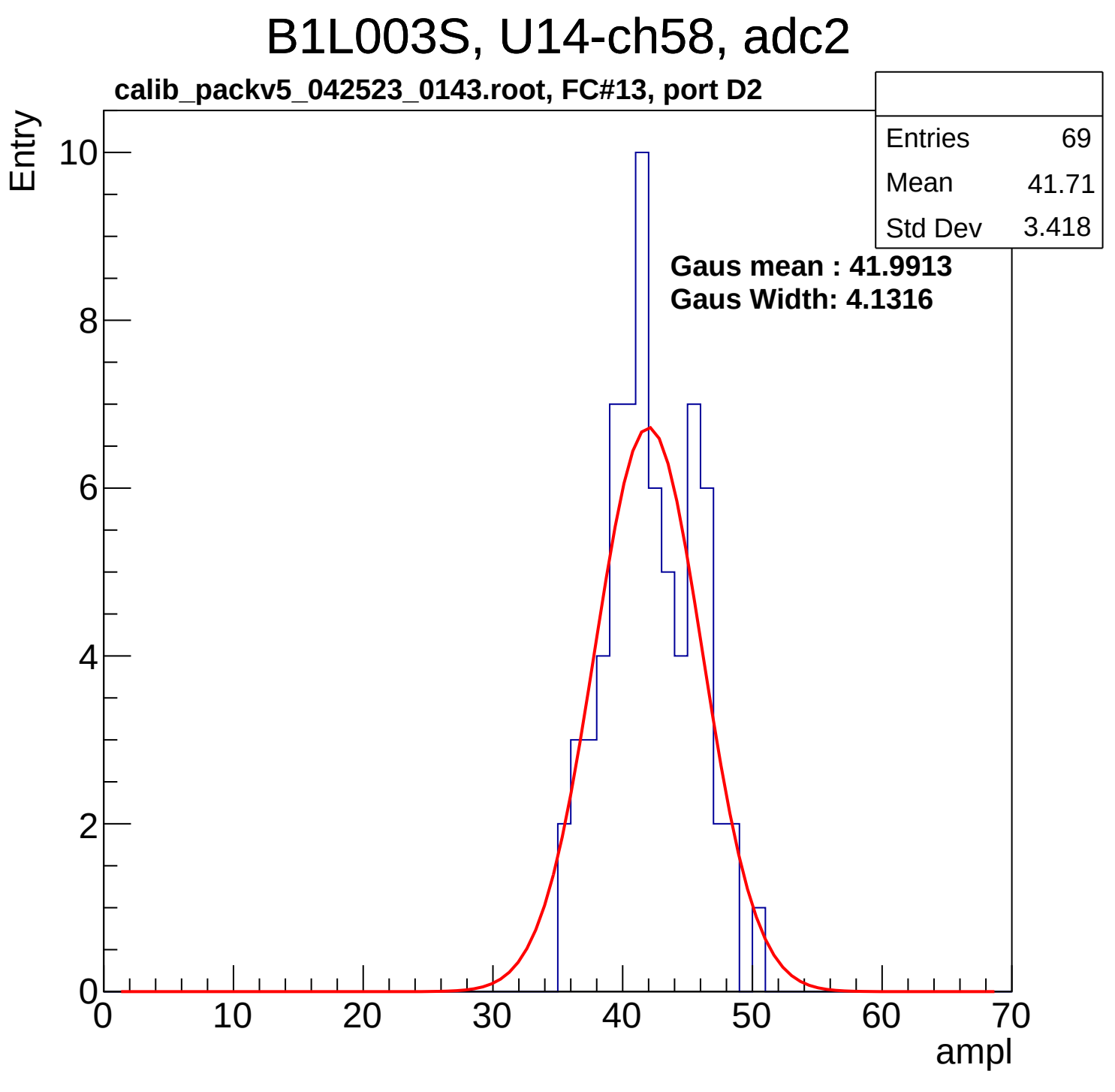

B1L003S, U14-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 29
Mean 60.17
Std Dev 1.949
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

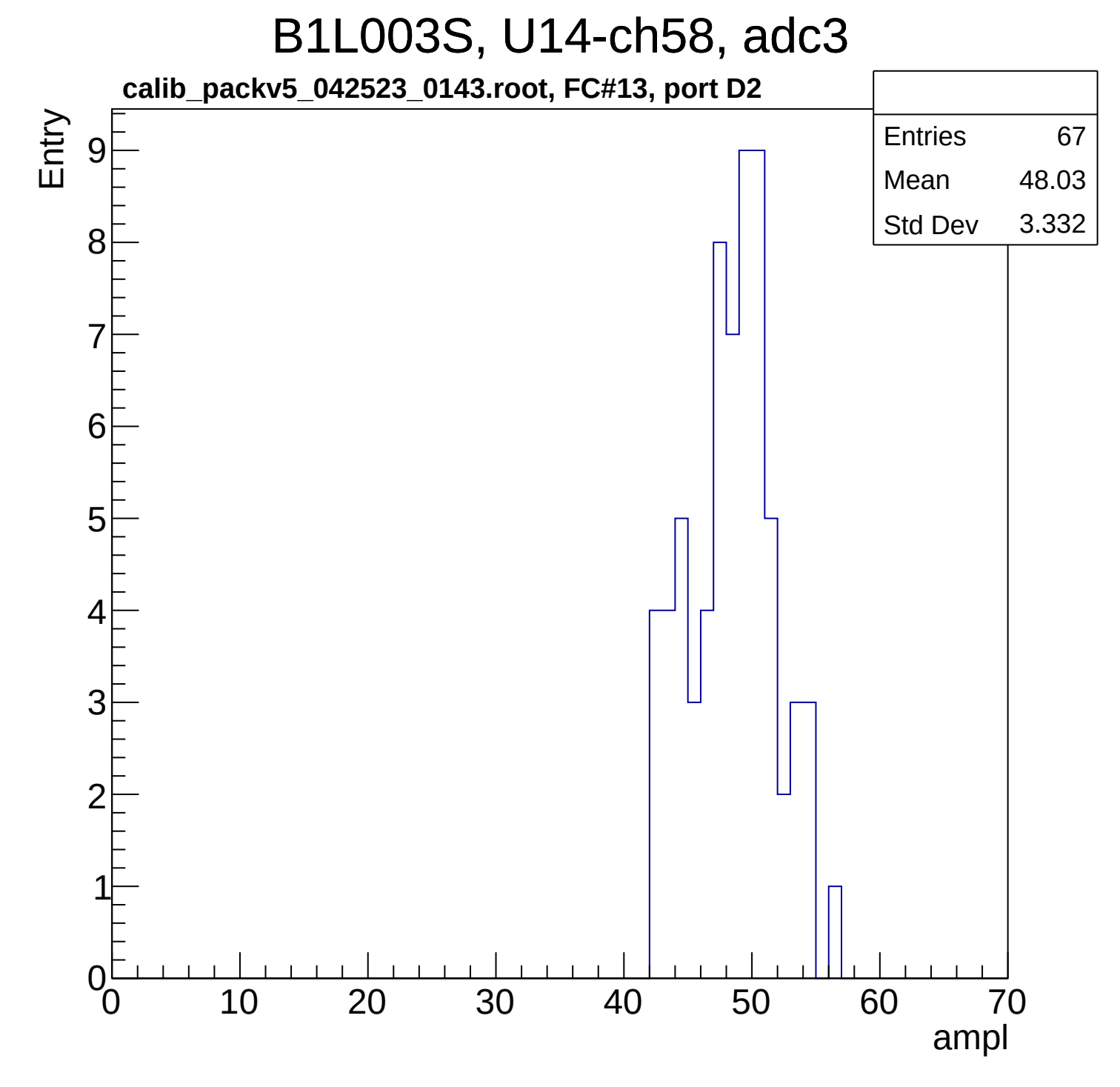

B1L003S, U14-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.5
Std Dev 16.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

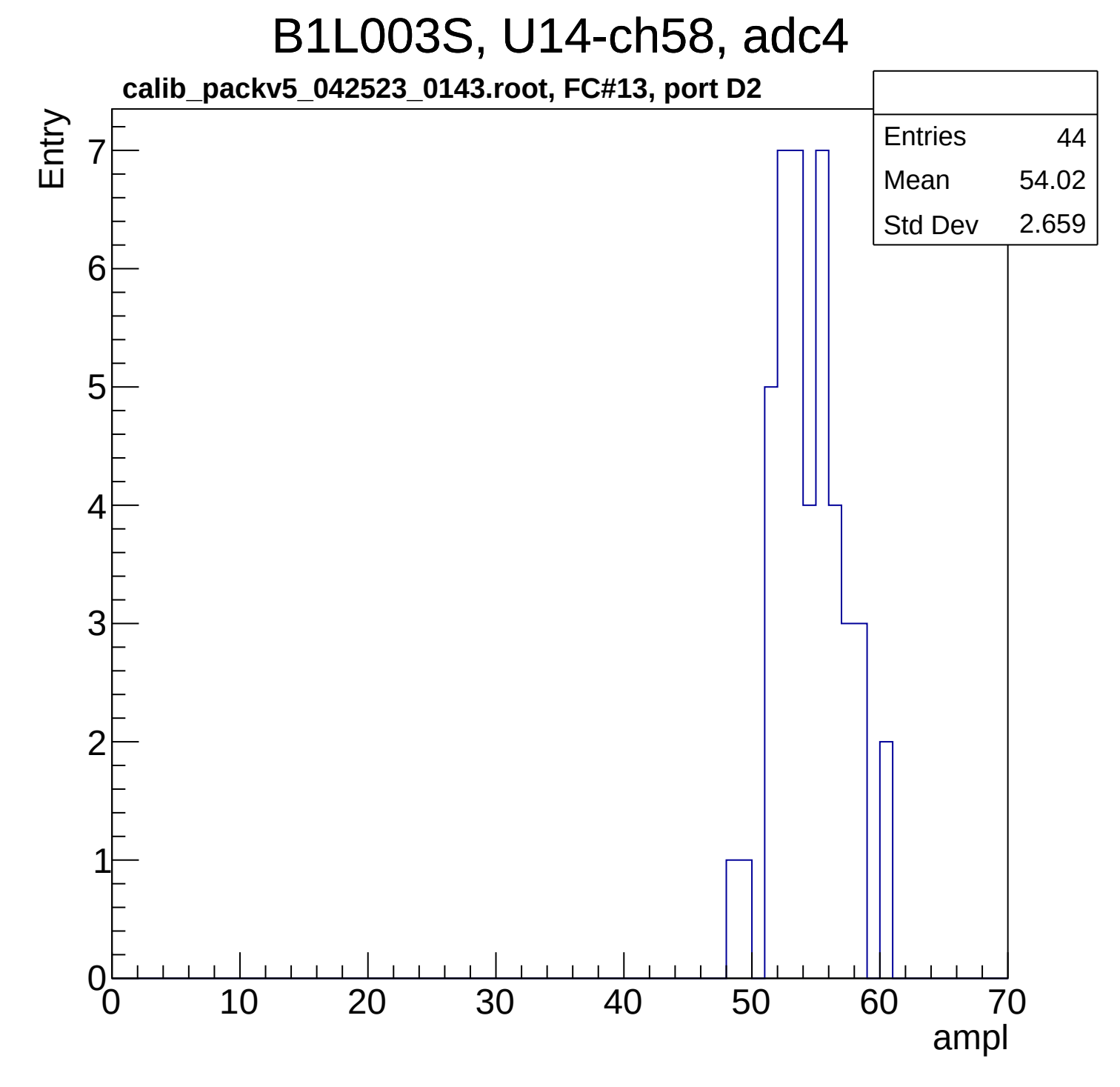

B1L003S, U14-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

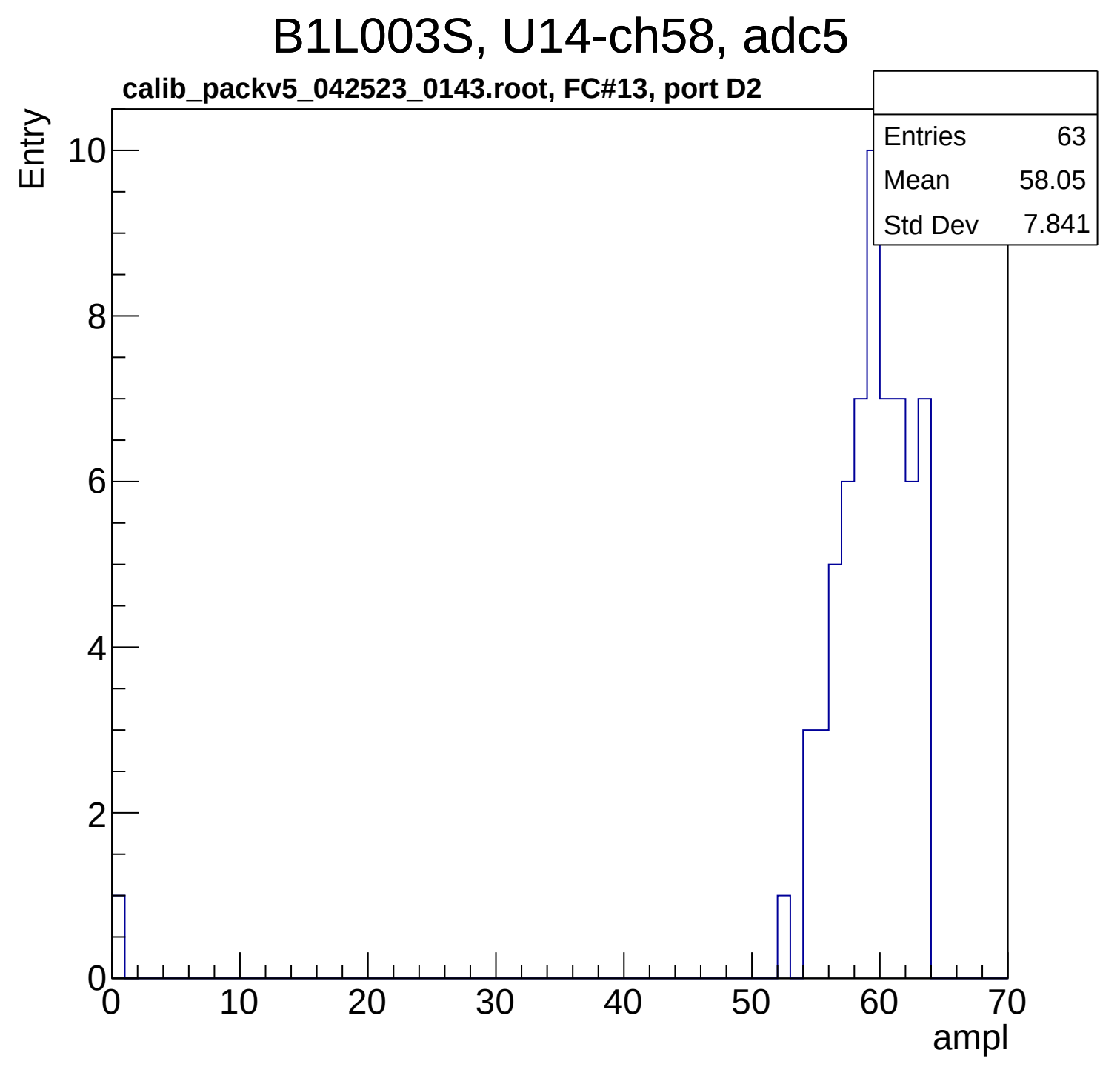

B1L003S, U14-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.07
Std Dev 4.885
Gaus mean : 29.8773
Gaus Width: 3.1553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

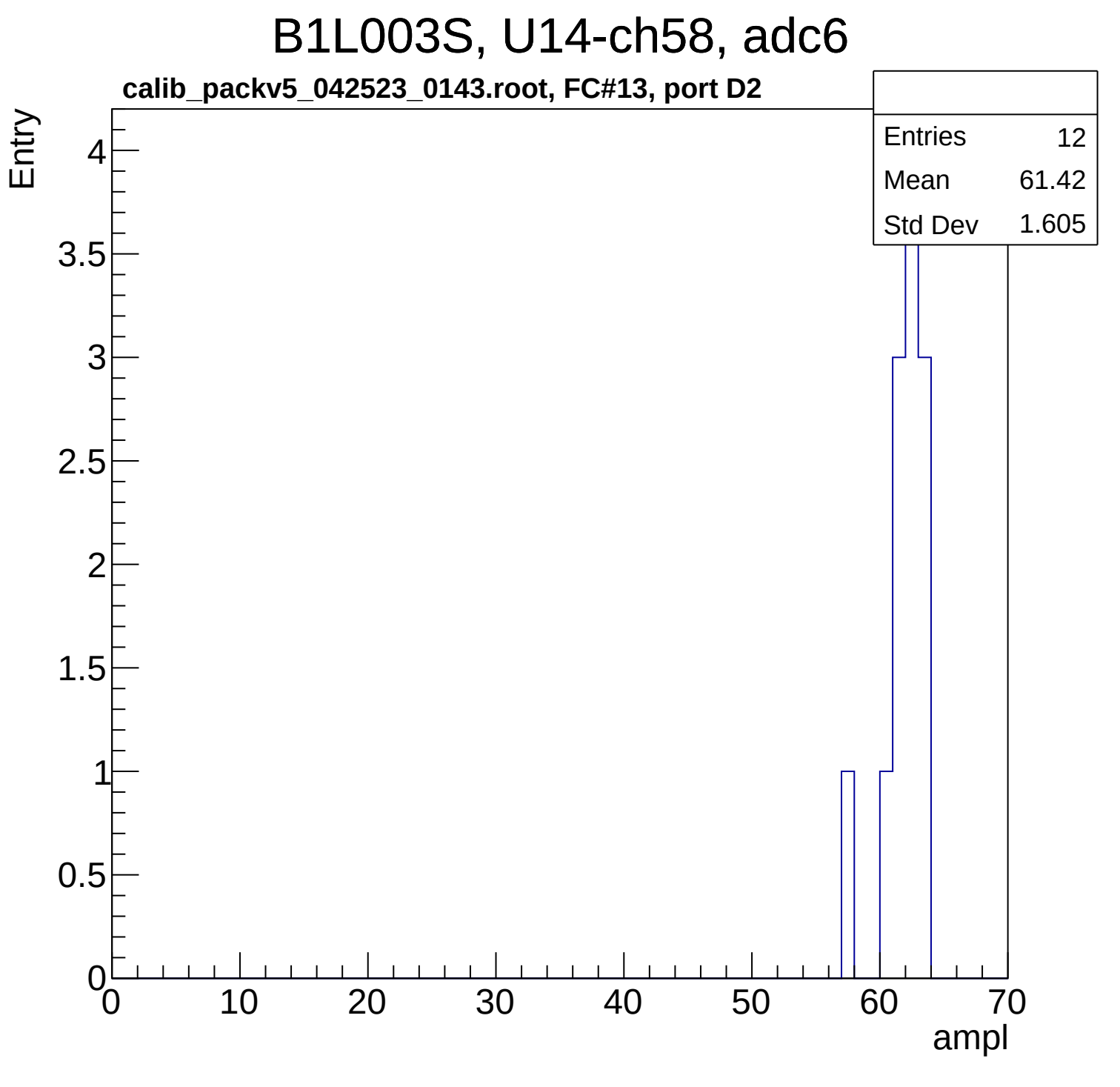

B1L003S, U14-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.23
Std Dev 3.19
Gaus mean : 35.8755
Gaus Width: 3.7114
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

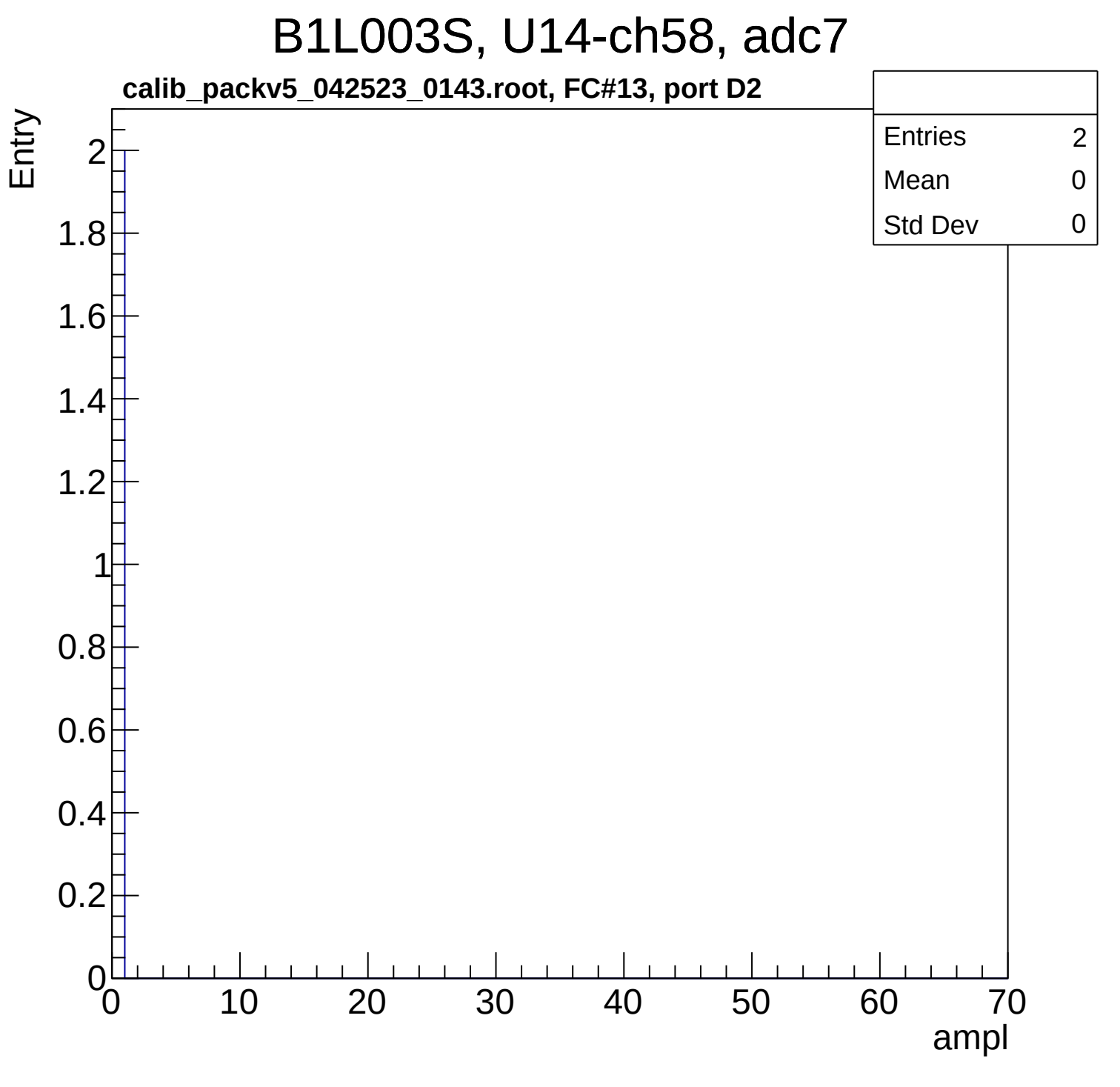

B1L003S, U14-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.71
Std Dev 3.418
Gaus mean : 41.9913
Gaus Width: 4.1316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

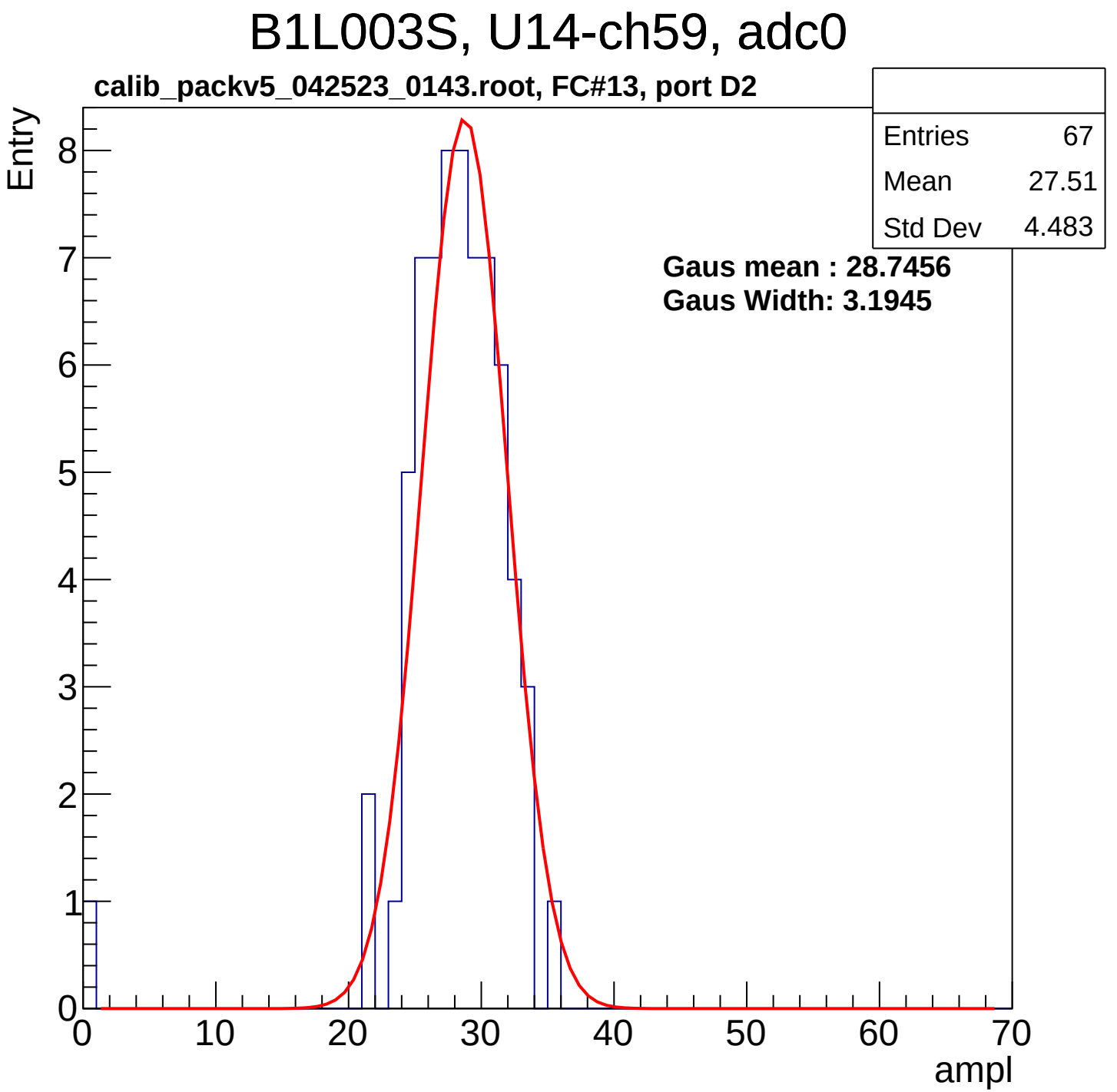

B1L003S, U14-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.03
Std Dev 3.332
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

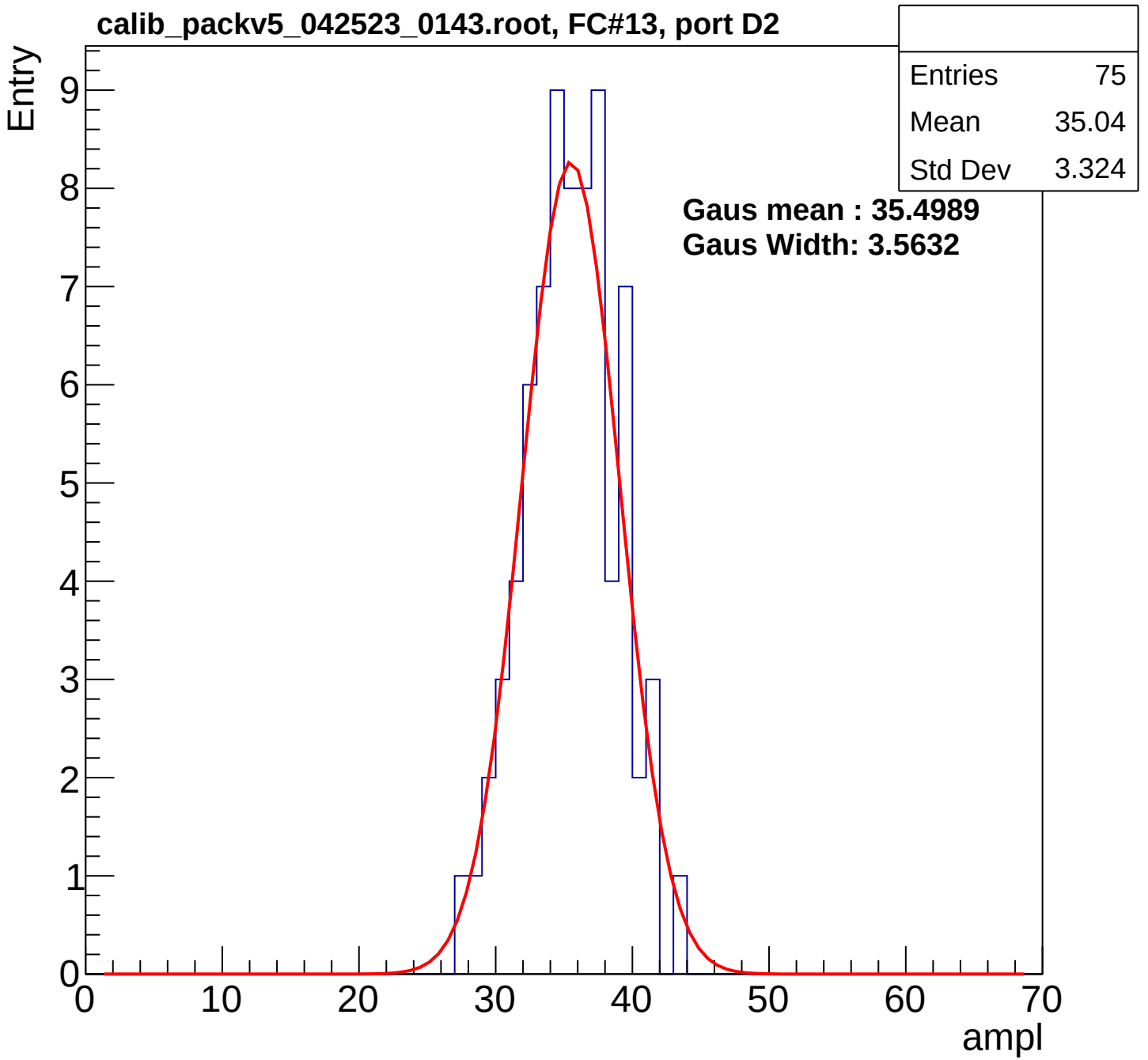

B1L003S, U14-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 54.02
Std Dev 2.659
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

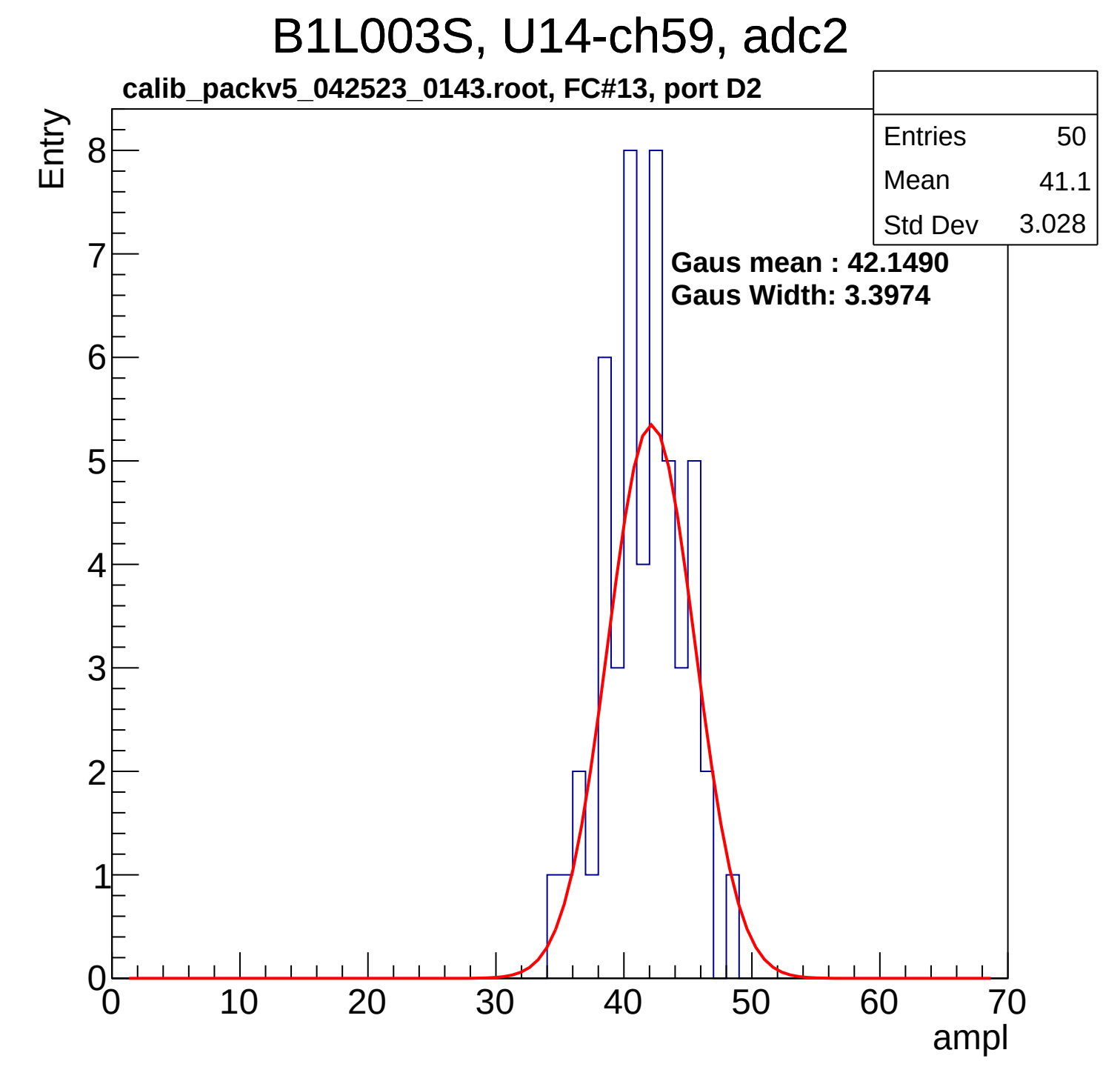

B1L003S, U14-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.05
Std Dev 7.841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

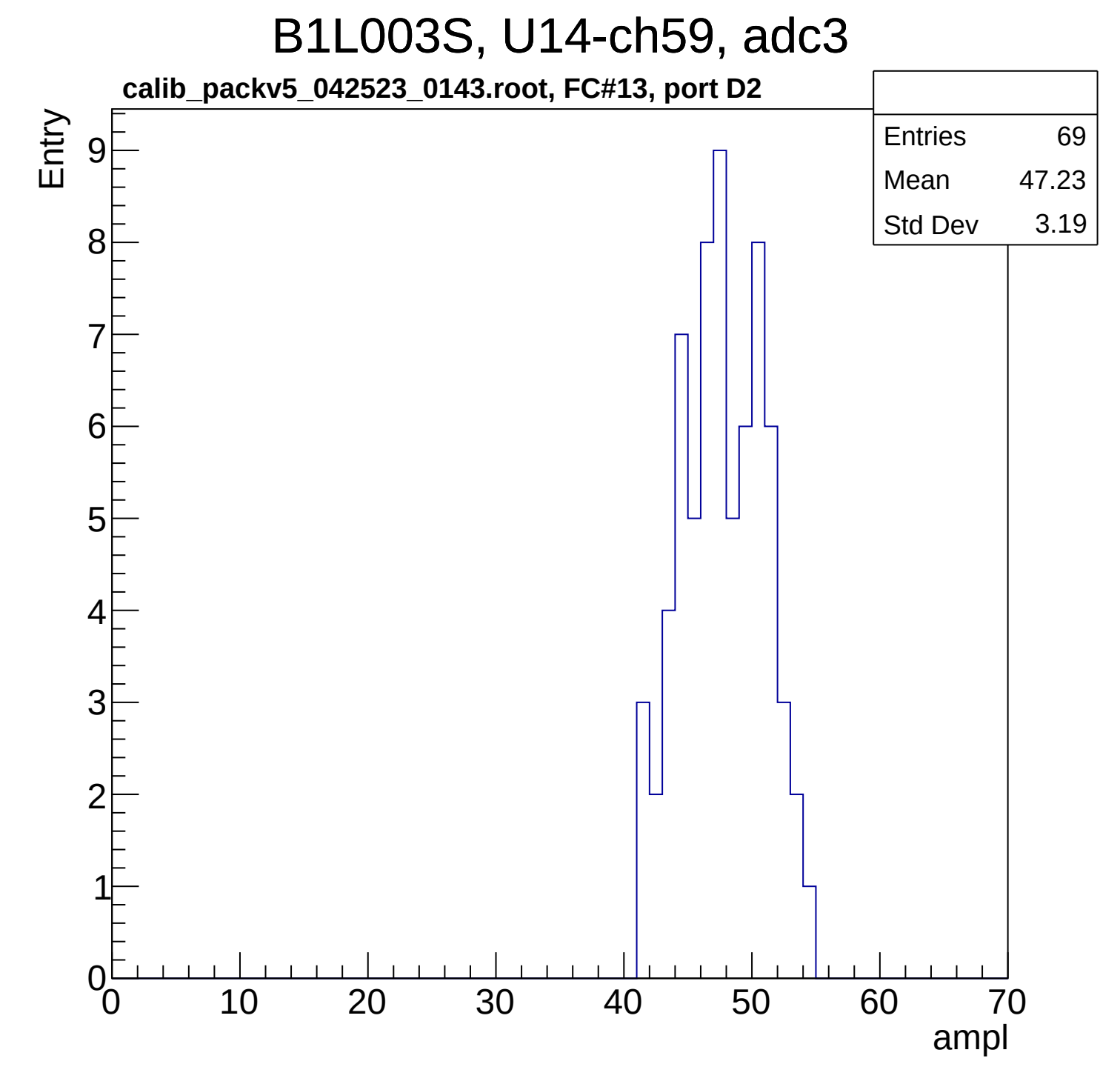

B1L003S, U14-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.42
Std Dev 1.605
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

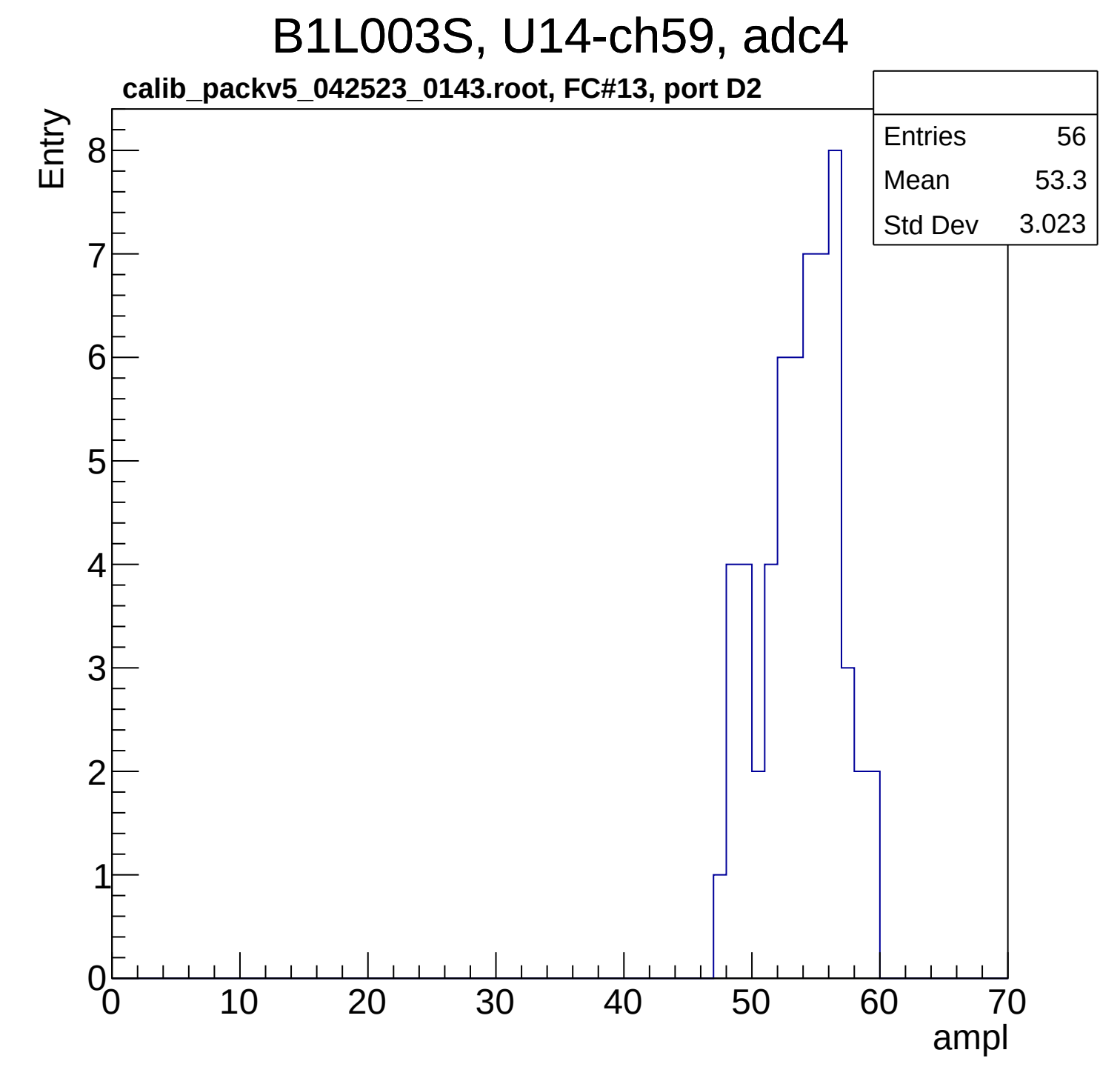

B1L003S, U14-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

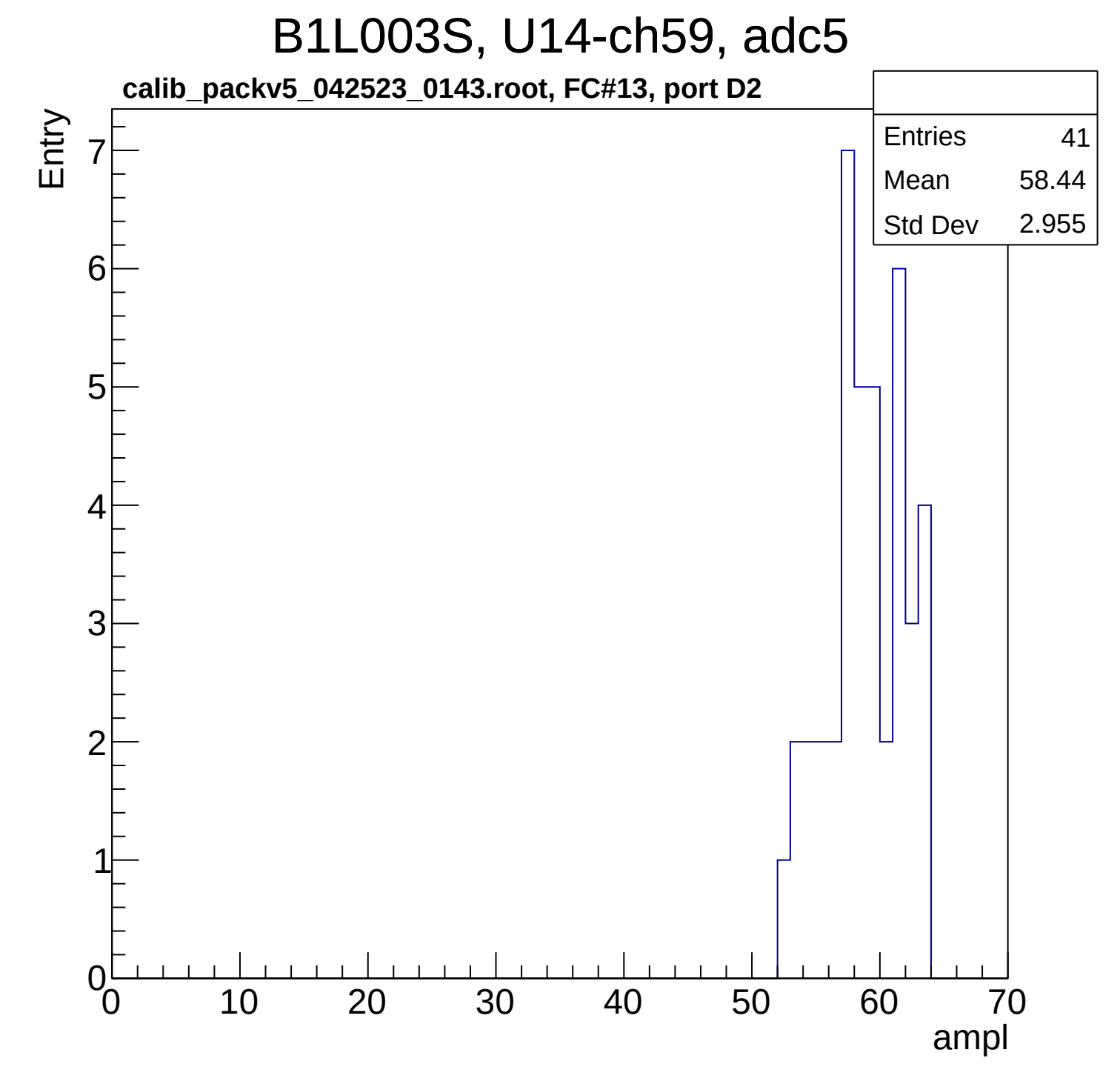

B1L003S, U14-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 27.51
Std Dev 4.483
Gaus mean : 28.7456
Gaus Width: 3.1945
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

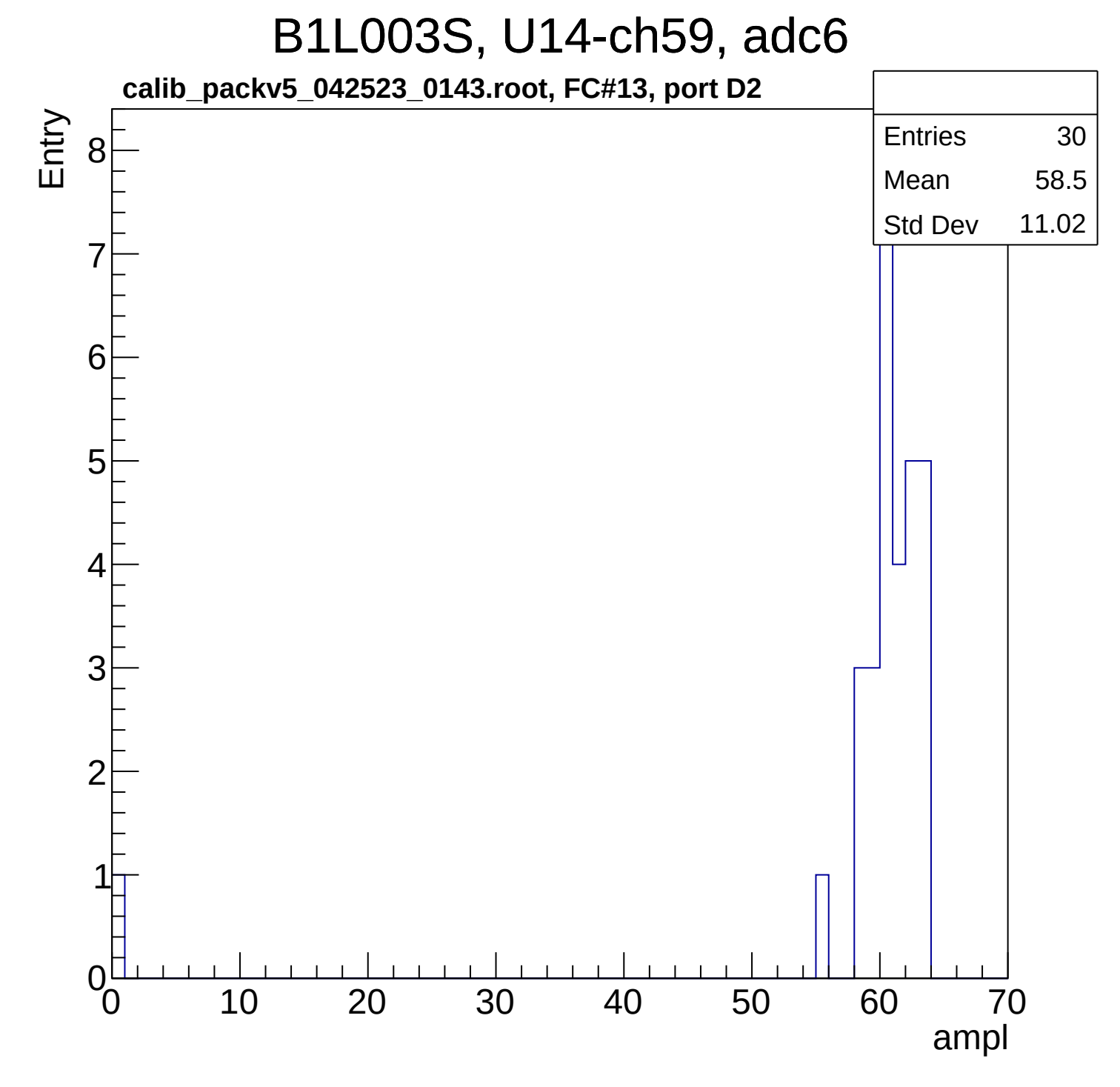

B1L003S, U14-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.04
Std Dev 3.324
Gaus mean : 35.4989
Gaus Width: 3.5632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

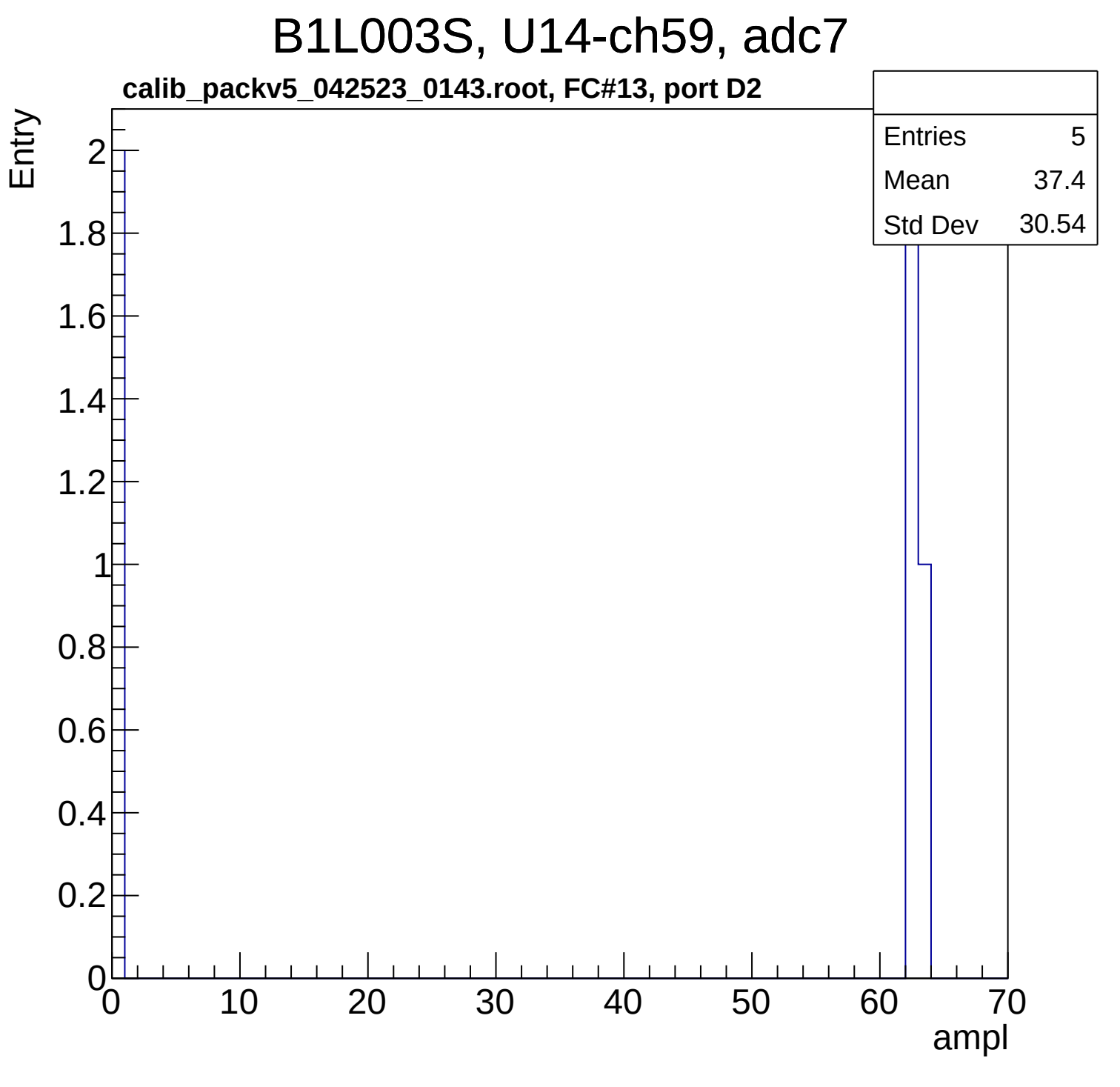

B1L003S, U14-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.1
Std Dev 3.028
Gaus mean : 42.1490
Gaus Width: 3.3974
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

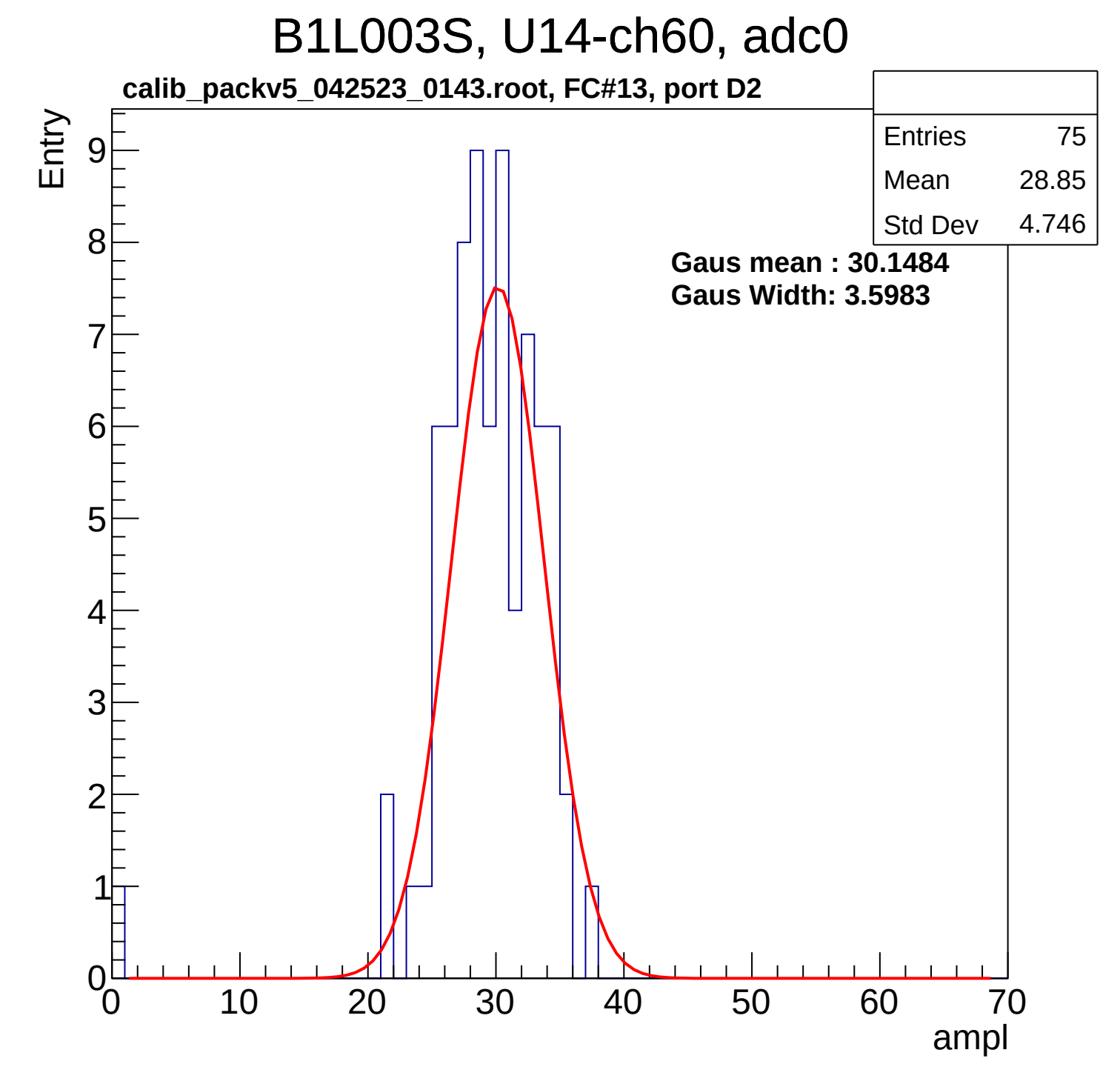

B1L003S, U14-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 47.23
Std Dev 3.19
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

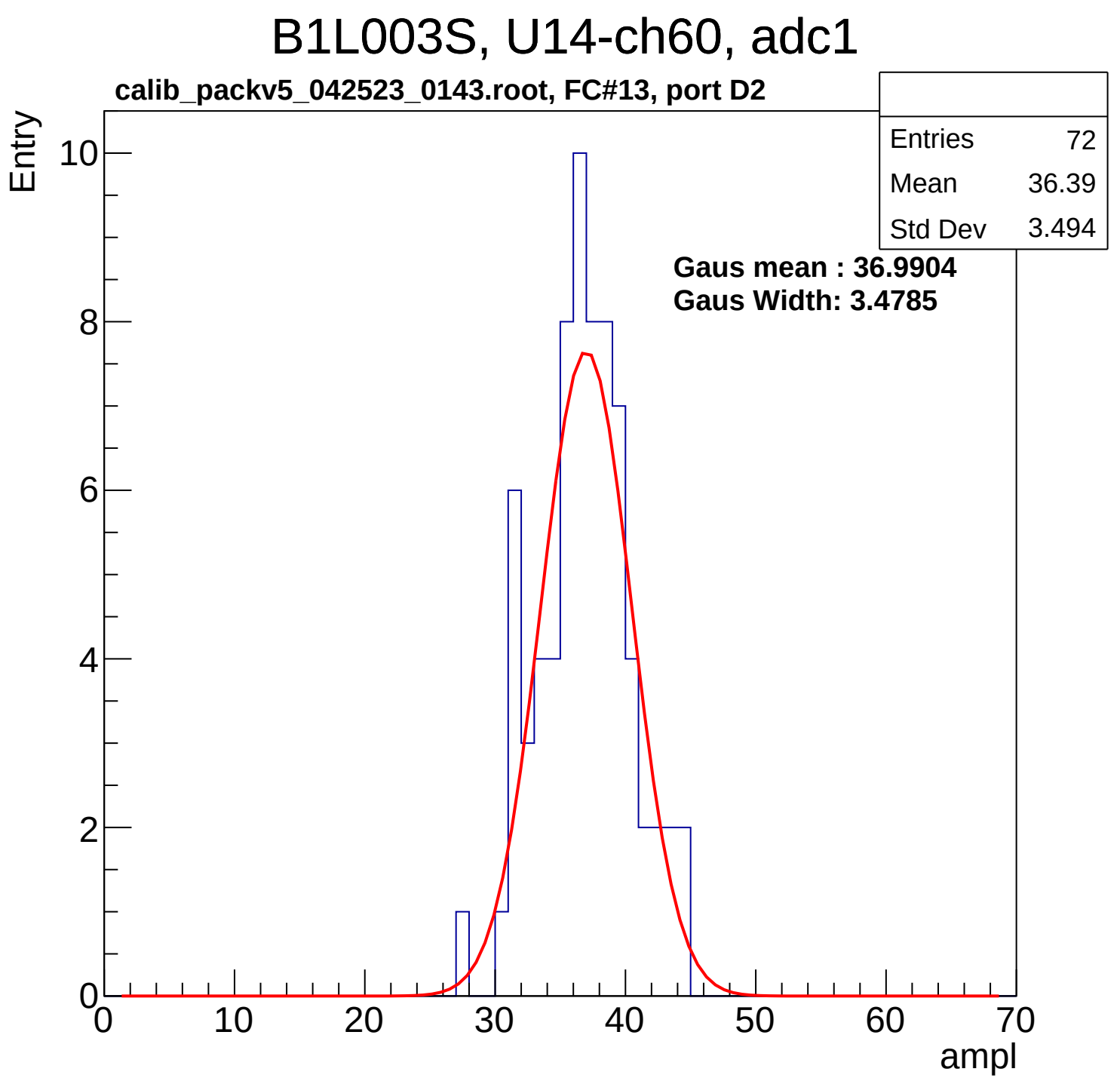

B1L003S, U14-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.3
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

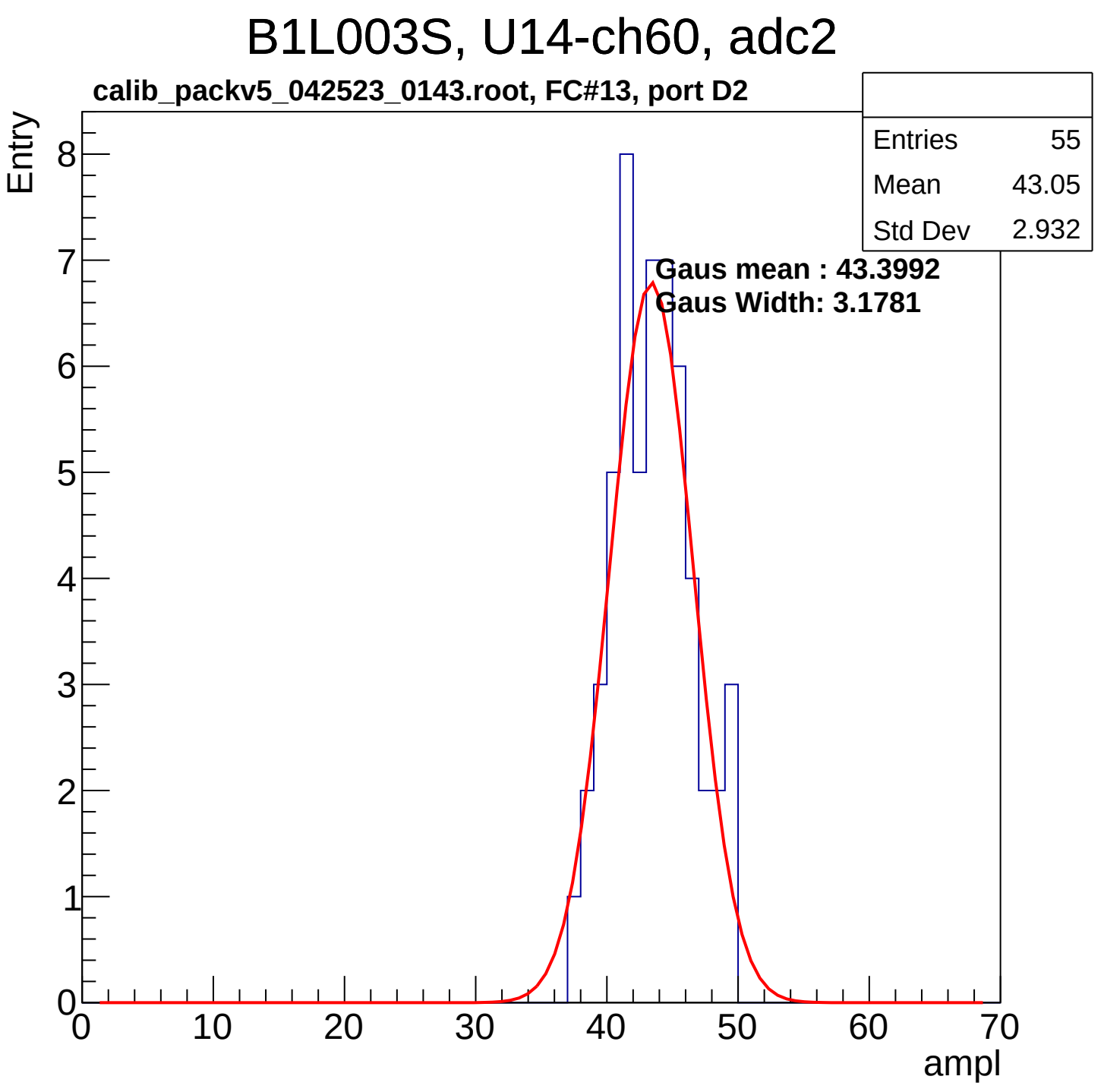

B1L003S, U14-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.44
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

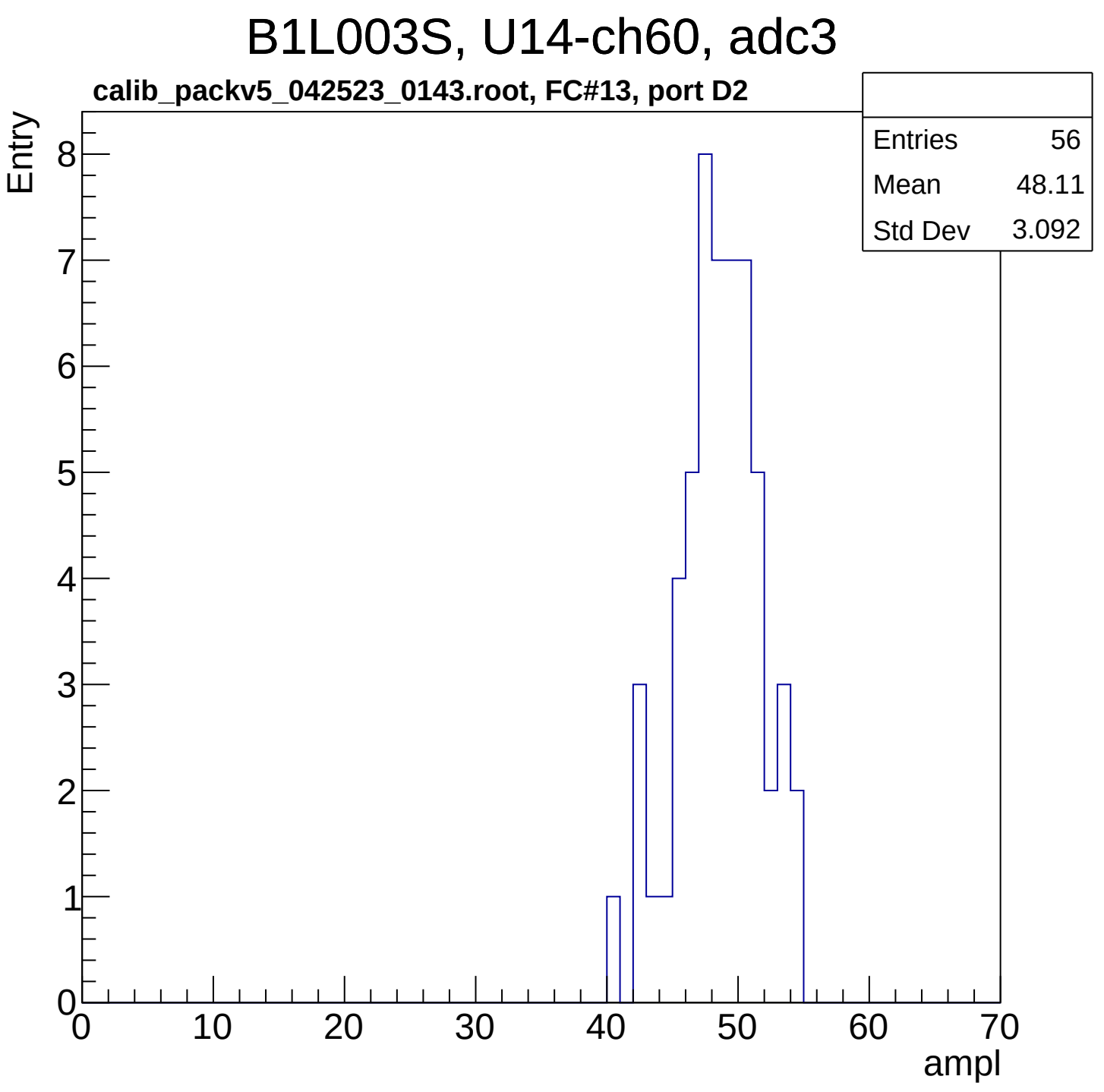

B1L003S, U14-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 58.5
Std Dev 11.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

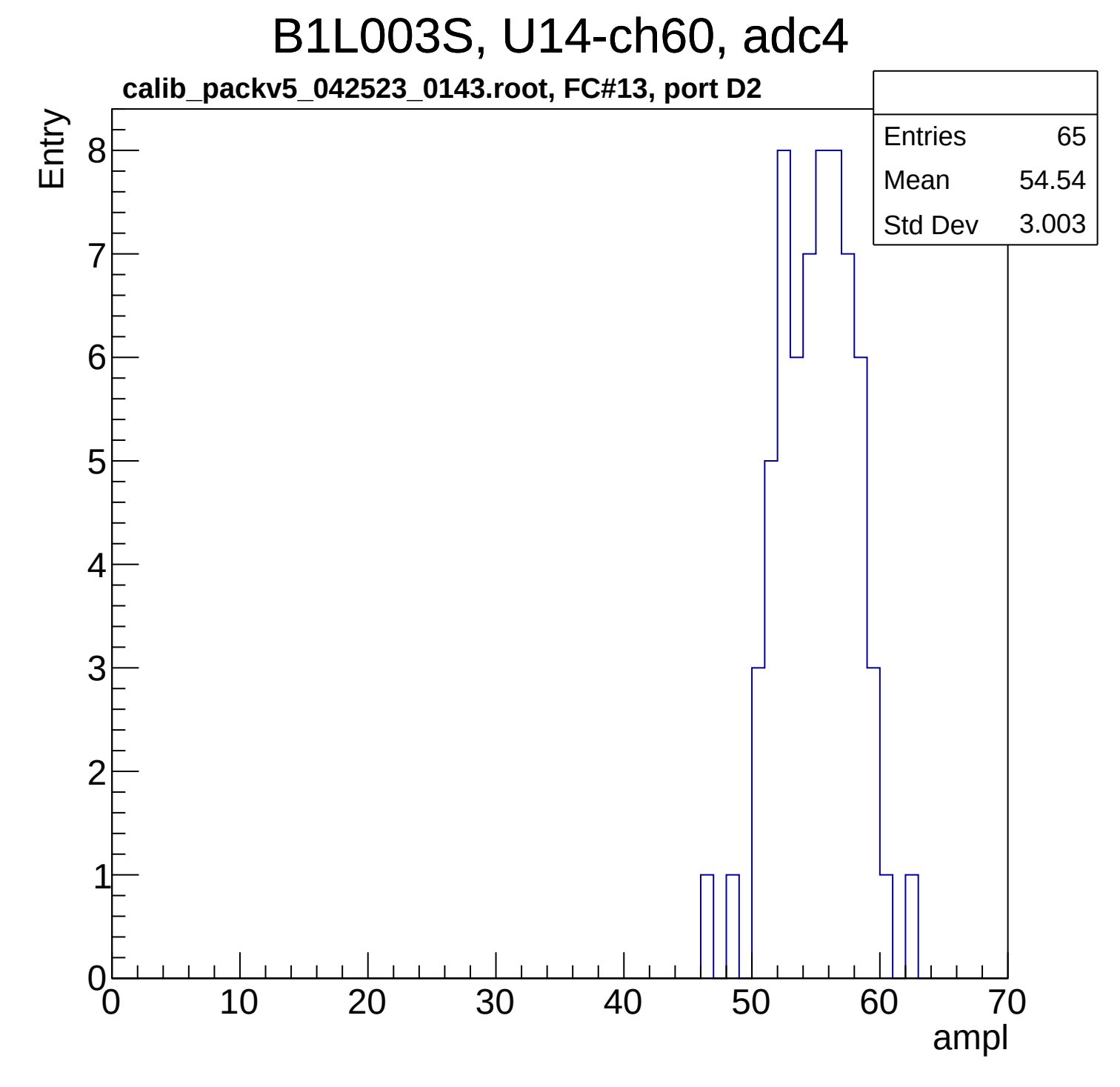

B1L003S, U14-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

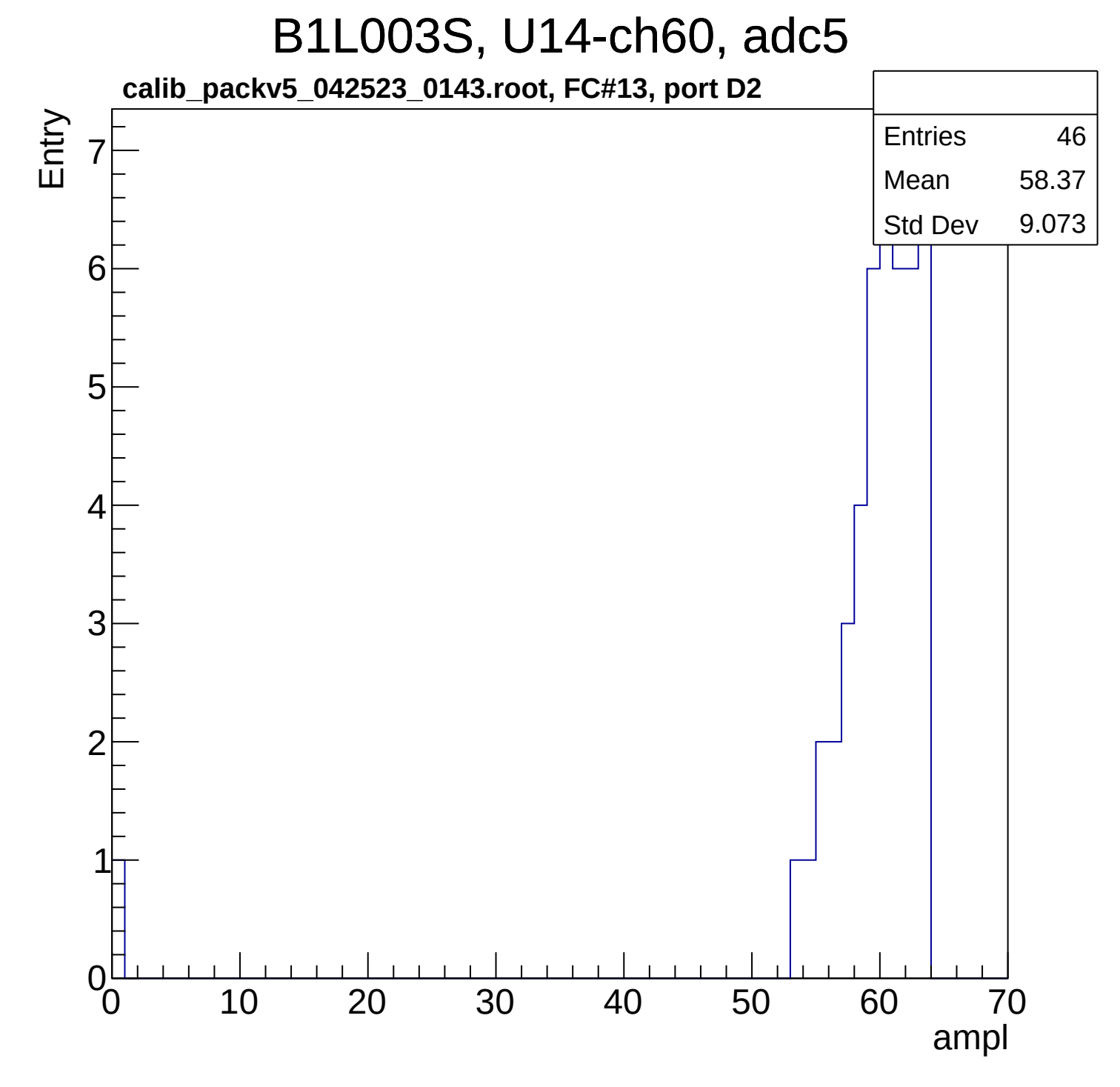

B1L003S, U14-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.85
Std Dev 4.746
Gaus mean : 30.1484
Gaus Width: 3.5983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

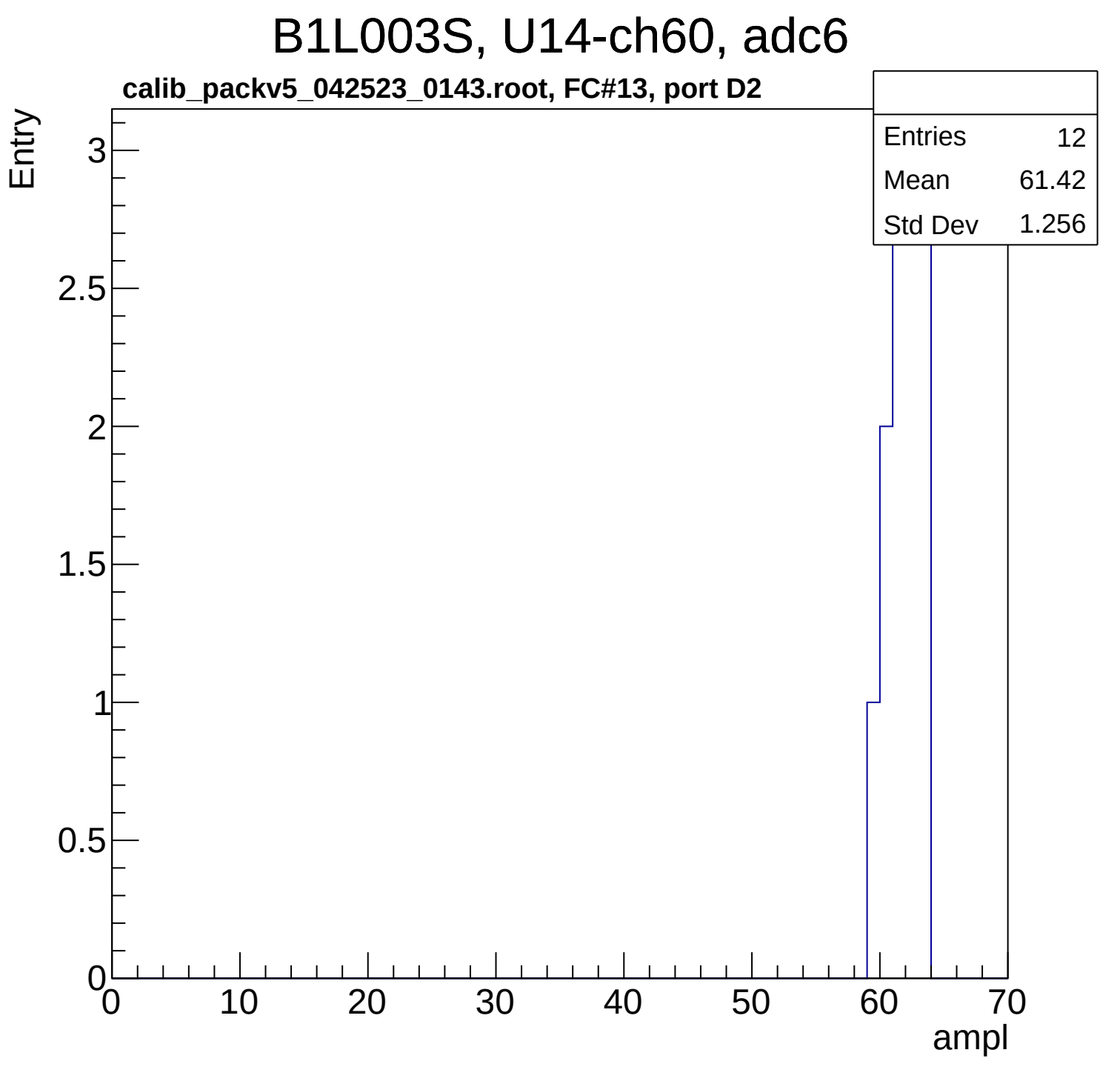

B1L003S, U14-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.39
Std Dev 3.494
Gaus mean : 36.9904
Gaus Width: 3.4785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

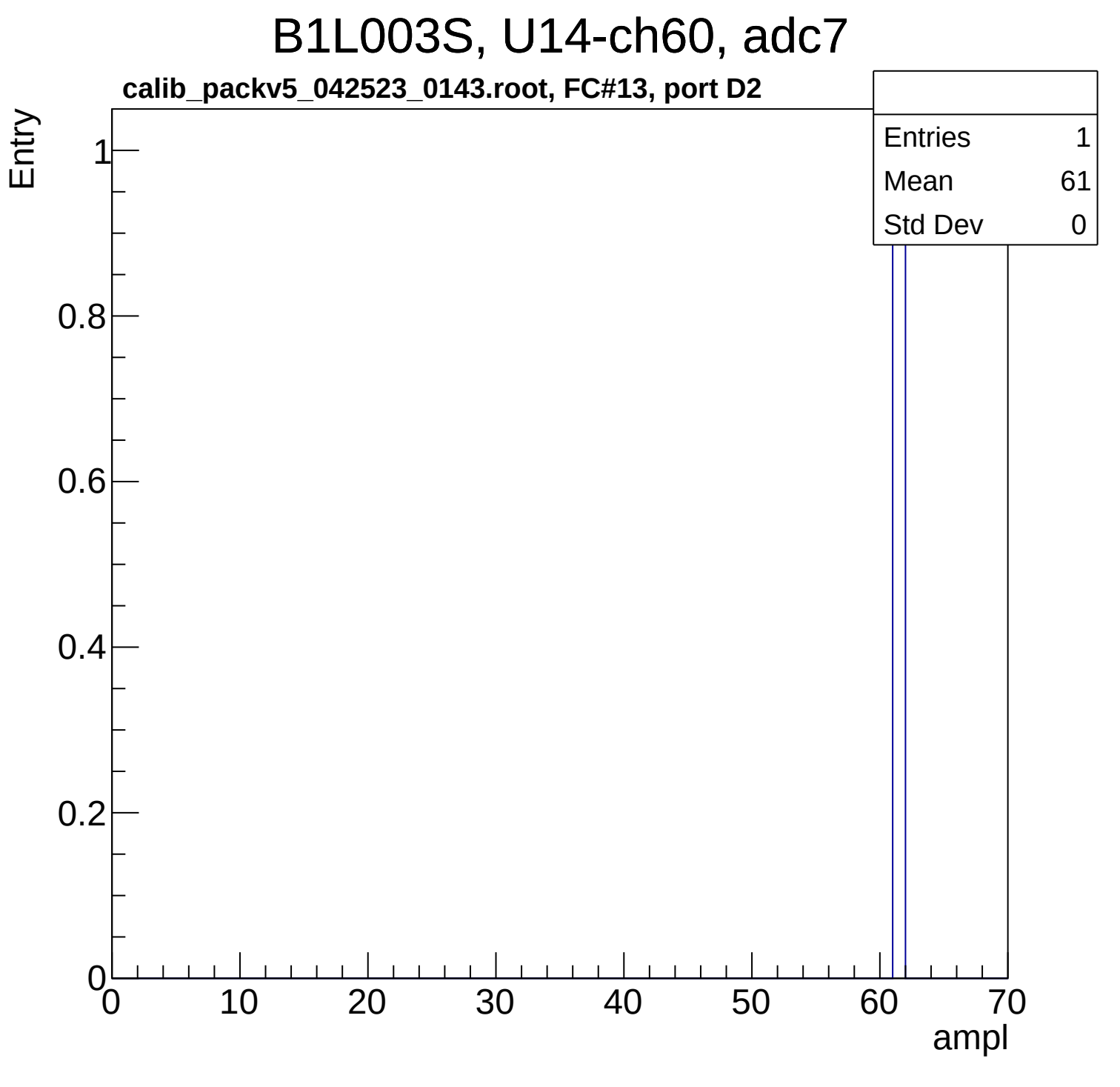

B1L003S, U14-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.05
Std Dev 2.932
Gaus mean : 43.3992
Gaus Width: 3.1781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

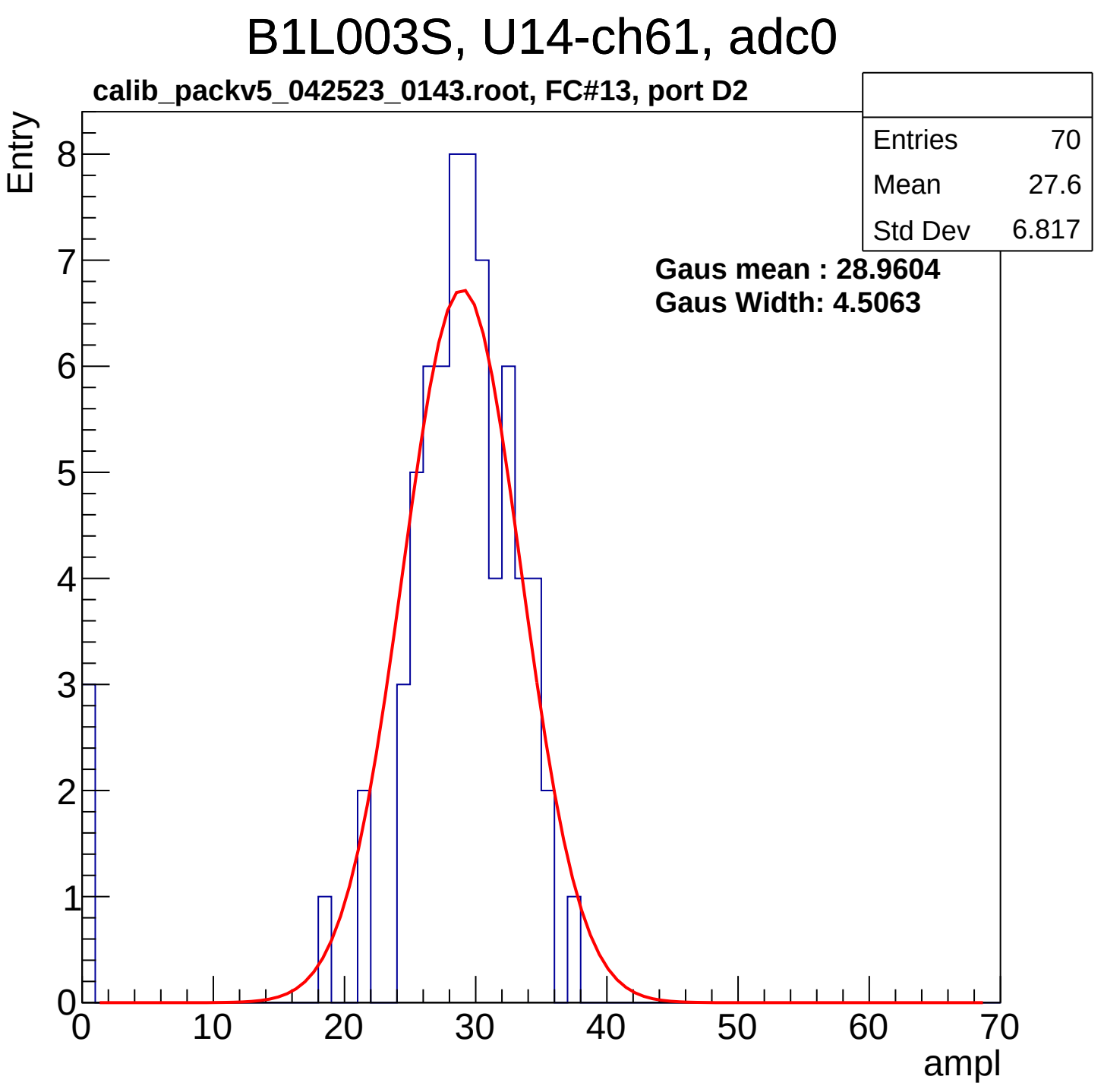

B1L003S, U14-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.11
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

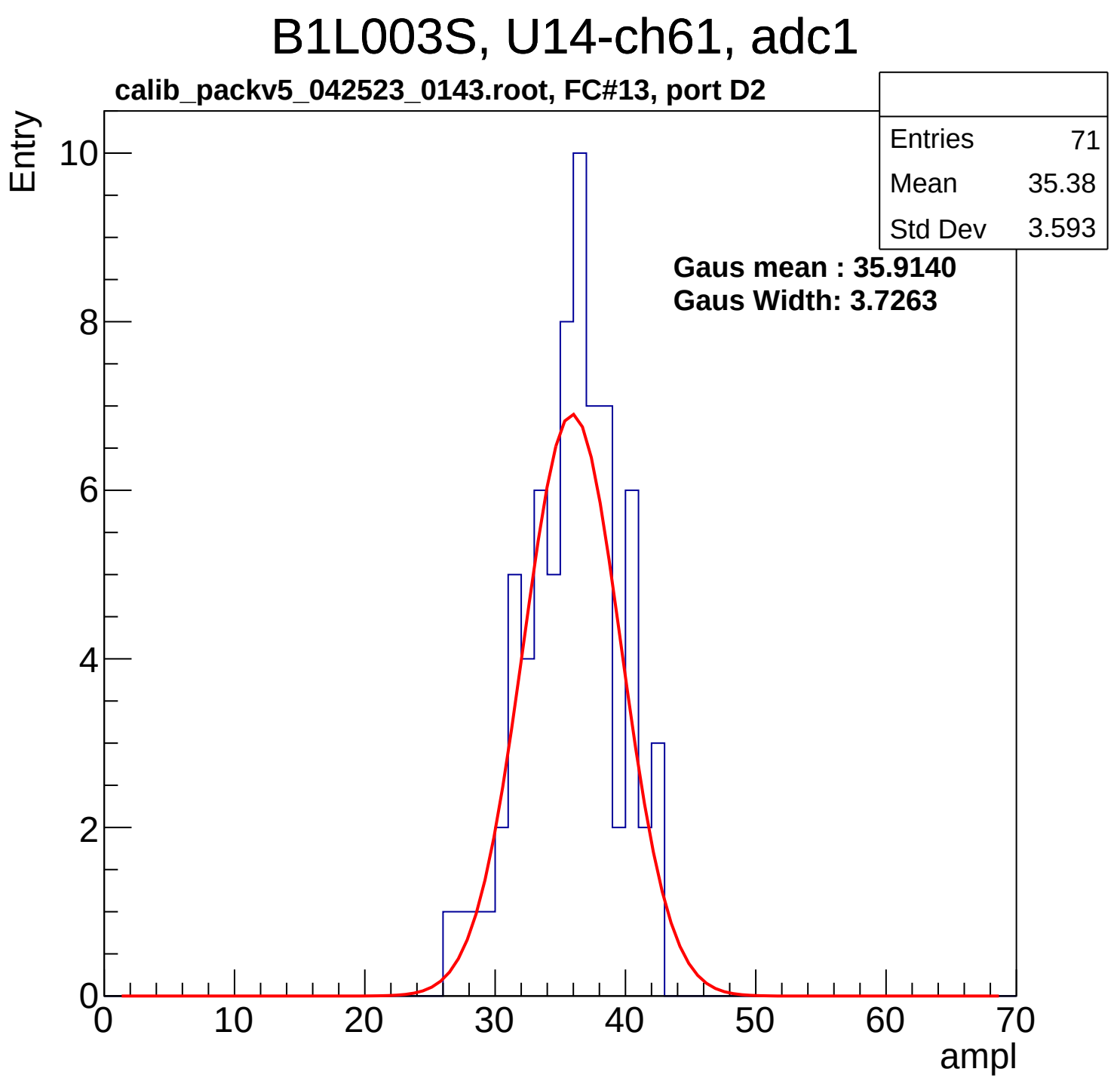

B1L003S, U14-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.54
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

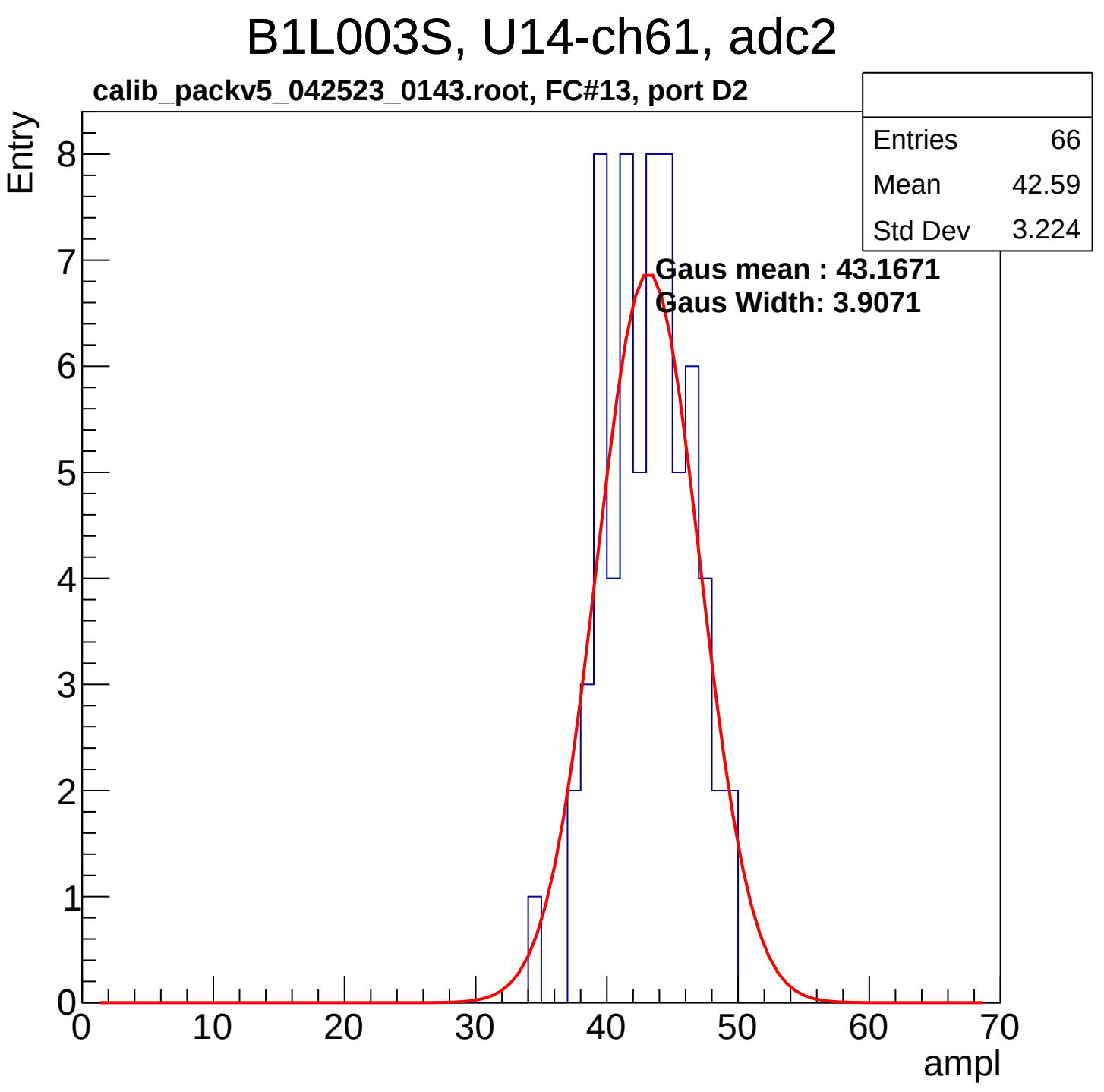

B1L003S, U14-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.37
Std Dev 9.073
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

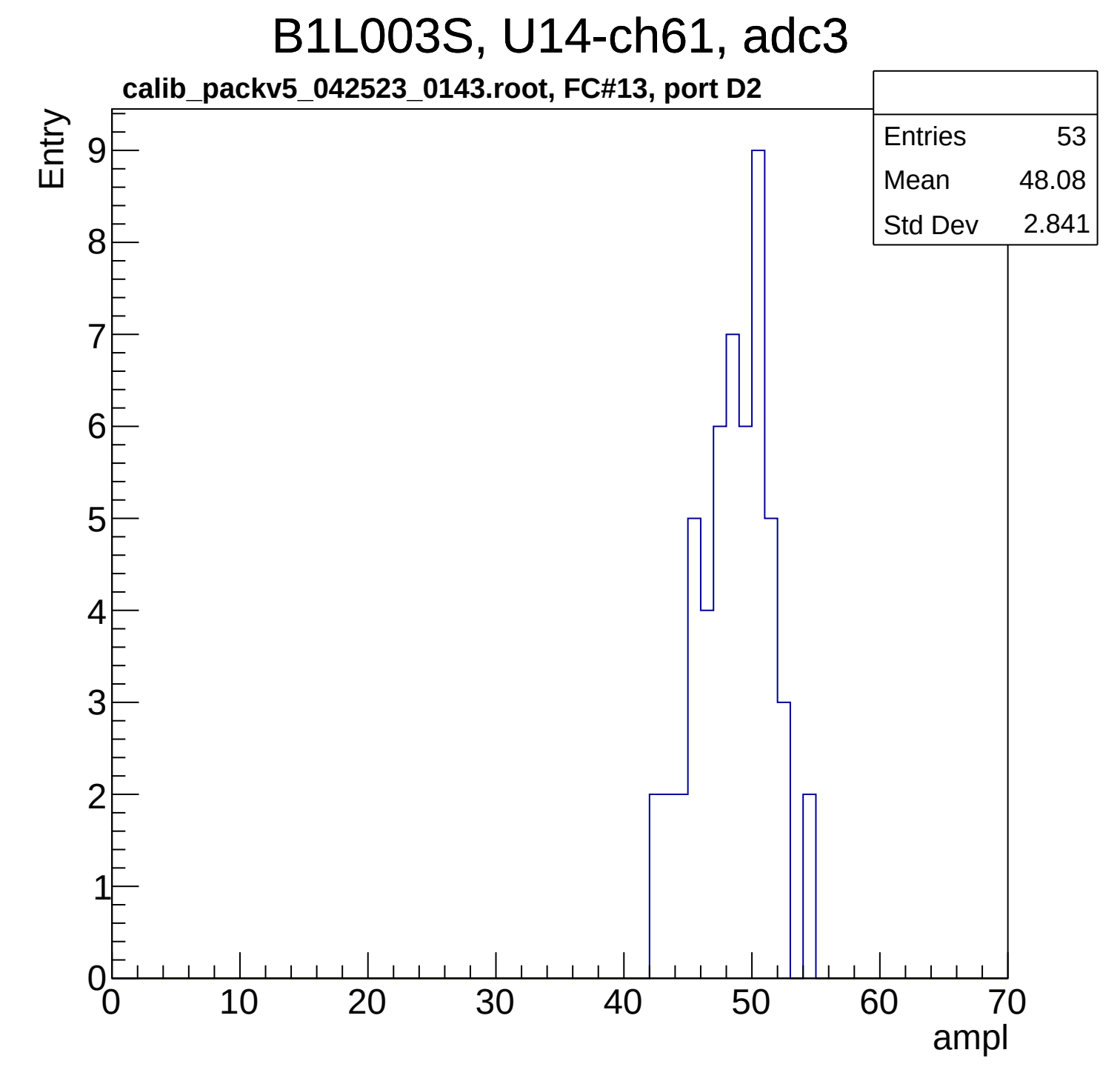

B1L003S, U14-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.42
Std Dev 1.256
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

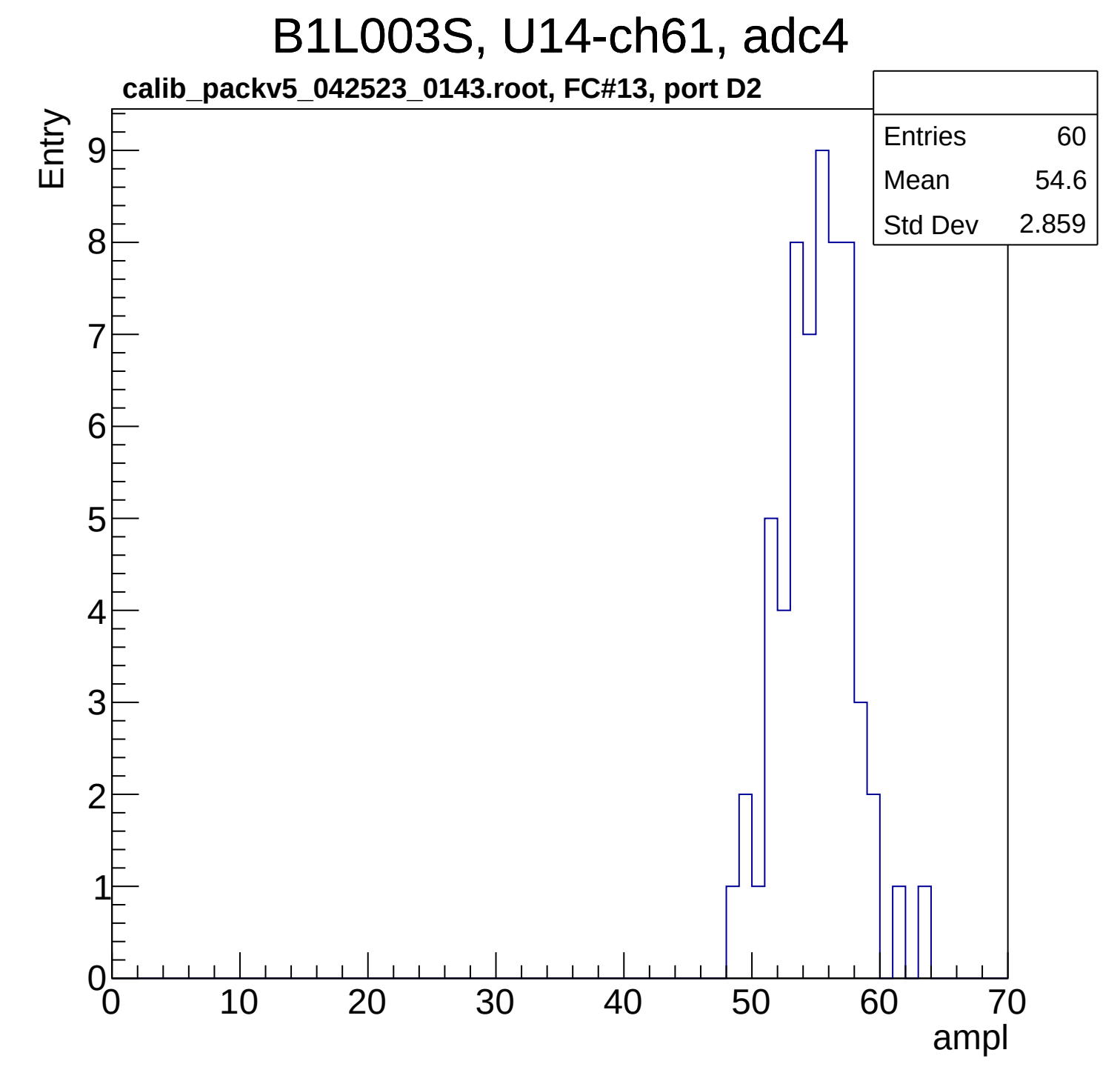

B1L003S, U14-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

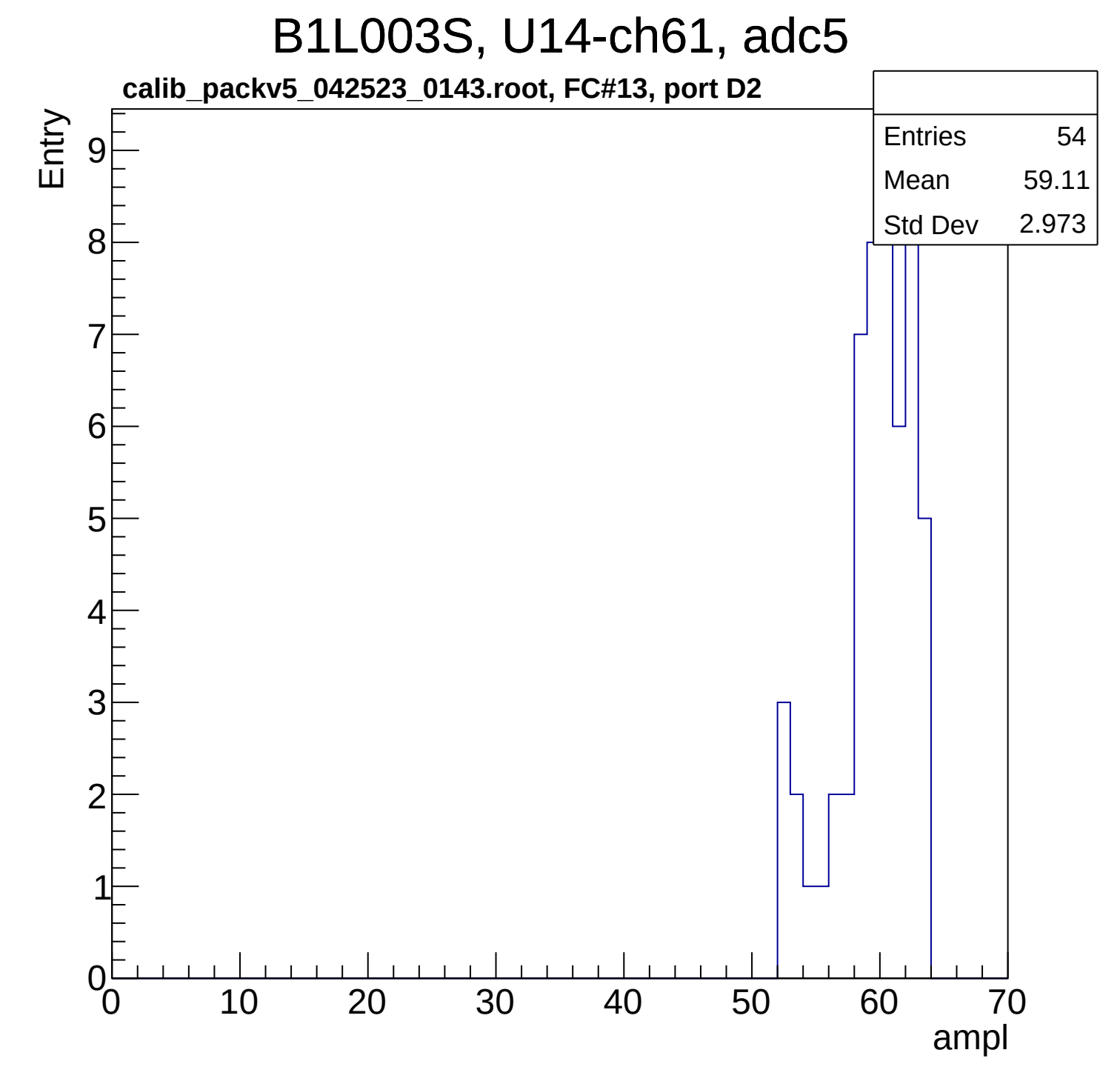

B1L003S, U14-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.6
Std Dev 6.817
Gaus mean : 28.9604
Gaus Width: 4.5063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

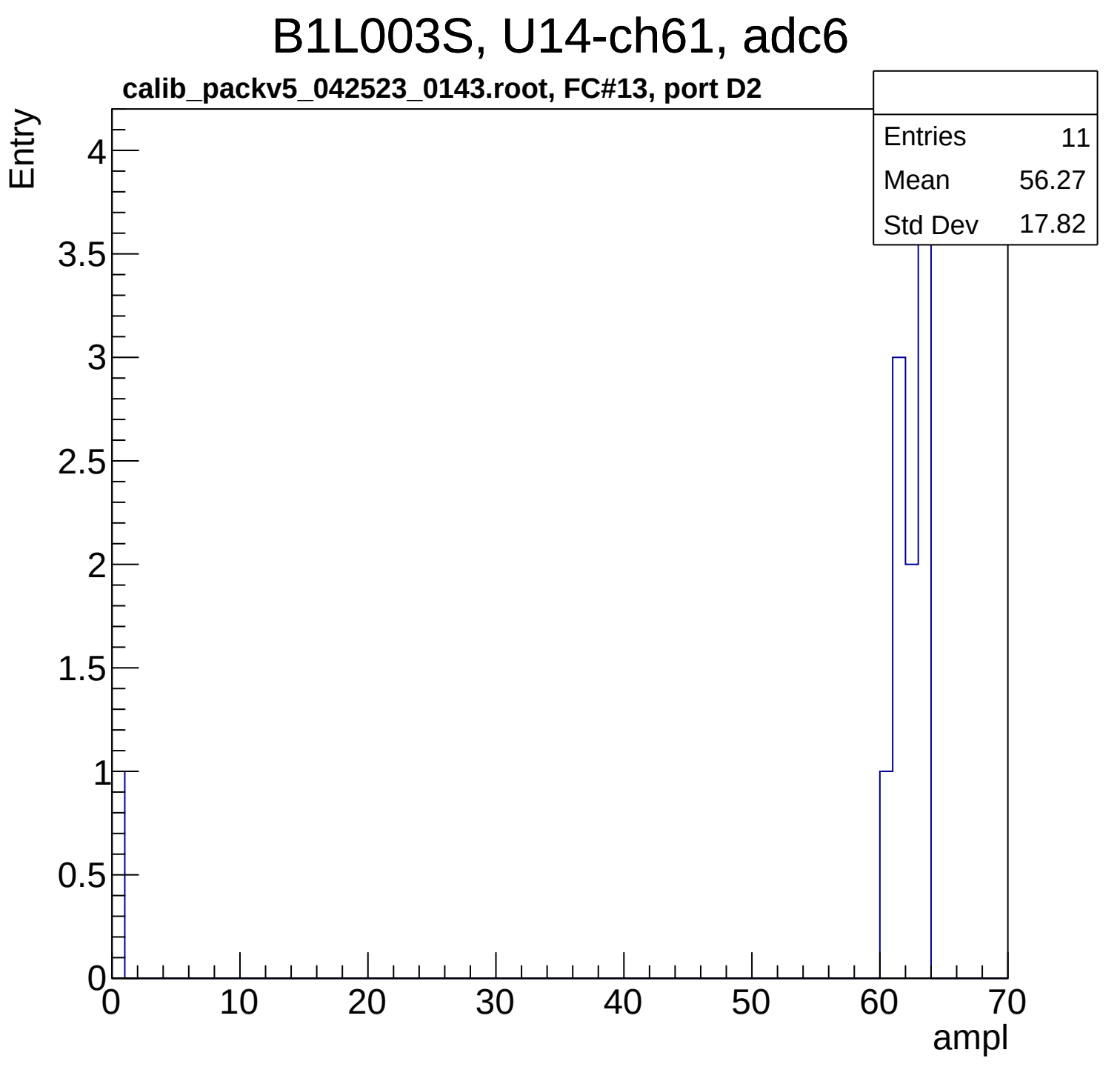

B1L003S, U14-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.38
Std Dev 3.593
Gaus mean : 35.9140
Gaus Width: 3.7263
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

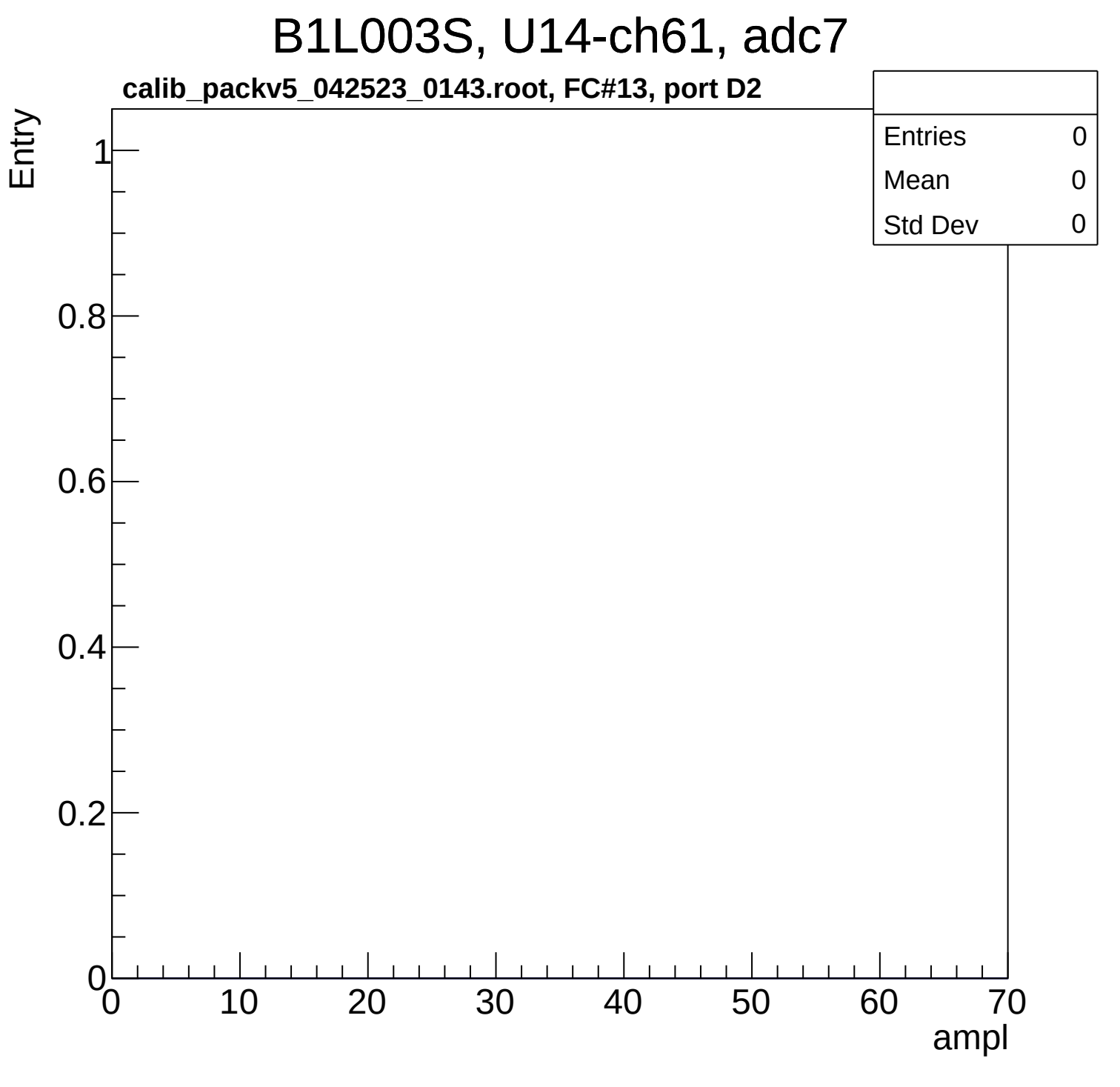

B1L003S, U14-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.59
Std Dev 3.224
Gaus mean : 43.1671
Gaus Width: 3.9071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

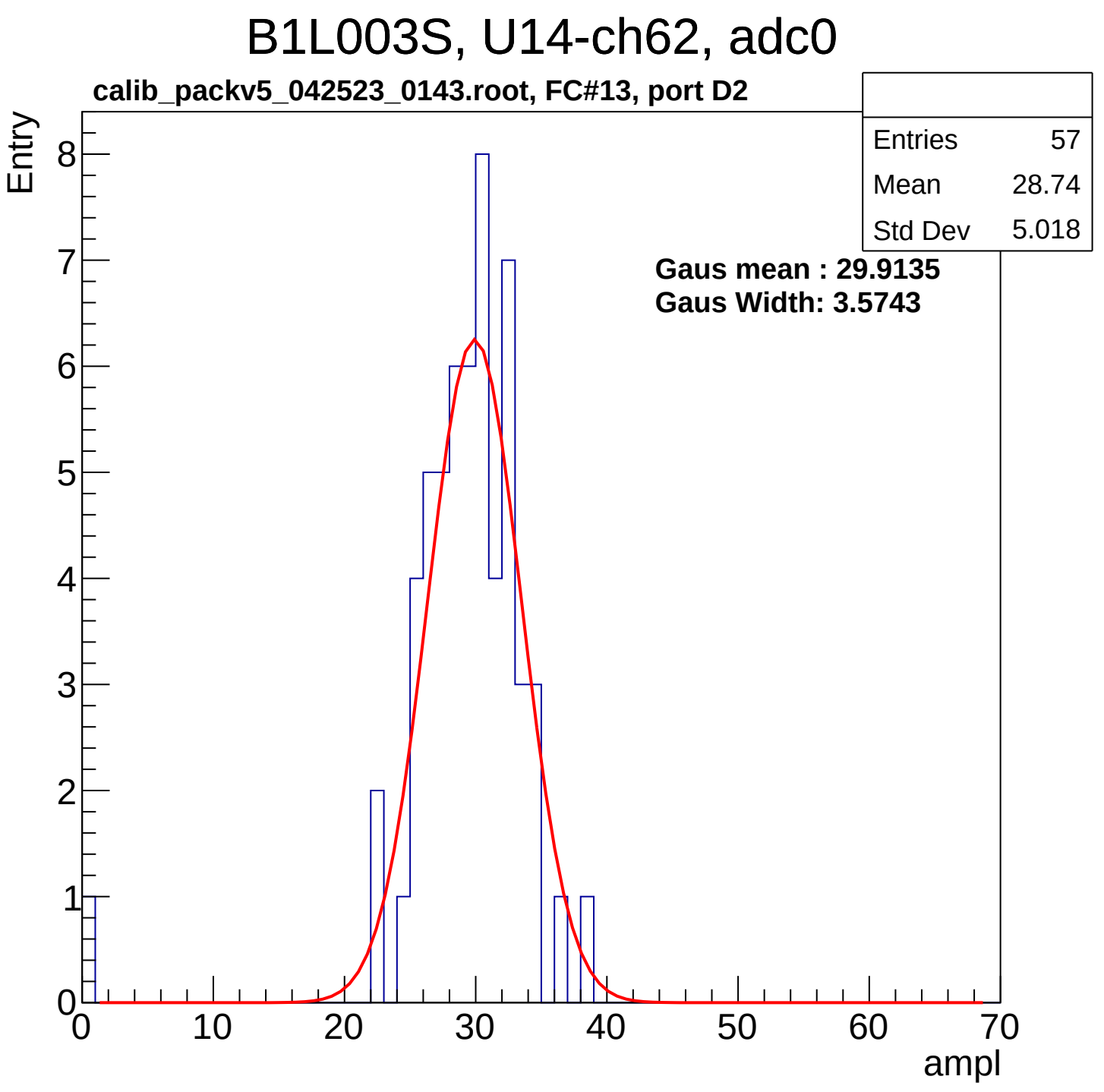

B1L003S, U14-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.08
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

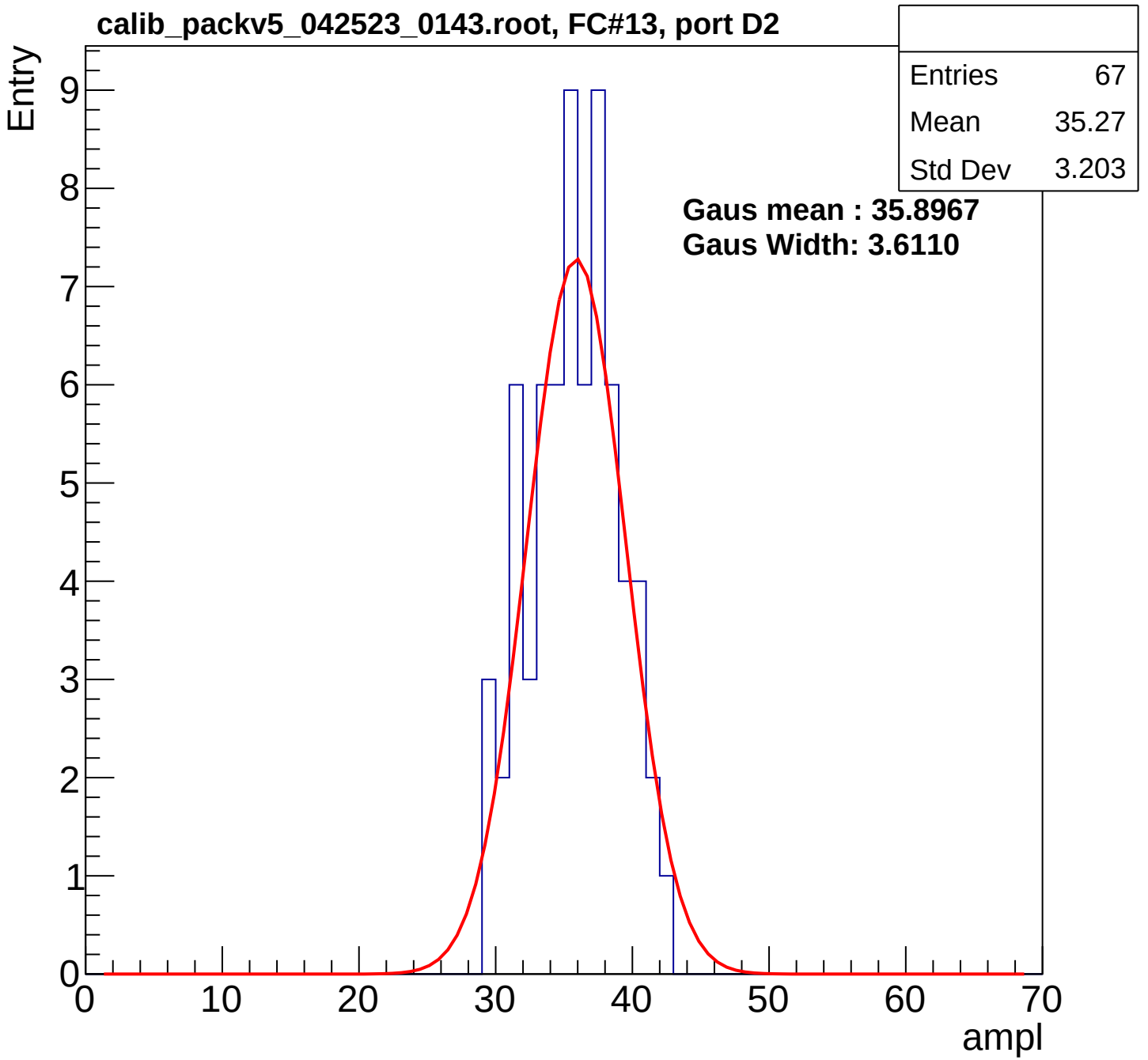

B1L003S, U14-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.6
Std Dev 2.859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

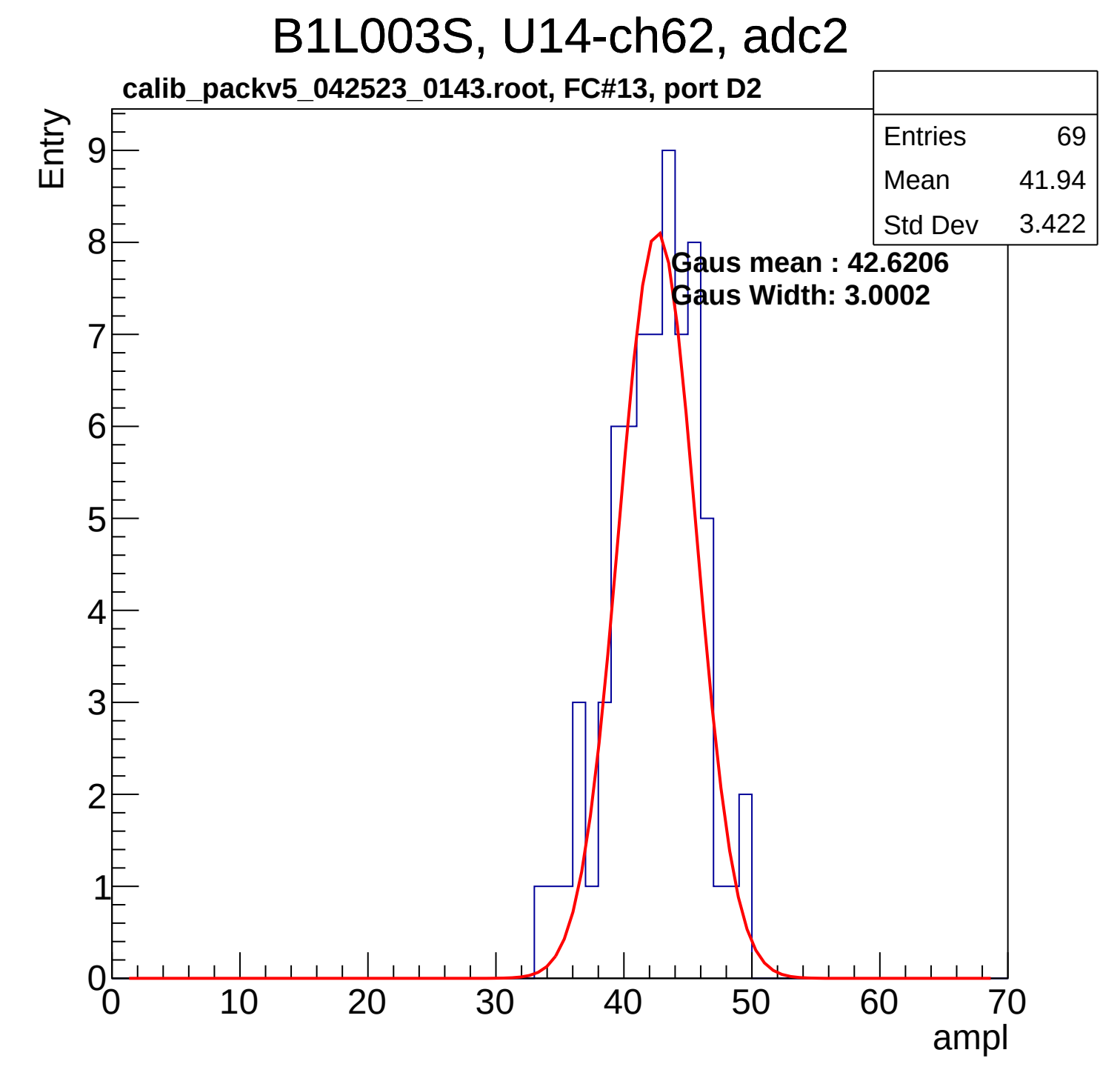

B1L003S, U14-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.11
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

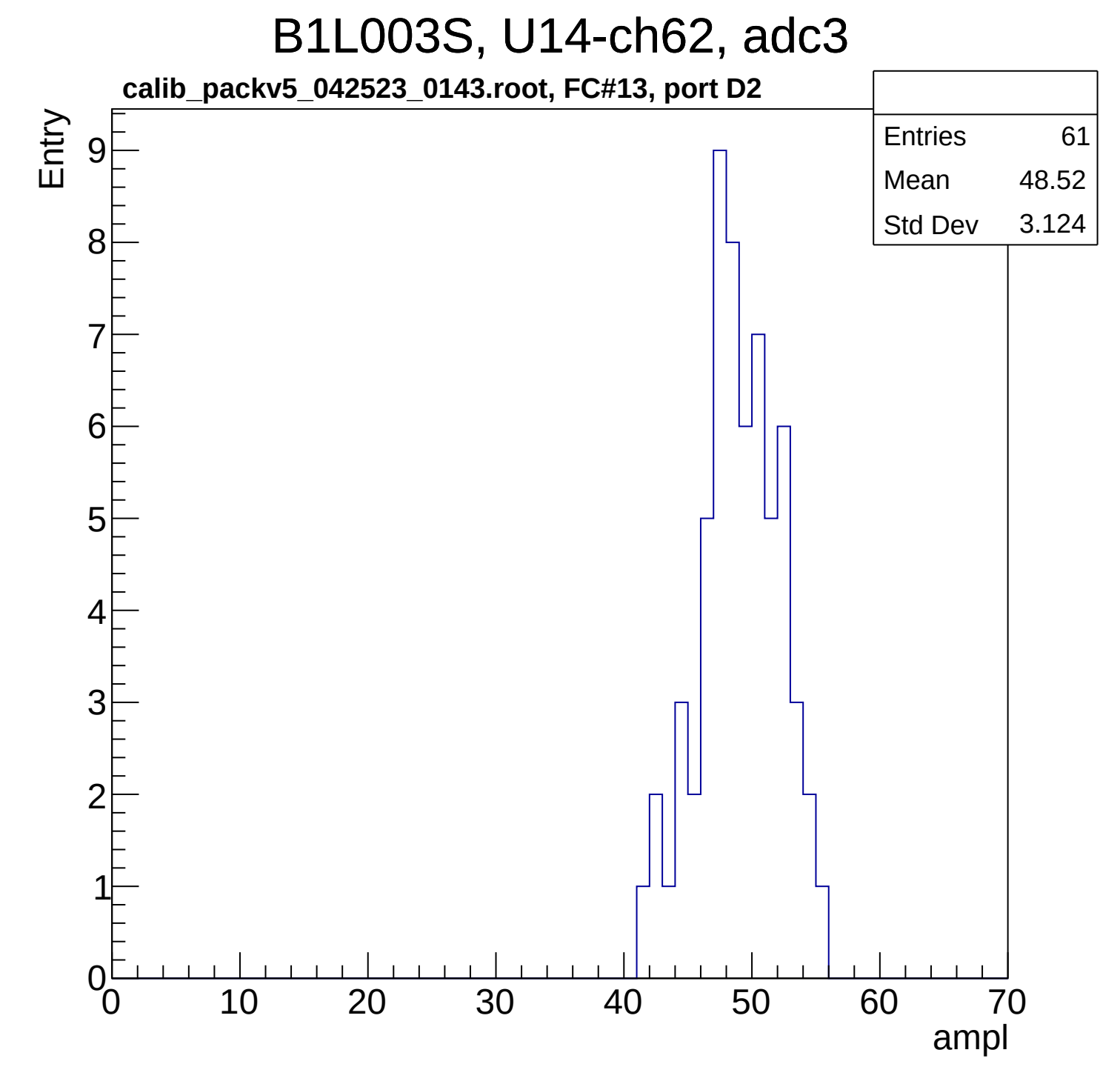

B1L003S, U14-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.27
Std Dev 17.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

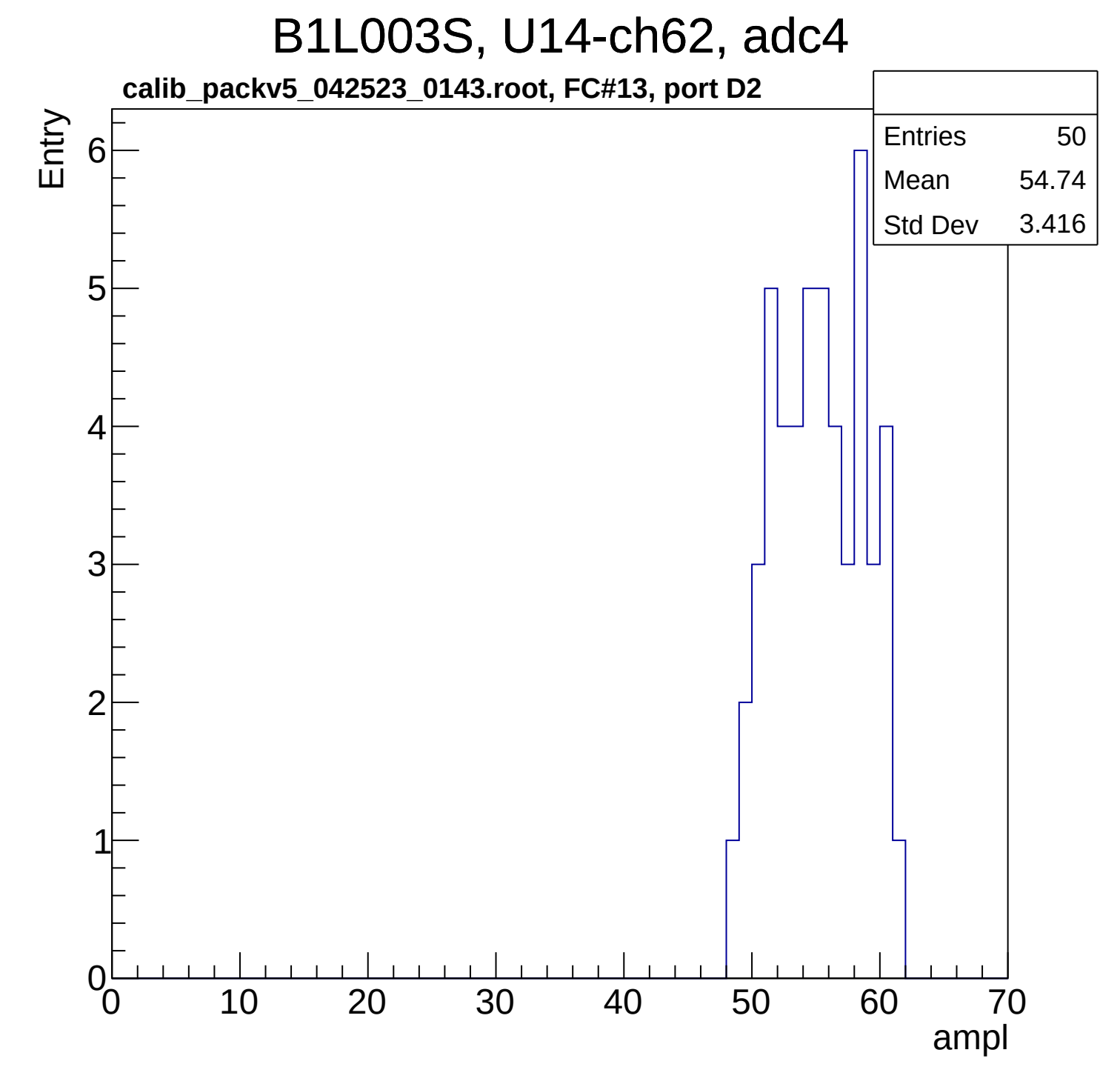

B1L003S, U14-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

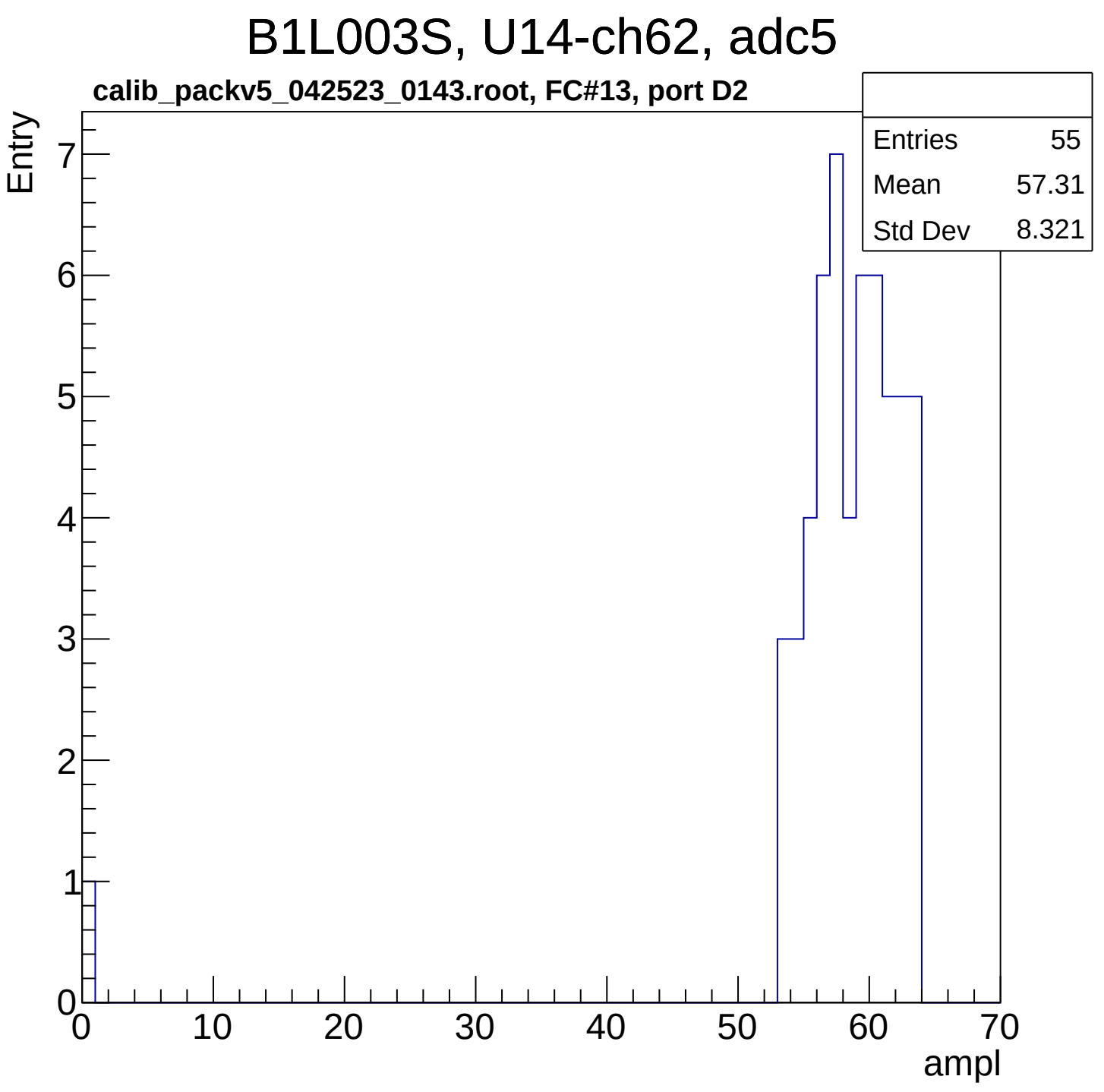

B1L003S, U14-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.74
Std Dev 5.018
Gaus mean : 29.9135
Gaus Width: 3.5743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

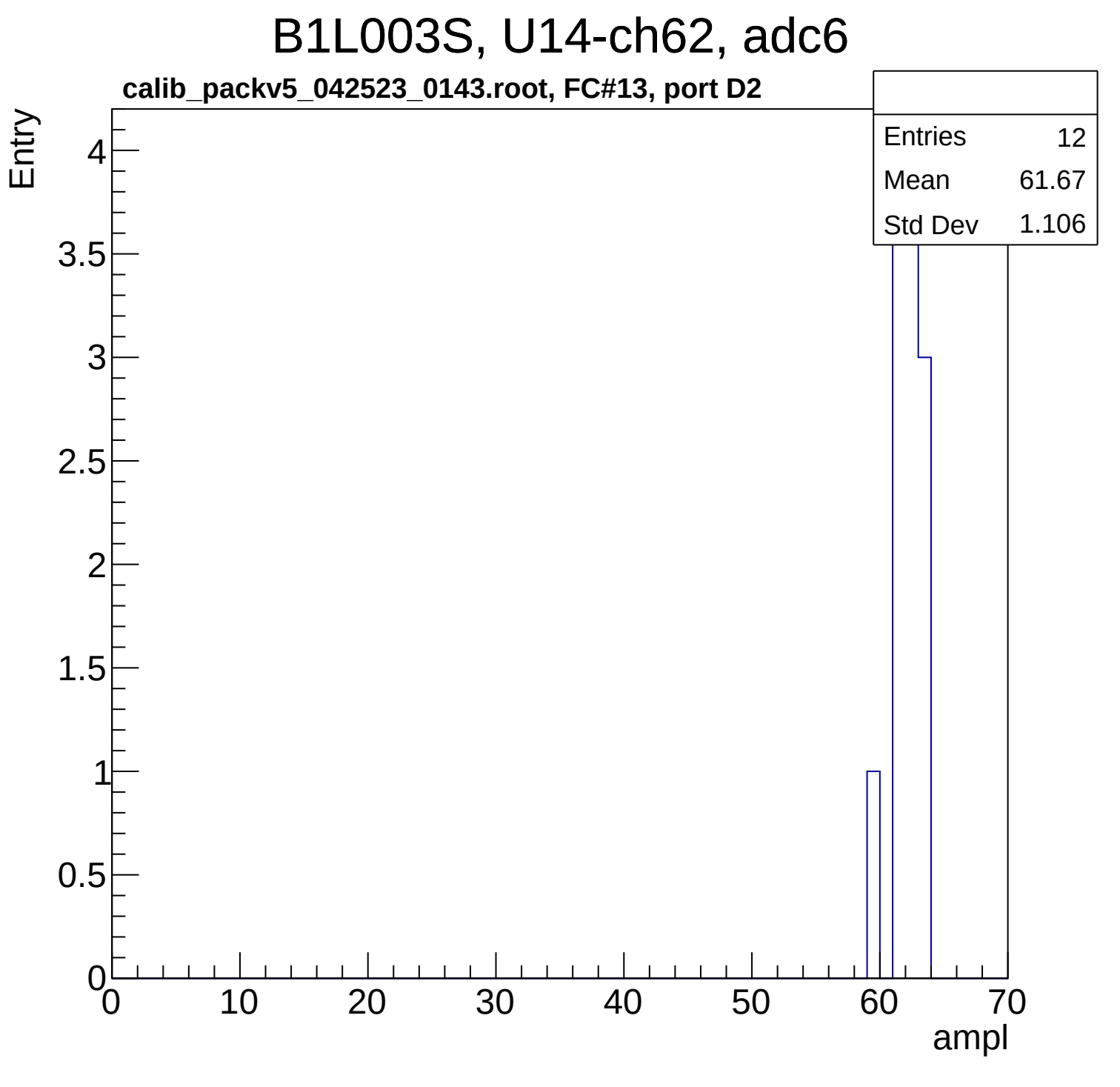

B1L003S, U14-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.27
Std Dev 3.203
Gaus mean : 35.8967
Gaus Width: 3.6110
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

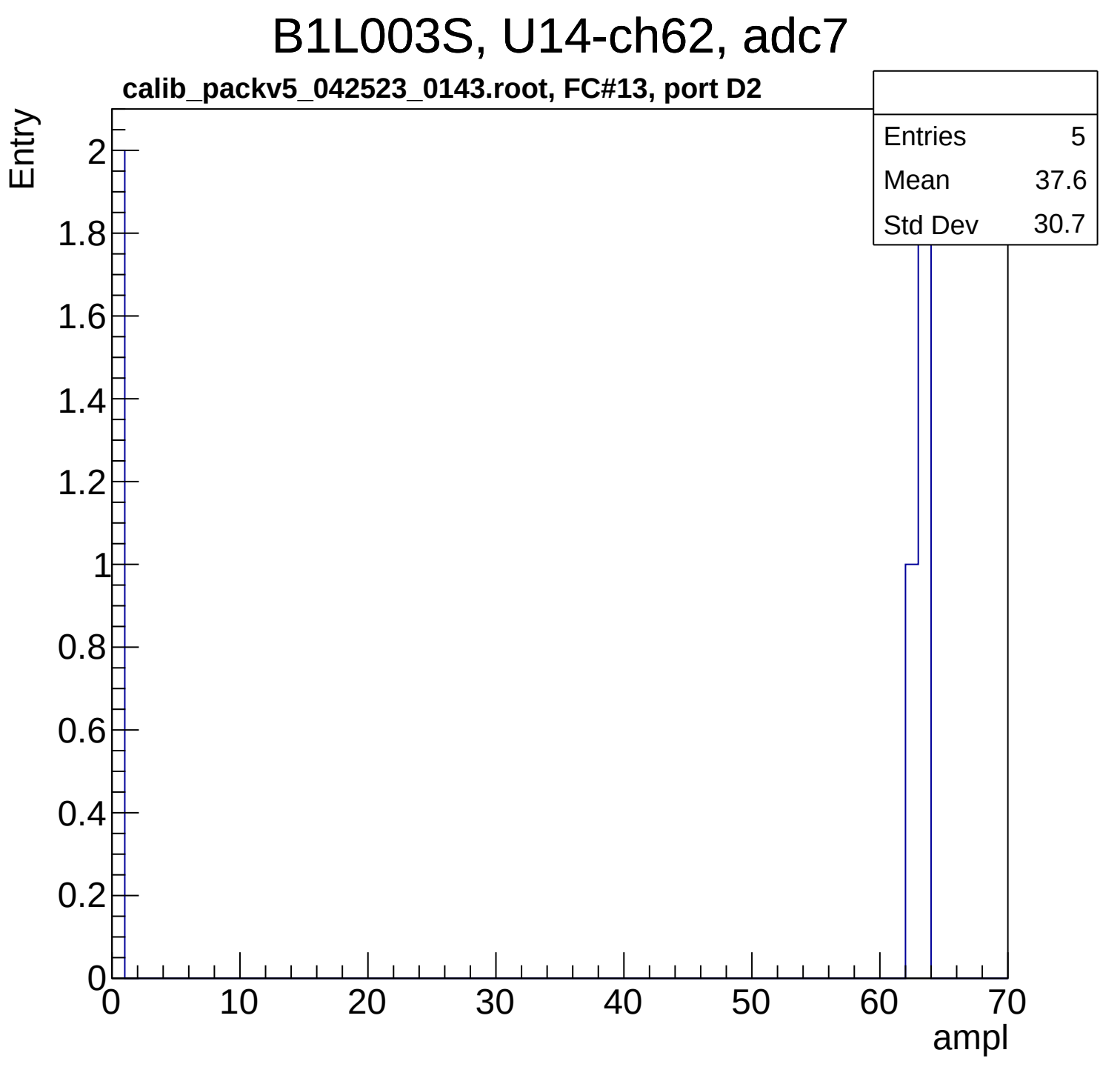

B1L003S, U14-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.94
Std Dev 3.422
Gaus mean : 42.6206
Gaus Width: 3.0002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

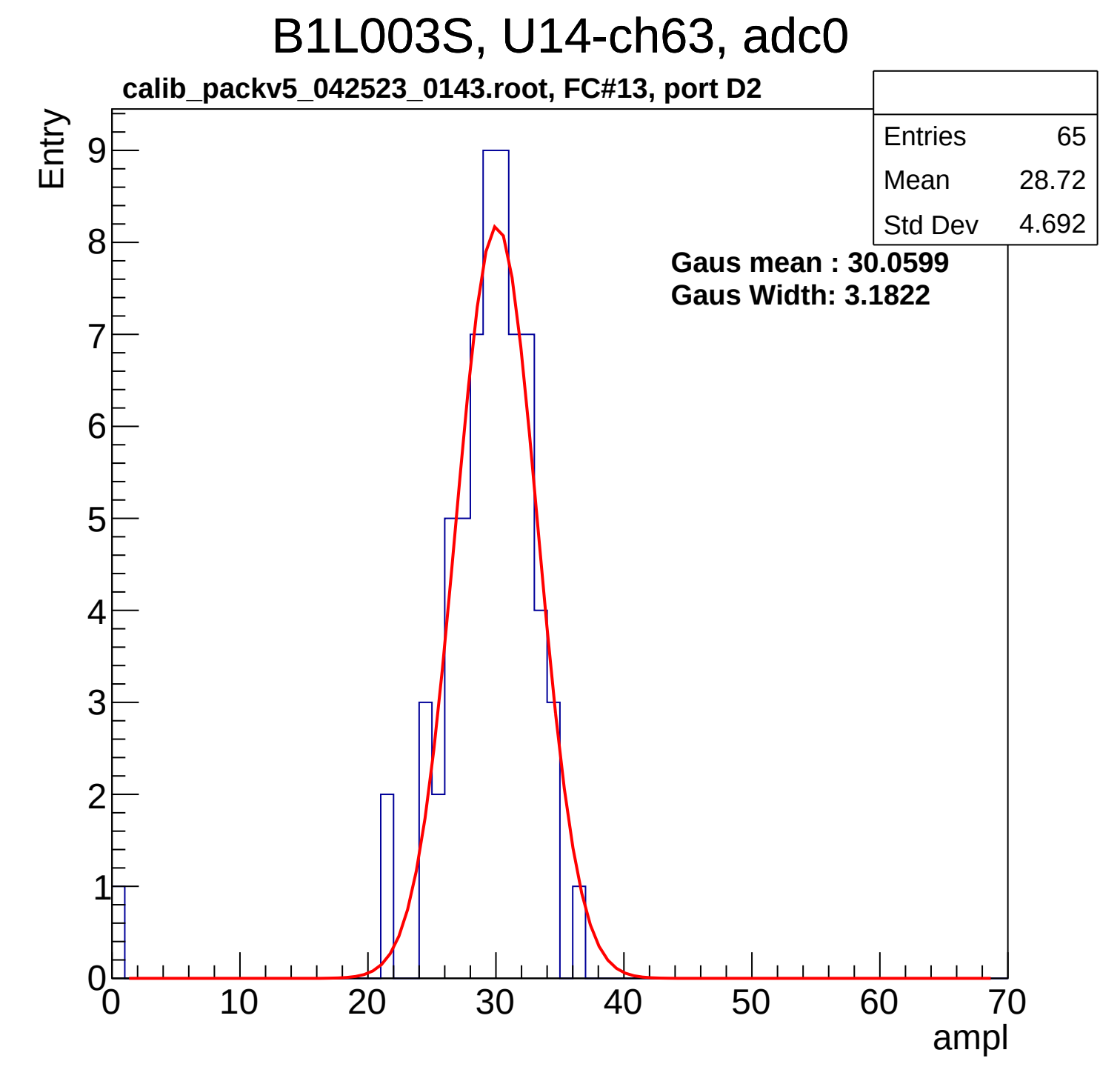

B1L003S, U14-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.52
Std Dev 3.124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

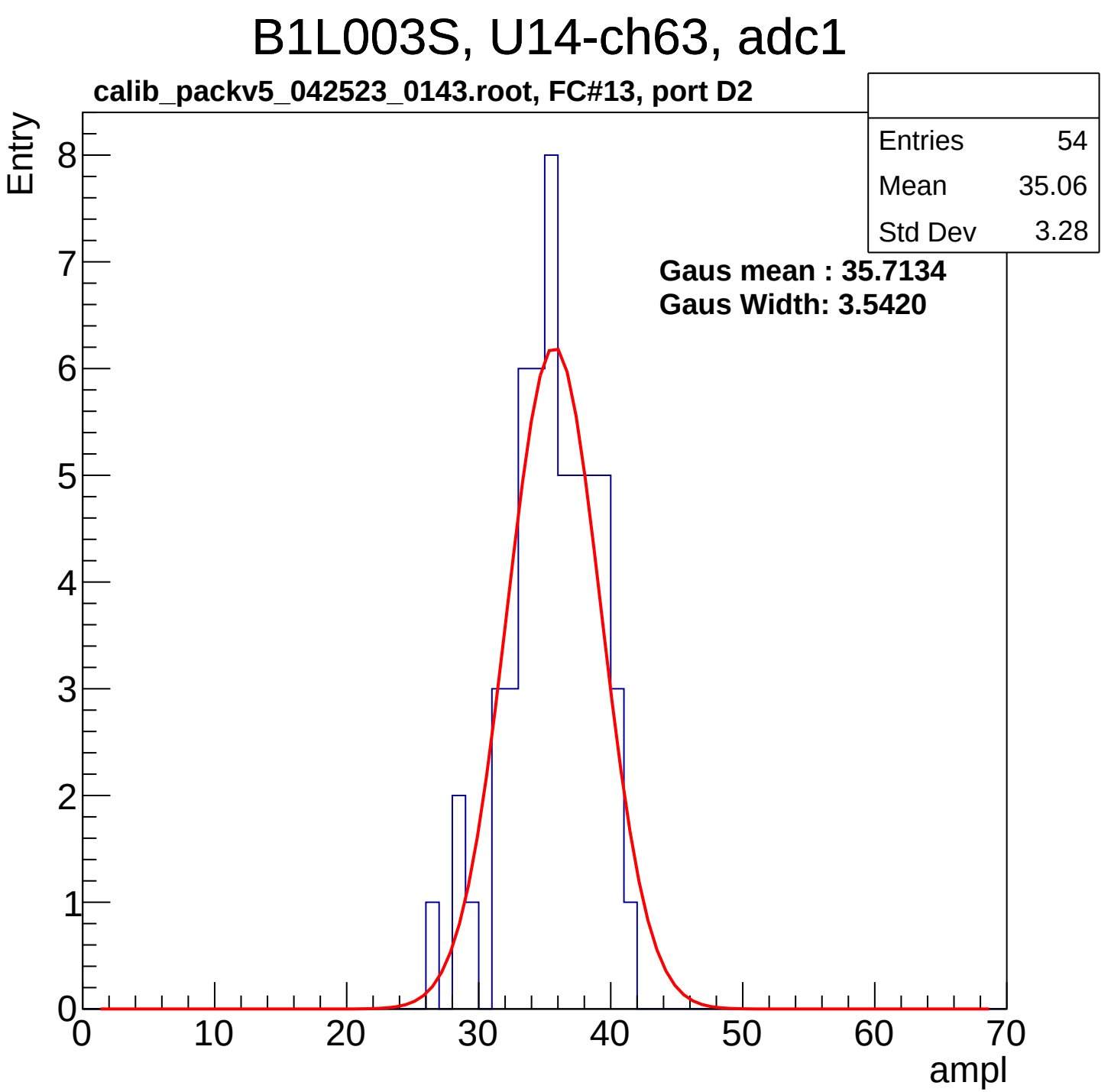

B1L003S, U14-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.74
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

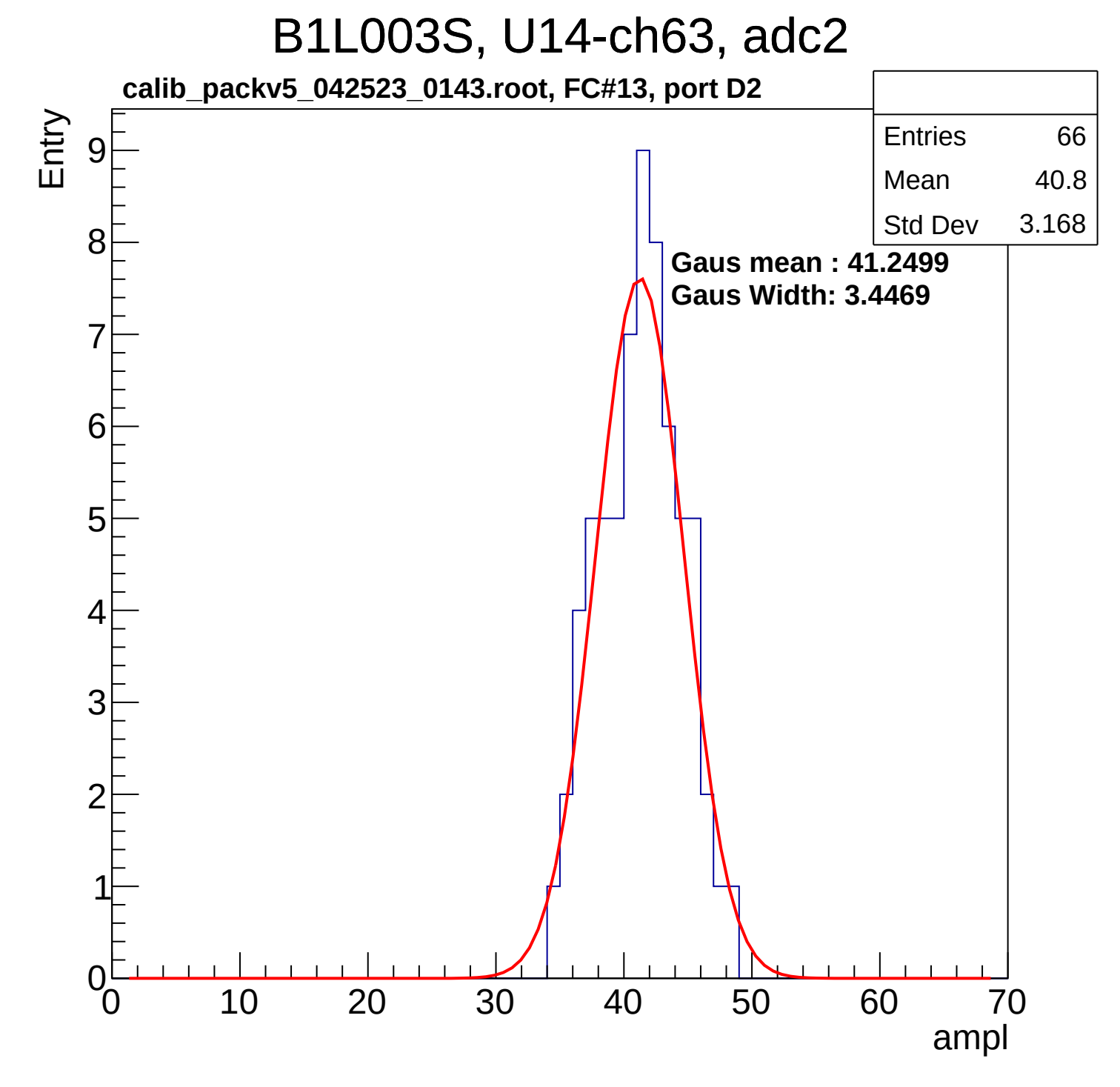

B1L003S, U14-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.31
Std Dev 8.321
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

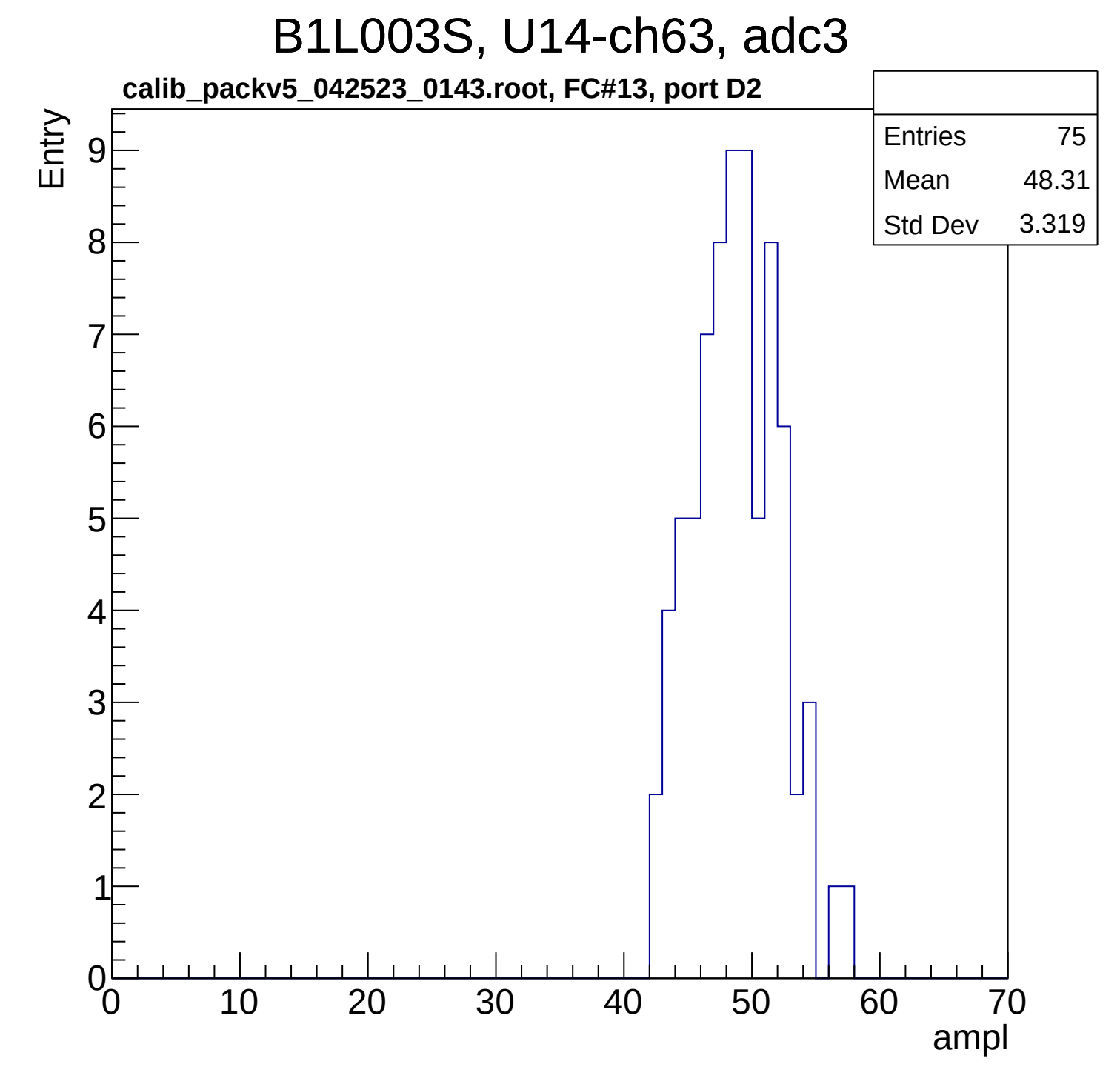

B1L003S, U14-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

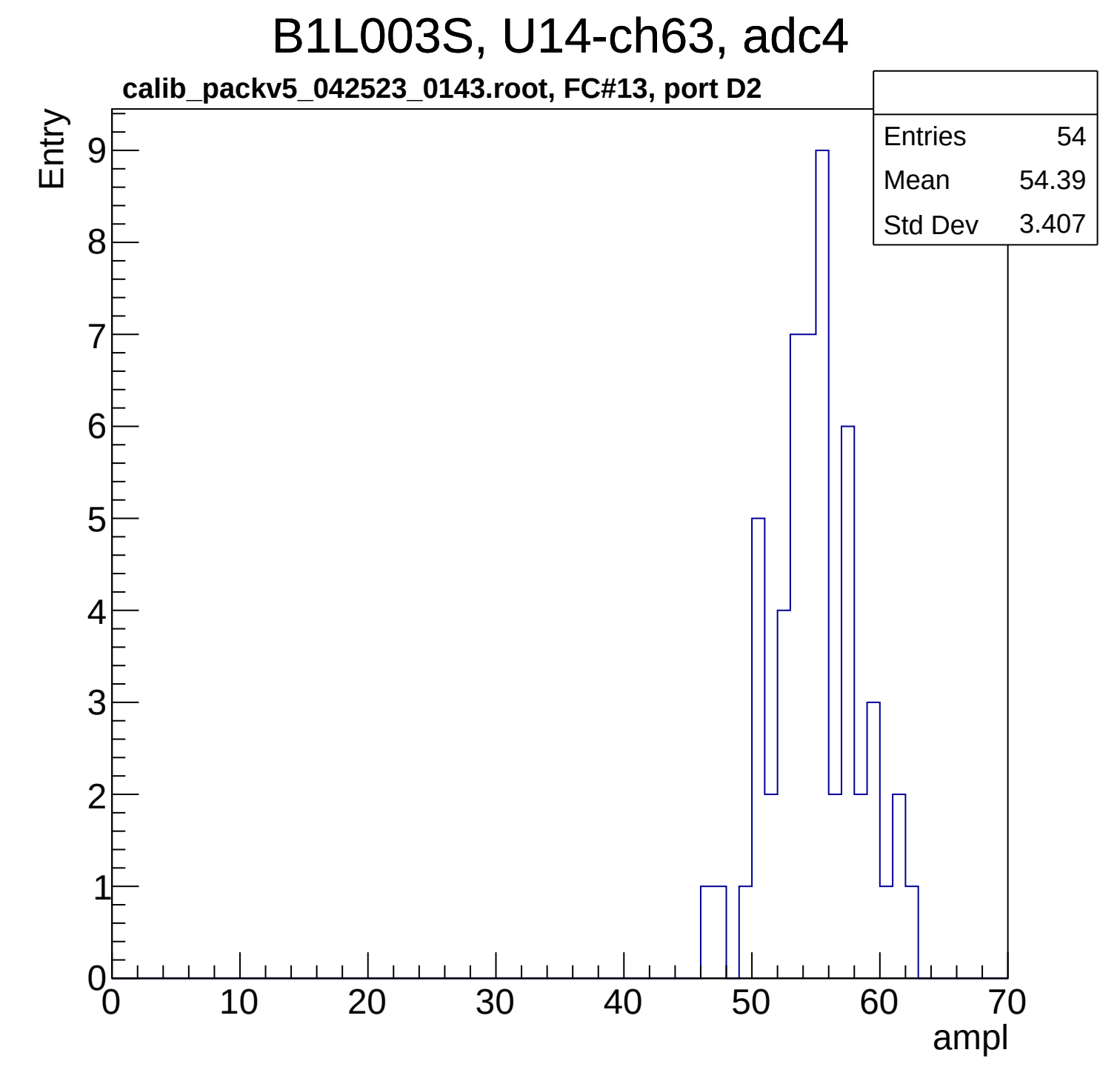

B1L003S, U14-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

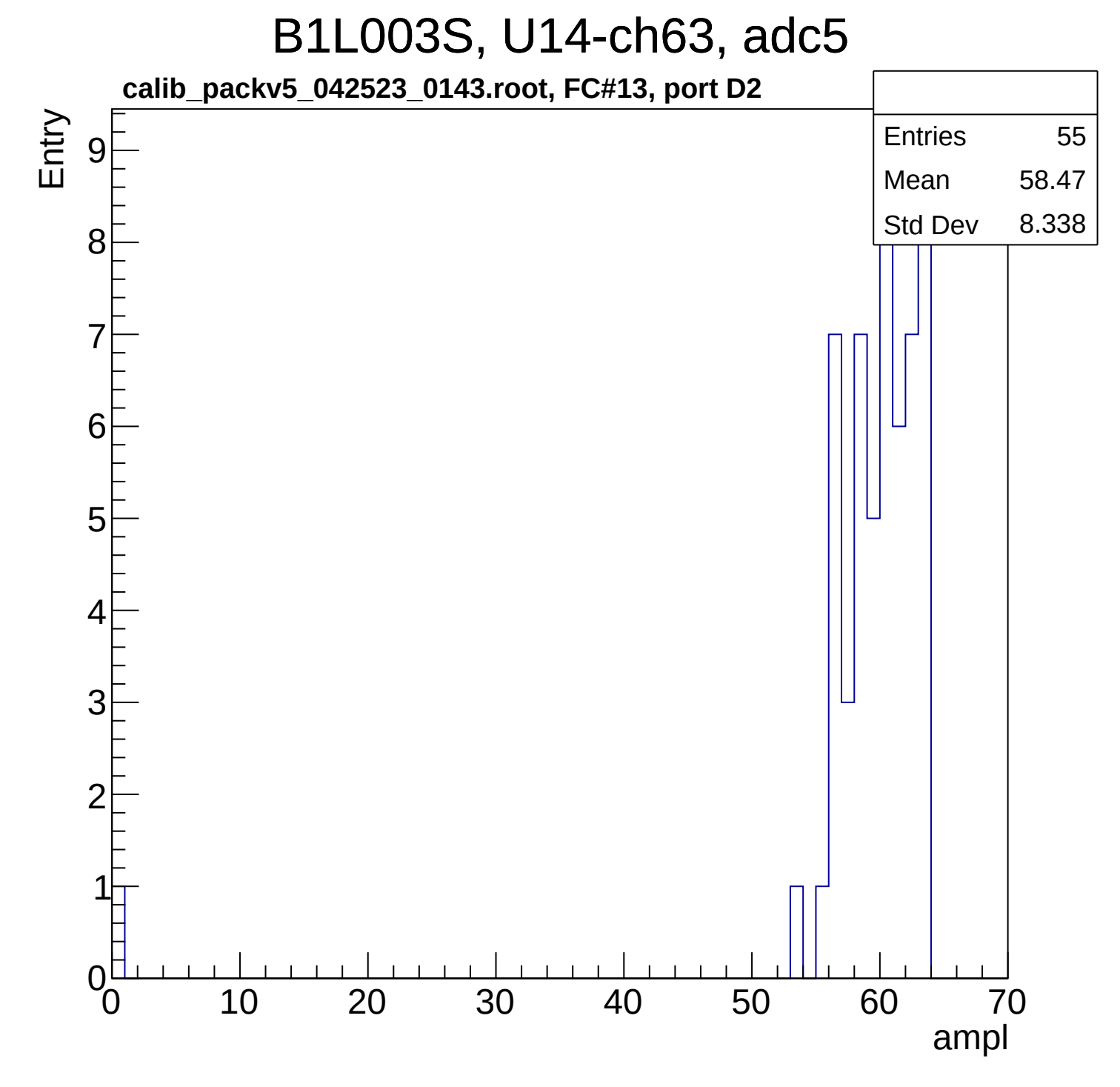

B1L003S, U14-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.72
Std Dev 4.692
Gaus mean : 30.0599
Gaus Width: 3.1822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

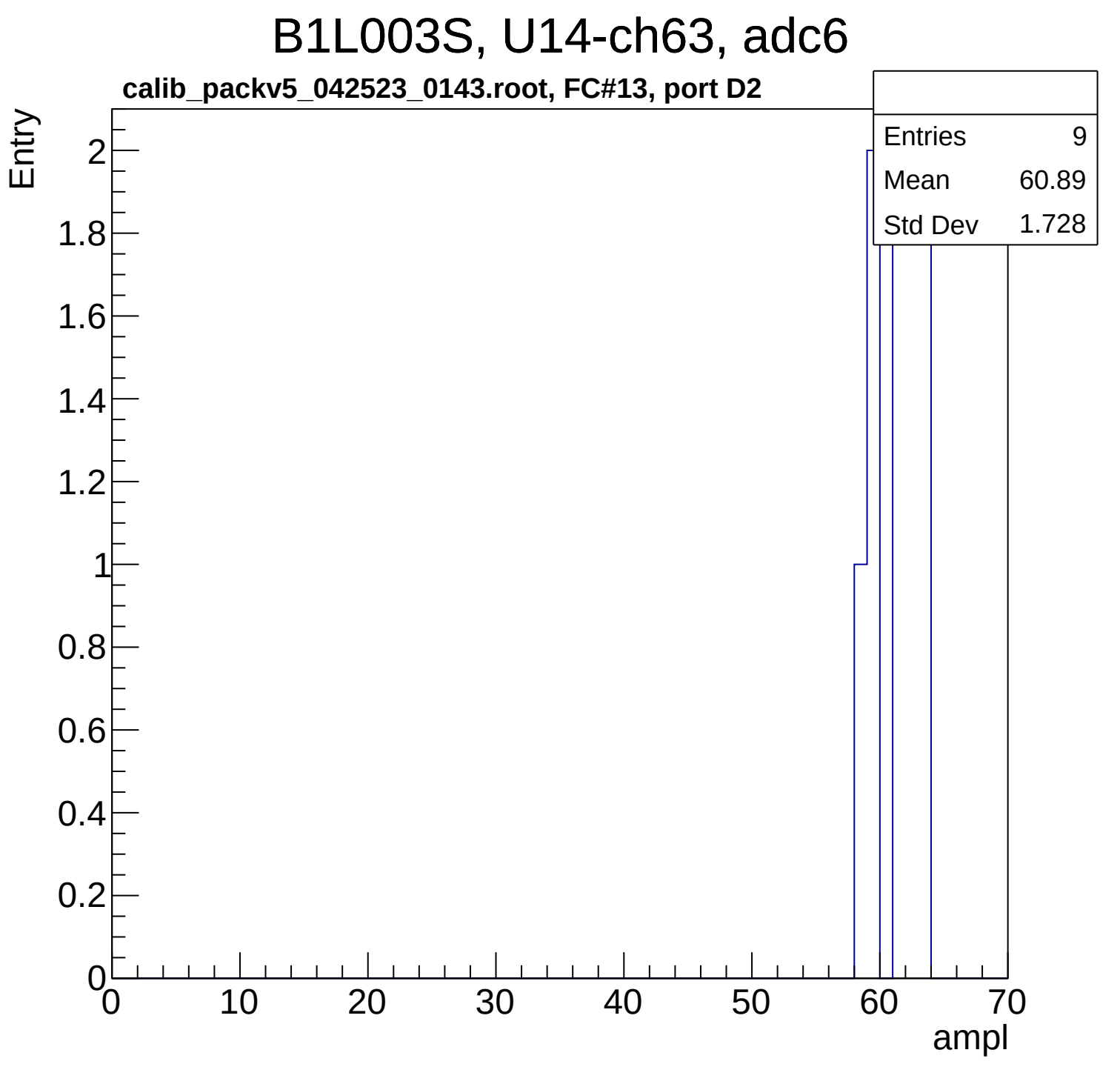

B1L003S, U14-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.06
Std Dev 3.28
Gaus mean : 35.7134
Gaus Width: 3.5420
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

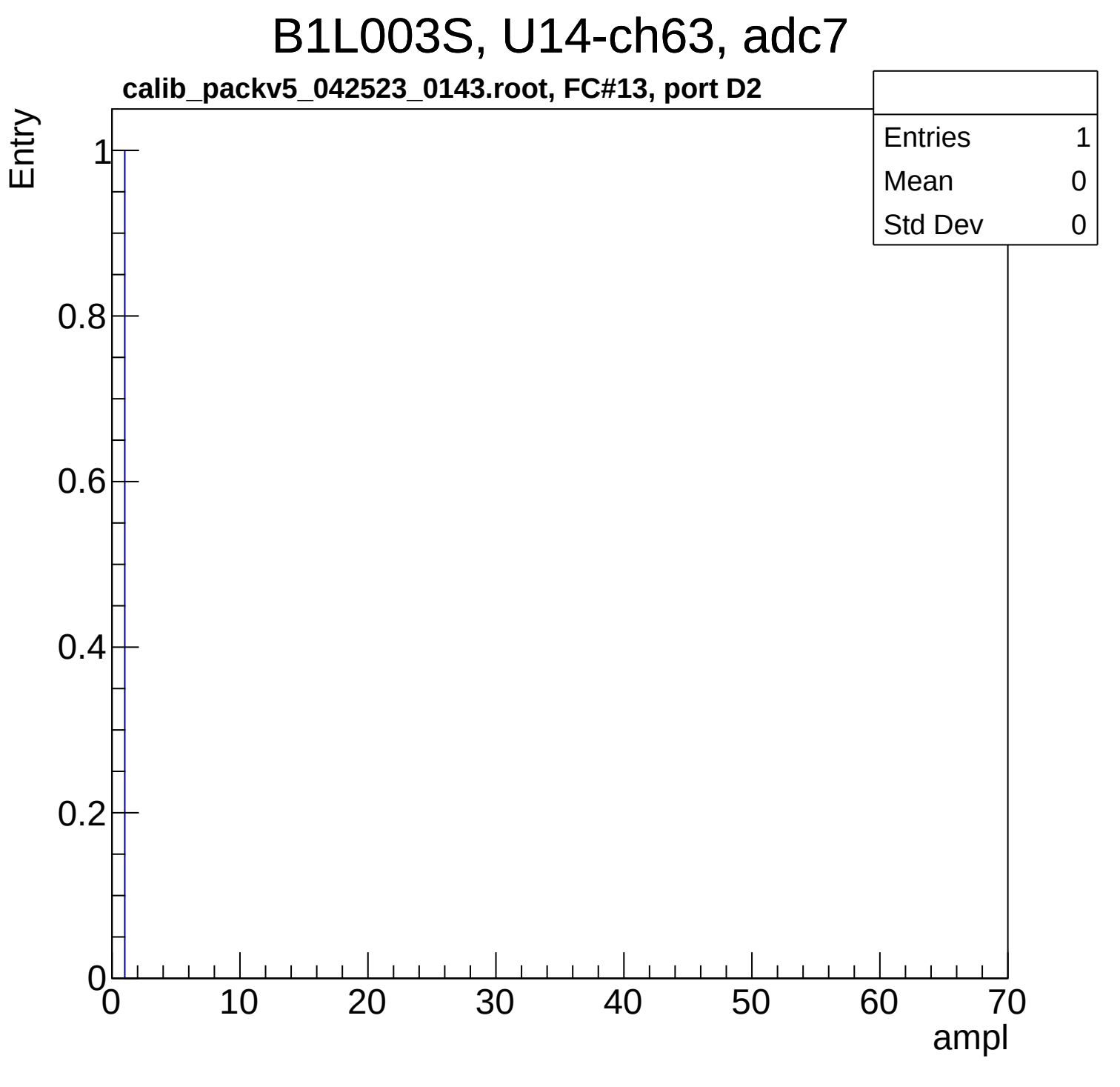

B1L003S, U14-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 40.8
Std Dev 3.168
Gaus mean : 41.2499
Gaus Width: 3.4469
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

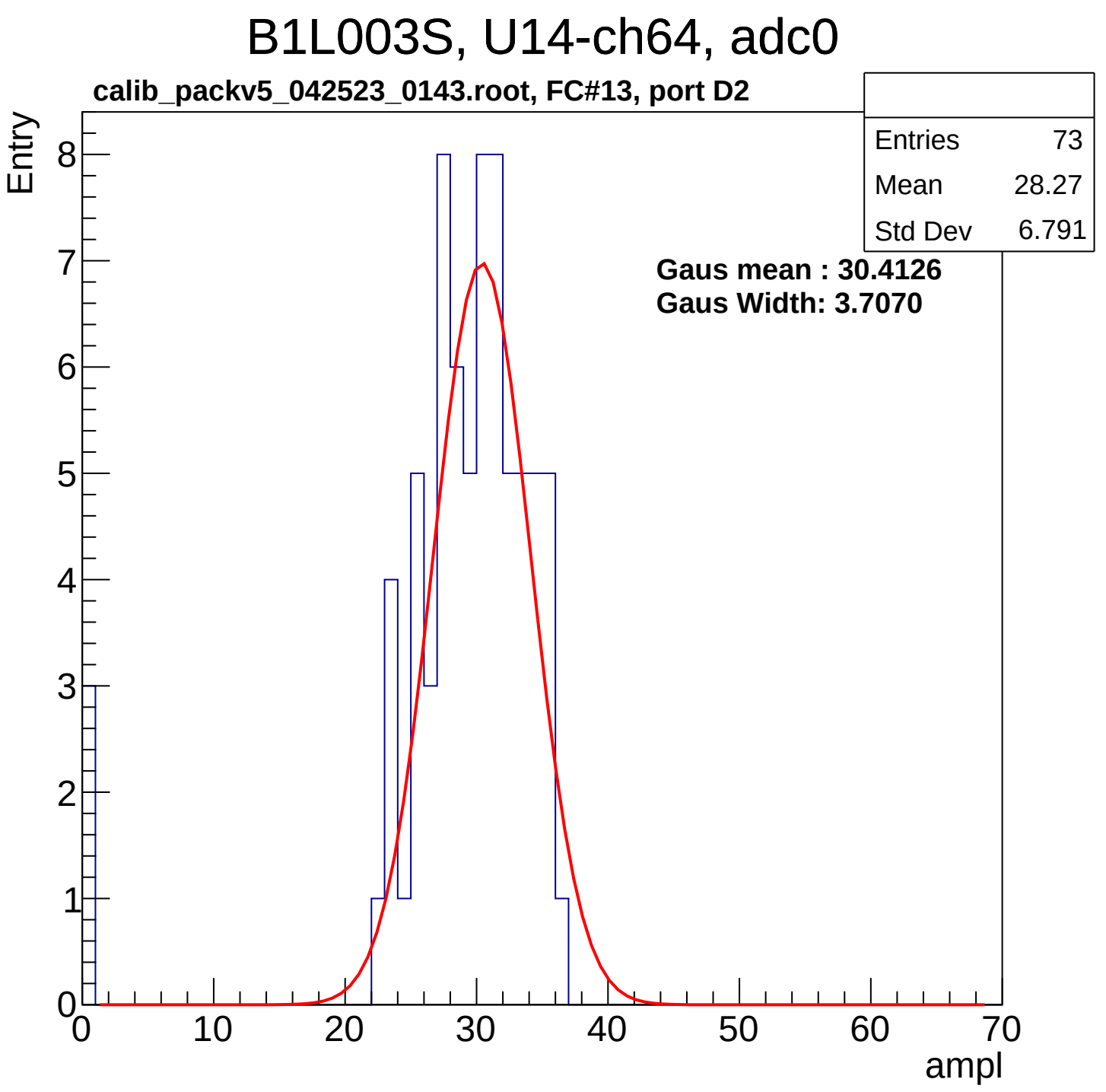

B1L003S, U14-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 48.31
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

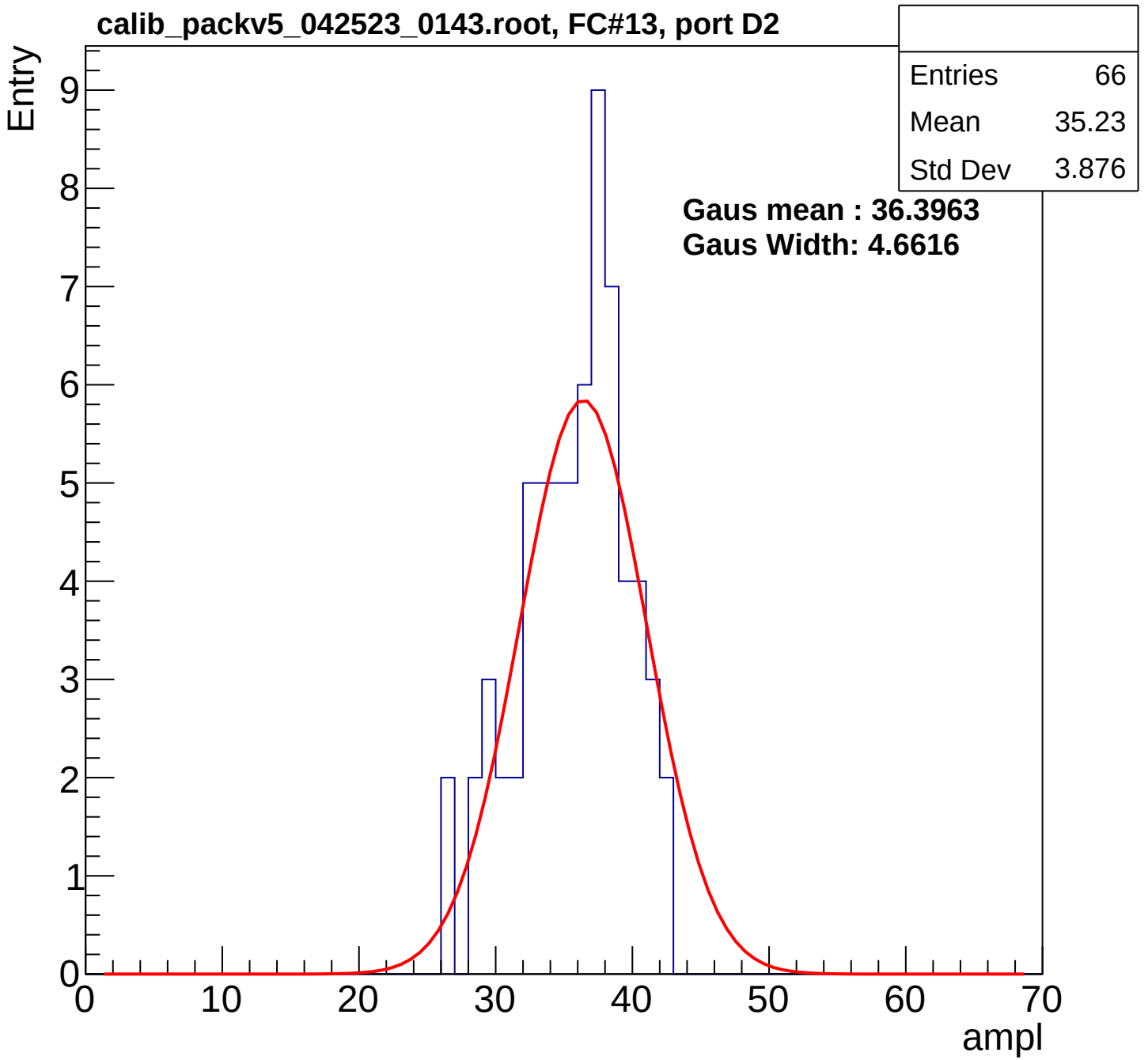

B1L003S, U14-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.39
Std Dev 3.407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

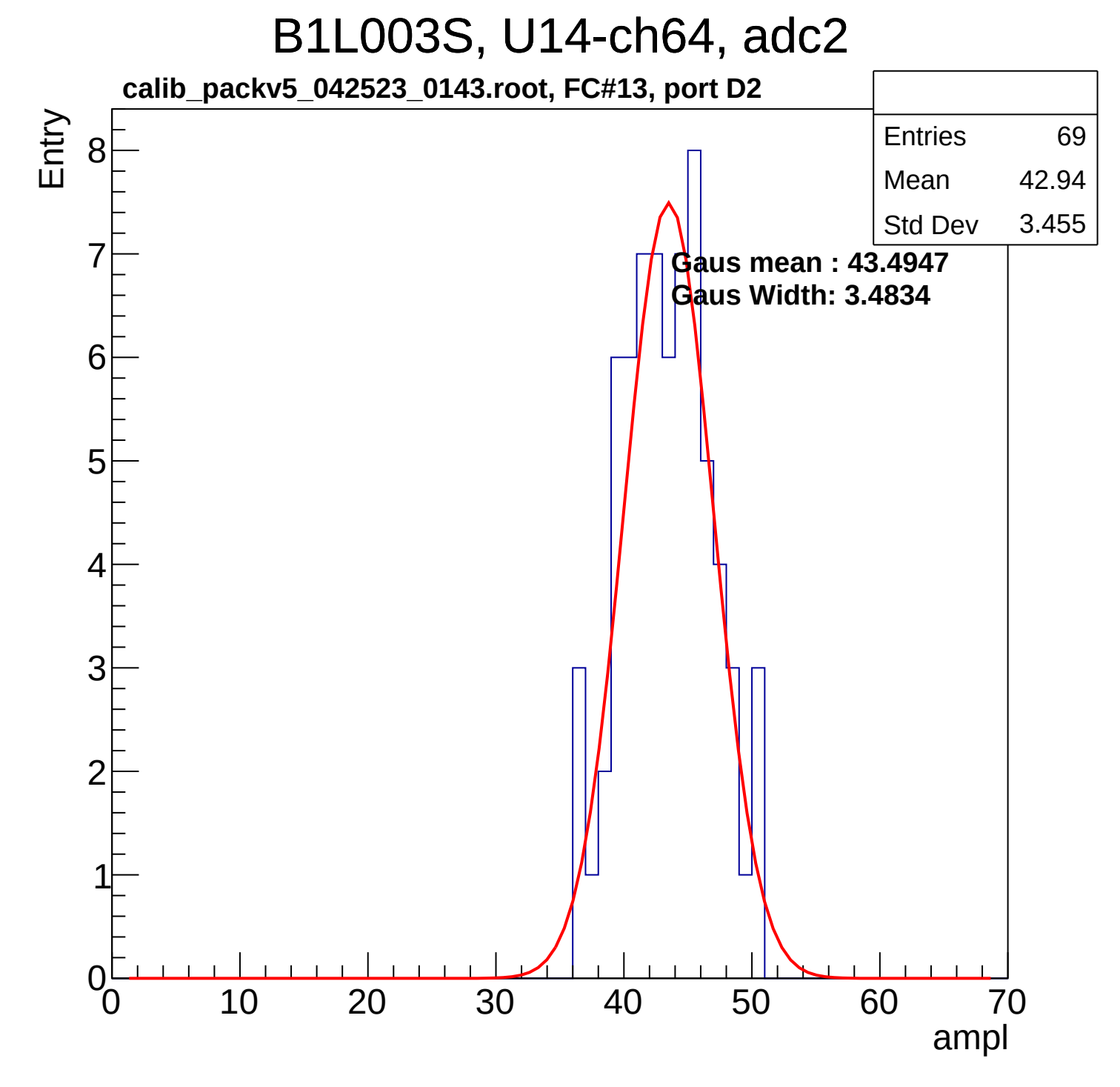

B1L003S, U14-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.47
Std Dev 8.338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

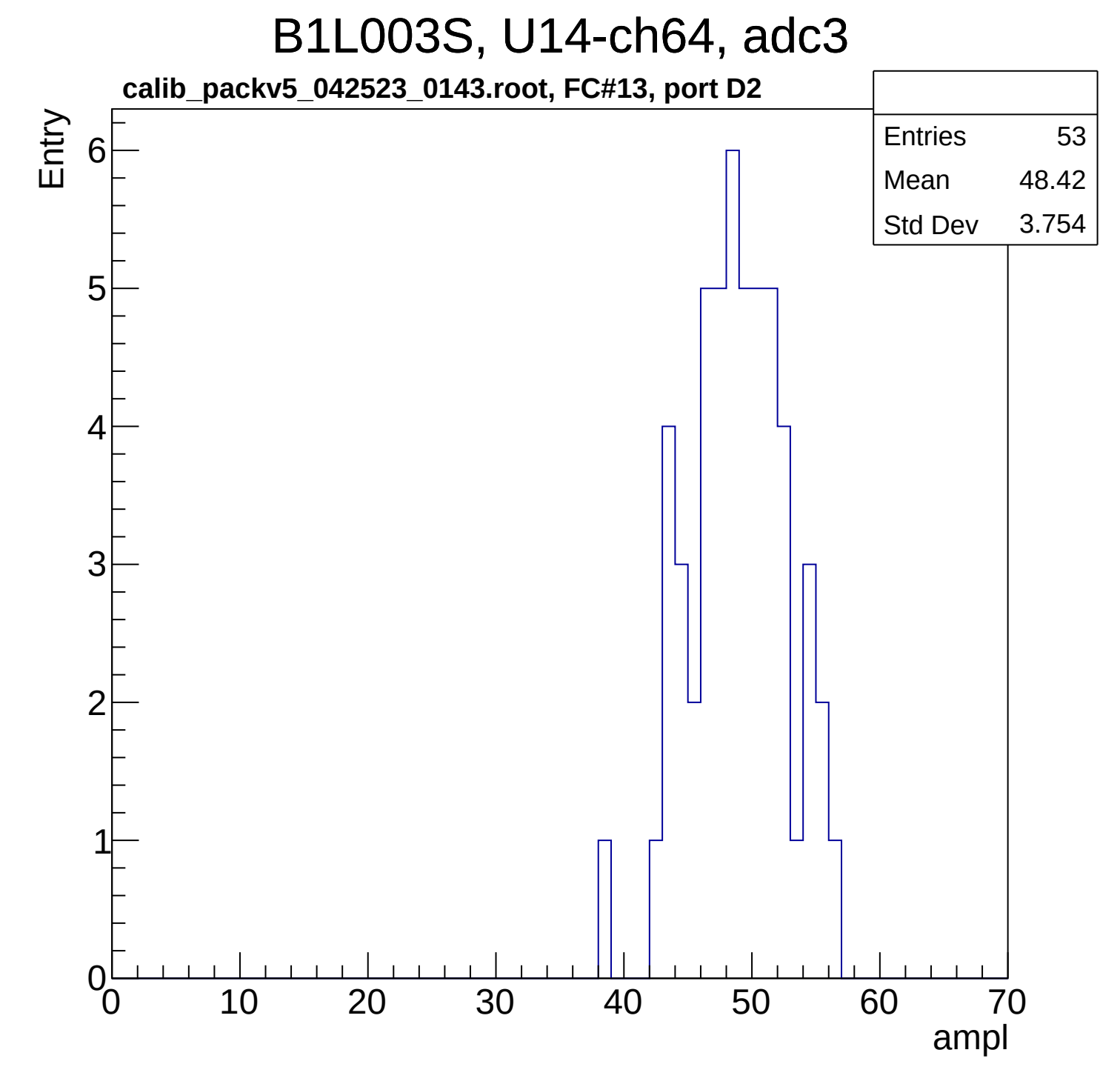

B1L003S, U14-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 60.89
Std Dev 1.728
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

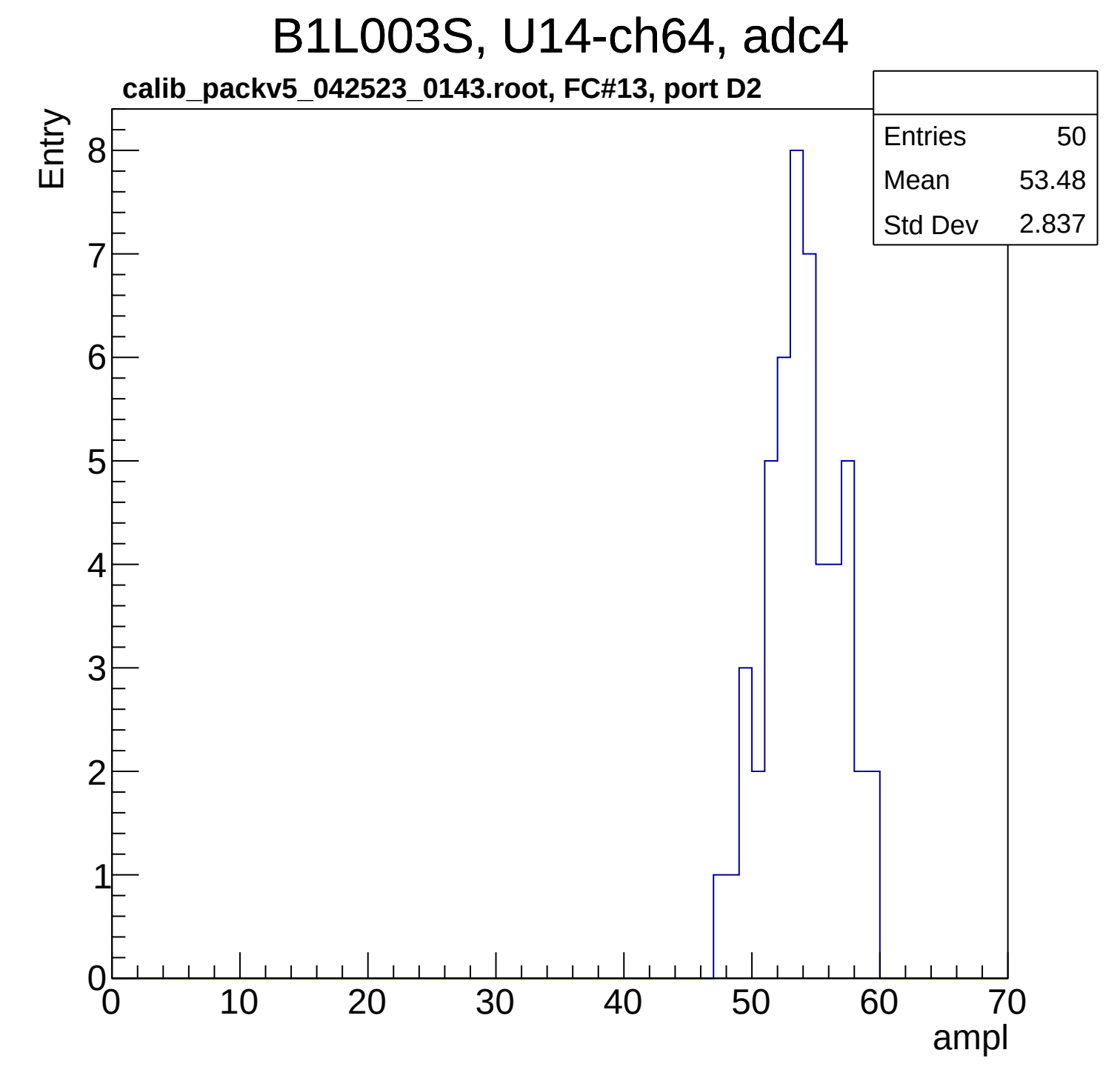

B1L003S, U14-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

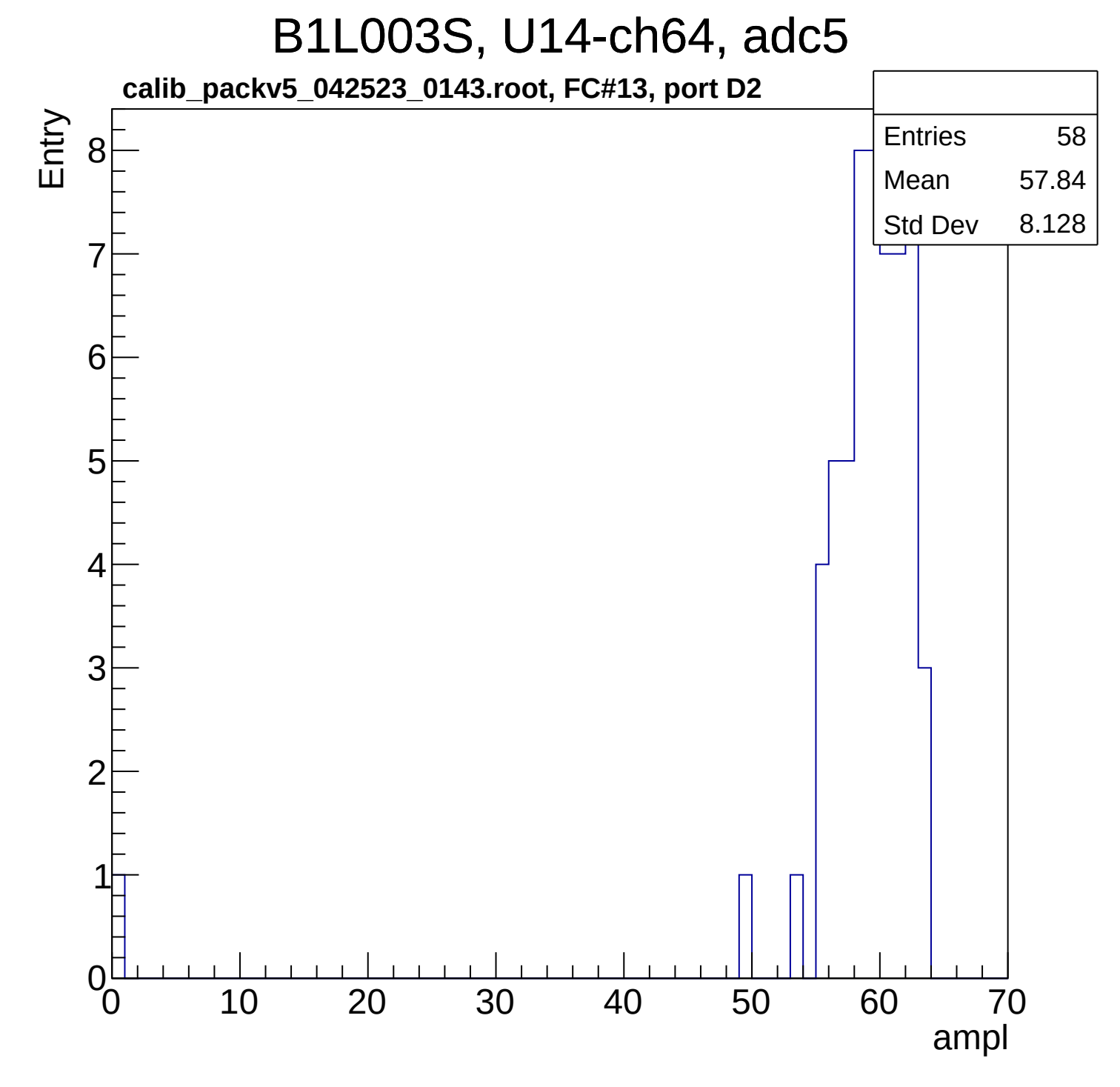

B1L003S, U14-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.27
Std Dev 6.791
Gaus mean : 30.4126
Gaus Width: 3.7070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

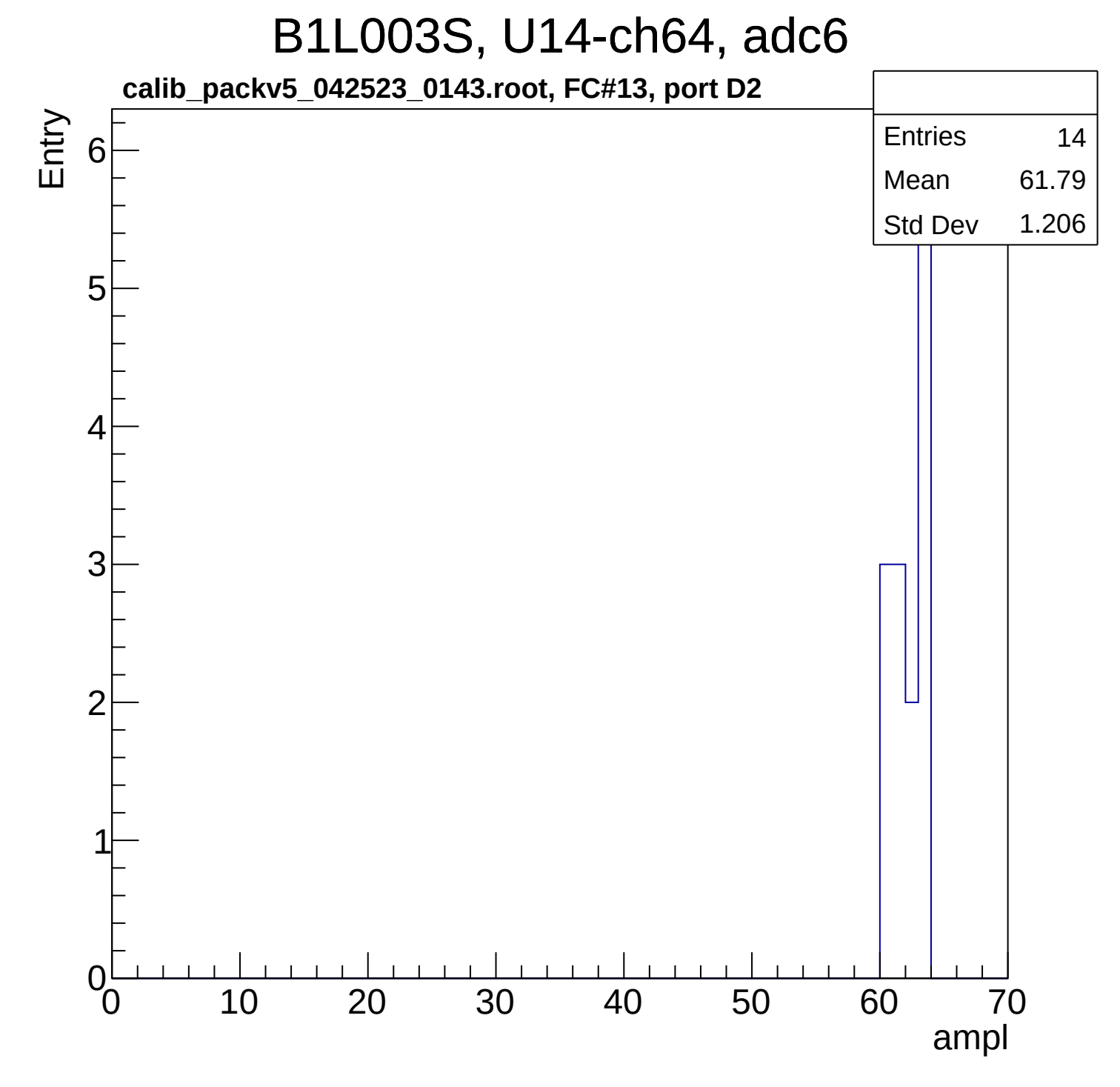

B1L003S, U14-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.23
Std Dev 3.876
Gaus mean : 36.3963
Gaus Width: 4.6616
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

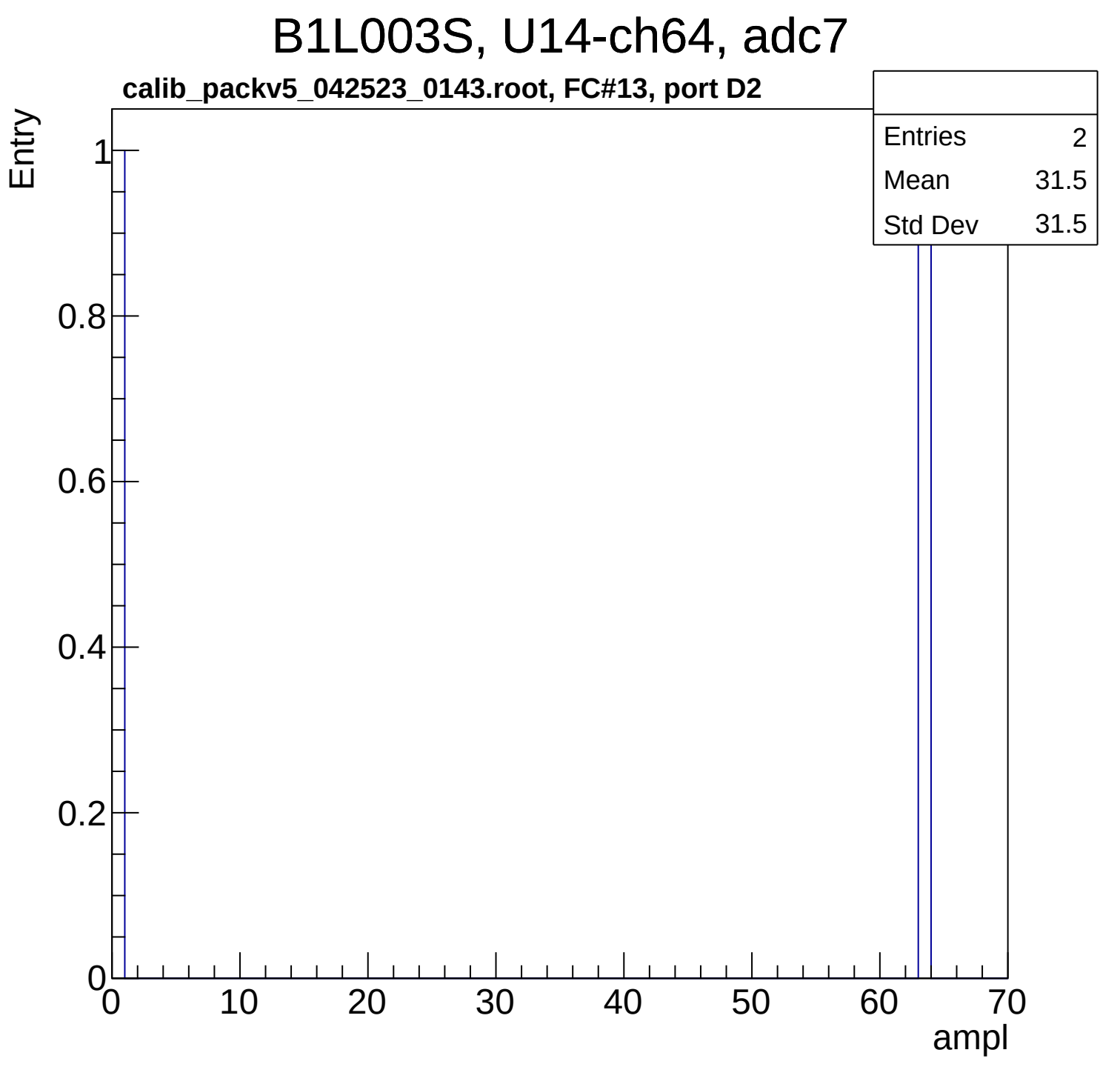

B1L003S, U14-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.94
Std Dev 3.455
Gaus mean : 43.4947
Gaus Width: 3.4834
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

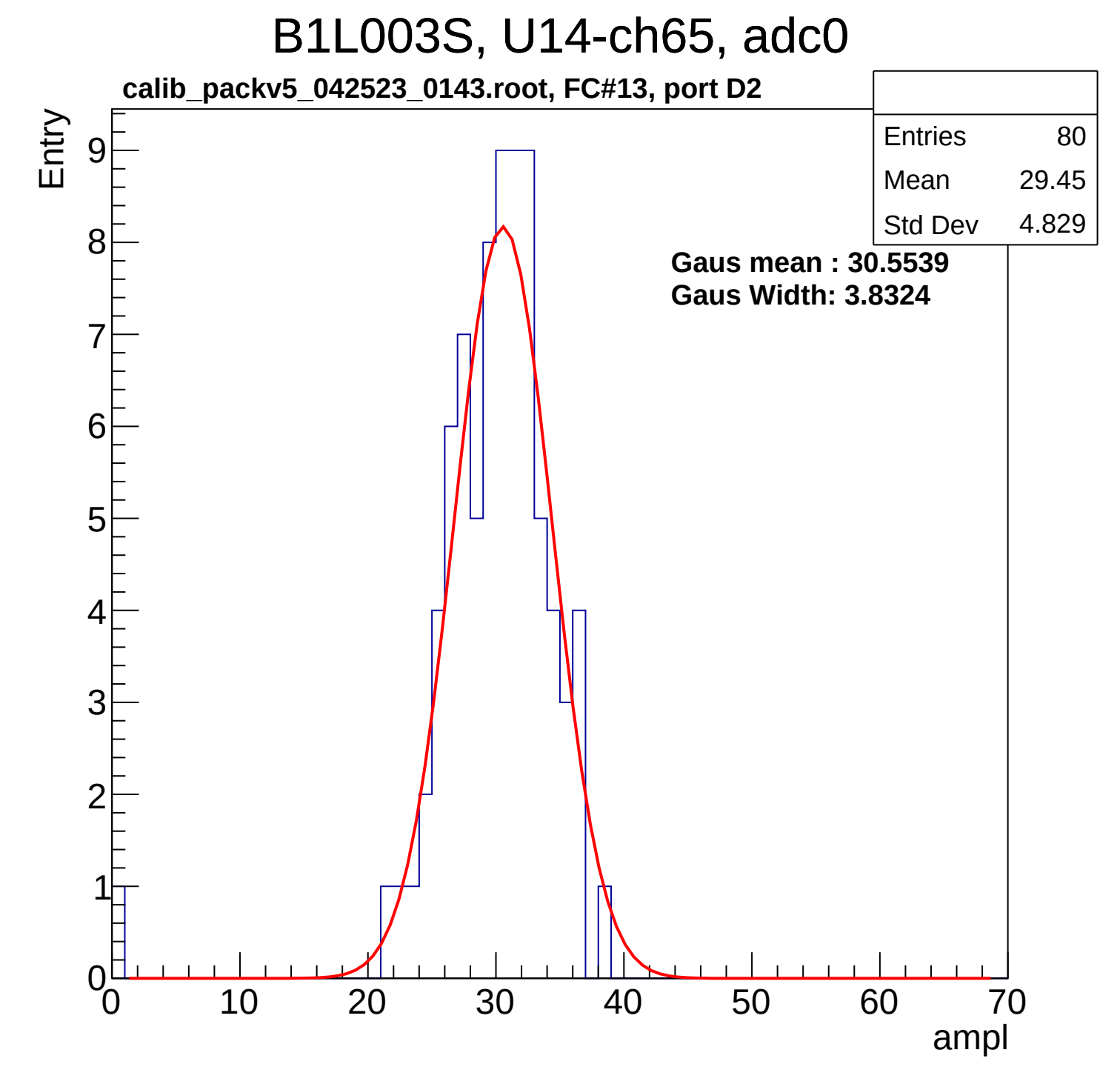

B1L003S, U14-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.42
Std Dev 3.754
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

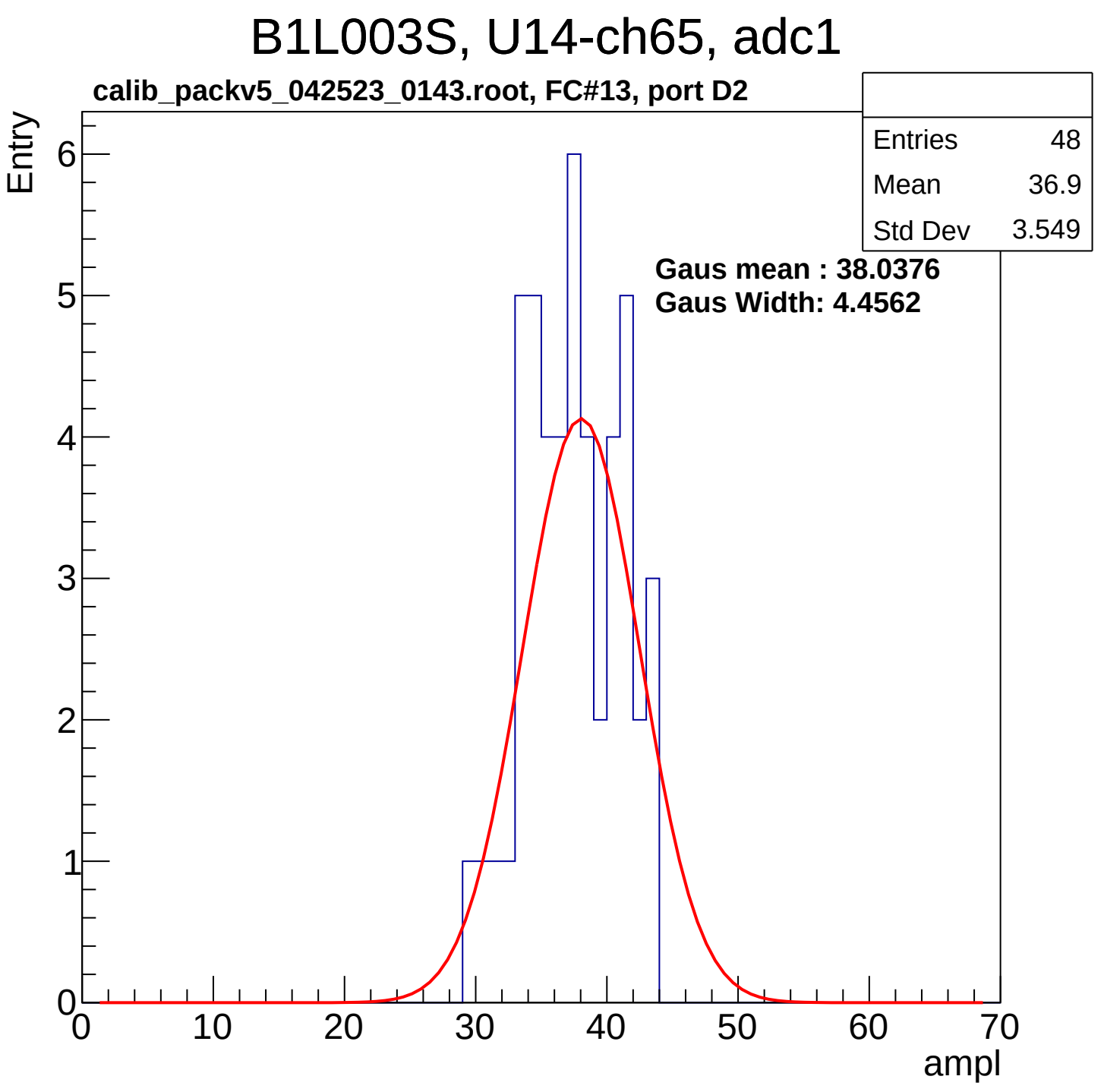

B1L003S, U14-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 53.48
Std Dev 2.837
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

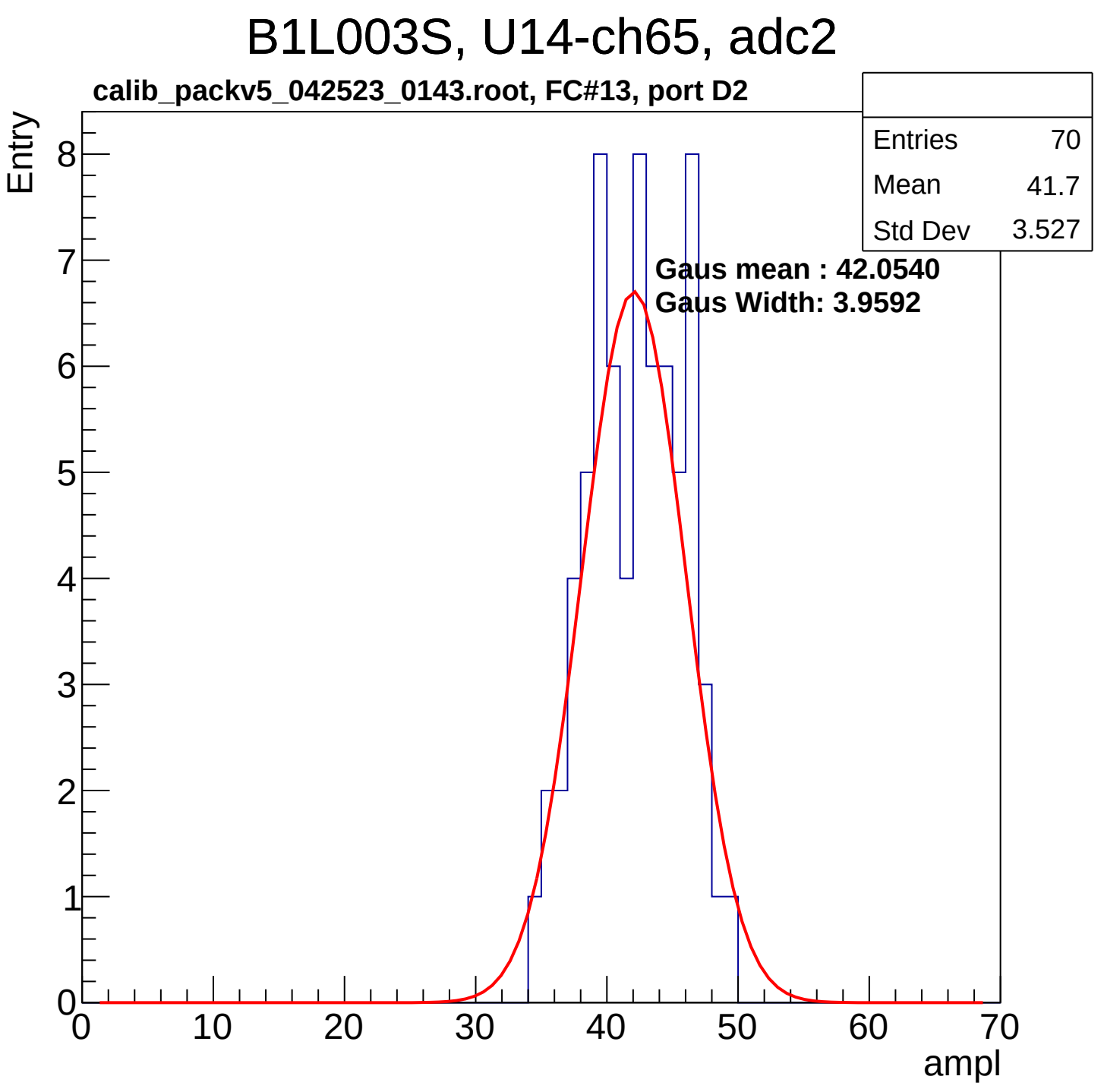

B1L003S, U14-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.84
Std Dev 8.128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

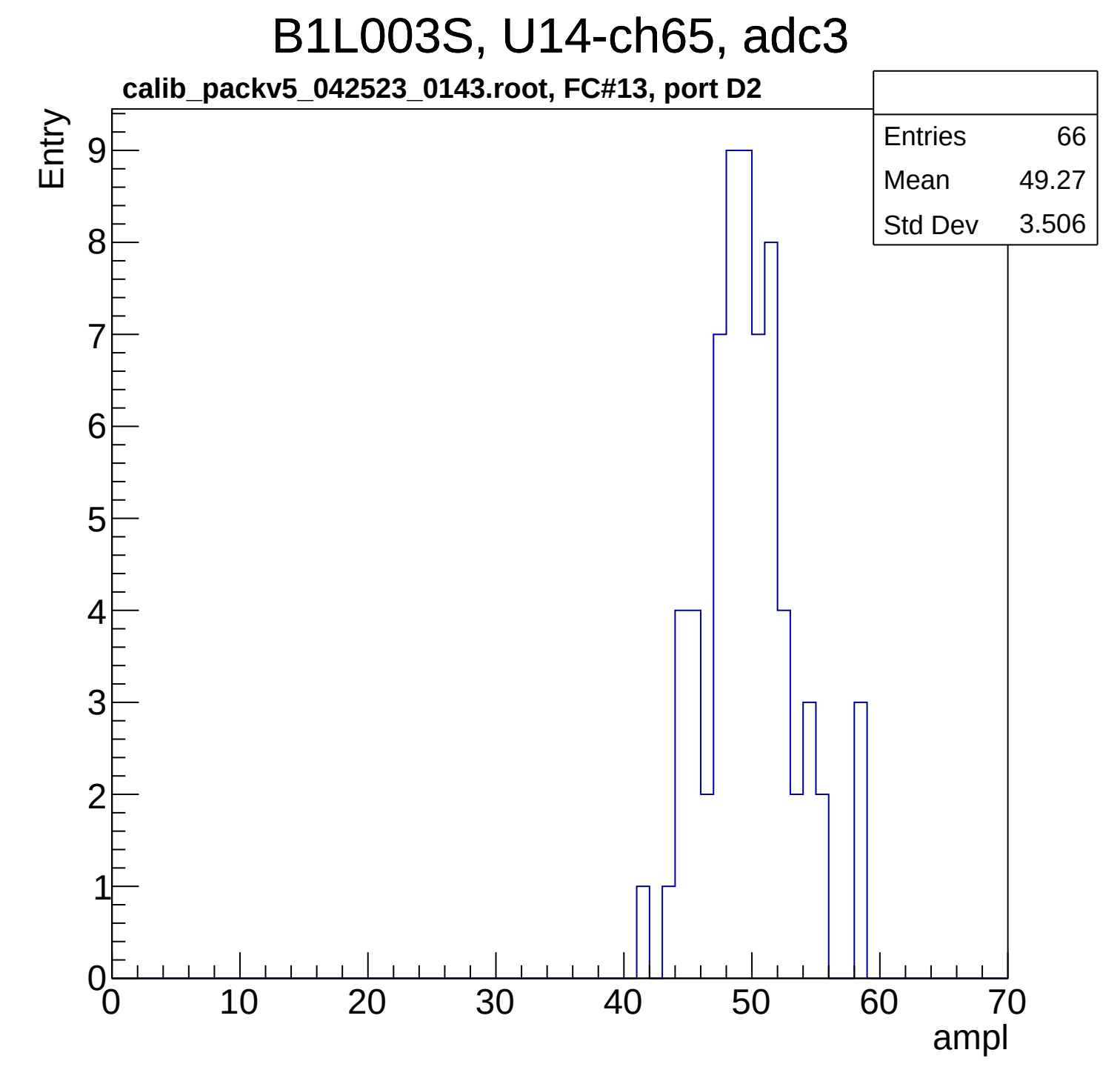

B1L003S, U14-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.79
Std Dev 1.206
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

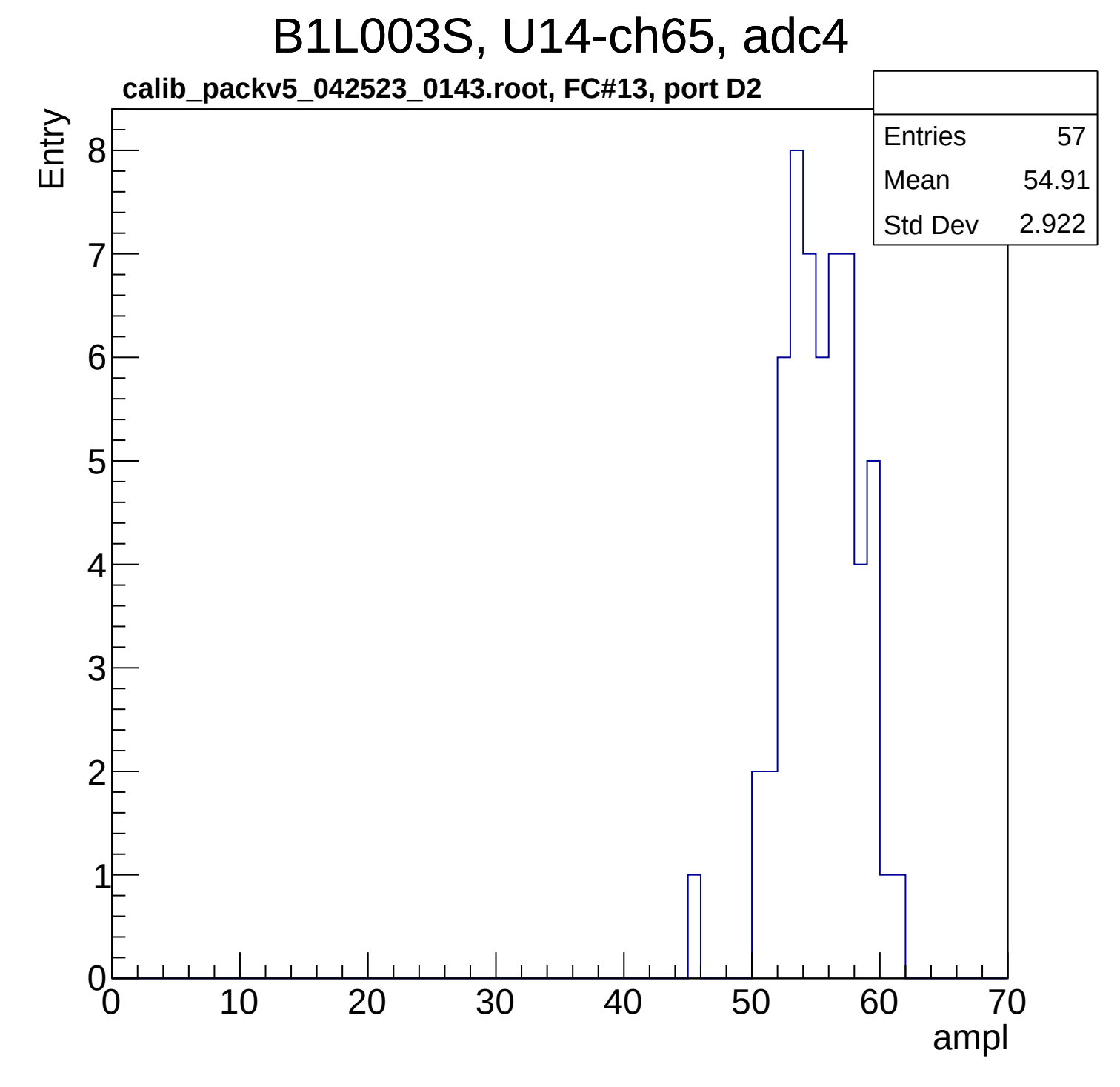

B1L003S, U14-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

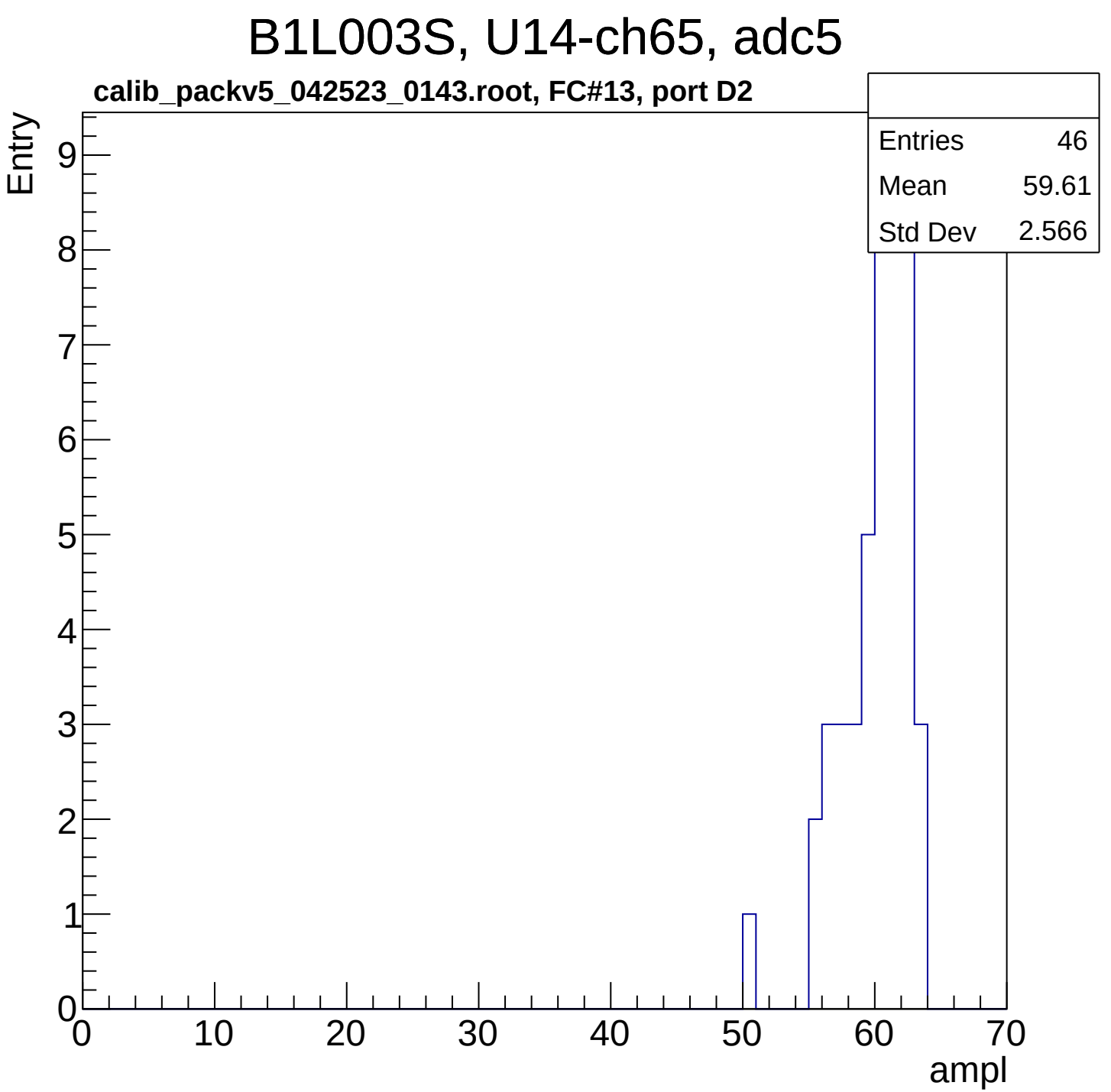

B1L003S, U14-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 29.45
Std Dev 4.829
Gaus mean : 30.5539
Gaus Width: 3.8324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

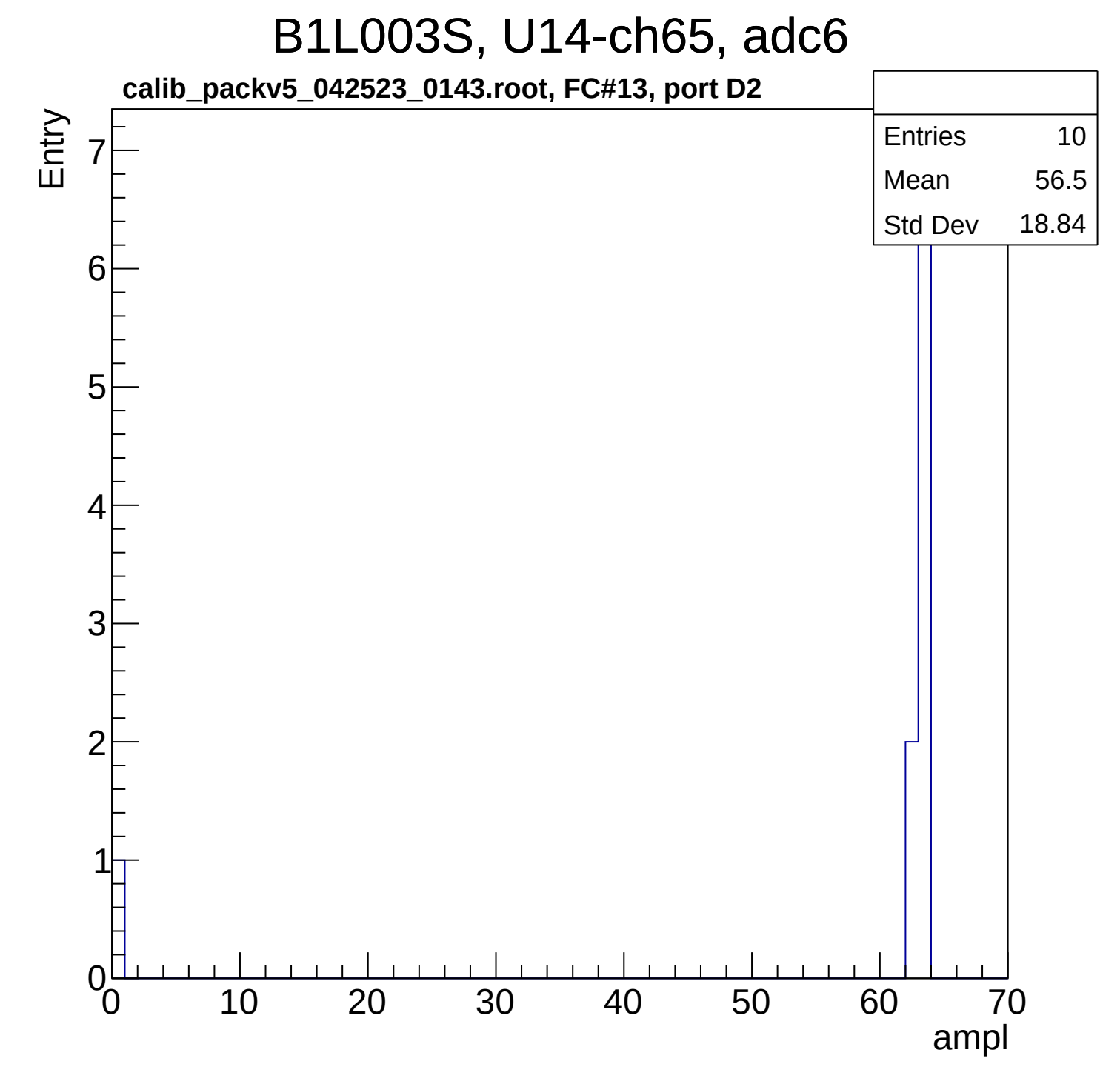

B1L003S, U14-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 36.9
Std Dev 3.549
Gaus mean : 38.0376
Gaus Width: 4.4562
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

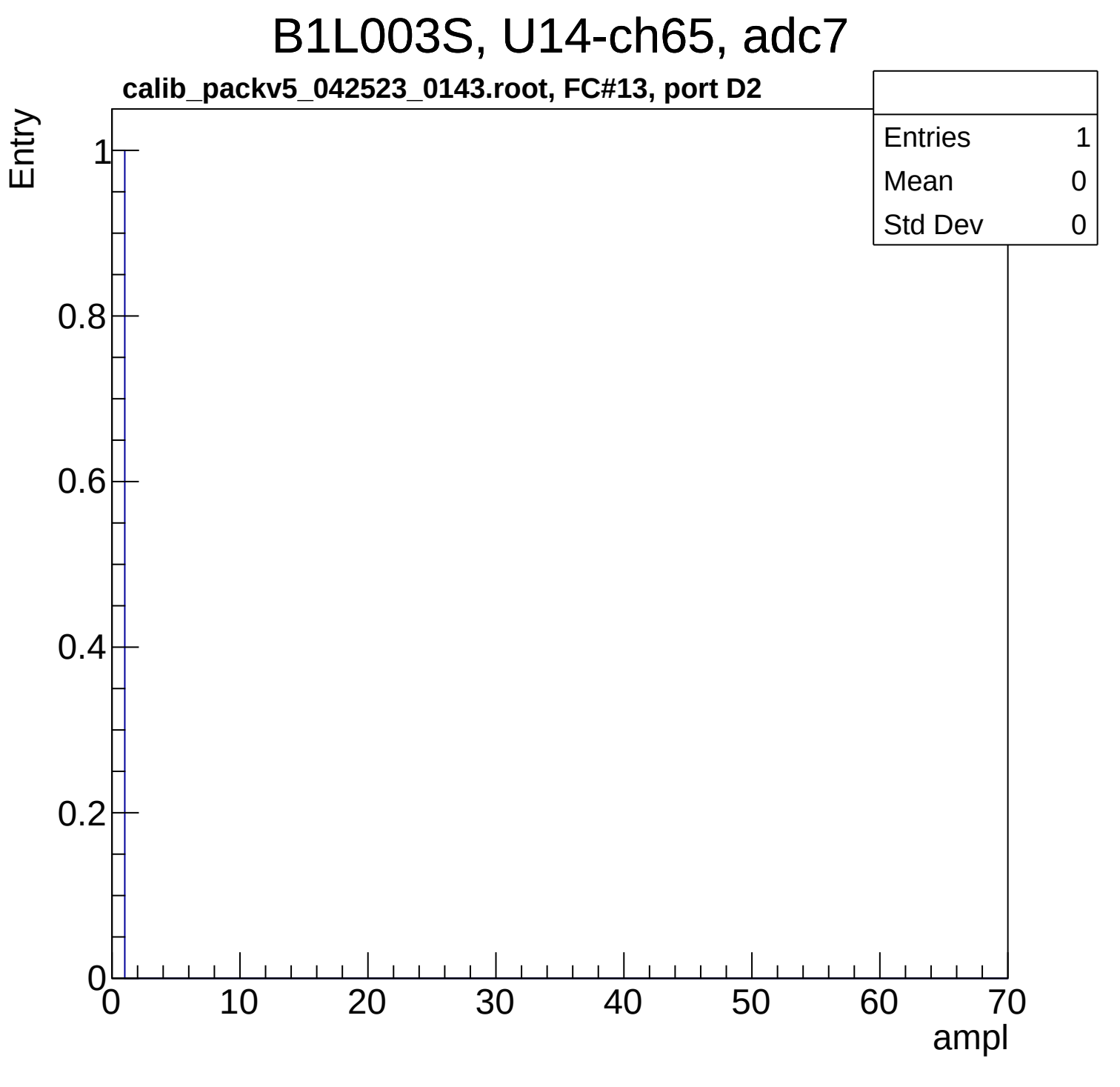

B1L003S, U14-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.7
Std Dev 3.527
Gaus mean : 42.0540
Gaus Width: 3.9592
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

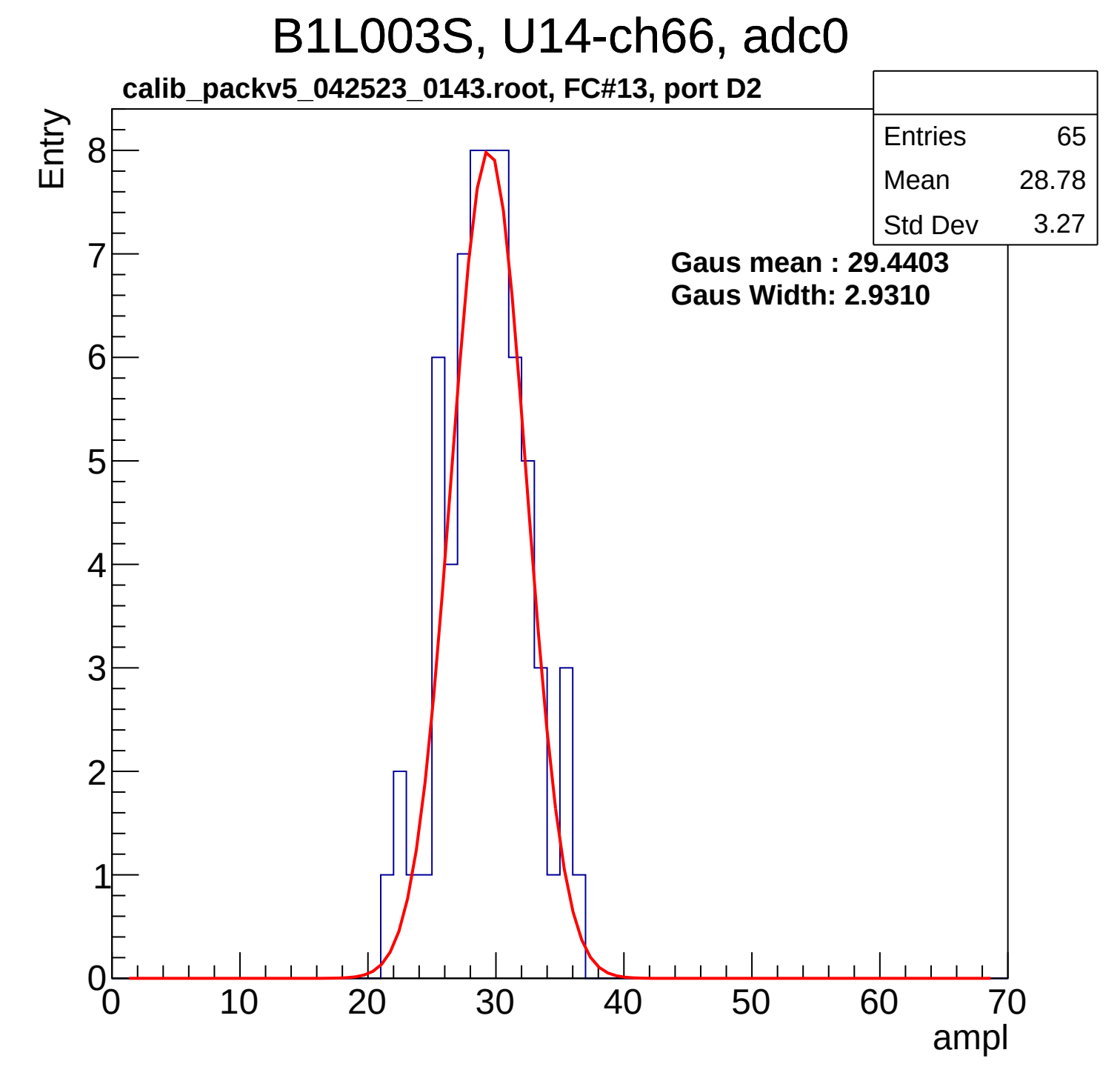

B1L003S, U14-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.27
Std Dev 3.506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

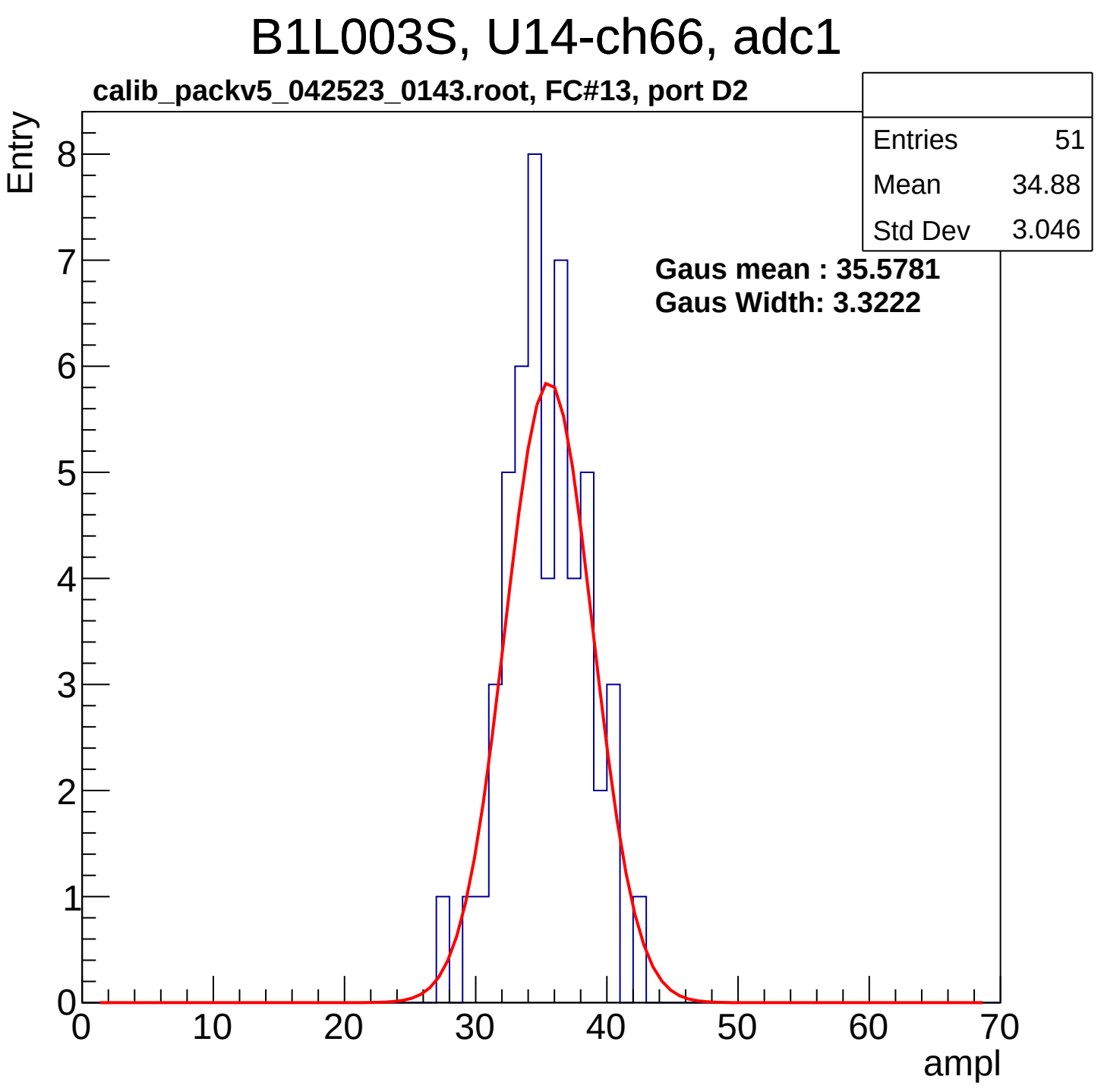

B1L003S, U14-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.91
Std Dev 2.922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

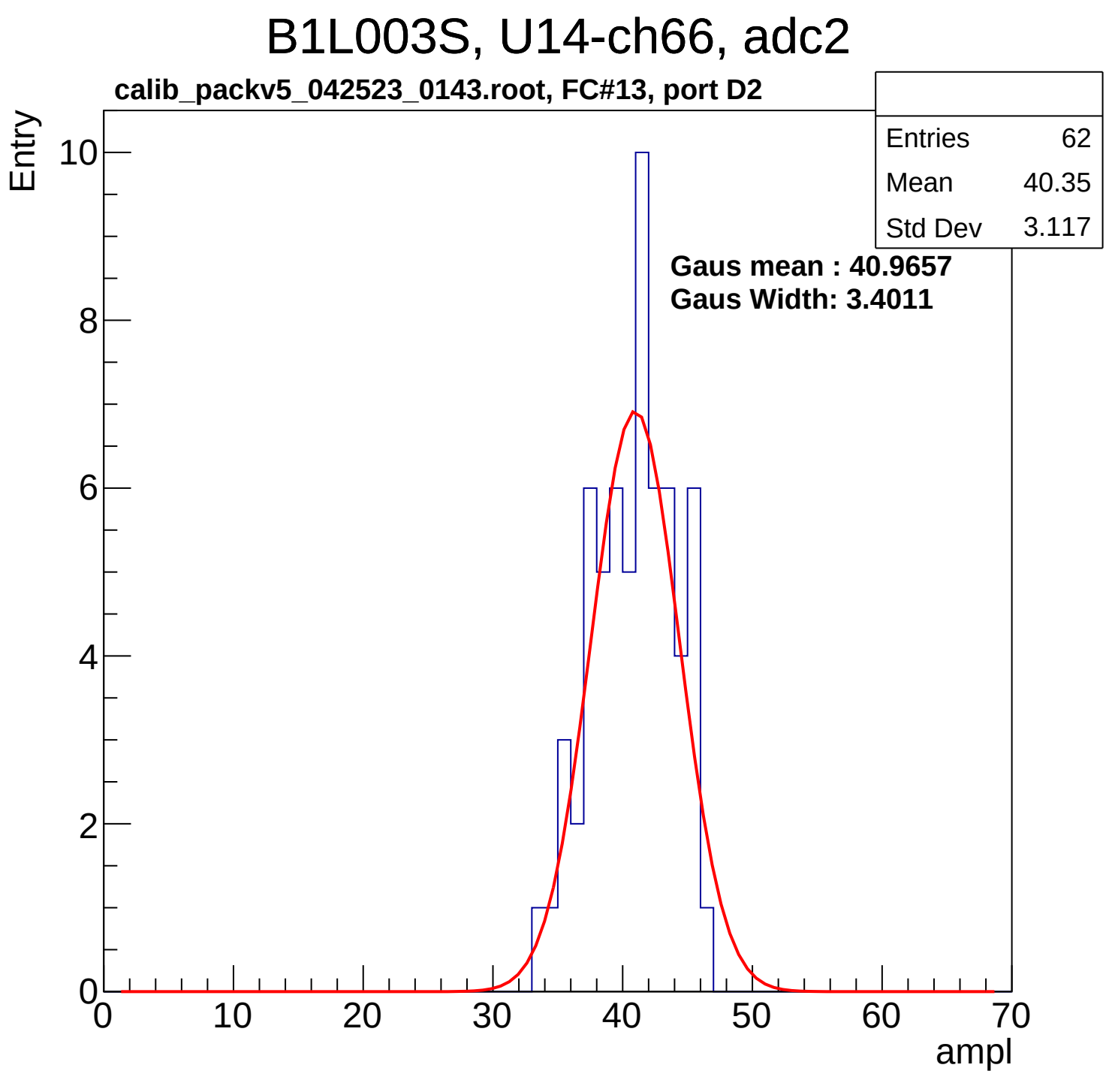

B1L003S, U14-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.61
Std Dev 2.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

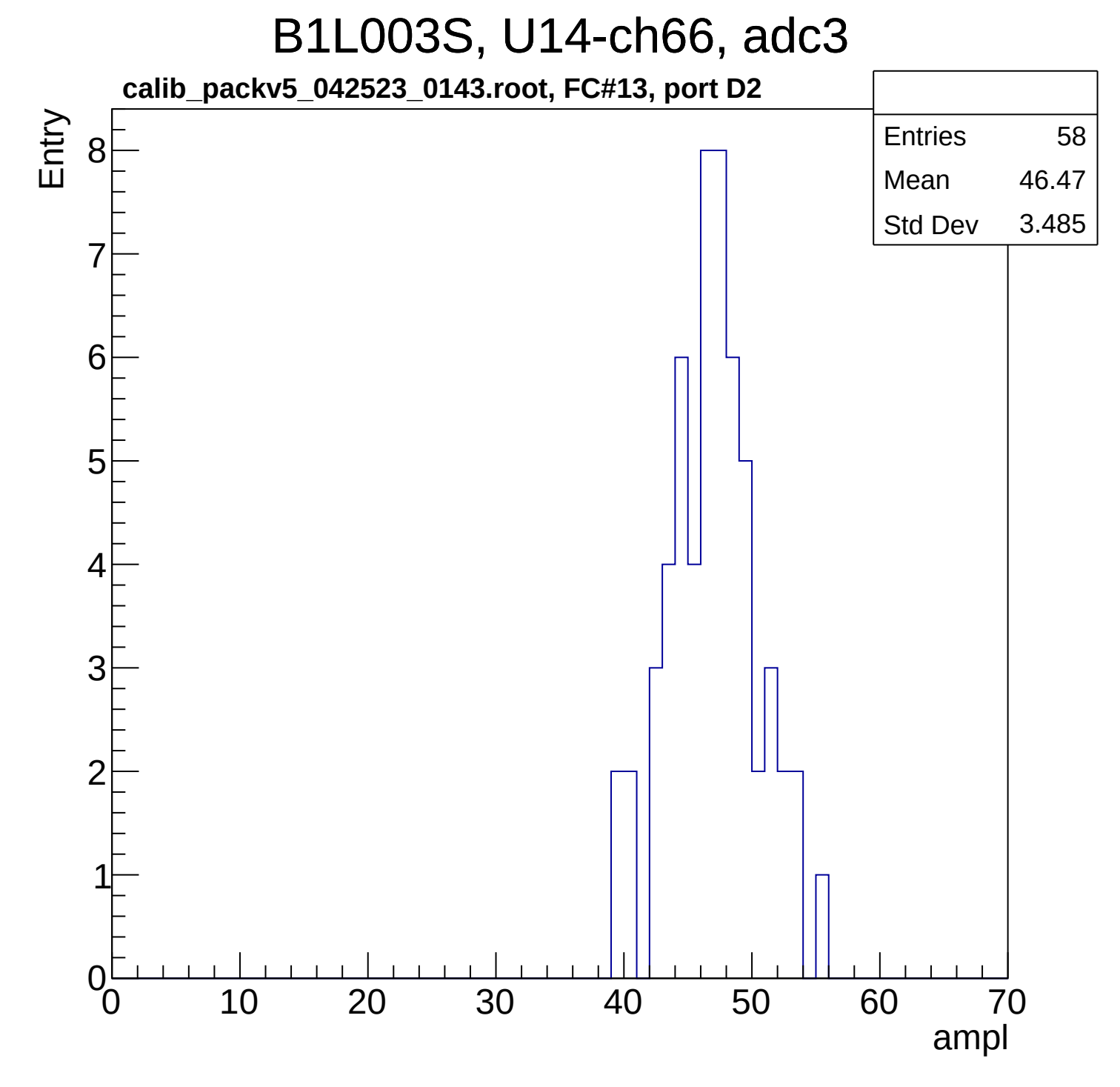

B1L003S, U14-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 56.5
Std Dev 18.84
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

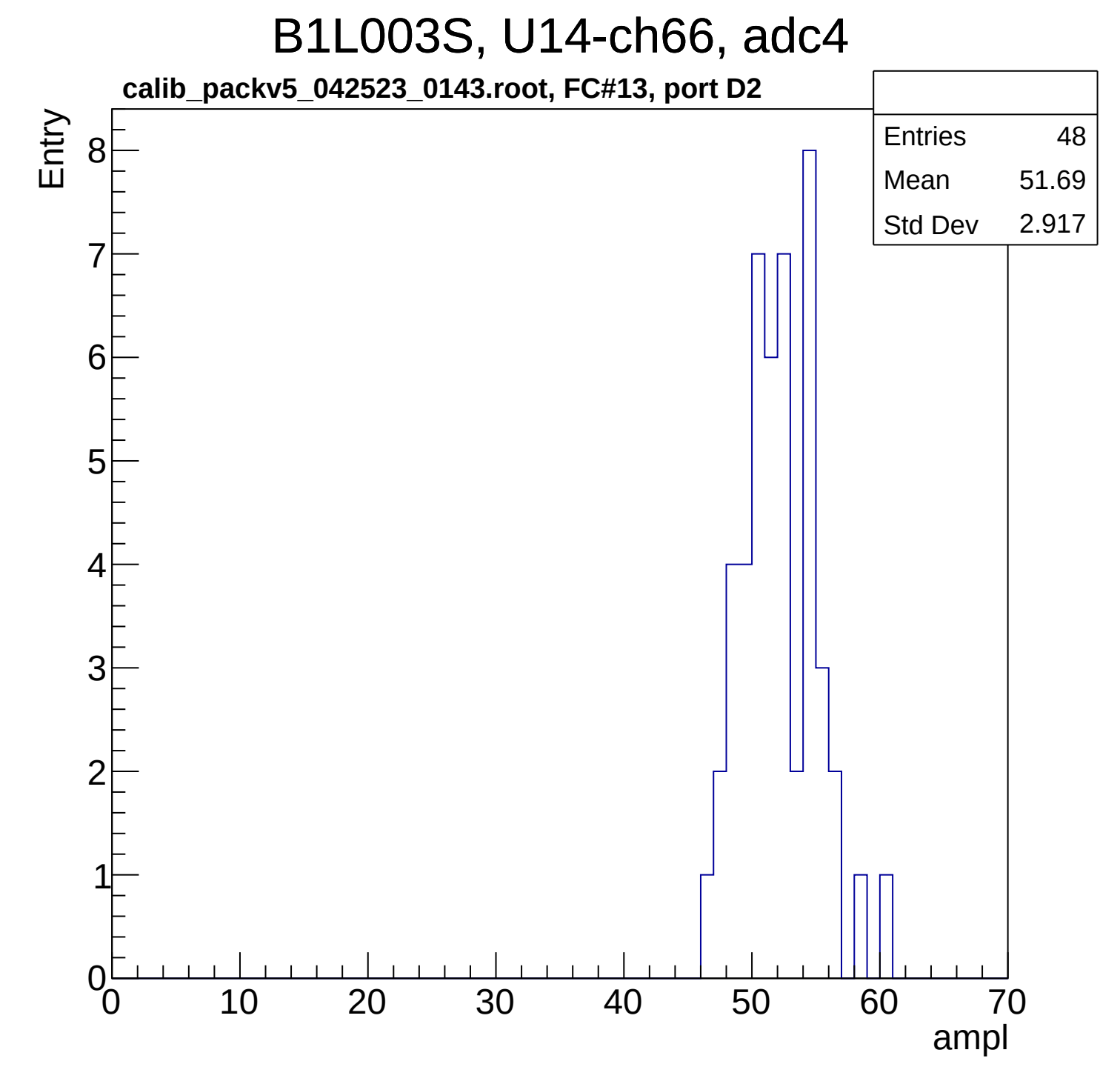

B1L003S, U14-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

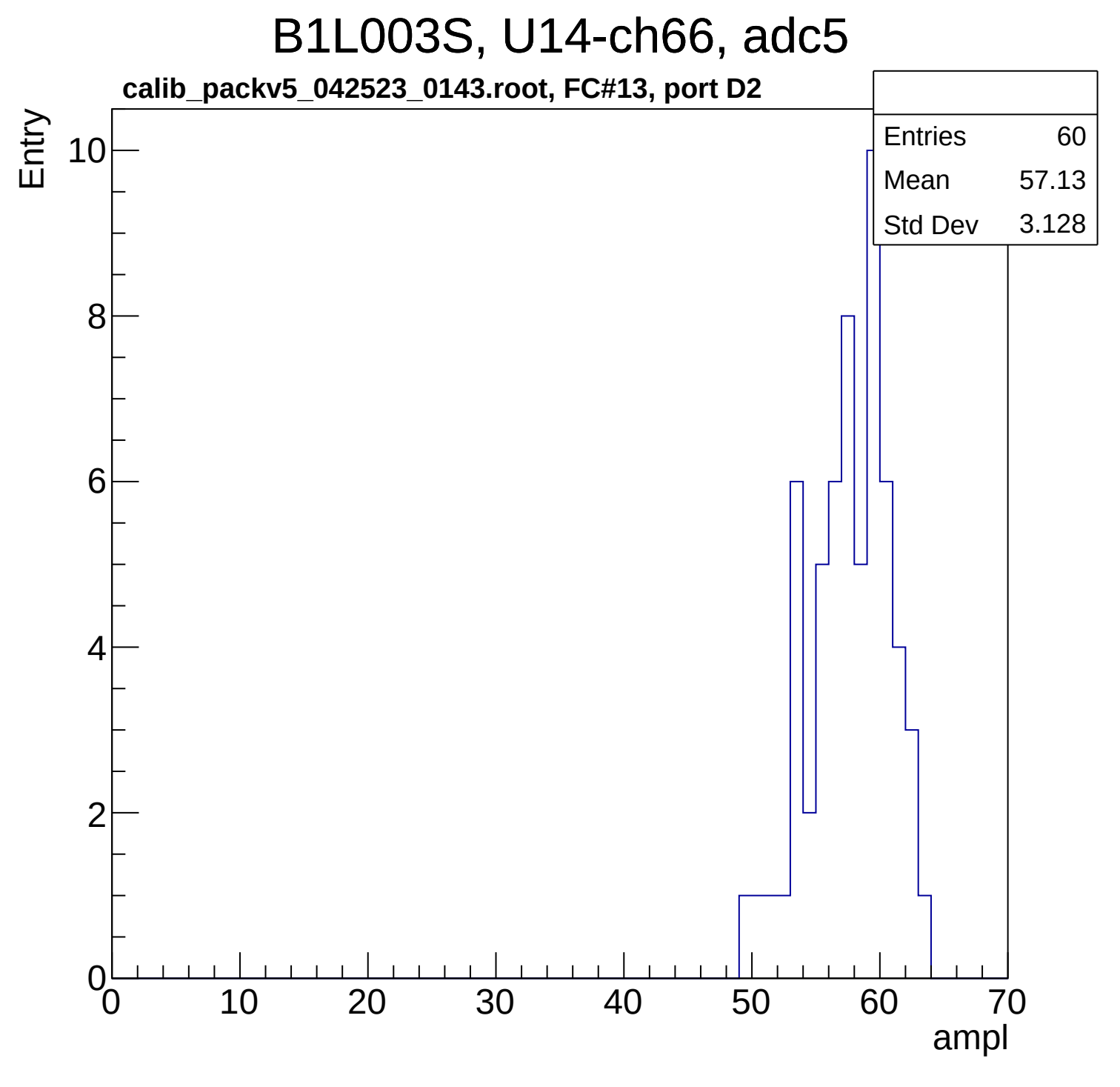

B1L003S, U14-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.78
Std Dev 3.27
Gaus mean : 29.4403
Gaus Width: 2.9310
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

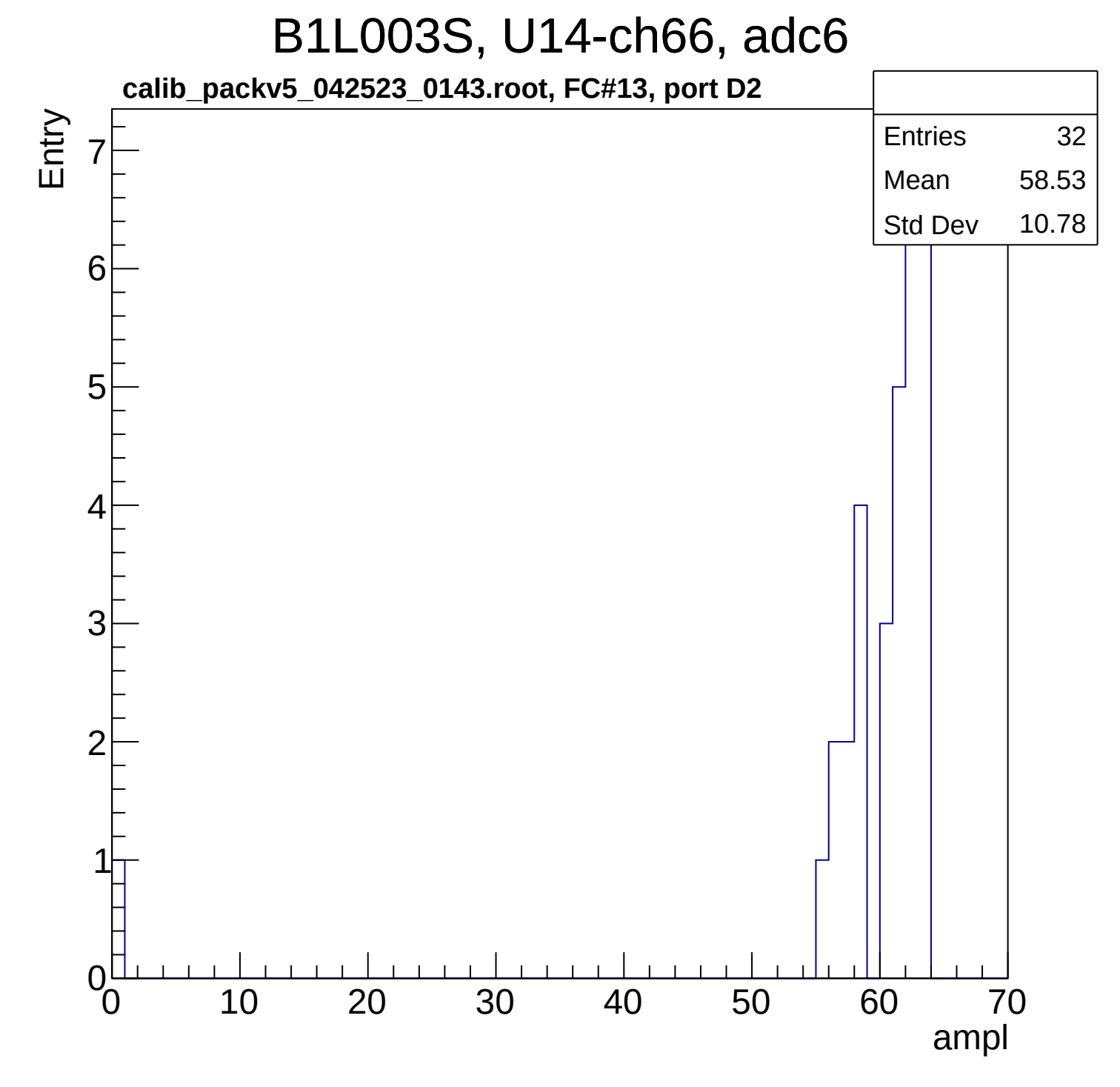

B1L003S, U14-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 34.88
Std Dev 3.046
Gaus mean : 35.5781
Gaus Width: 3.3222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

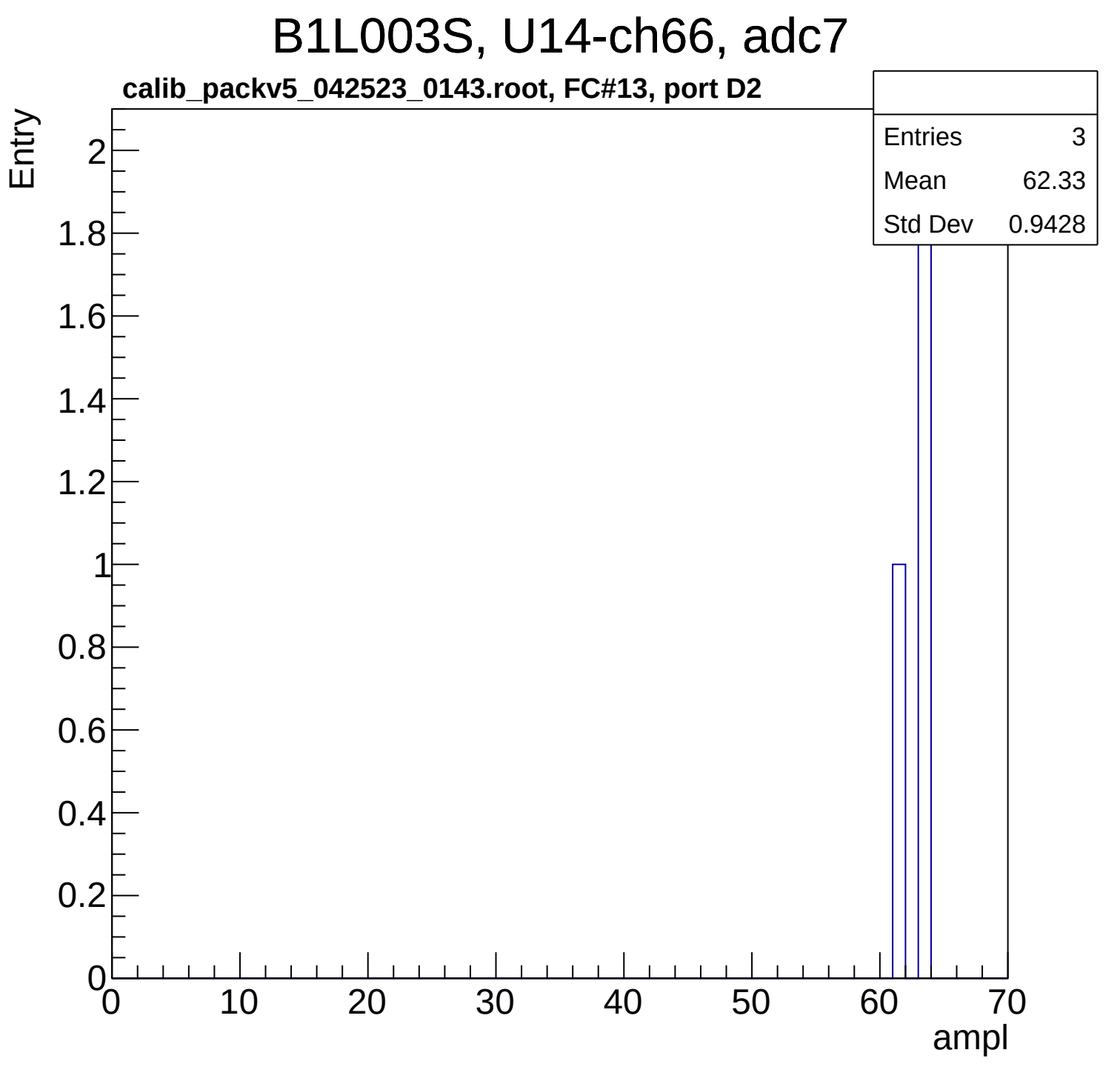

B1L003S, U14-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 40.35
Std Dev 3.117
Gaus mean : 40.9657
Gaus Width: 3.4011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

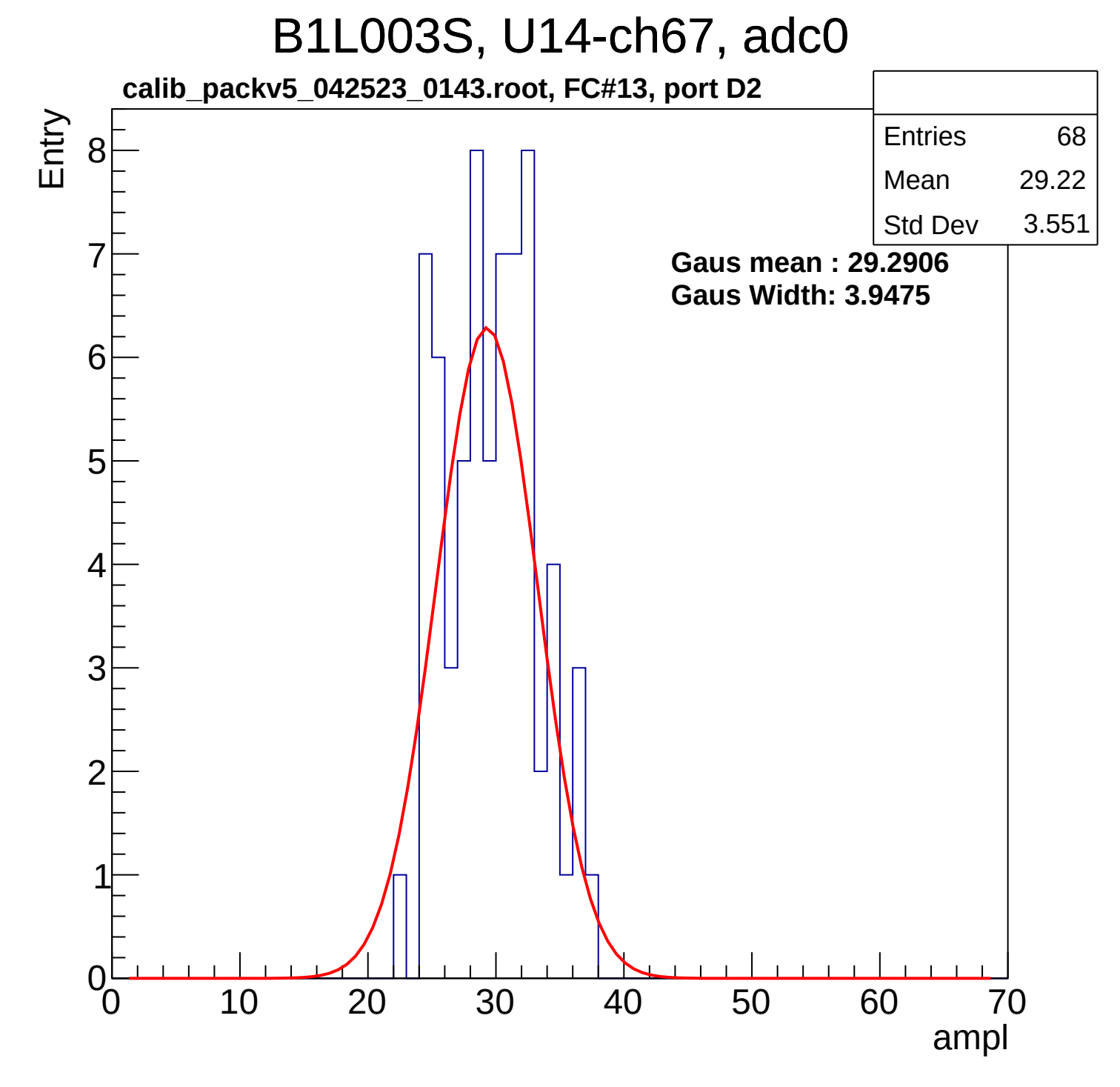

B1L003S, U14-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 46.47
Std Dev 3.485
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

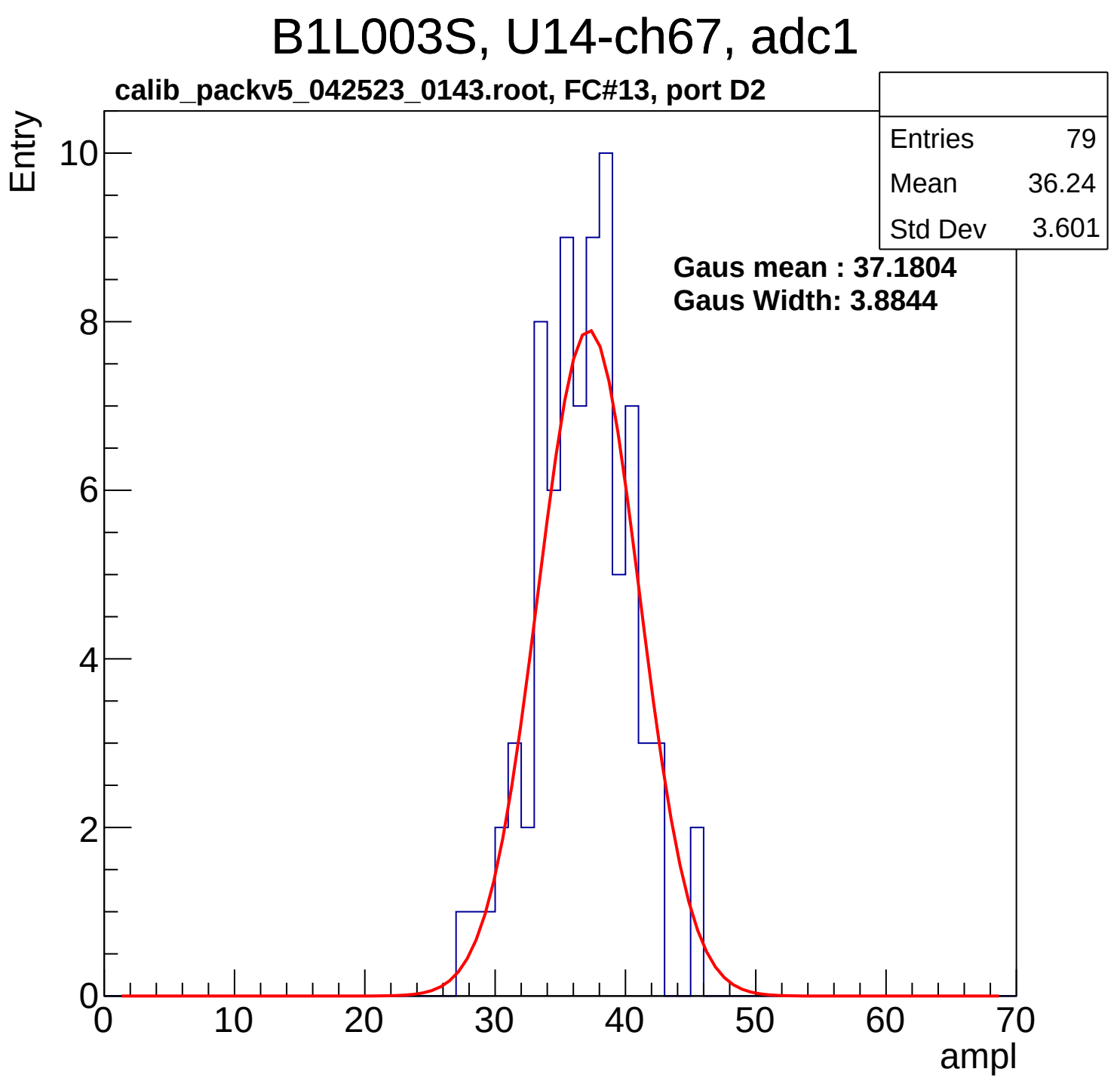

B1L003S, U14-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 51.69
Std Dev 2.917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

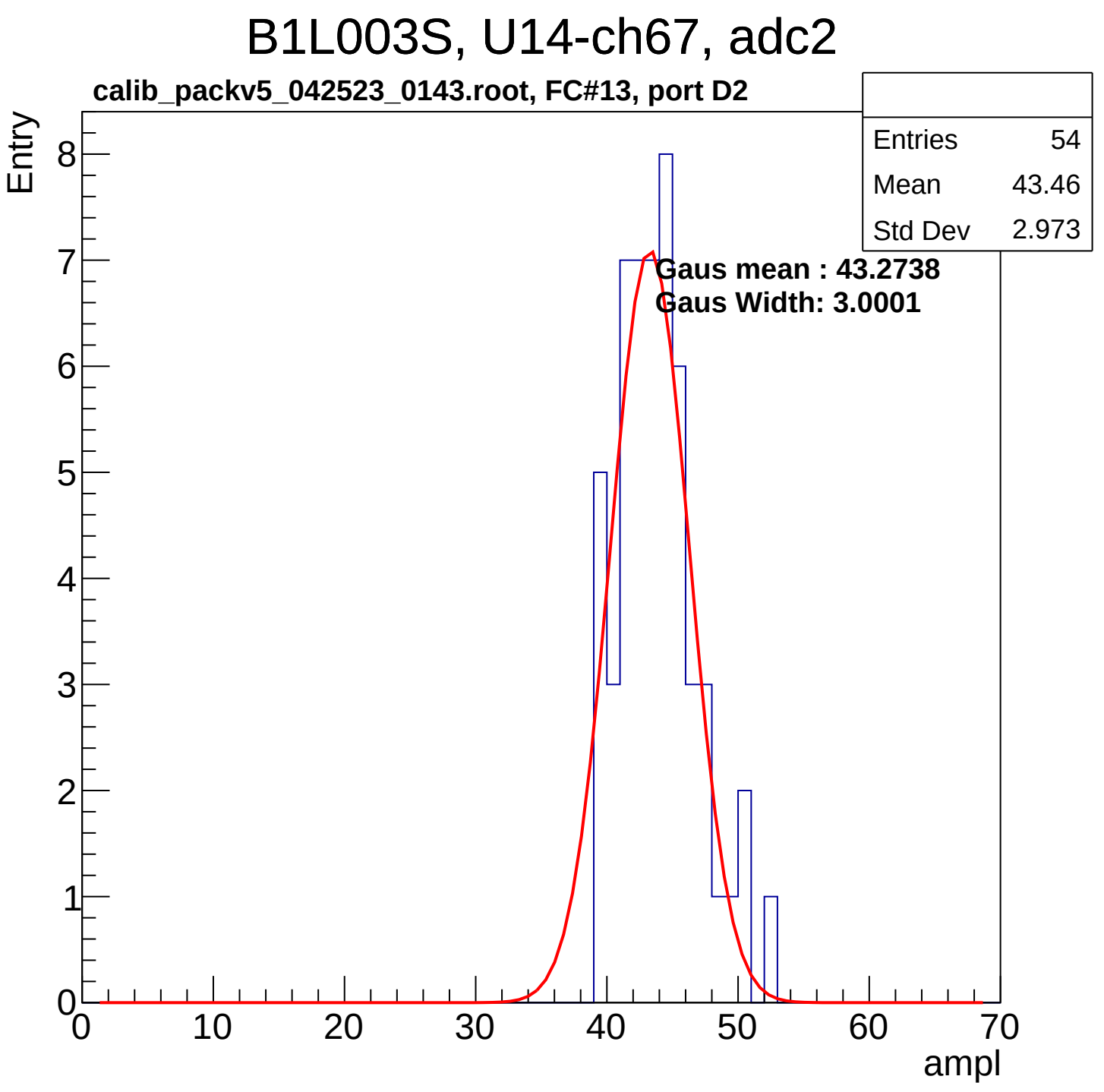

B1L003S, U14-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.13
Std Dev 3.128
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

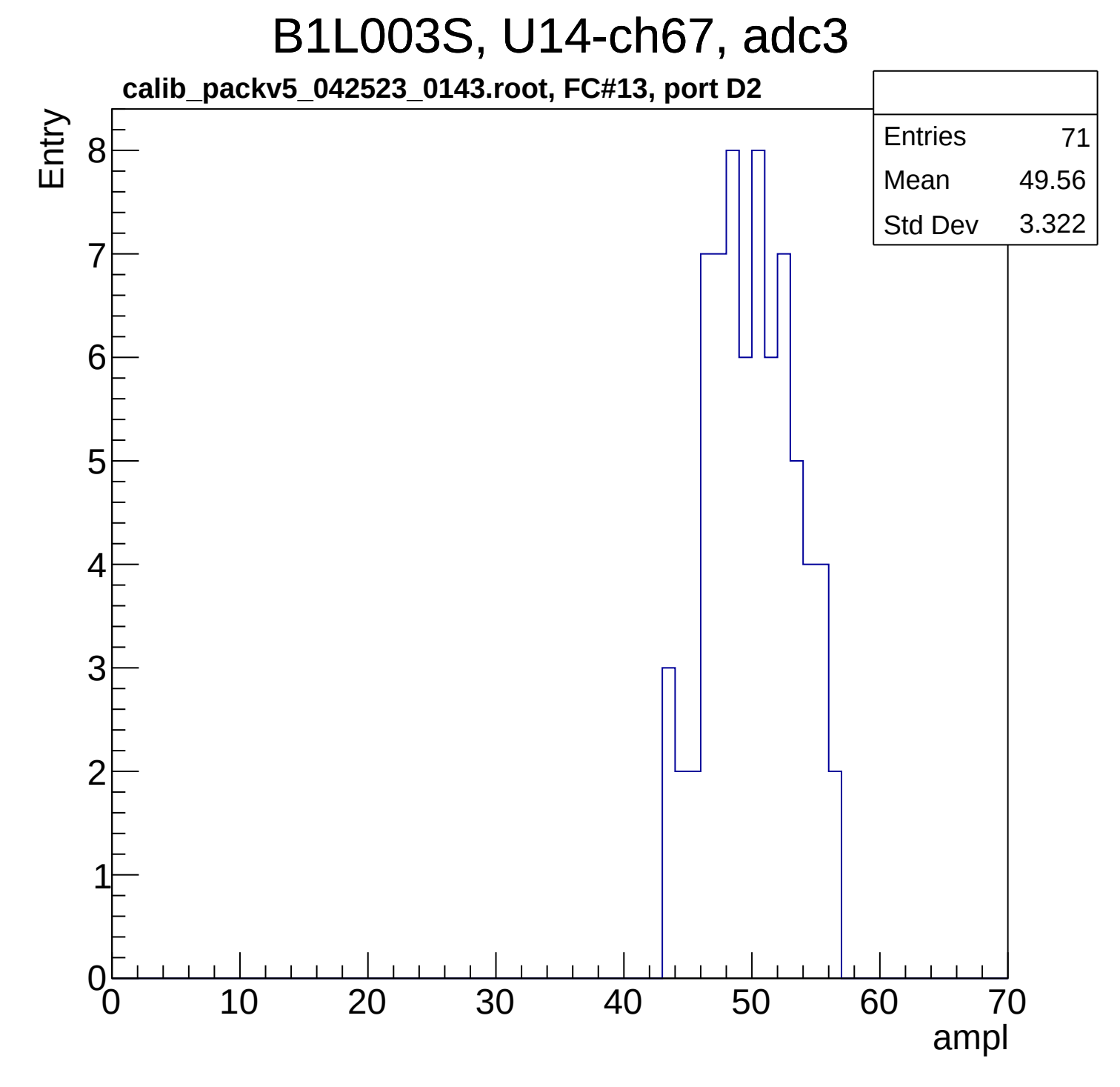

B1L003S, U14-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 58.53
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

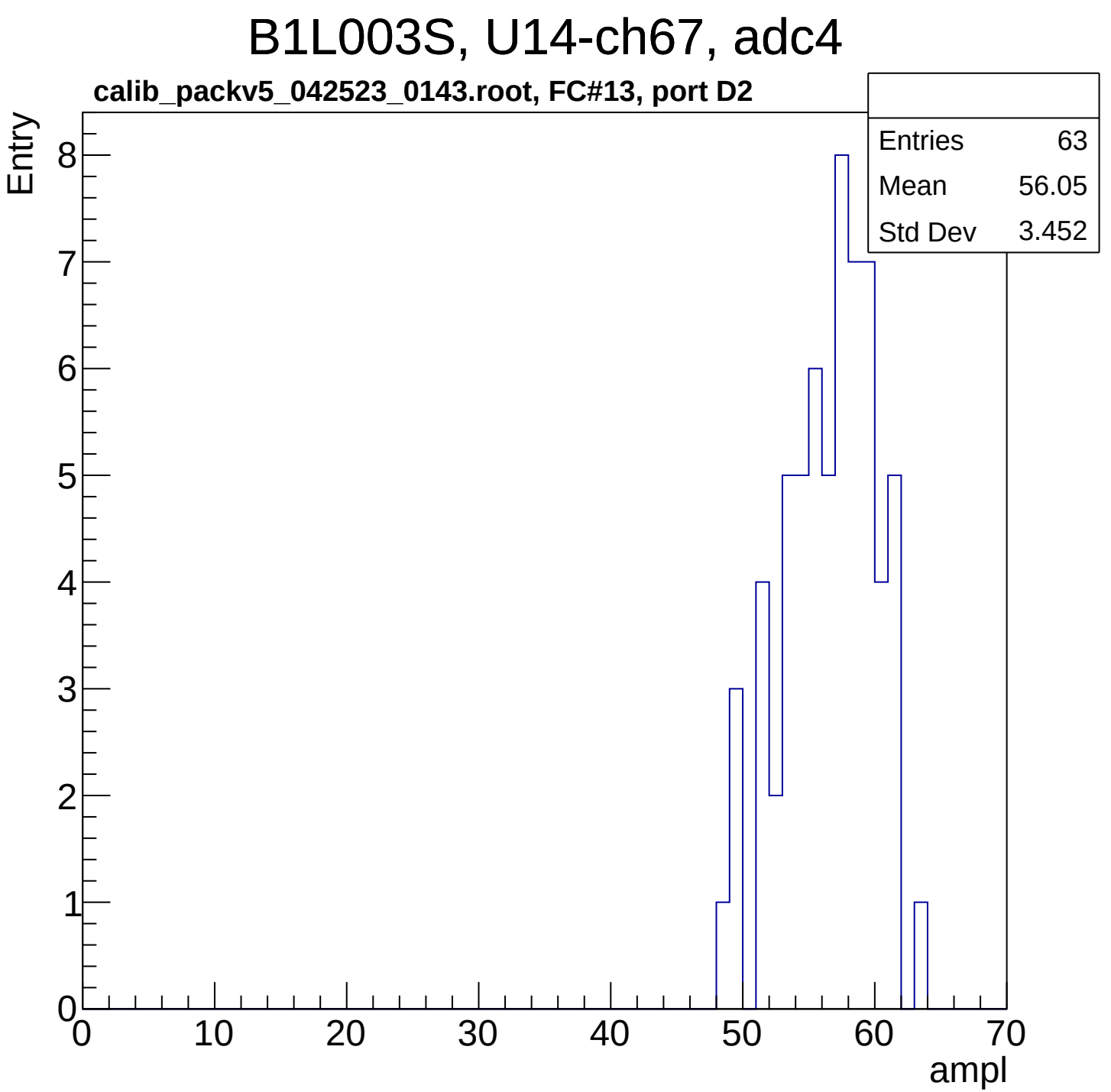

B1L003S, U14-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

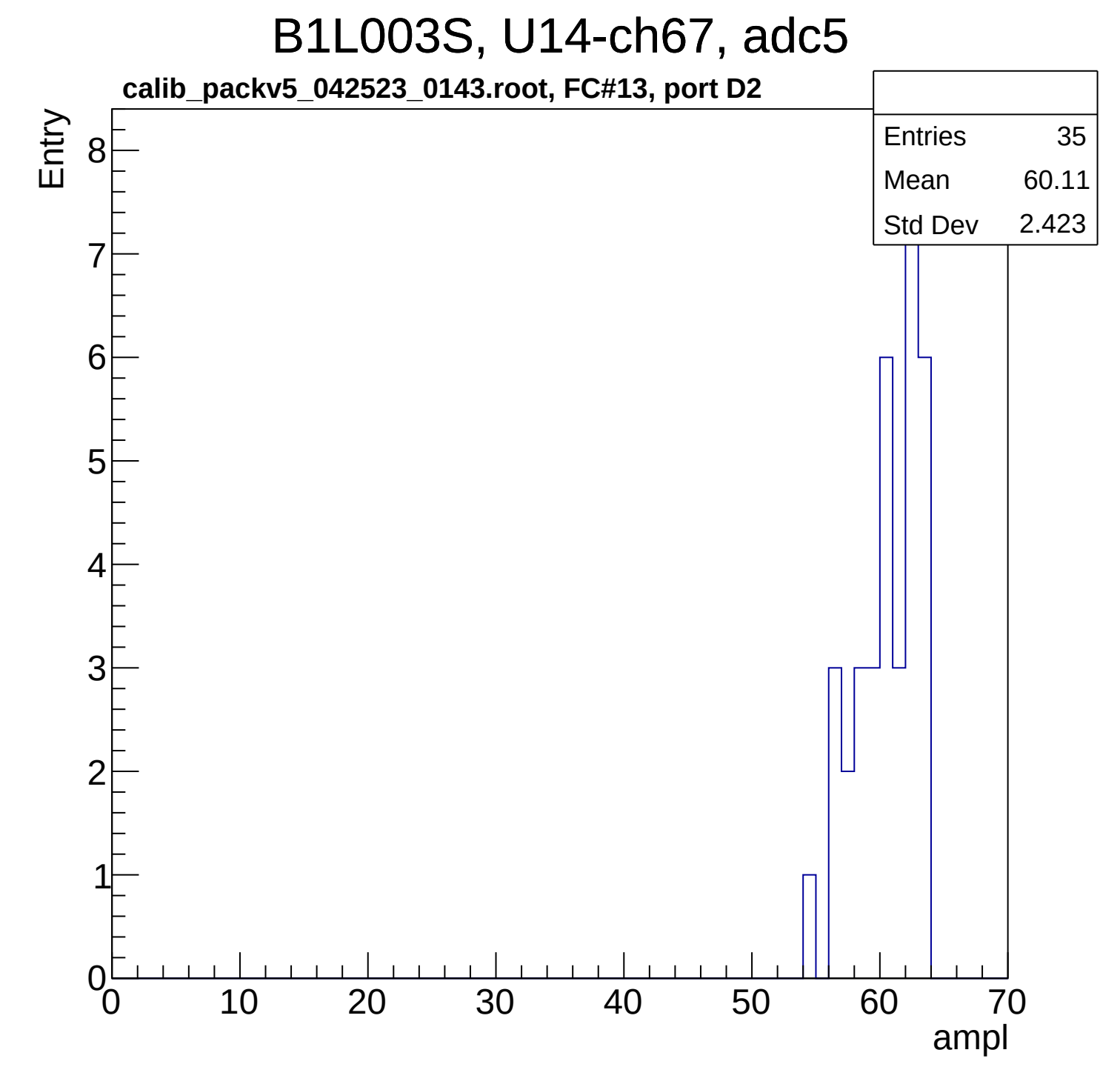

B1L003S, U14-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.22
Std Dev 3.551
Gaus mean : 29.2906
Gaus Width: 3.9475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

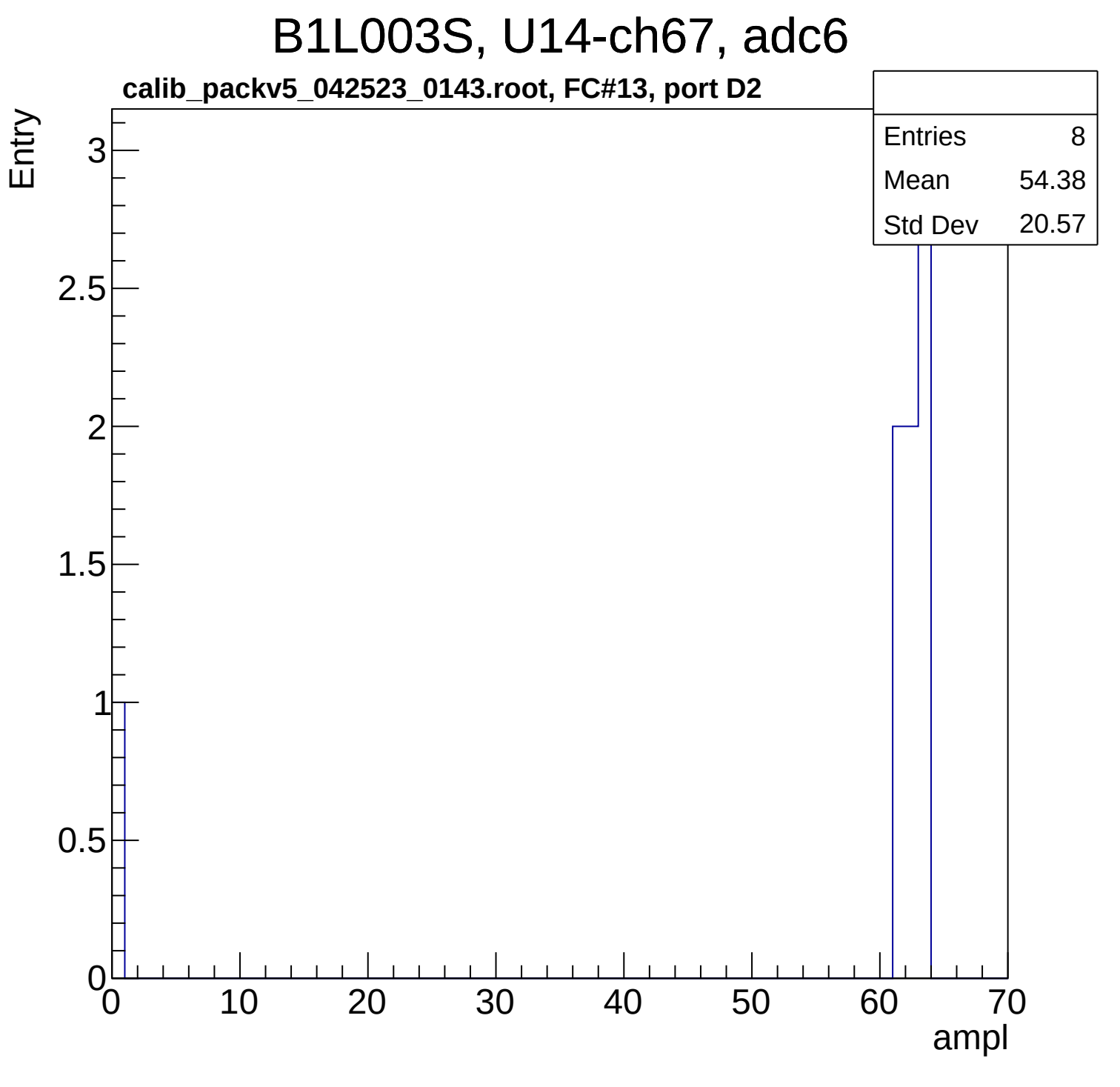

B1L003S, U14-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.24
Std Dev 3.601
Gaus mean : 37.1804
Gaus Width: 3.8844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

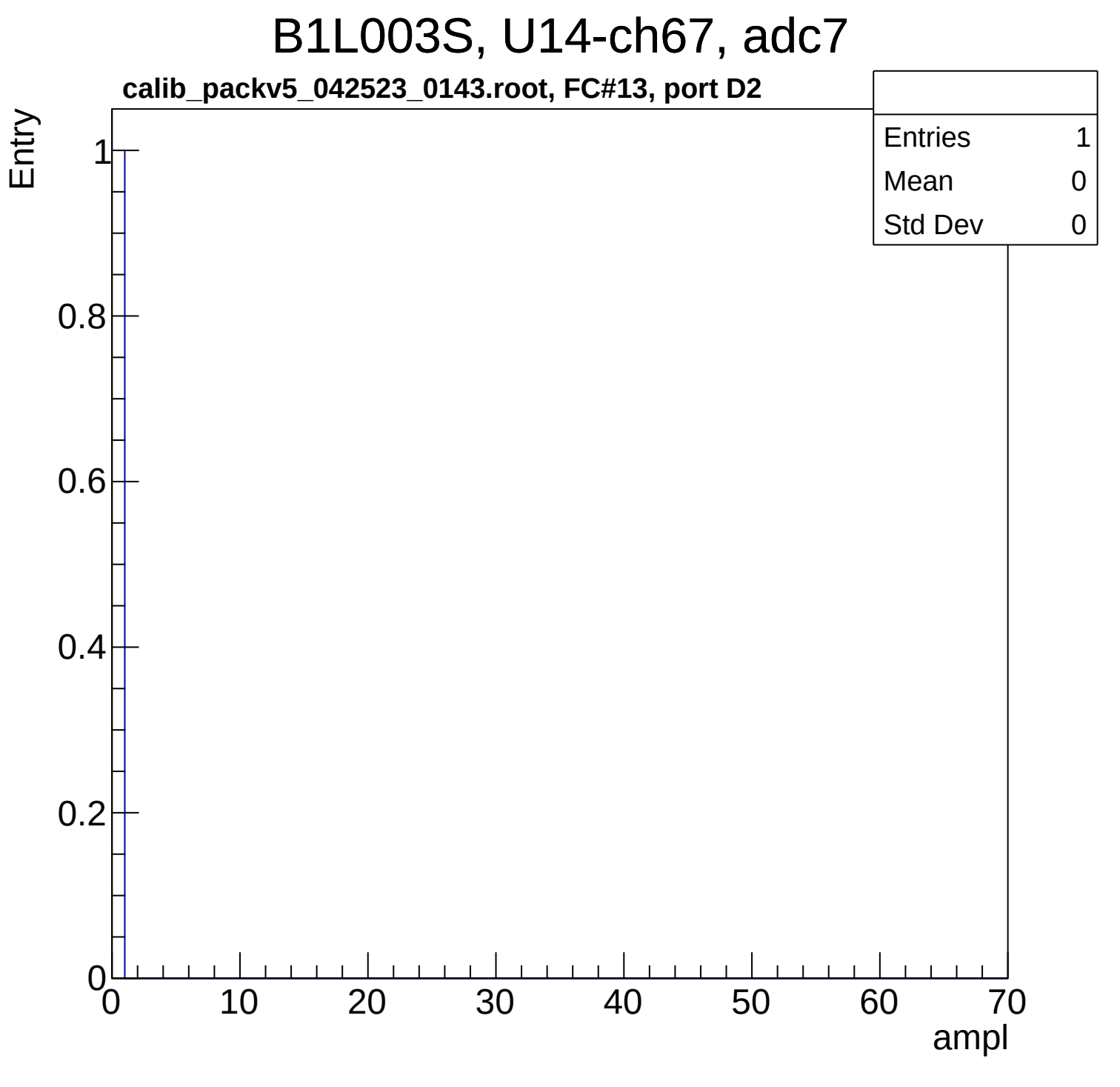

B1L003S, U14-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.46
Std Dev 2.973
Gaus mean : 43.2738
Gaus Width: 3.0001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

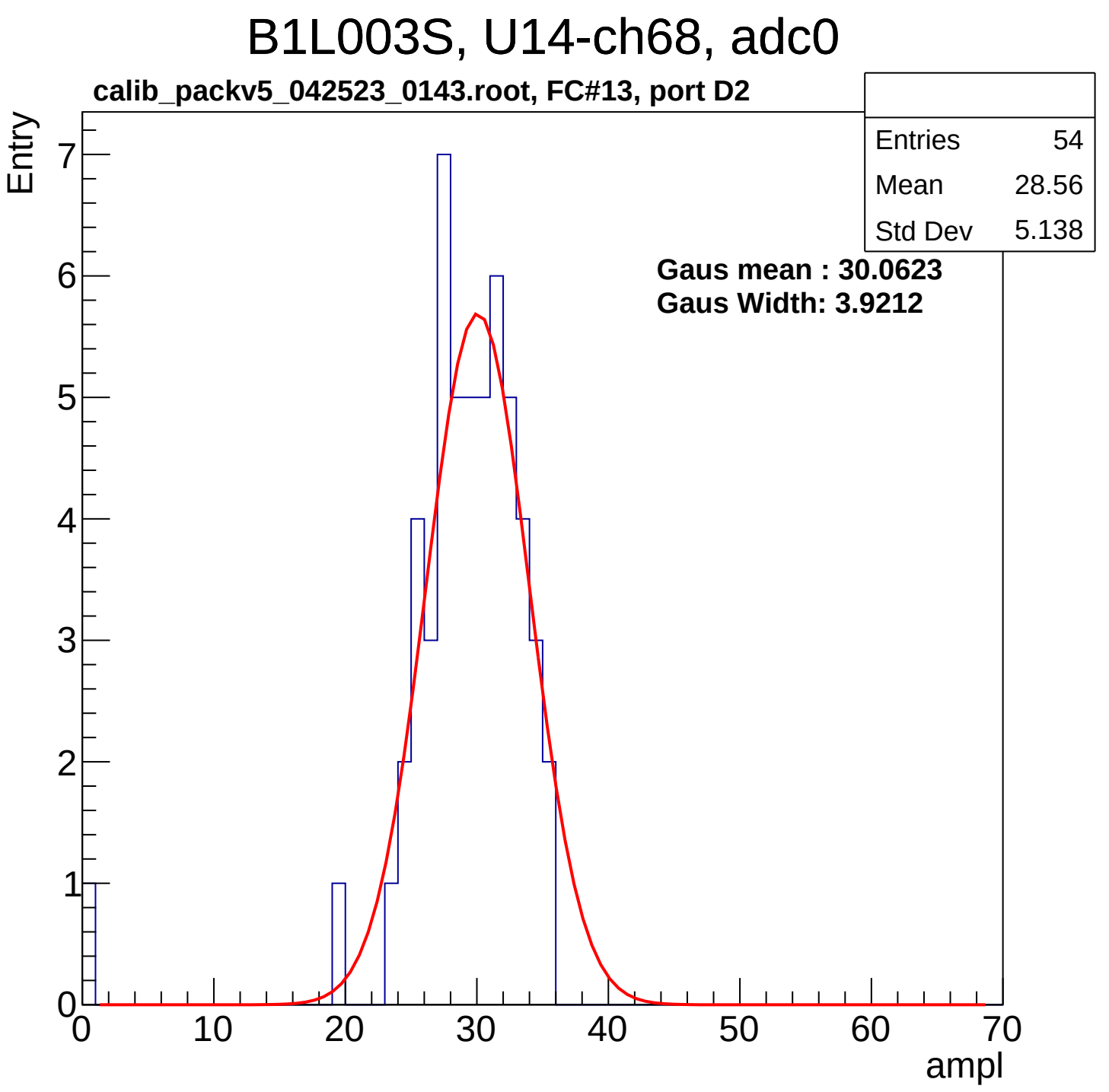

B1L003S, U14-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.56
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

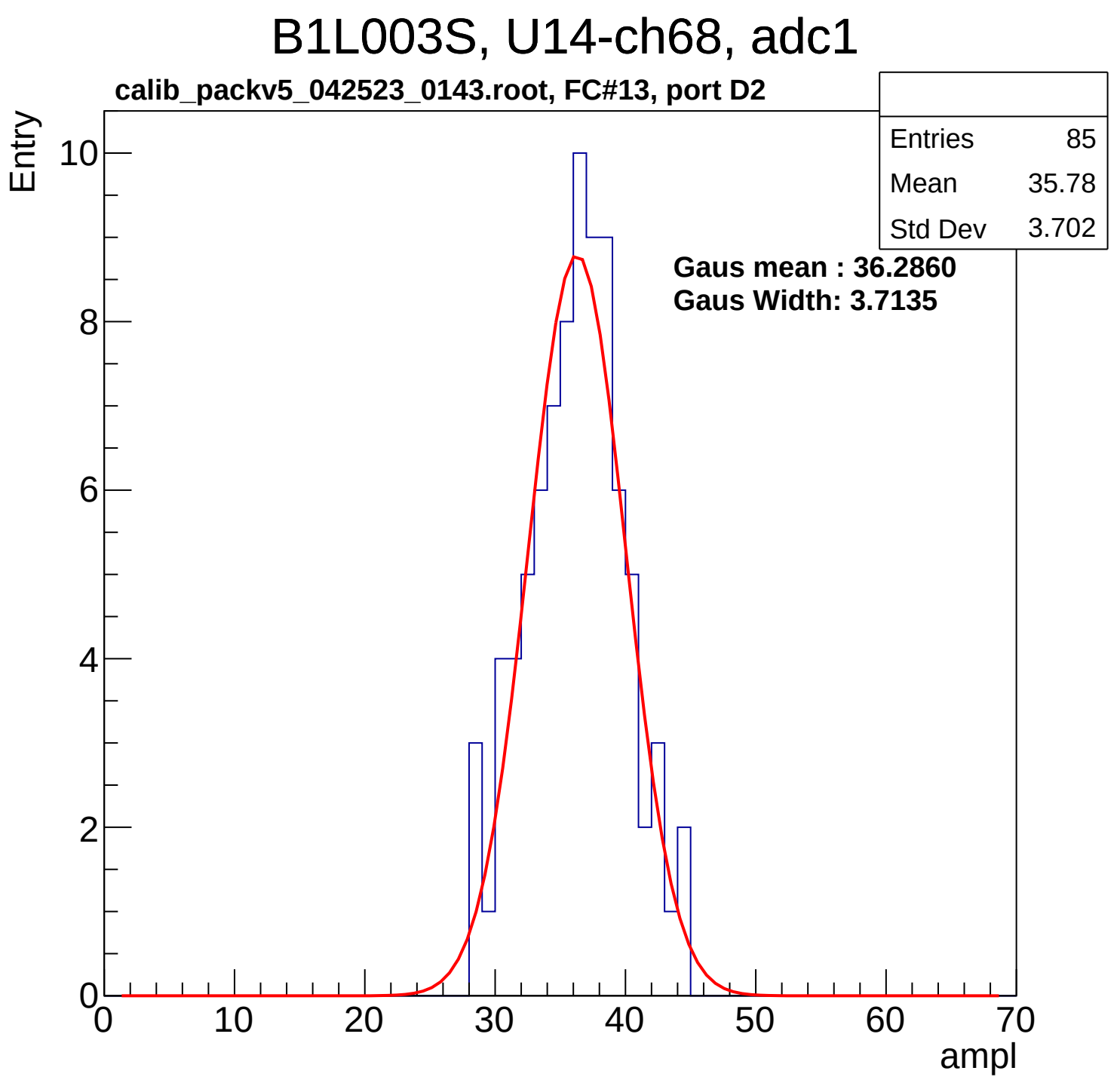

B1L003S, U14-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 56.05
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

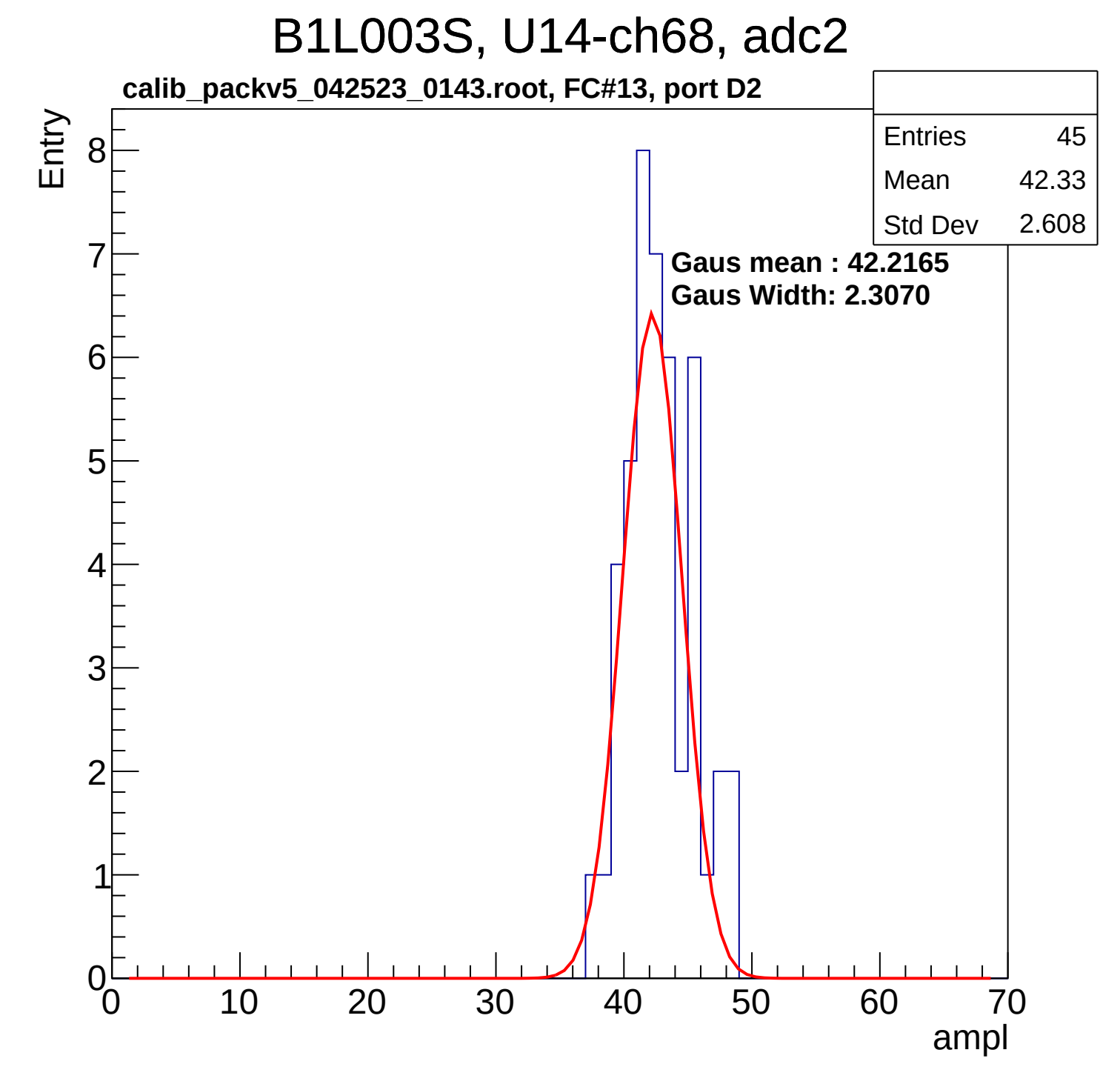

B1L003S, U14-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 60.11
Std Dev 2.423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

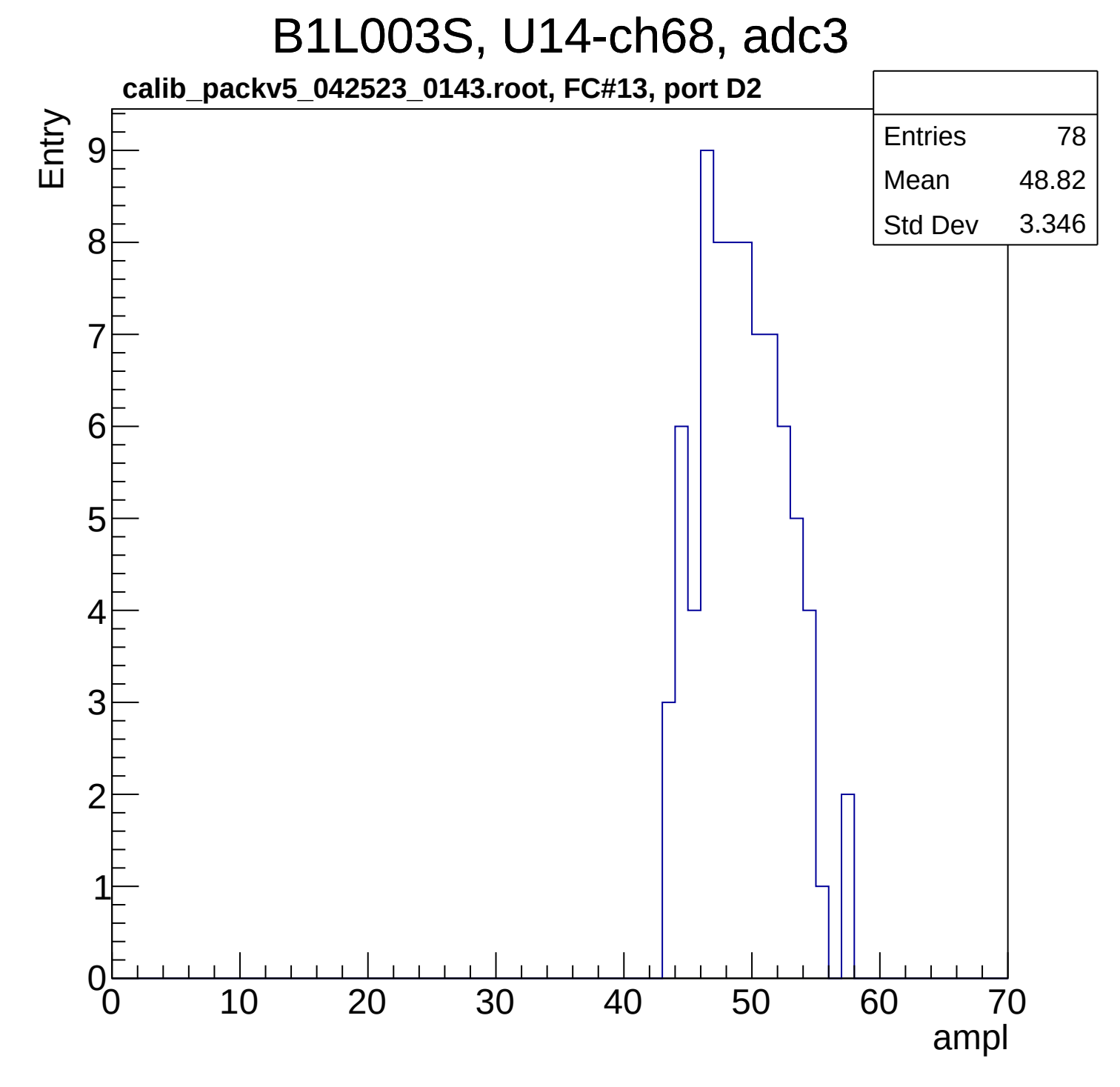

B1L003S, U14-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

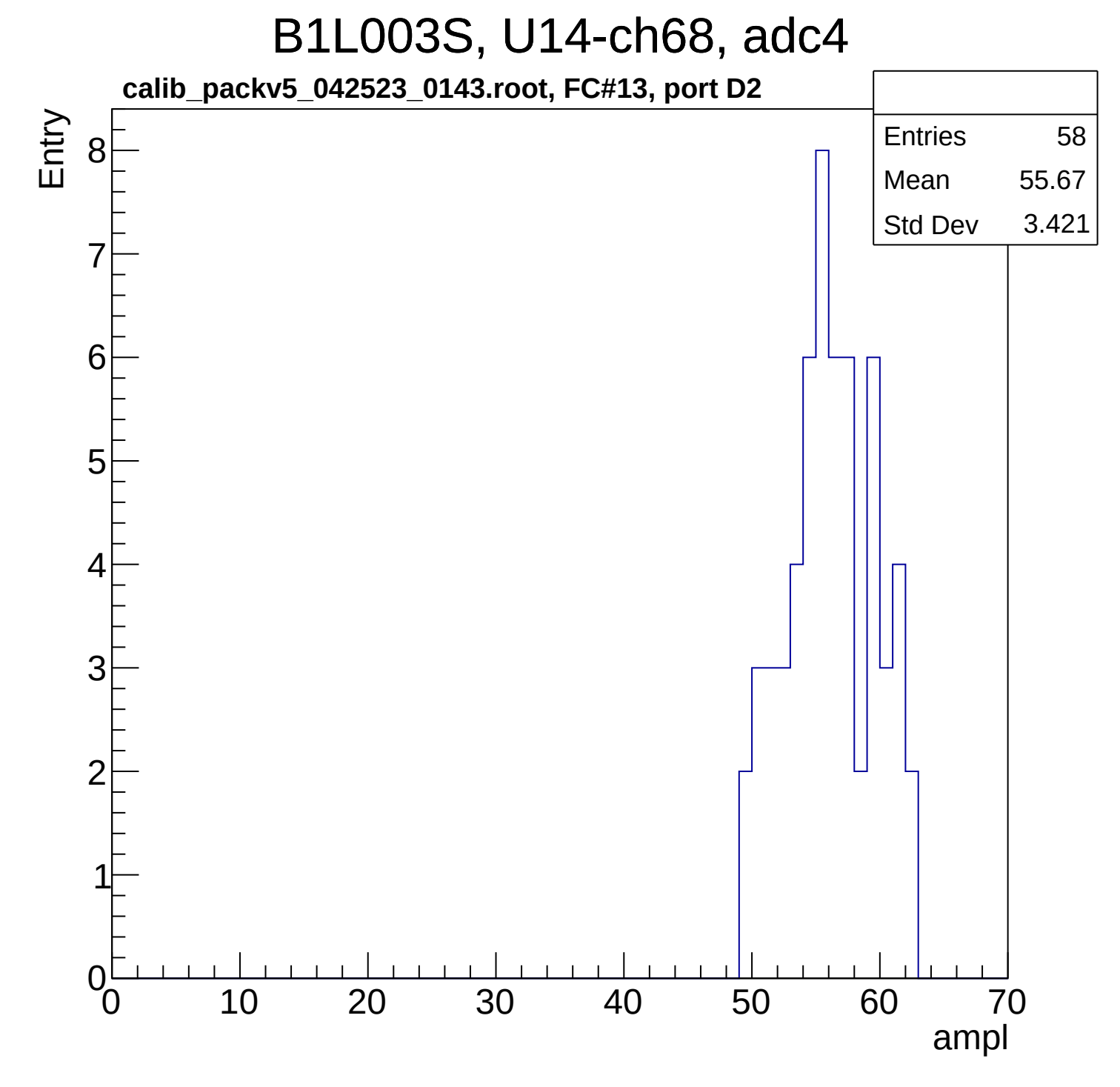

B1L003S, U14-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

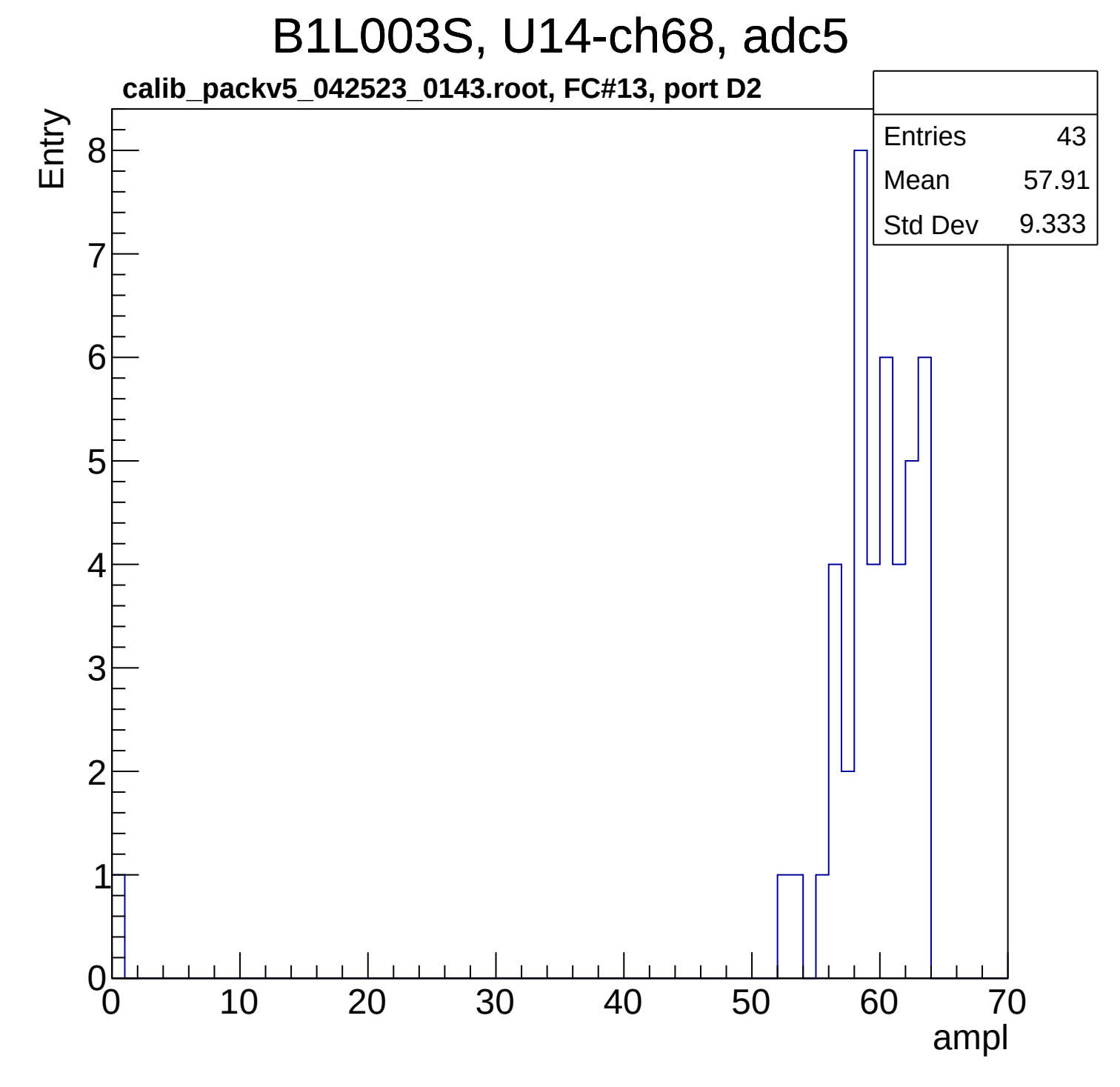

B1L003S, U14-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 28.56
Std Dev 5.138
Gaus mean : 30.0623
Gaus Width: 3.9212
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

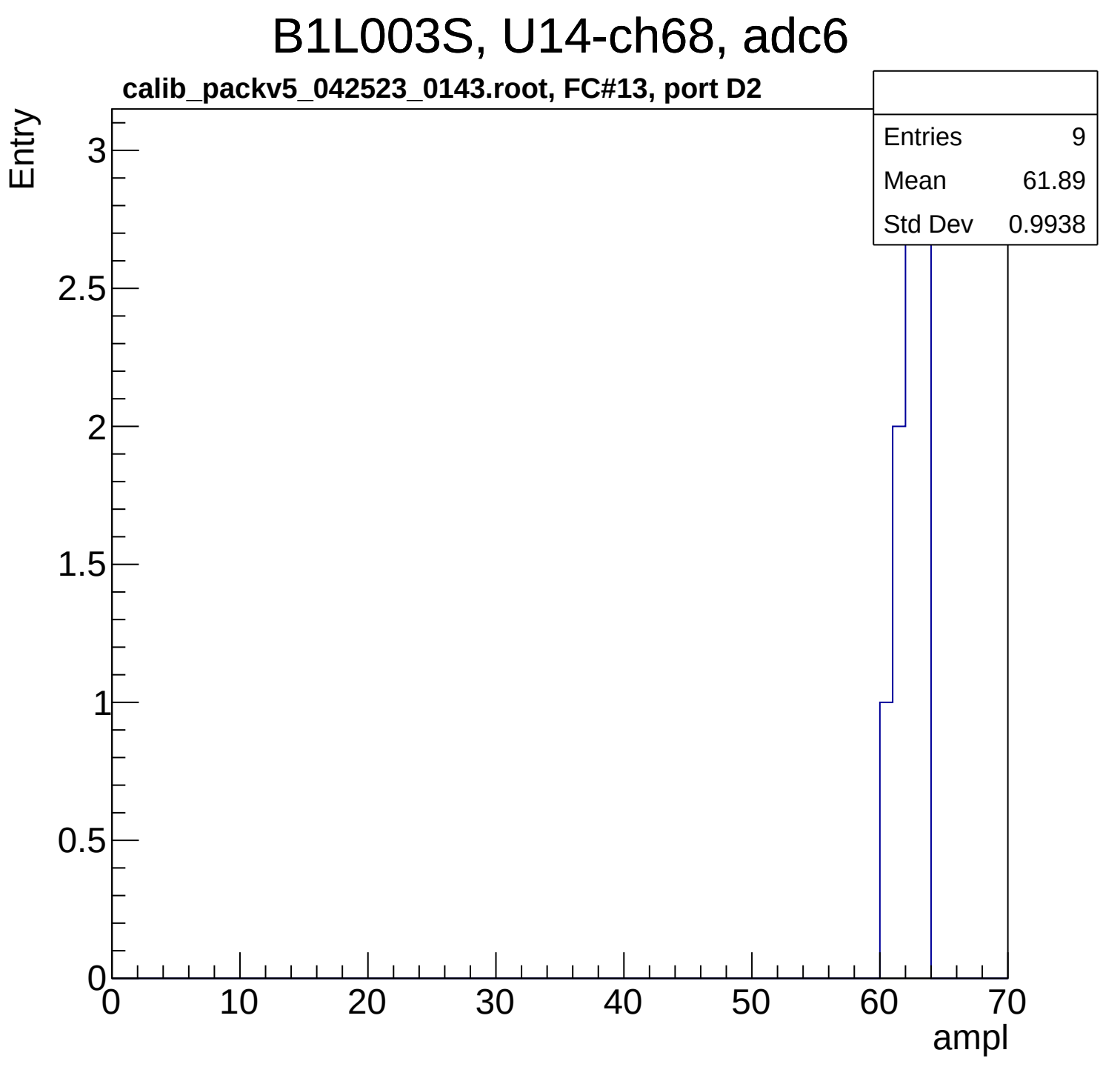

B1L003S, U14-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 35.78
Std Dev 3.702
Gaus mean : 36.2860
Gaus Width: 3.7135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

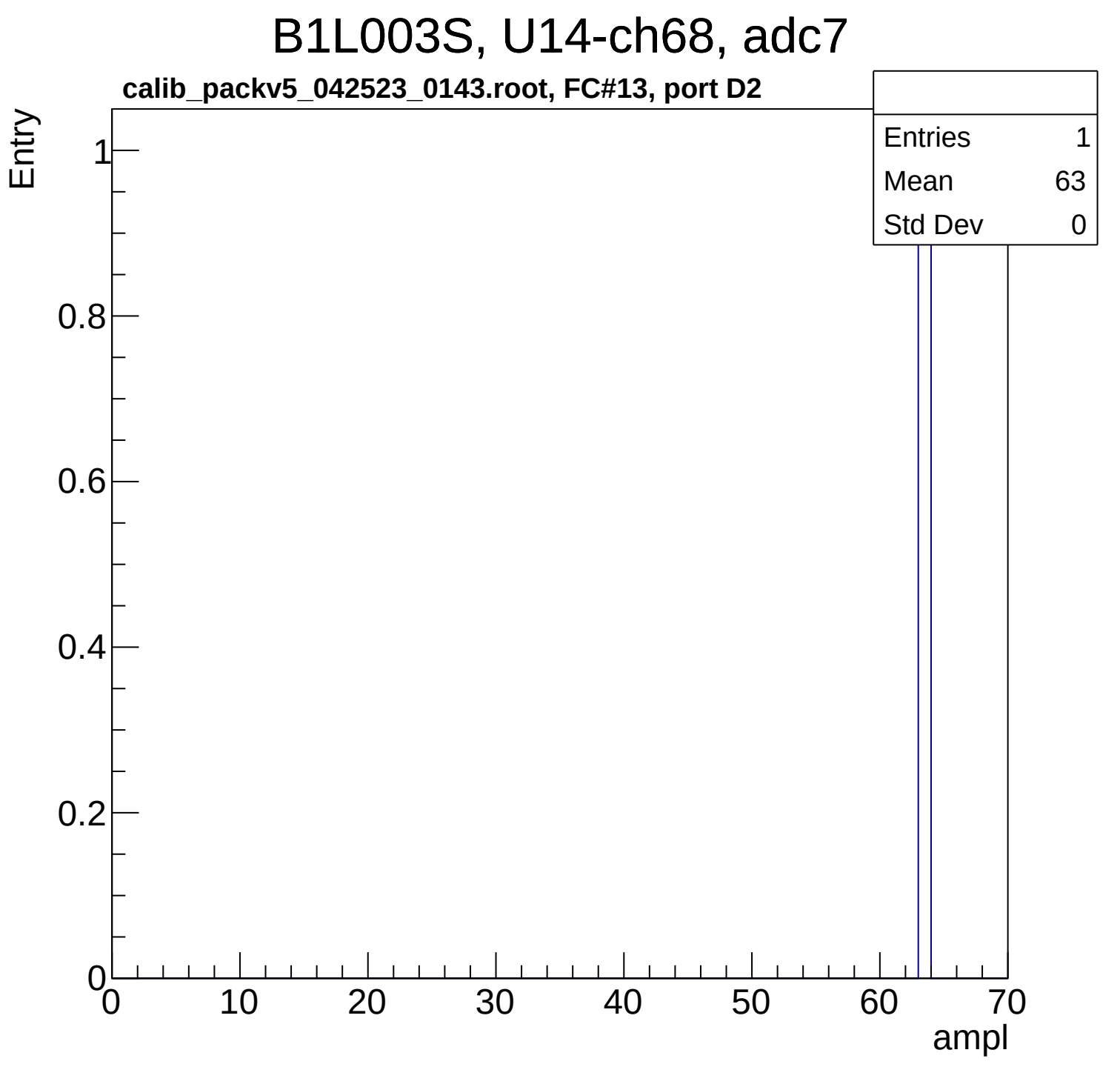

B1L003S, U14-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 42.33
Std Dev 2.608
Gaus mean : 42.2165
Gaus Width: 2.3070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

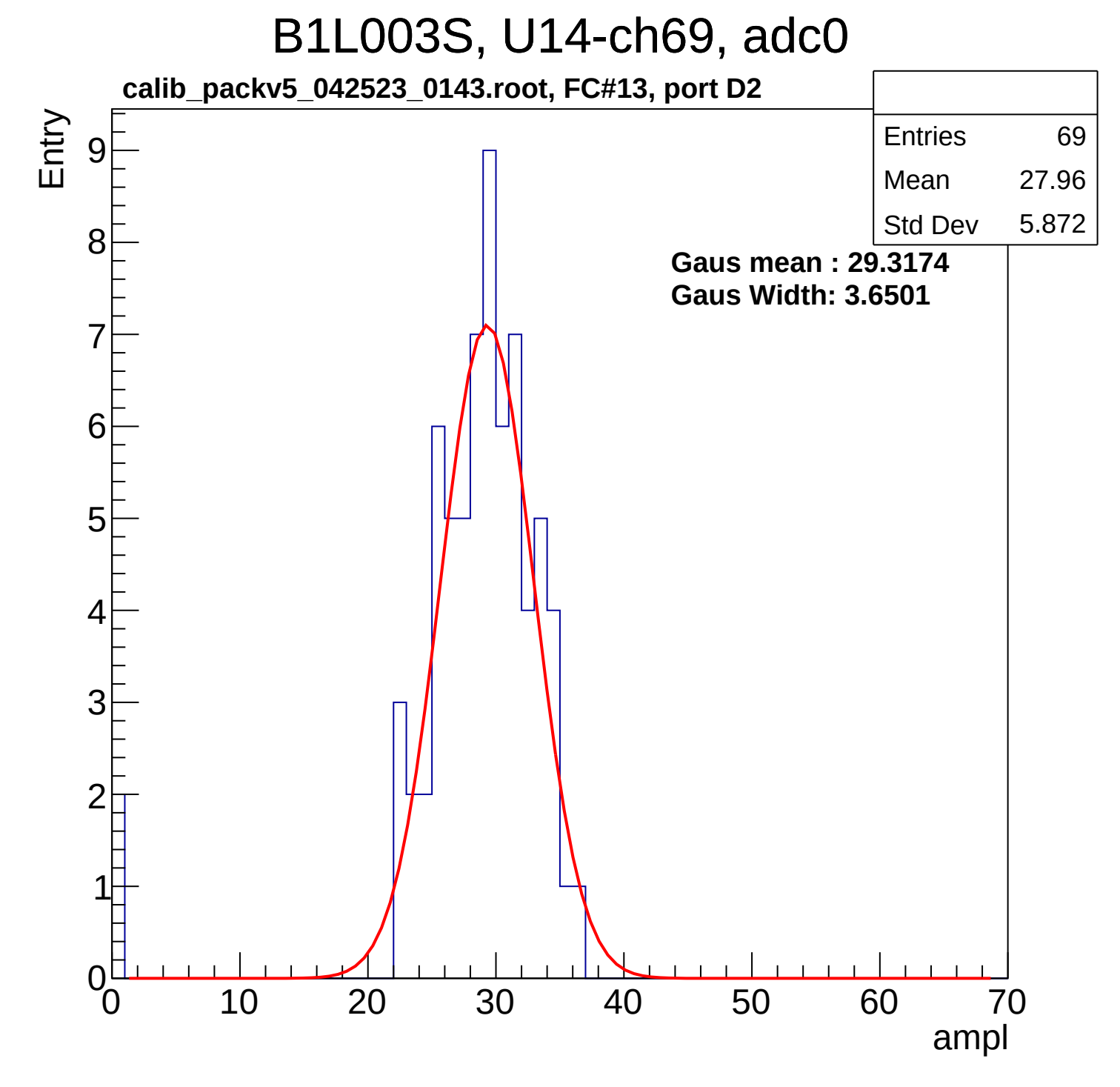

B1L003S, U14-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 48.82
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

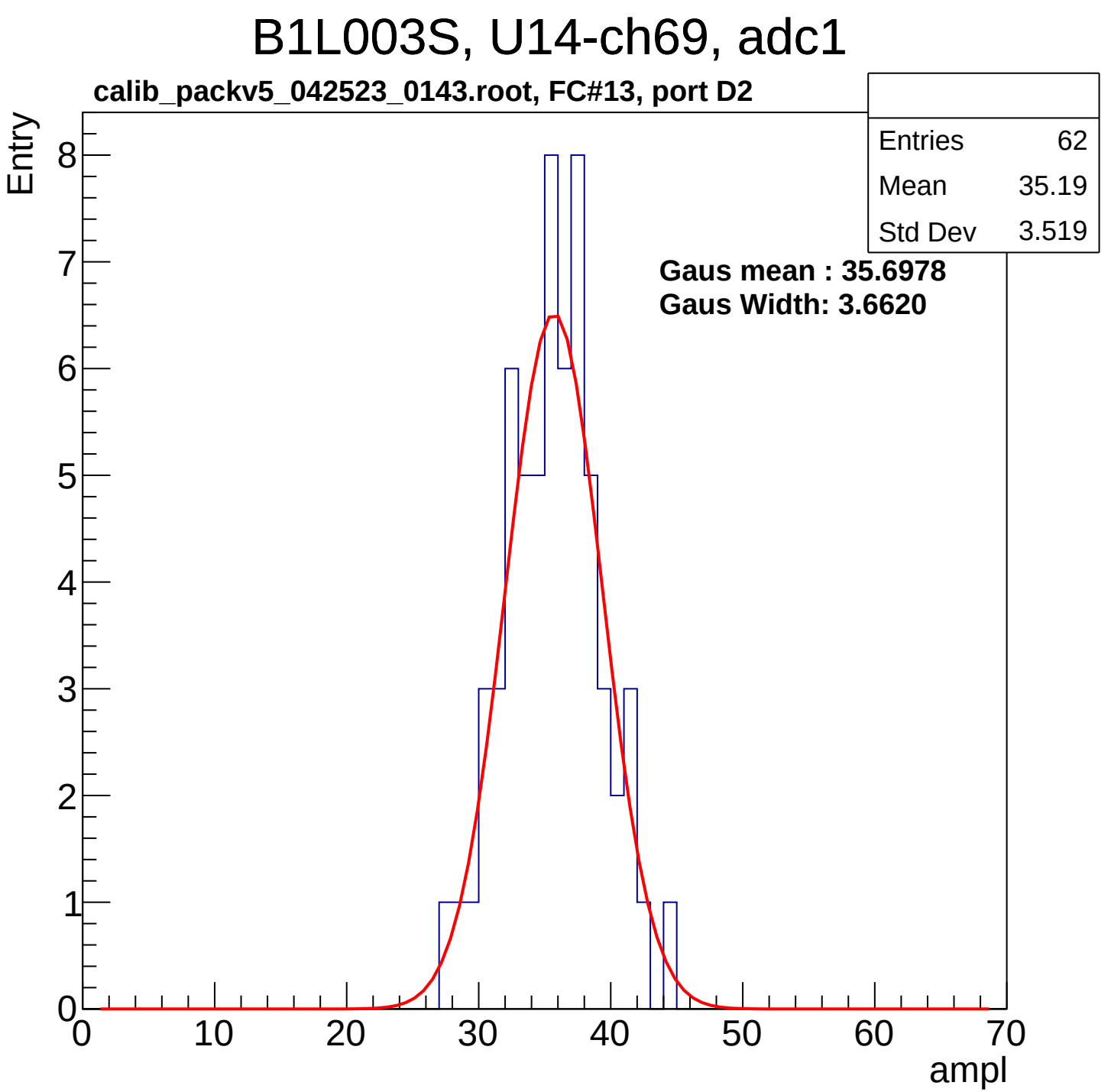

B1L003S, U14-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.67
Std Dev 3.421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

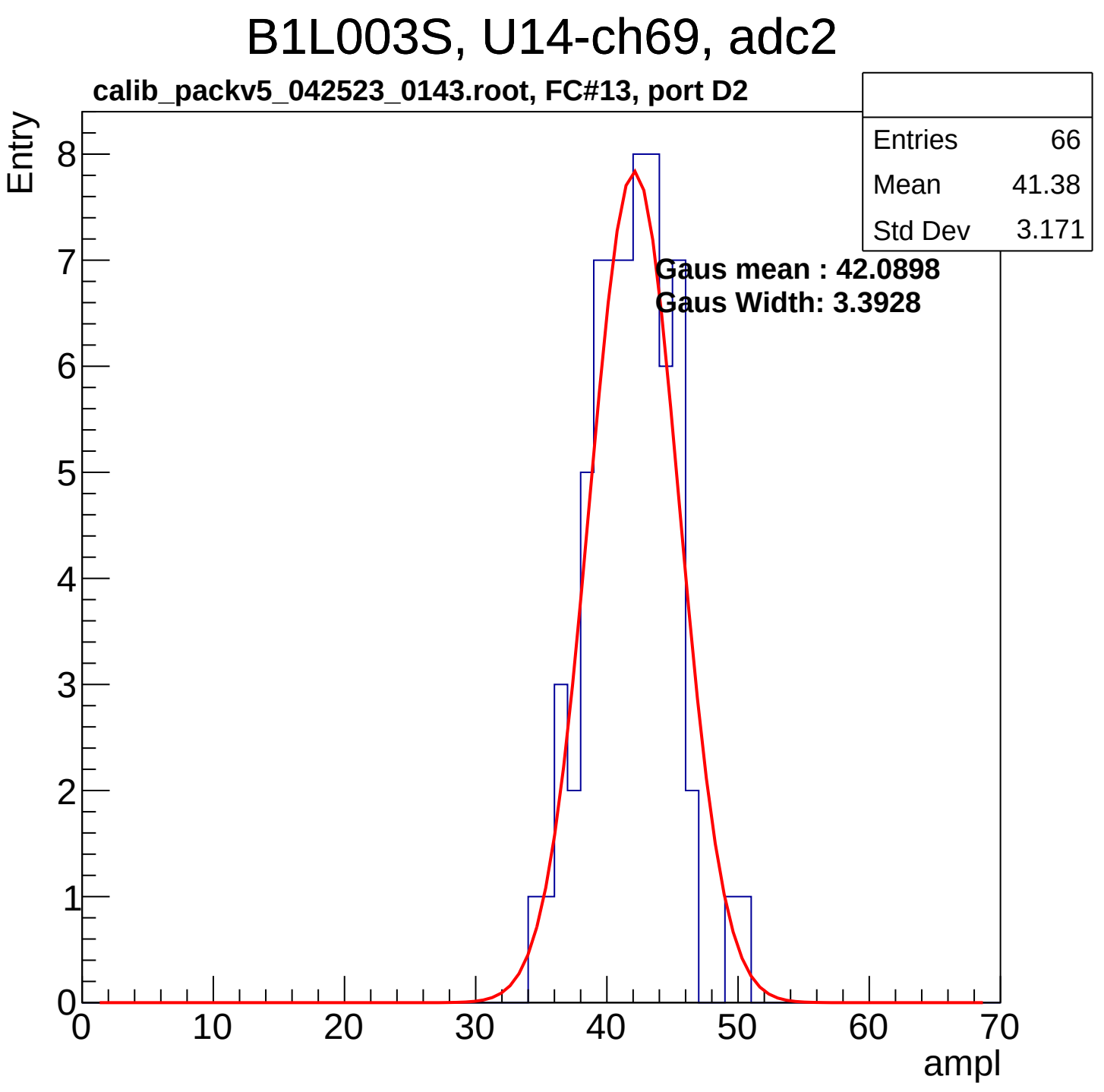

B1L003S, U14-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 57.91
Std Dev 9.333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

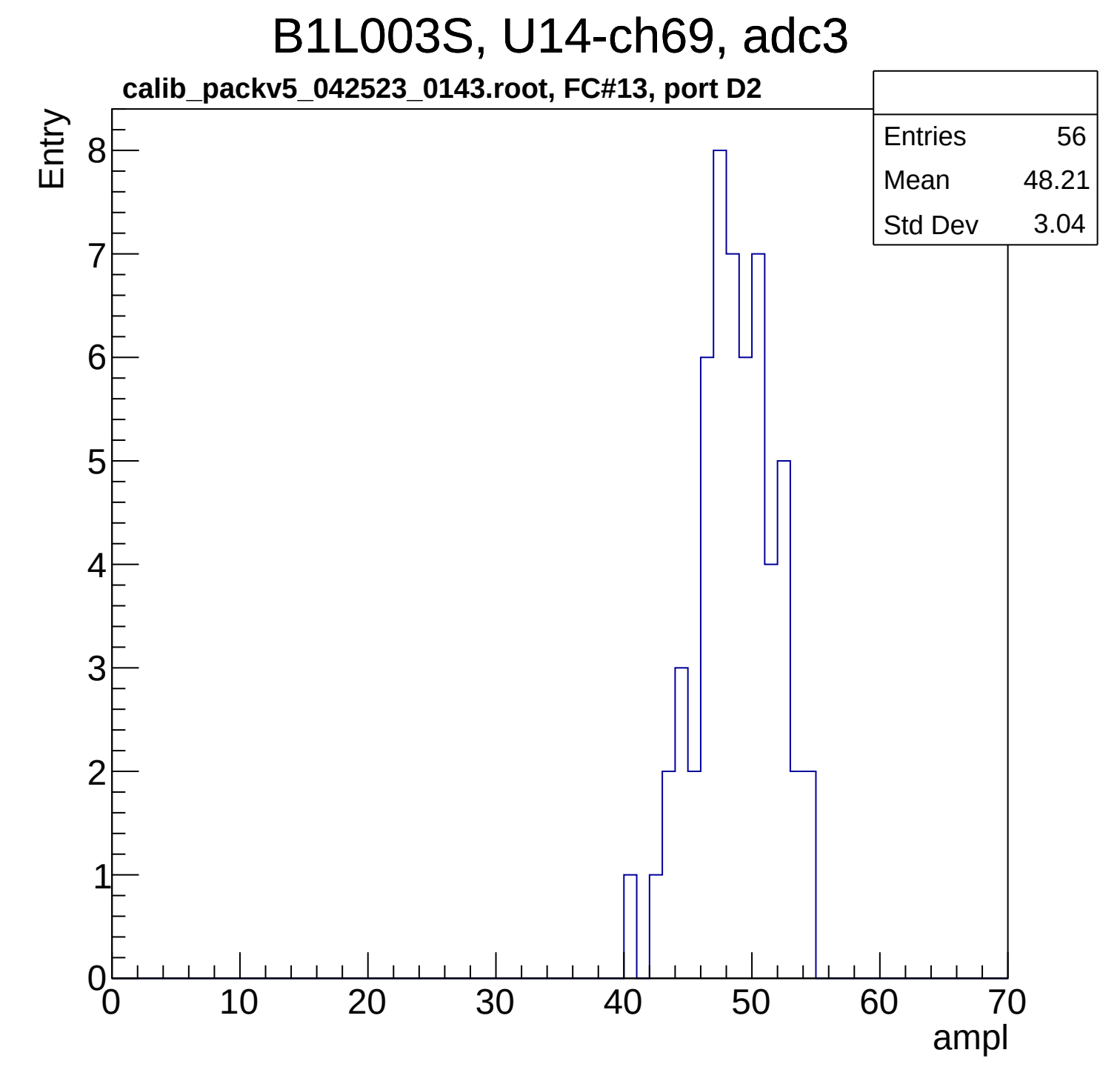

B1L003S, U14-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.89
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

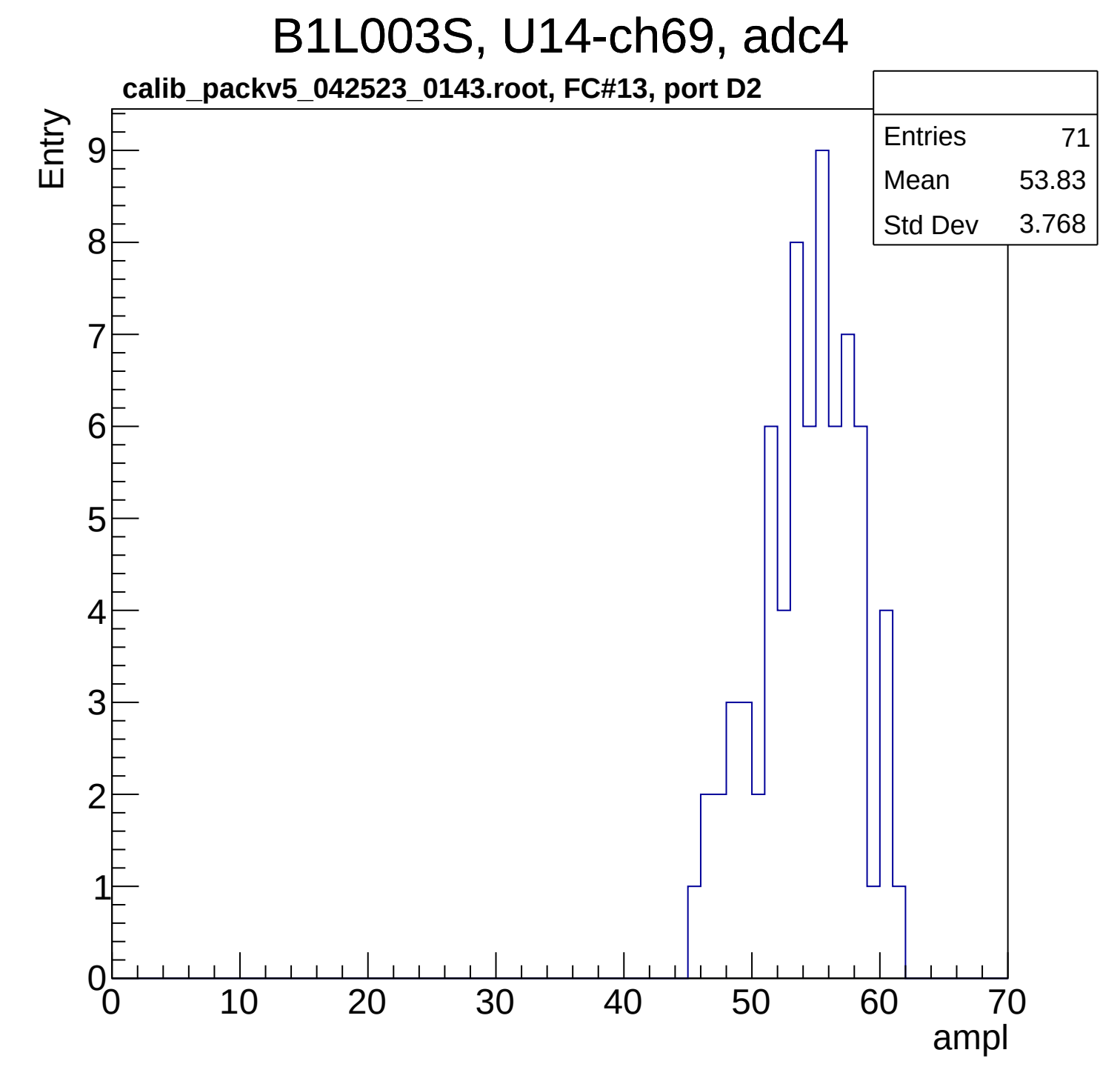

B1L003S, U14-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

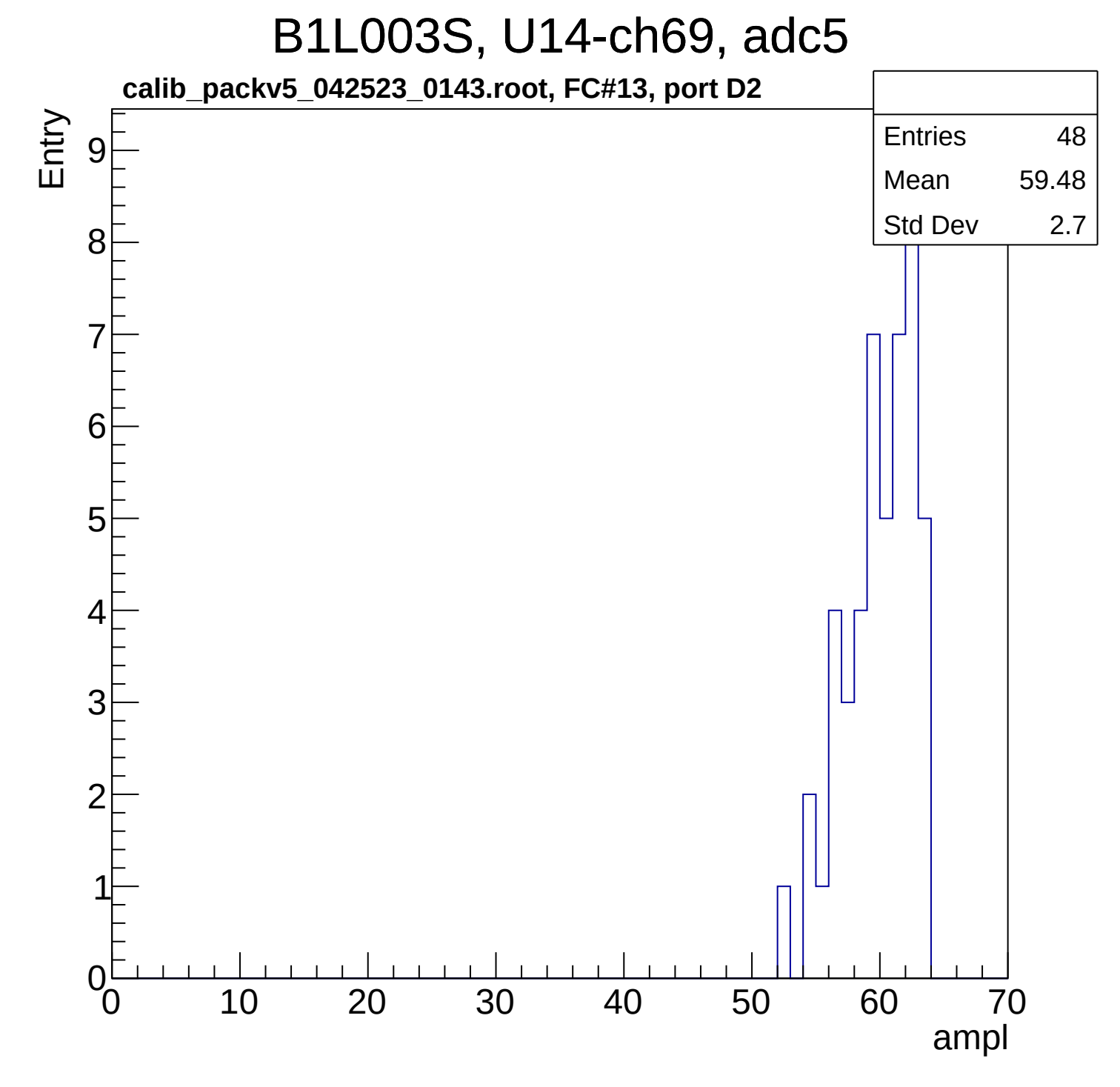

B1L003S, U14-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.96
Std Dev 5.872
Gaus mean : 29.3174
Gaus Width: 3.6501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

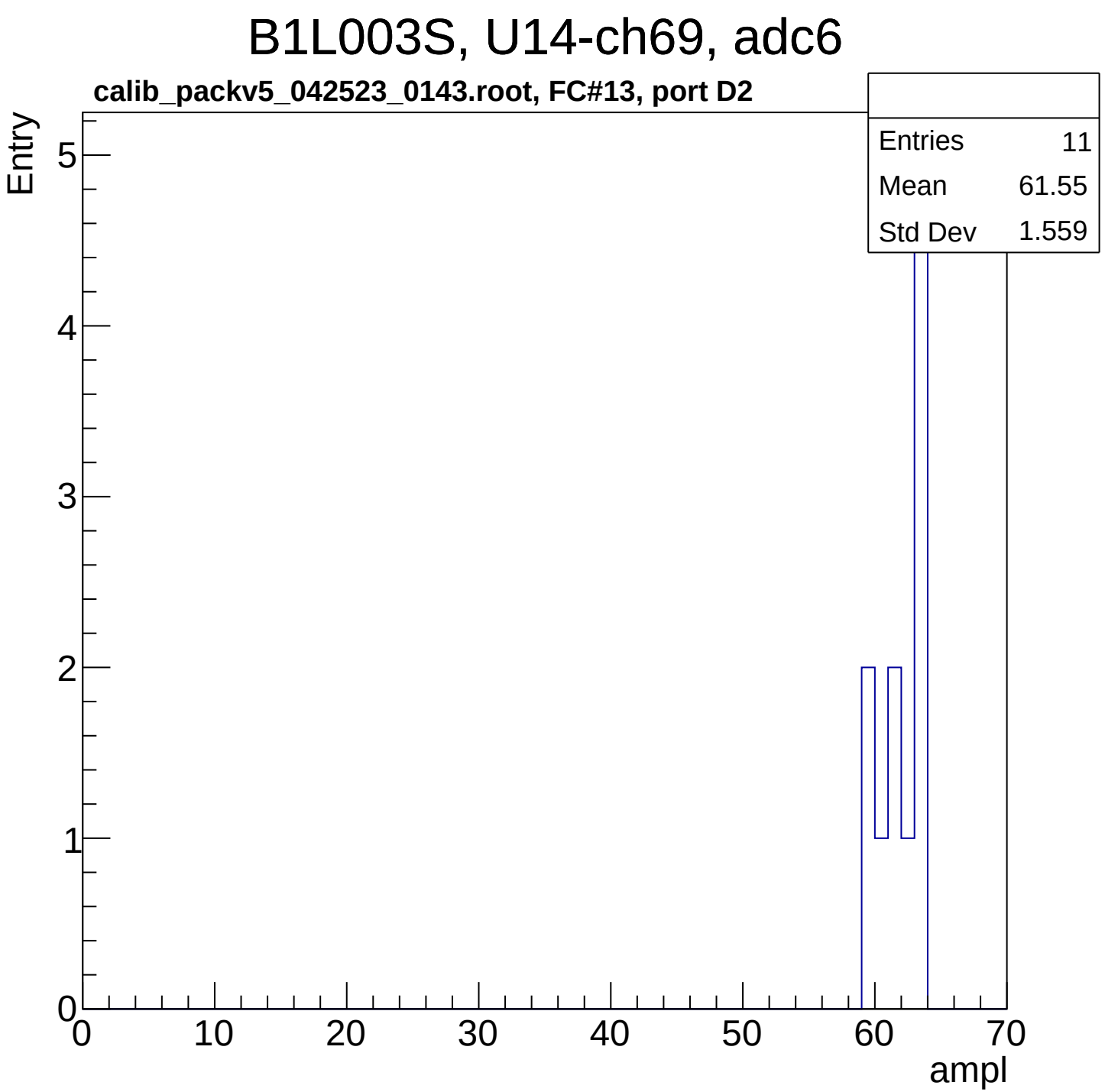

B1L003S, U14-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.19
Std Dev 3.519
Gaus mean : 35.6978
Gaus Width: 3.6620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

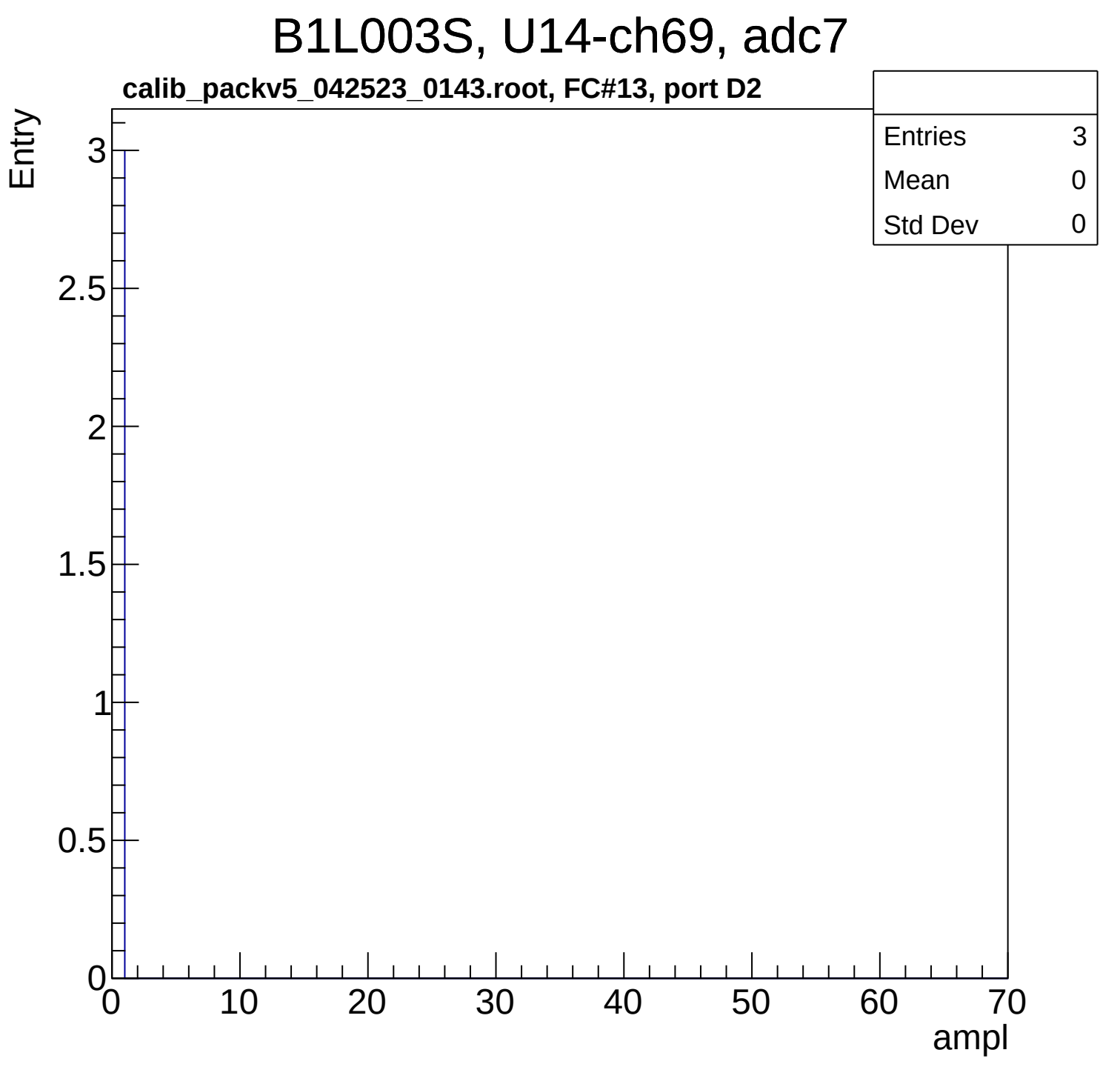

B1L003S, U14-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 41.38
Std Dev 3.171
Gaus mean : 42.0898
Gaus Width: 3.3928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

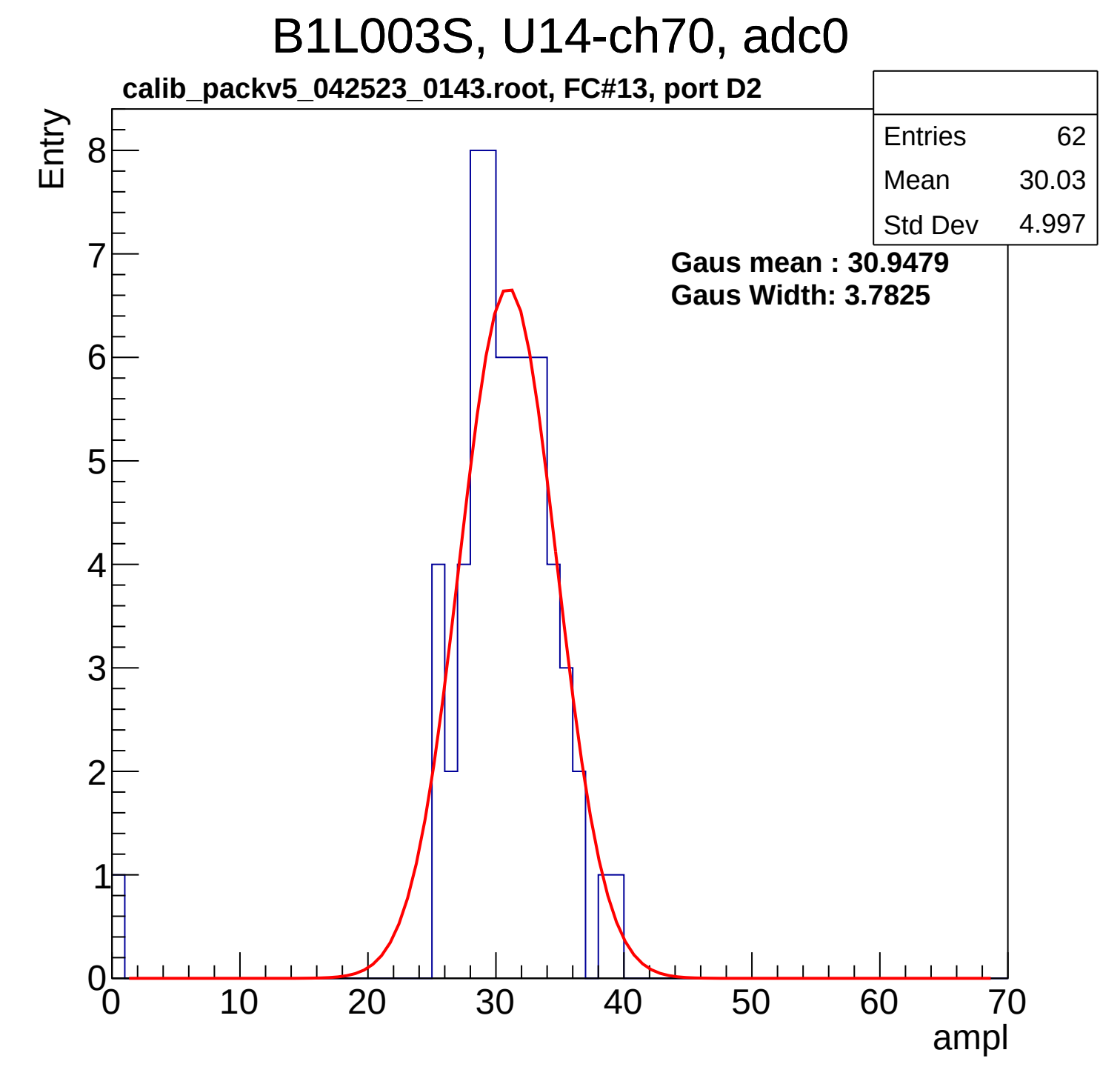

B1L003S, U14-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.21
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

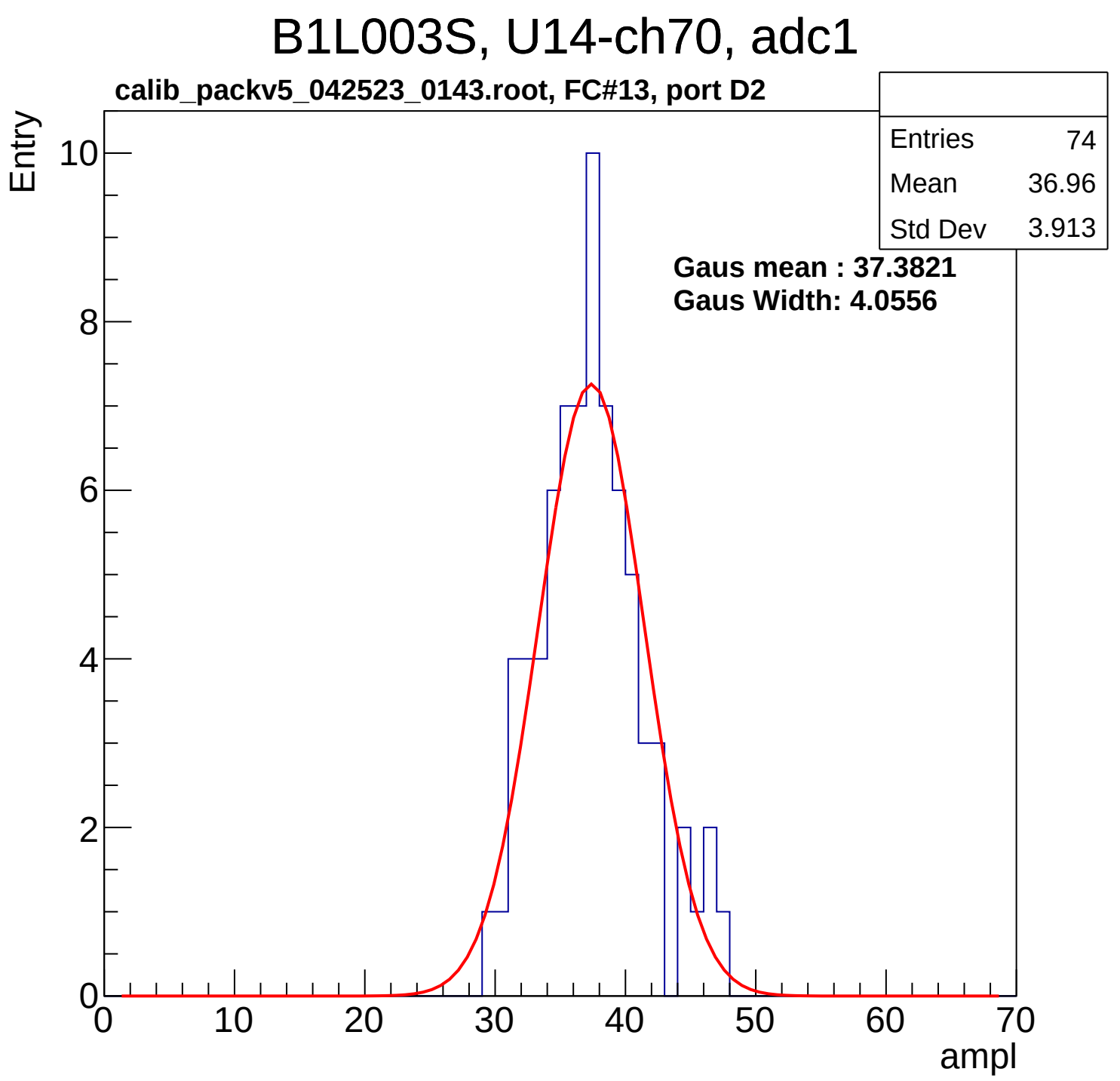

B1L003S, U14-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 53.83
Std Dev 3.768
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

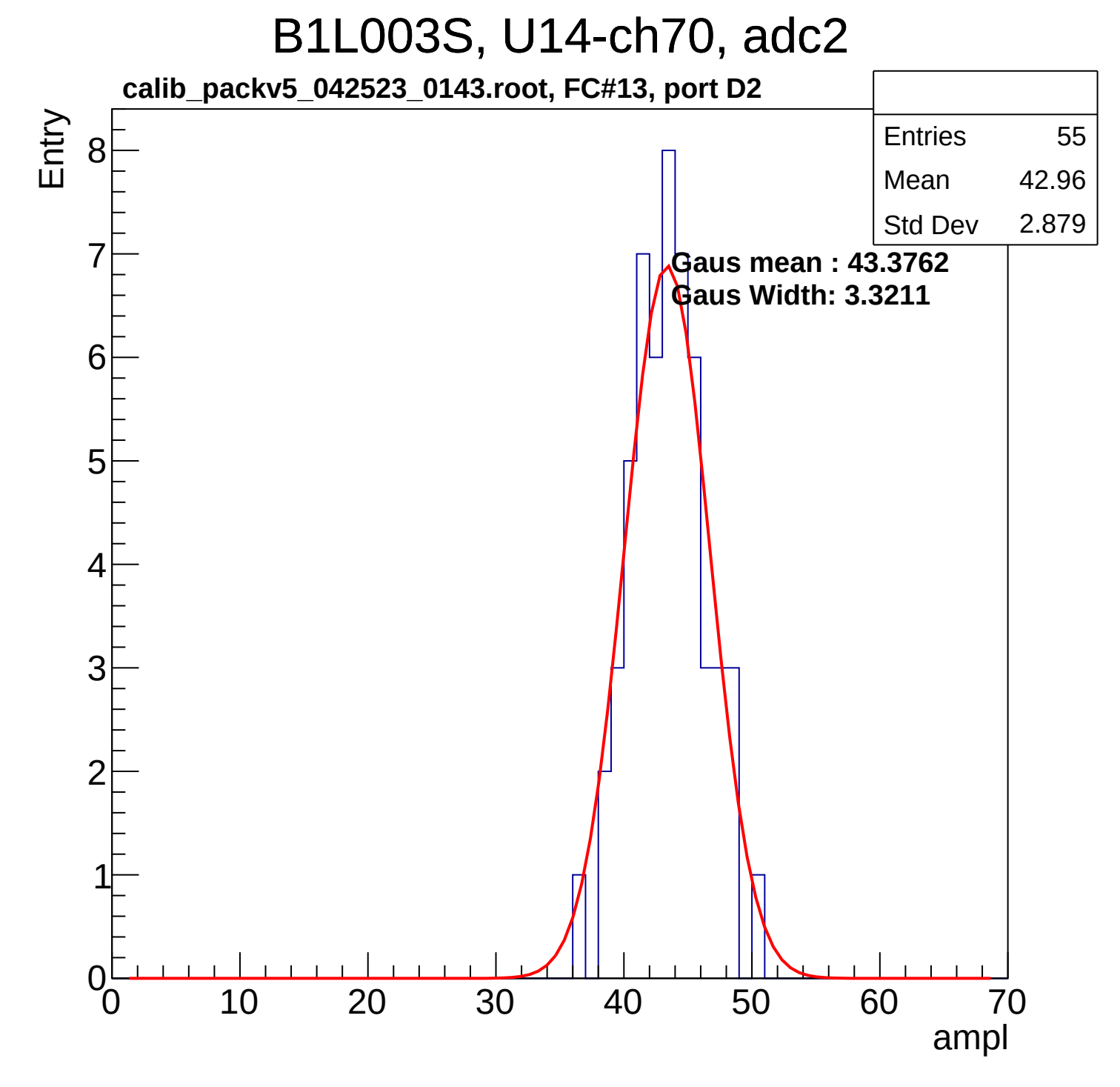

B1L003S, U14-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.48
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

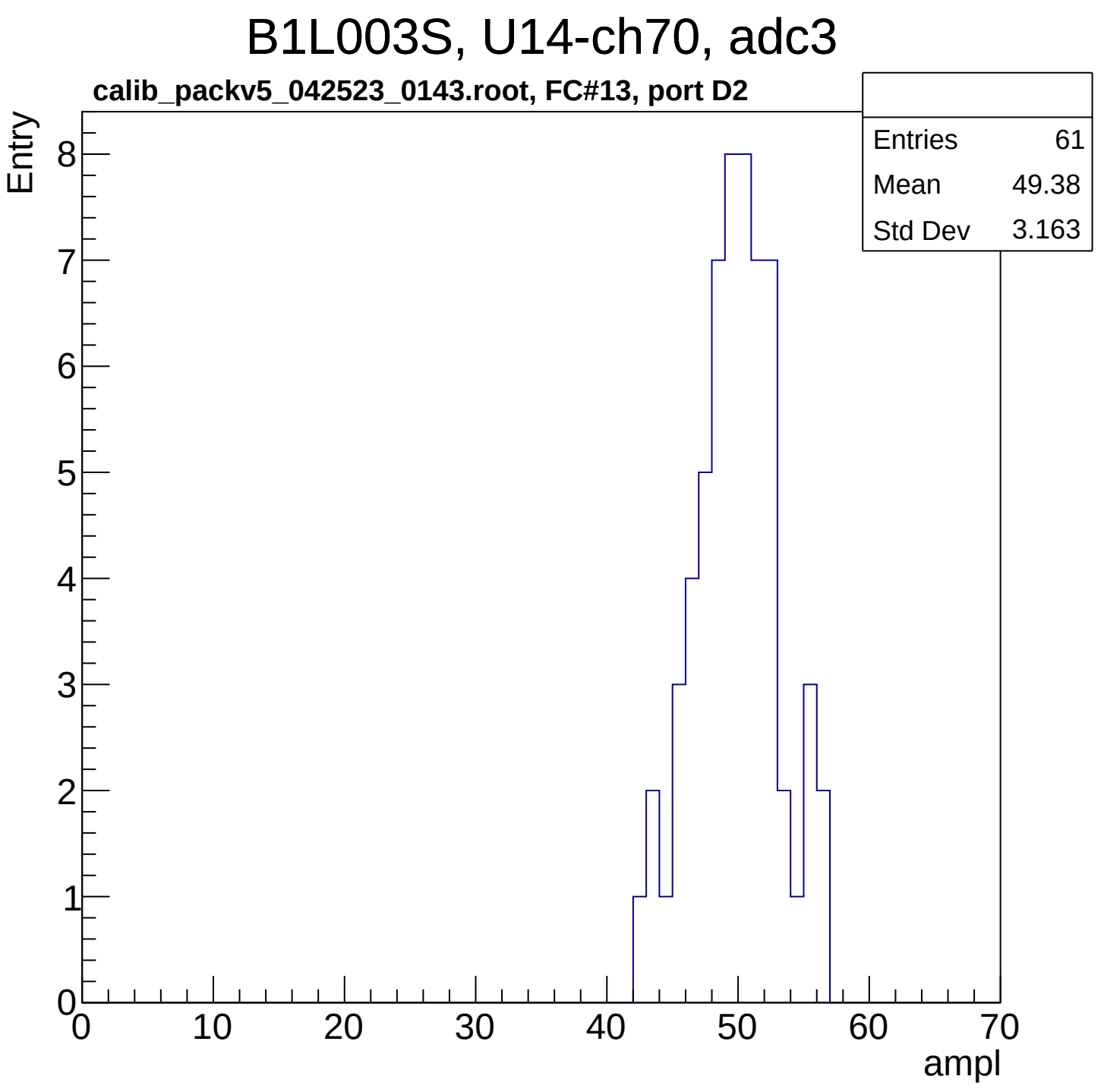

B1L003S, U14-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.55
Std Dev 1.559
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

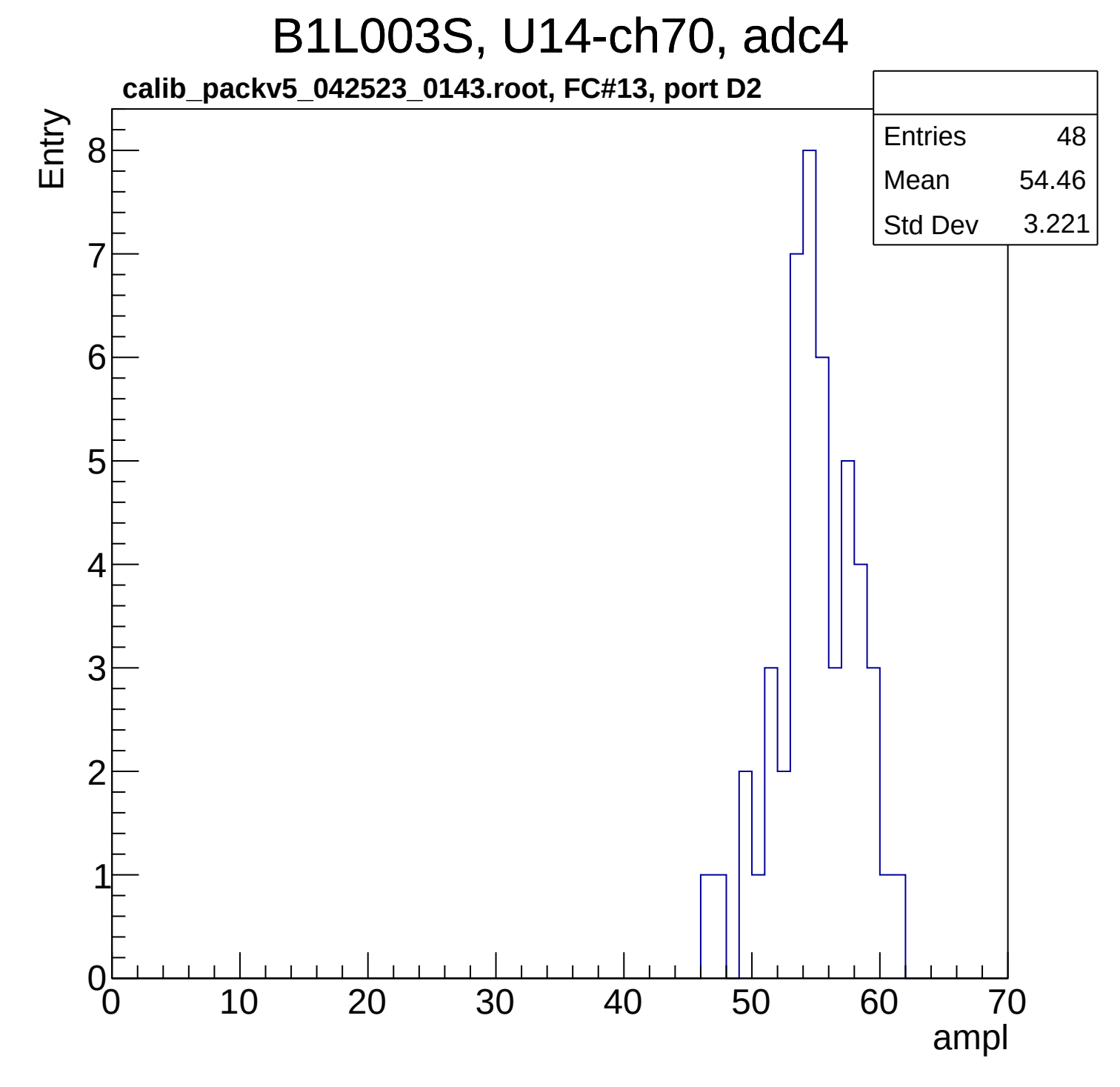

B1L003S, U14-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

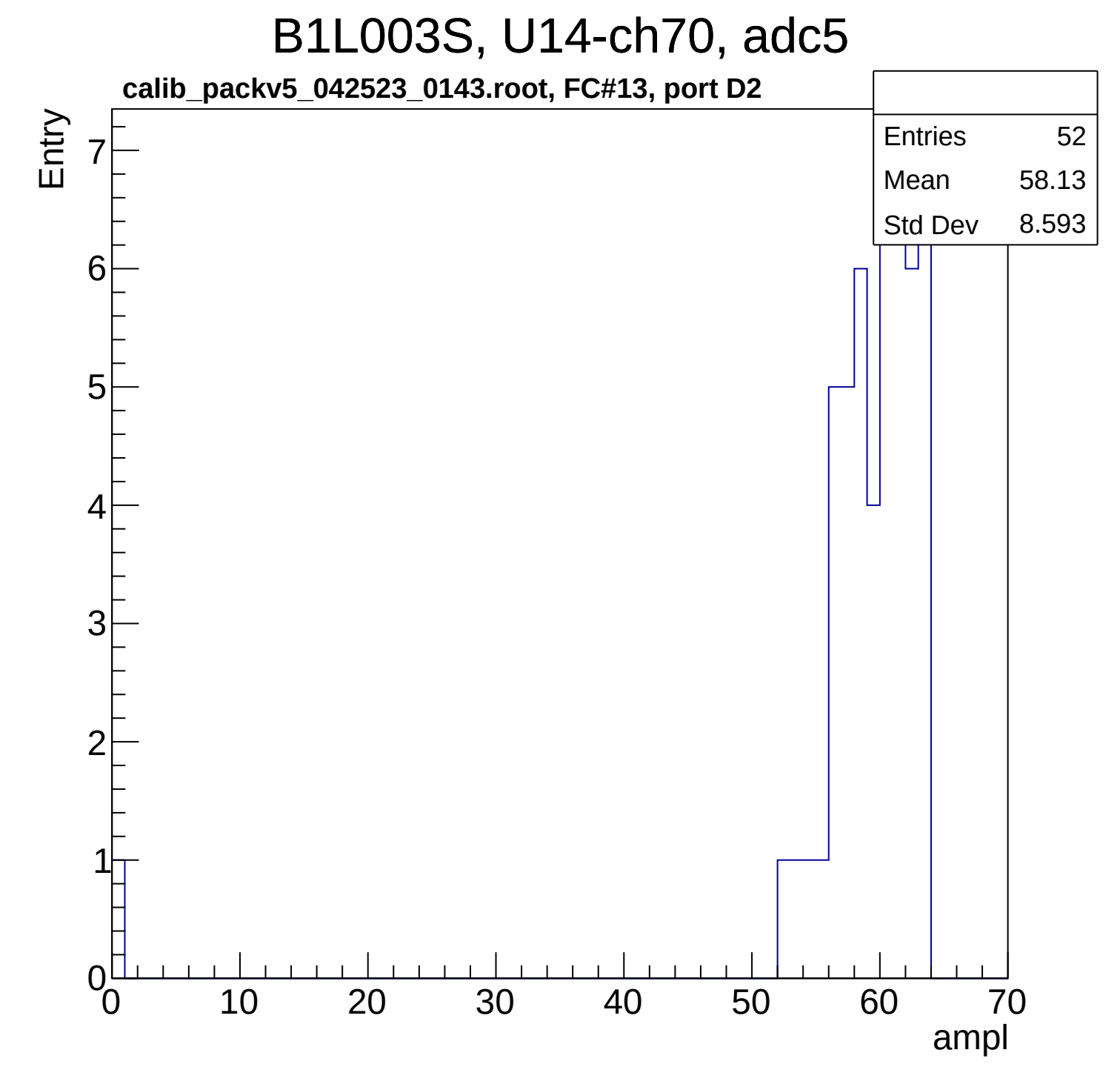

B1L003S, U14-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 30.03
Std Dev 4.997
Gaus mean : 30.9479
Gaus Width: 3.7825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

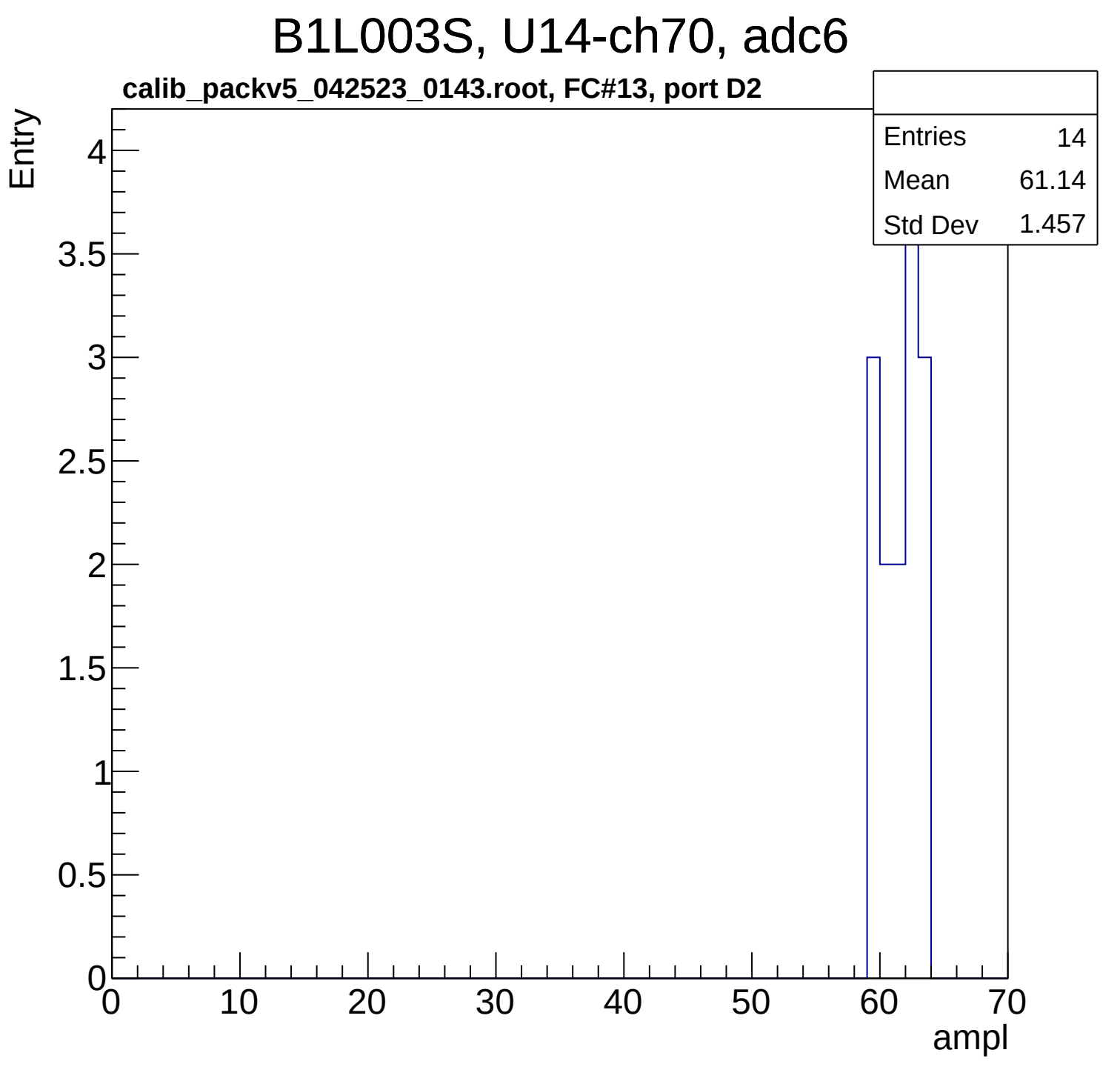

B1L003S, U14-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.96
Std Dev 3.913
Gaus mean : 37.3821
Gaus Width: 4.0556
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

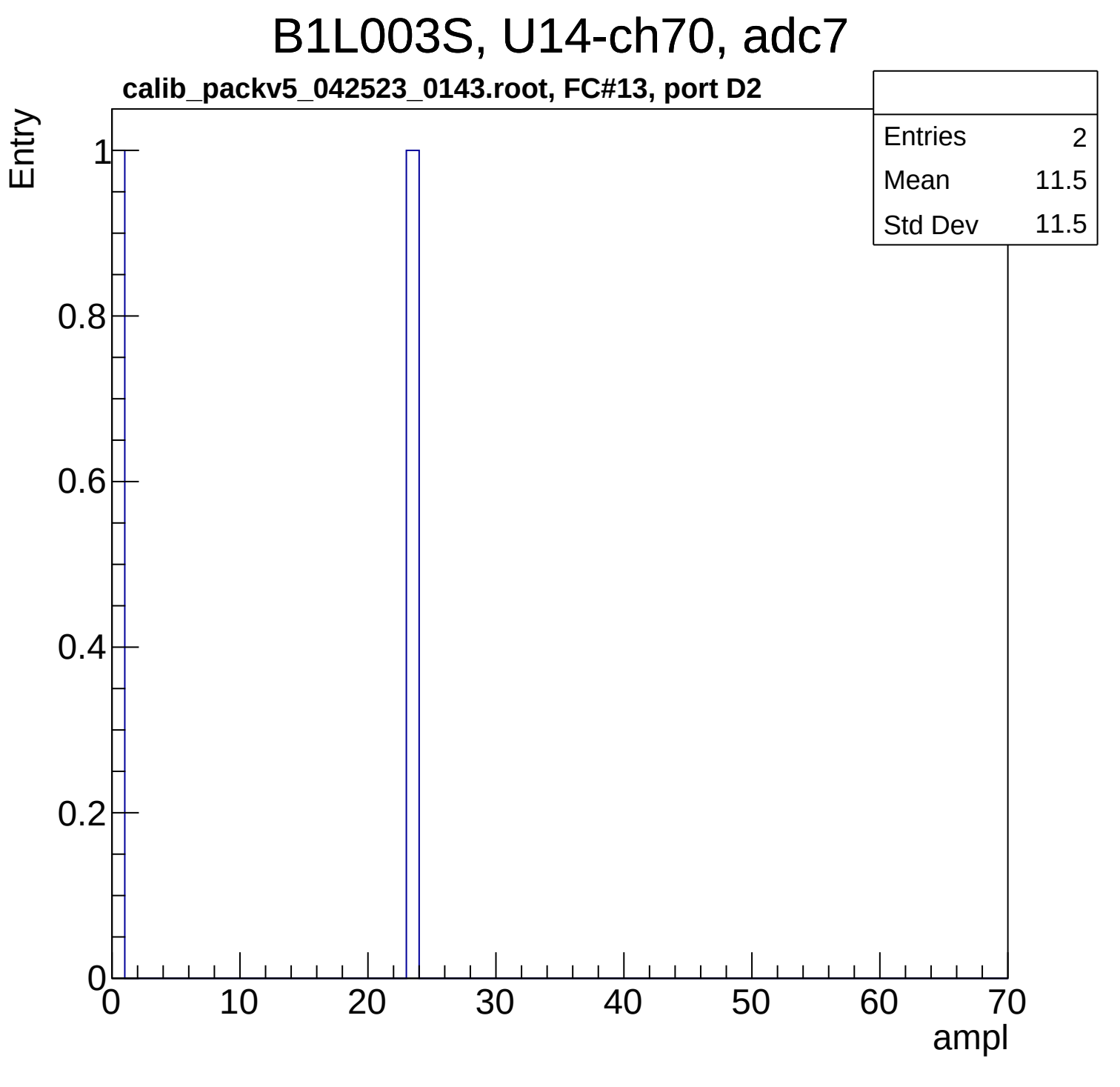

B1L003S, U14-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.96
Std Dev 2.879
Gaus mean : 43.3762
Gaus Width: 3.3211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

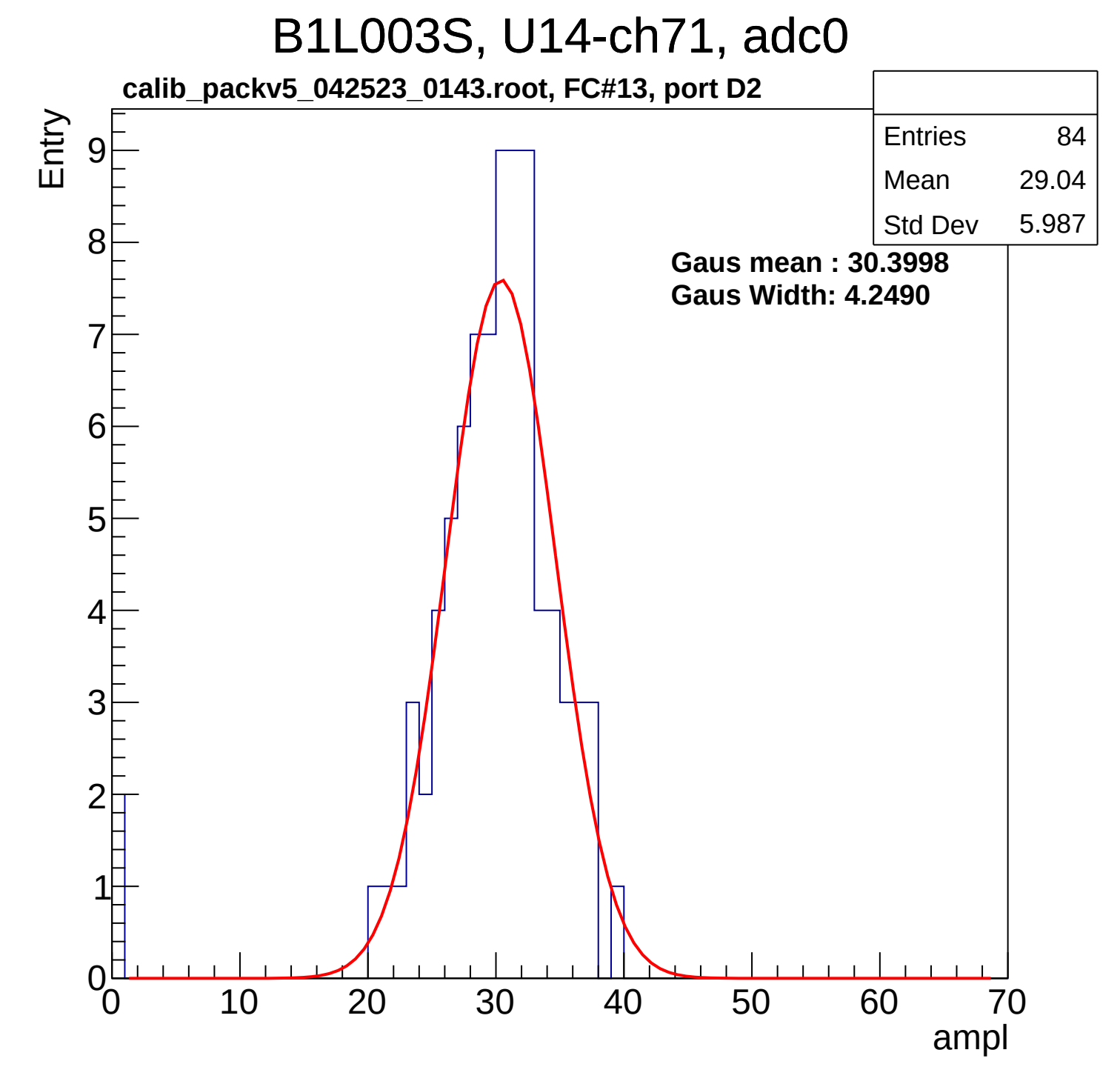

B1L003S, U14-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.38
Std Dev 3.163
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

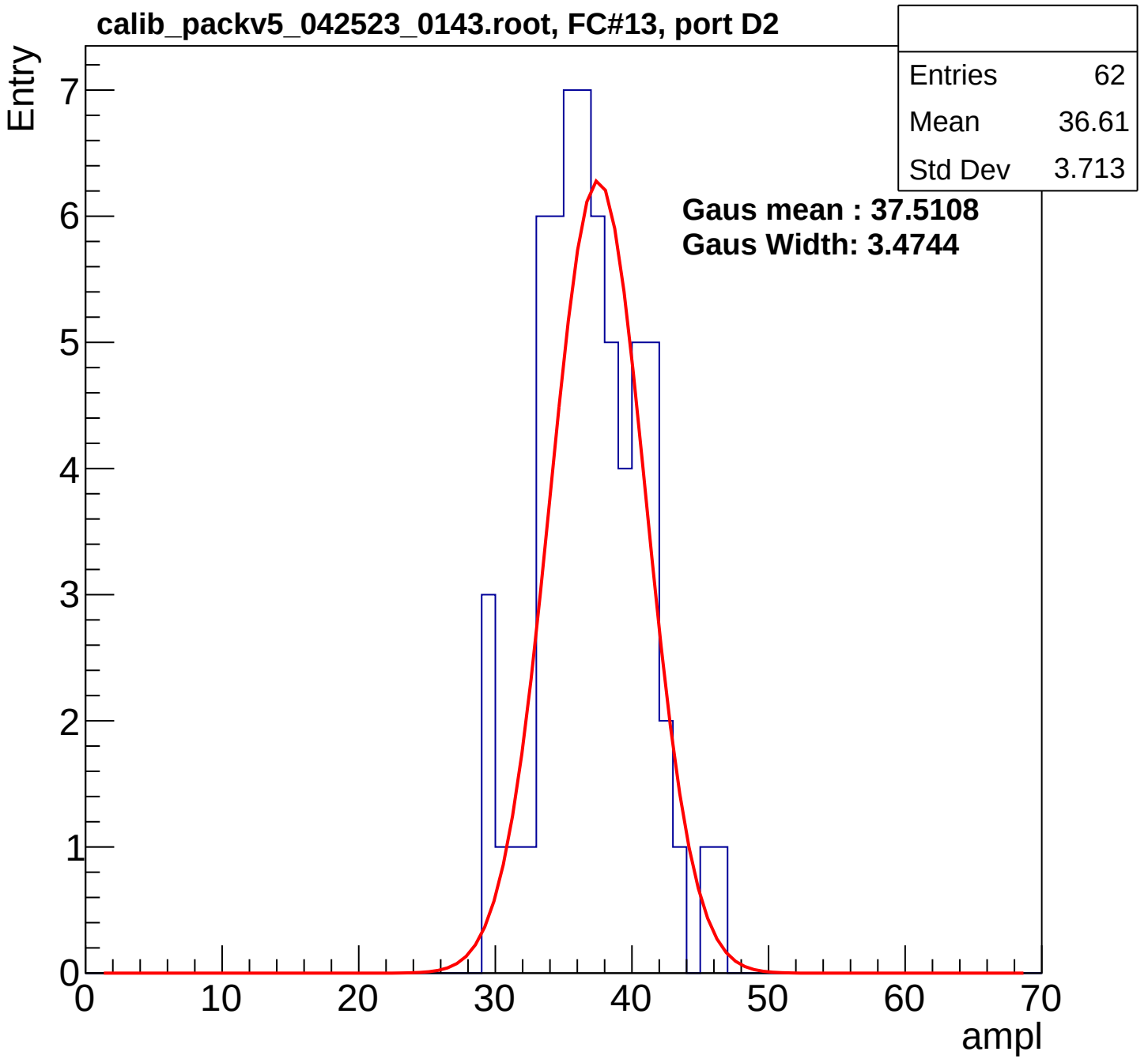

B1L003S, U14-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.46
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

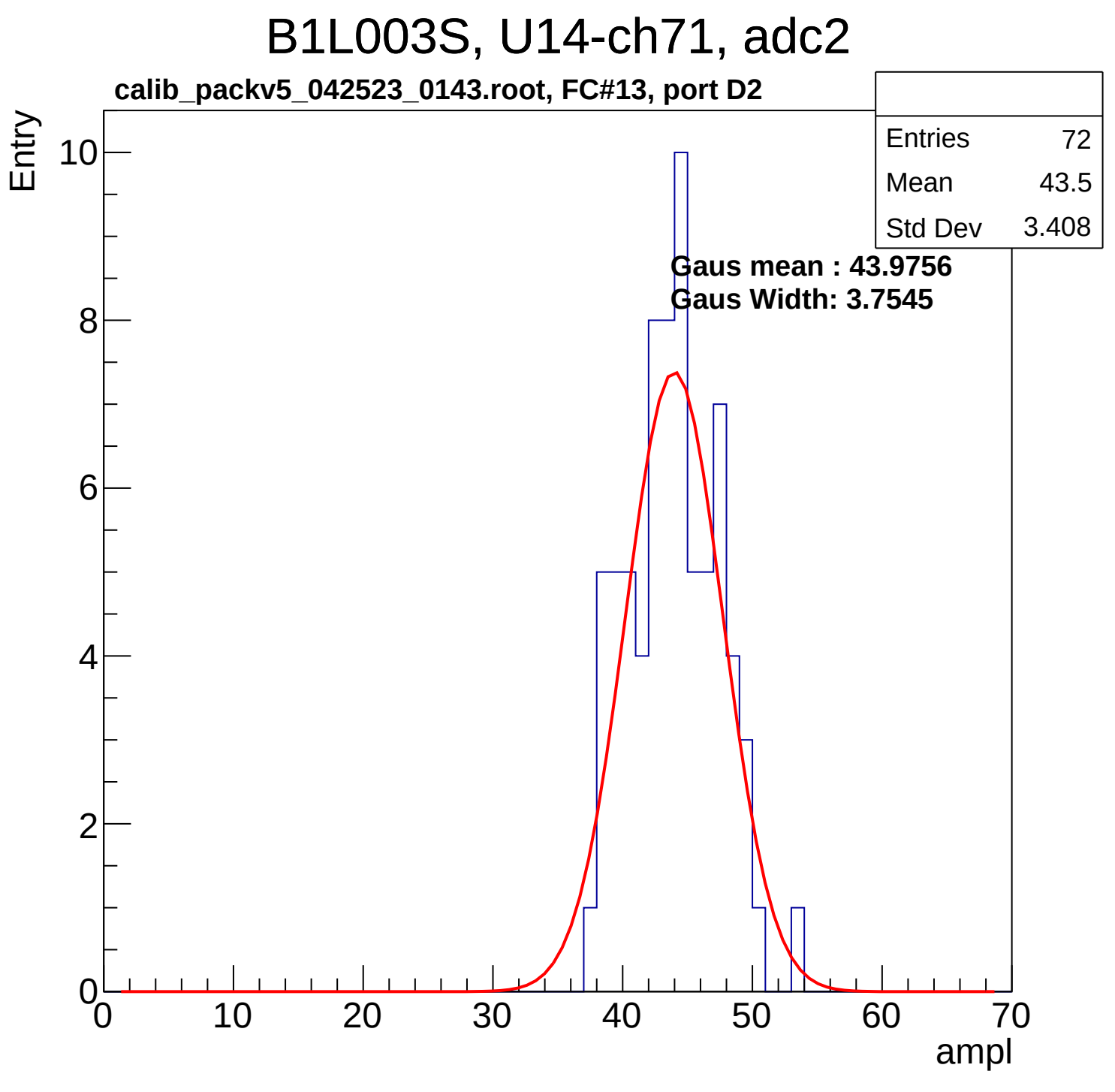

B1L003S, U14-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.13
Std Dev 8.593
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

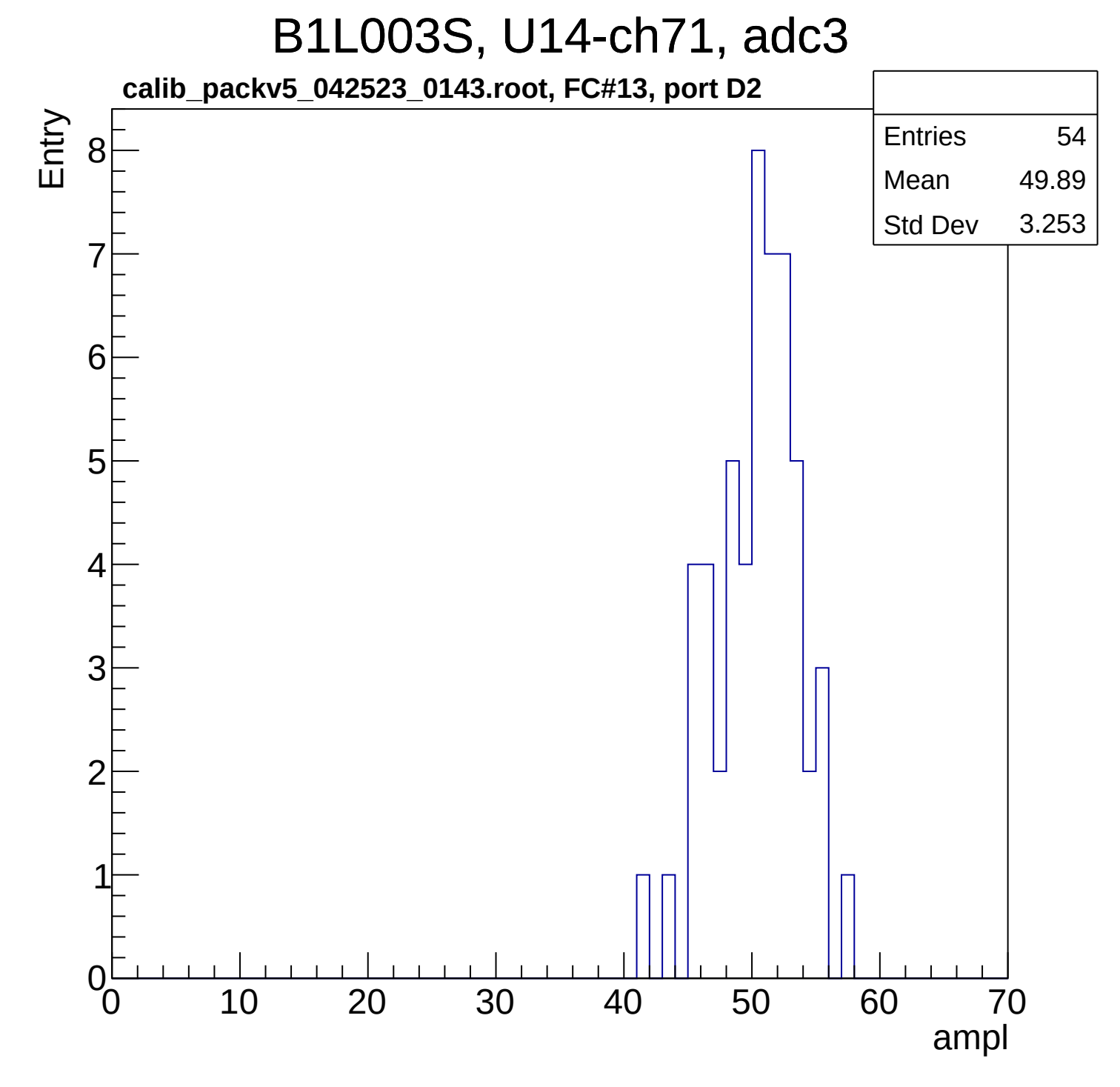

B1L003S, U14-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.14
Std Dev 1.457
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

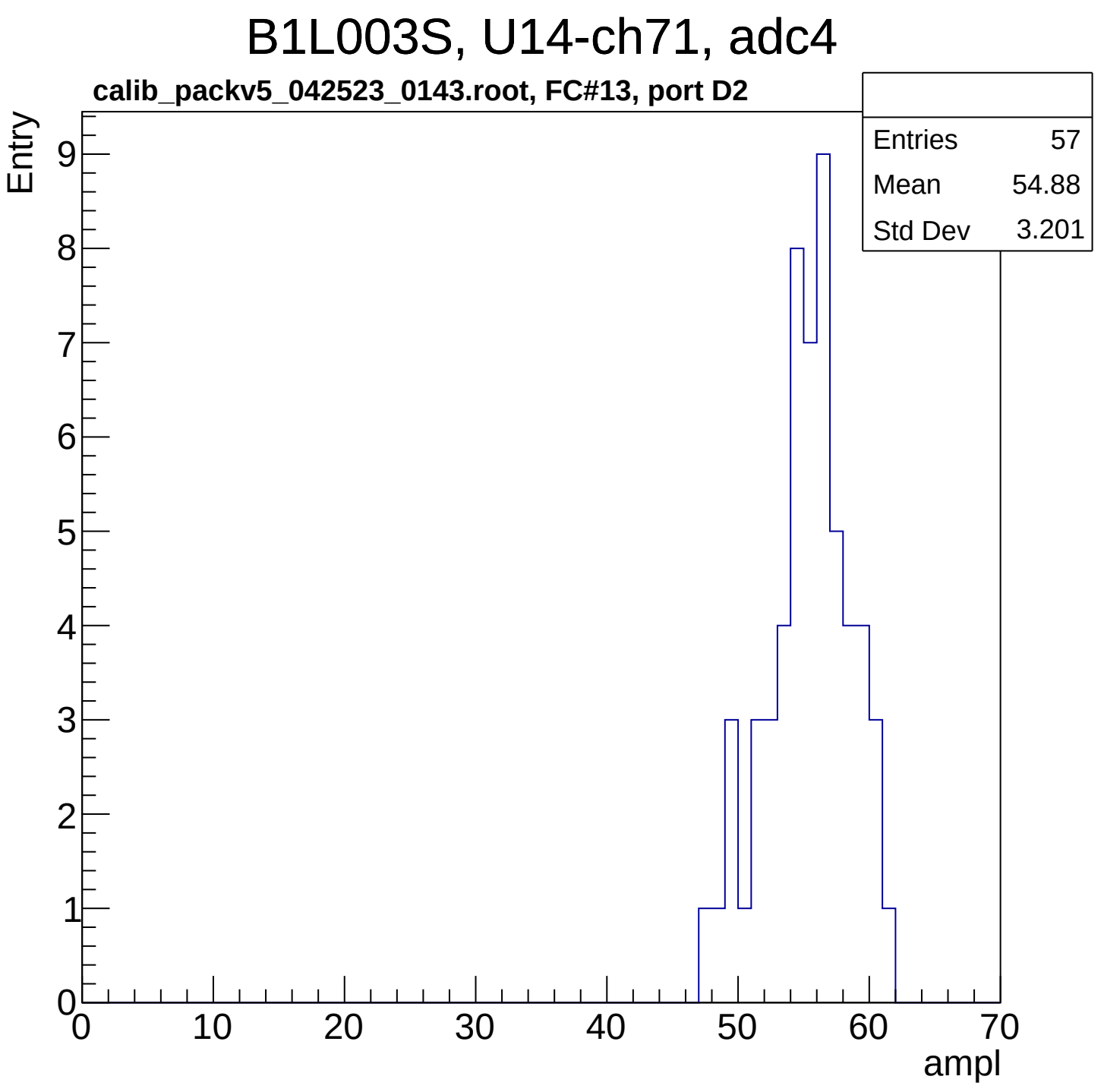

B1L003S, U14-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

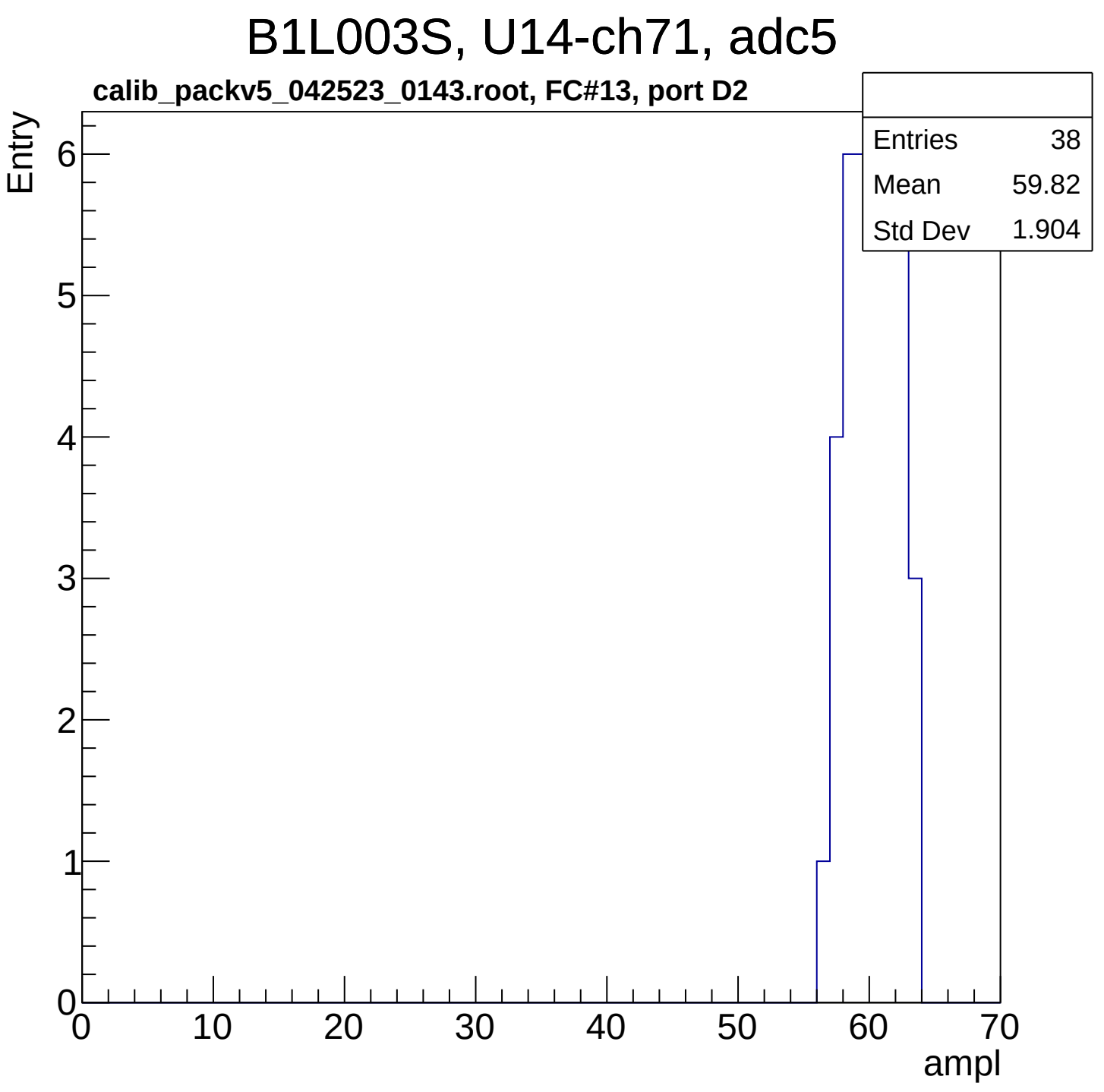

B1L003S, U14-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 29.04
Std Dev 5.987
Gaus mean : 30.3998
Gaus Width: 4.2490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

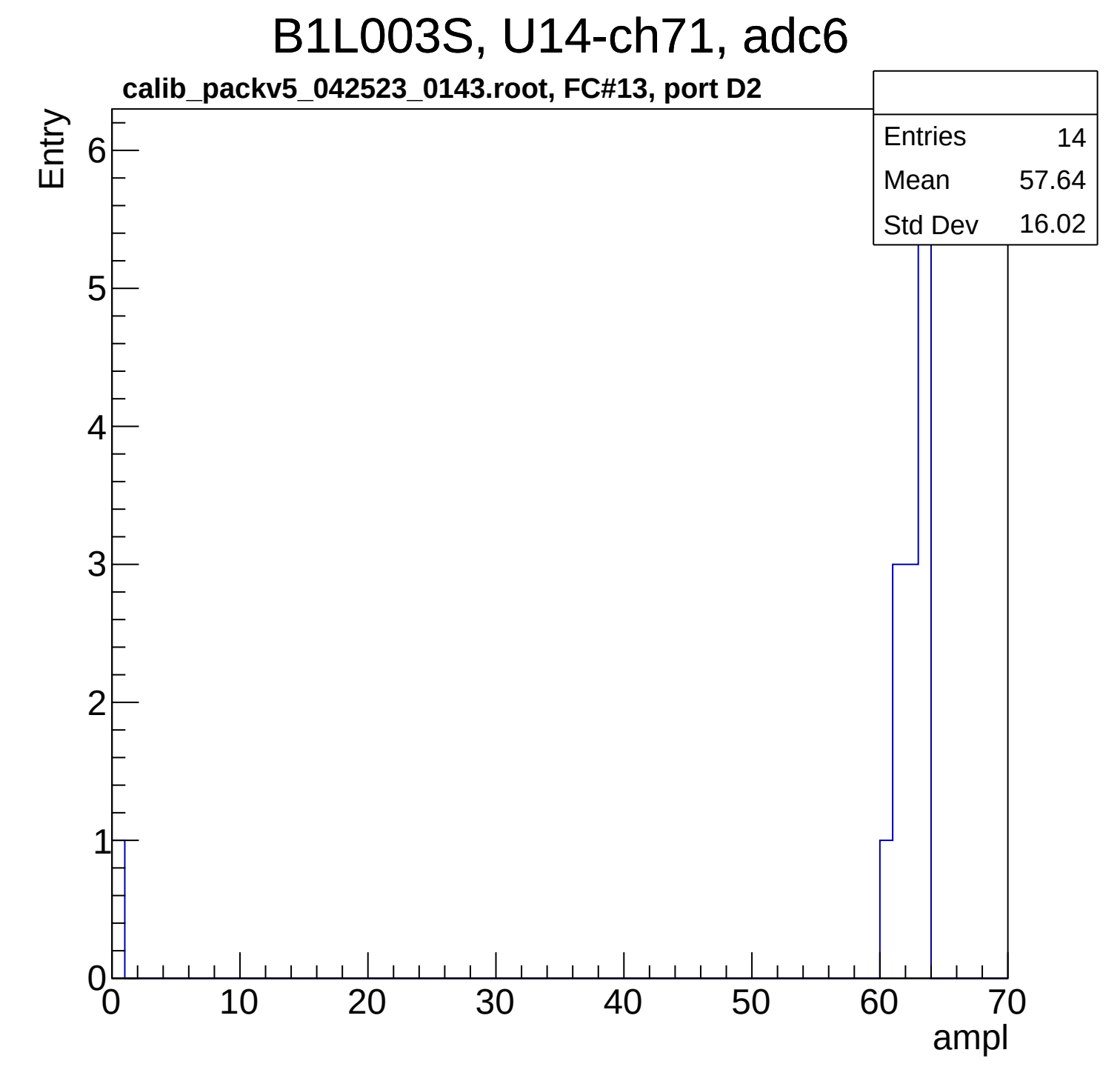

B1L003S, U14-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.61
Std Dev 3.713
Gaus mean : 37.5108
Gaus Width: 3.4744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

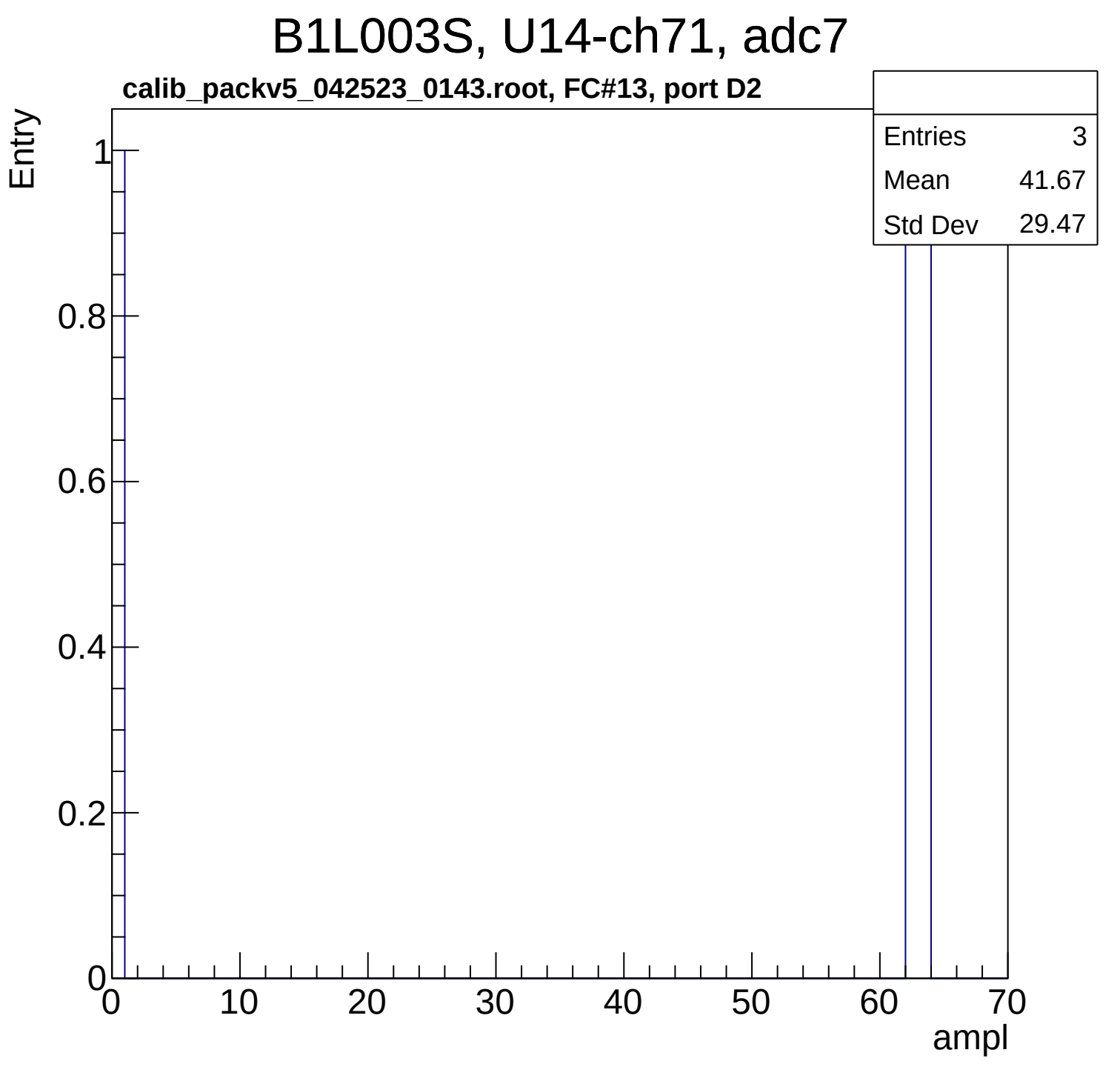

B1L003S, U14-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 43.5
Std Dev 3.408
Gaus mean : 43.9756
Gaus Width: 3.7545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

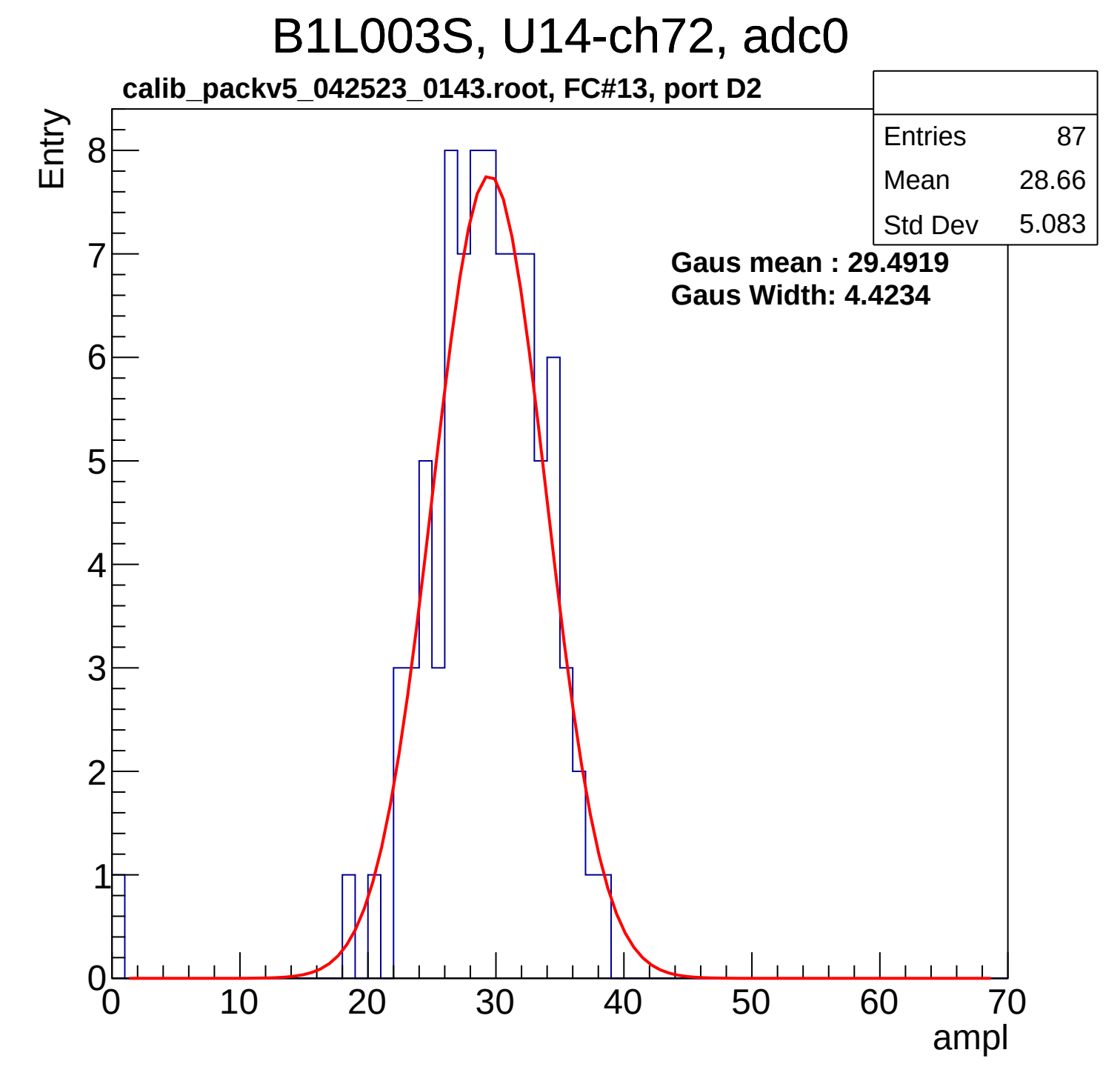

B1L003S, U14-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.89
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

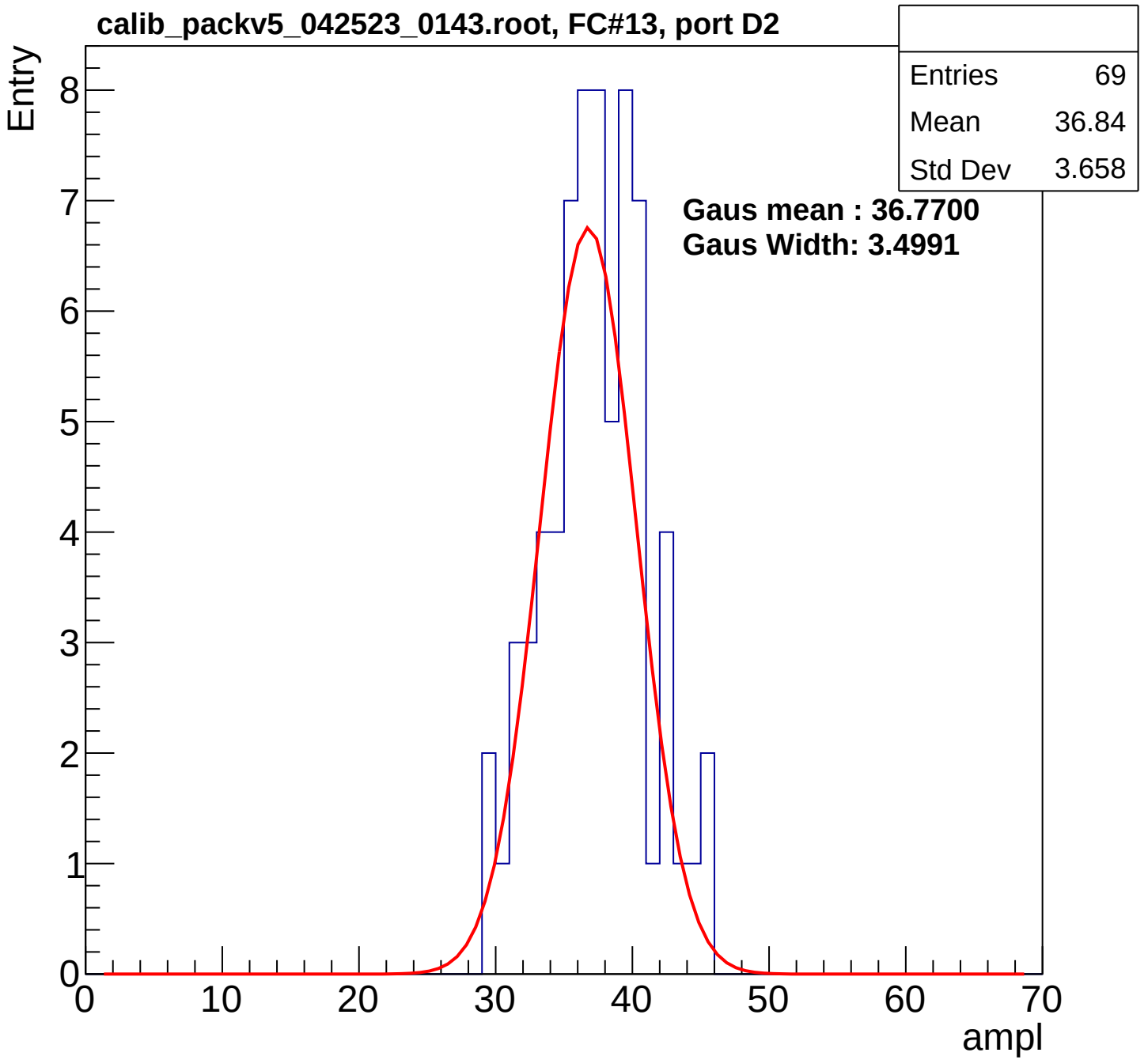

B1L003S, U14-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.88
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

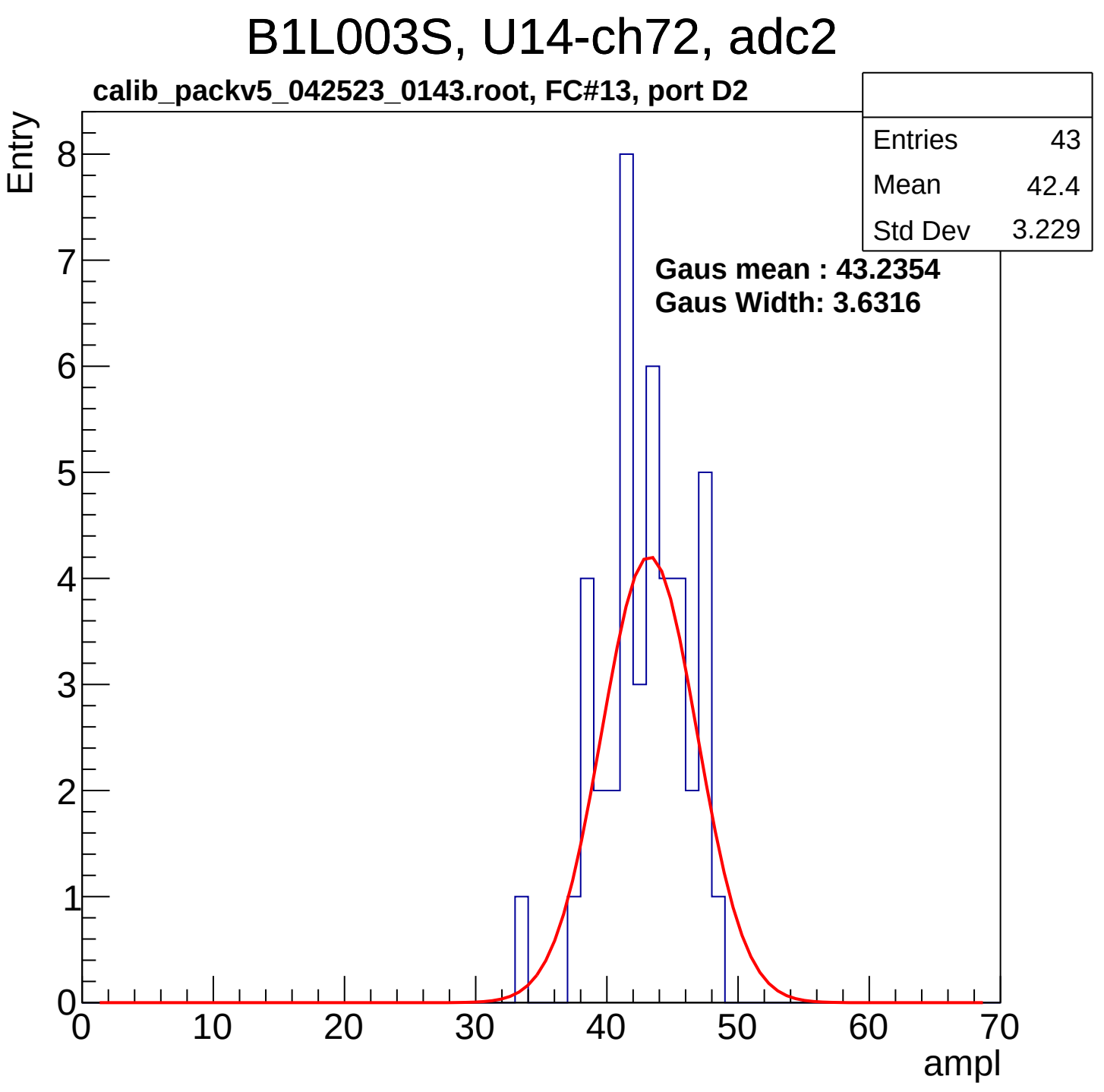

B1L003S, U14-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.82
Std Dev 1.904
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

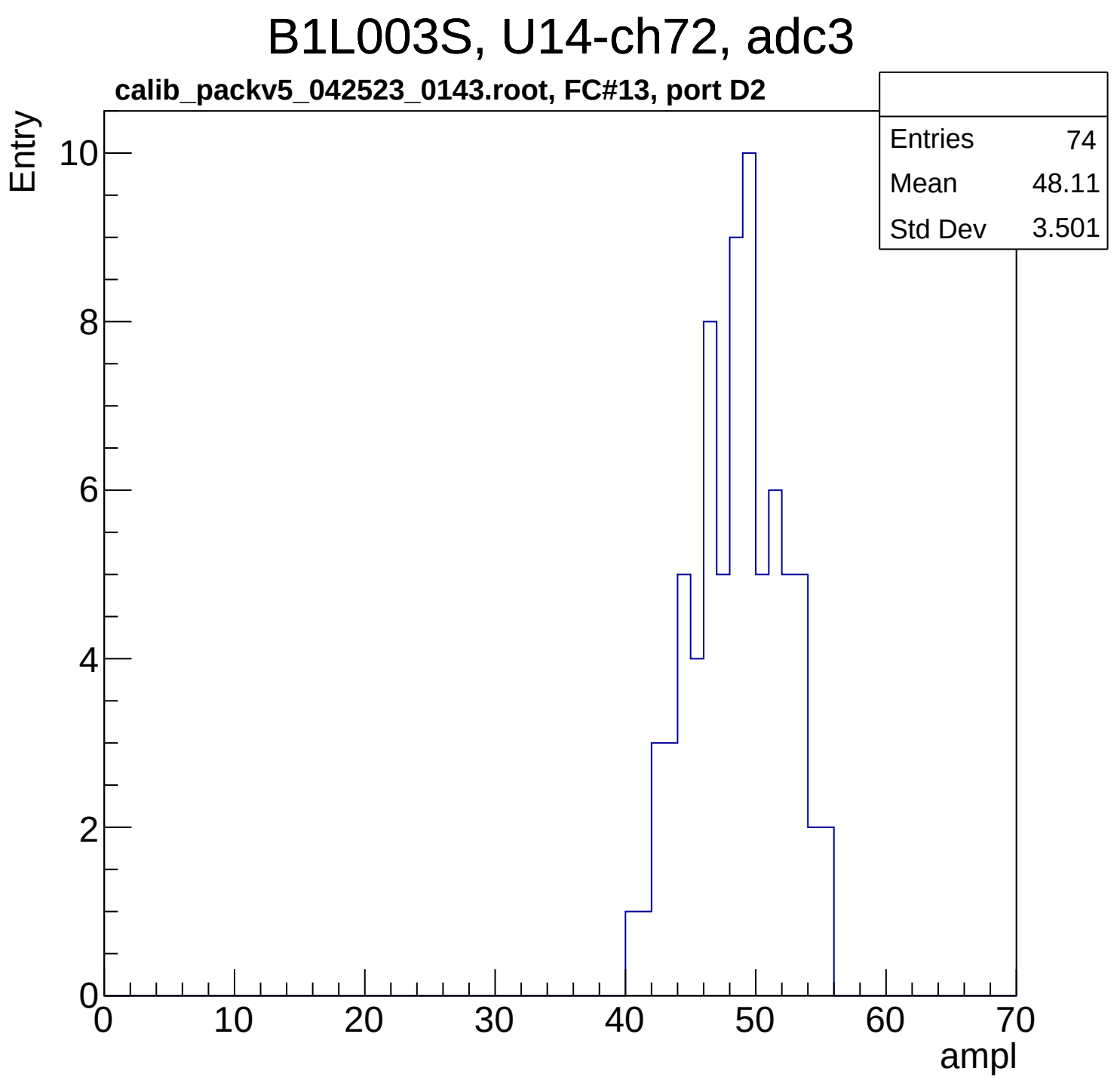

B1L003S, U14-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.64
Std Dev 16.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

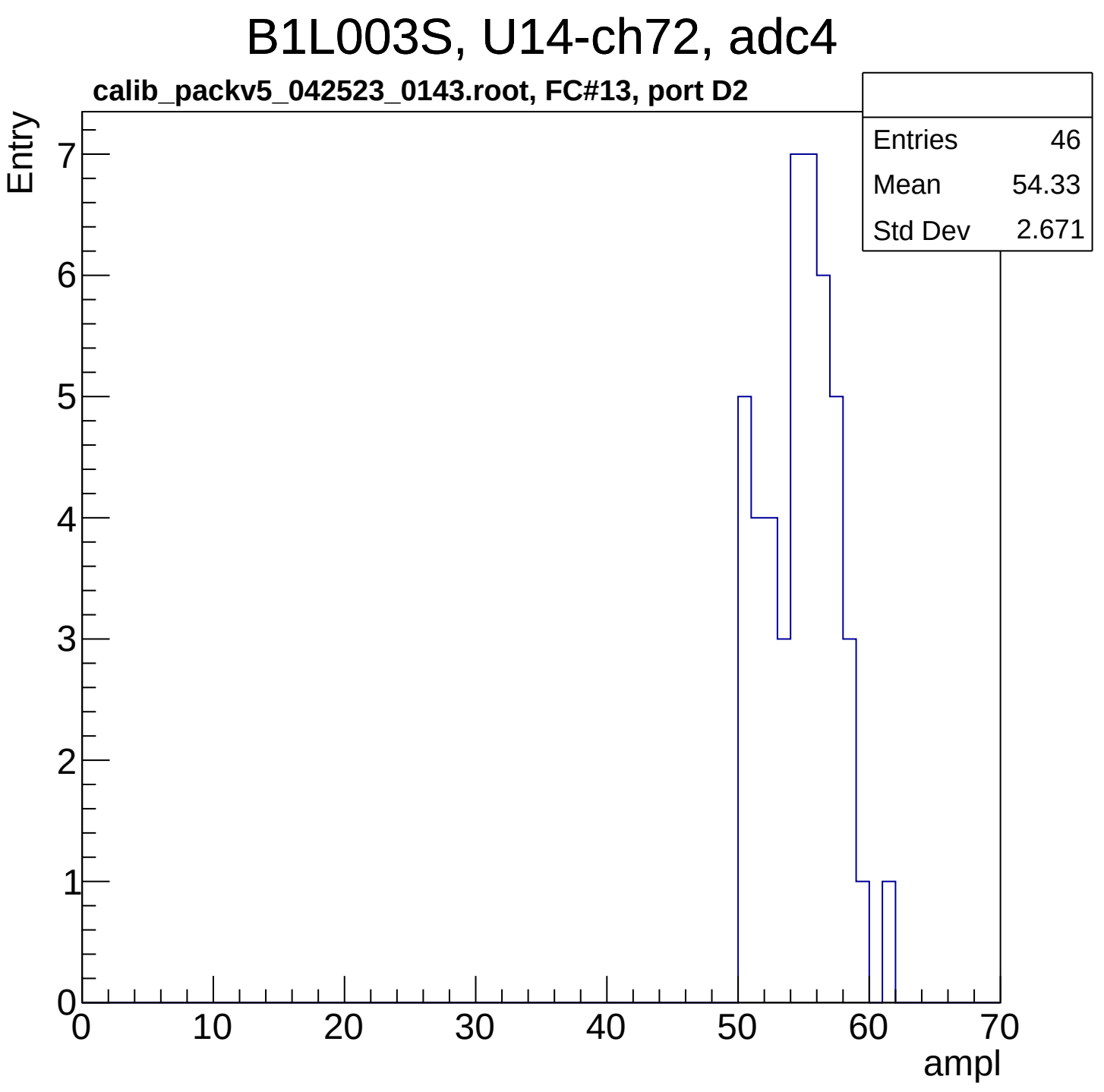

B1L003S, U14-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

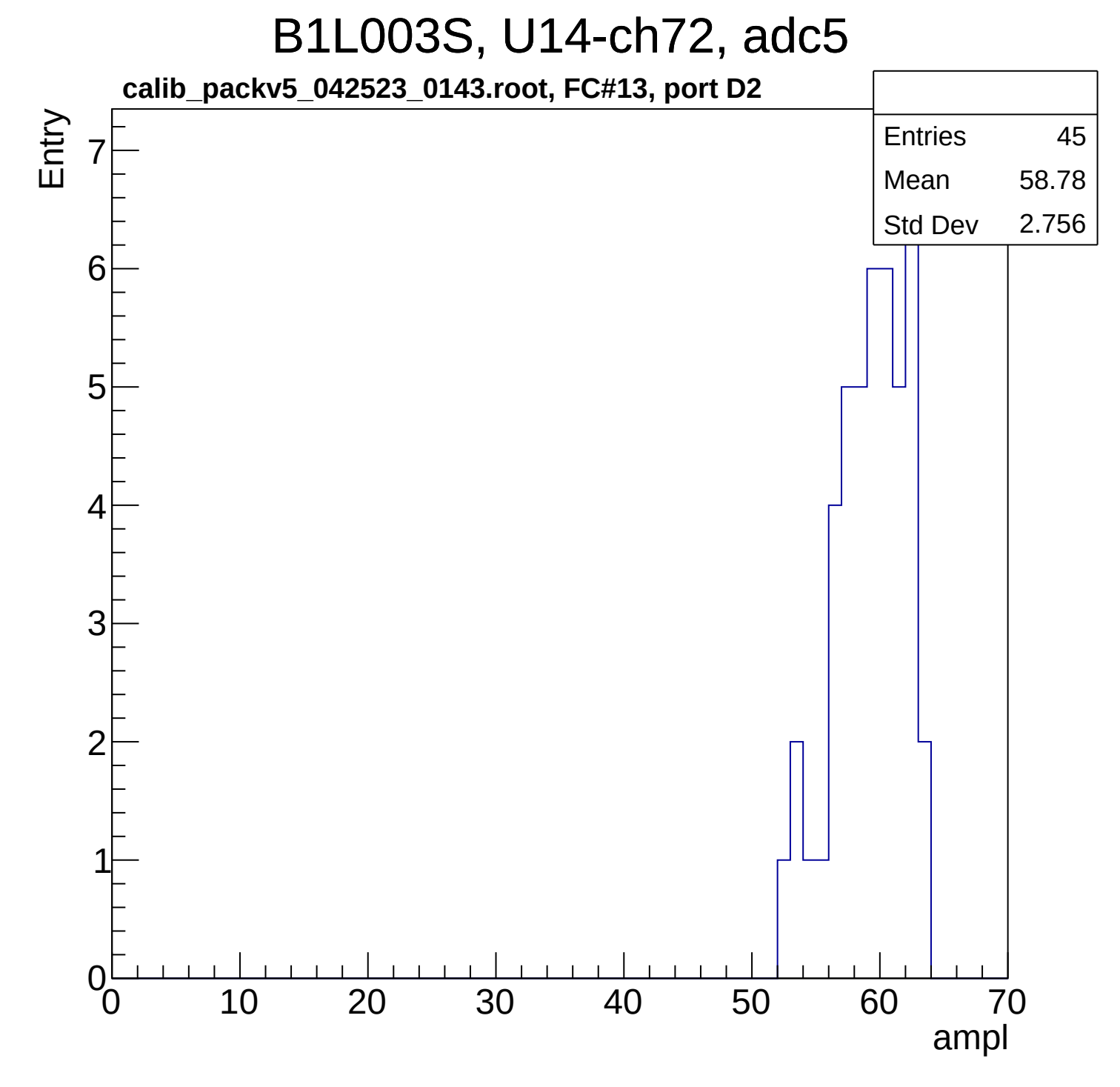

B1L003S, U14-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 28.66
Std Dev 5.083
Gaus mean : 29.4919
Gaus Width: 4.4234
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

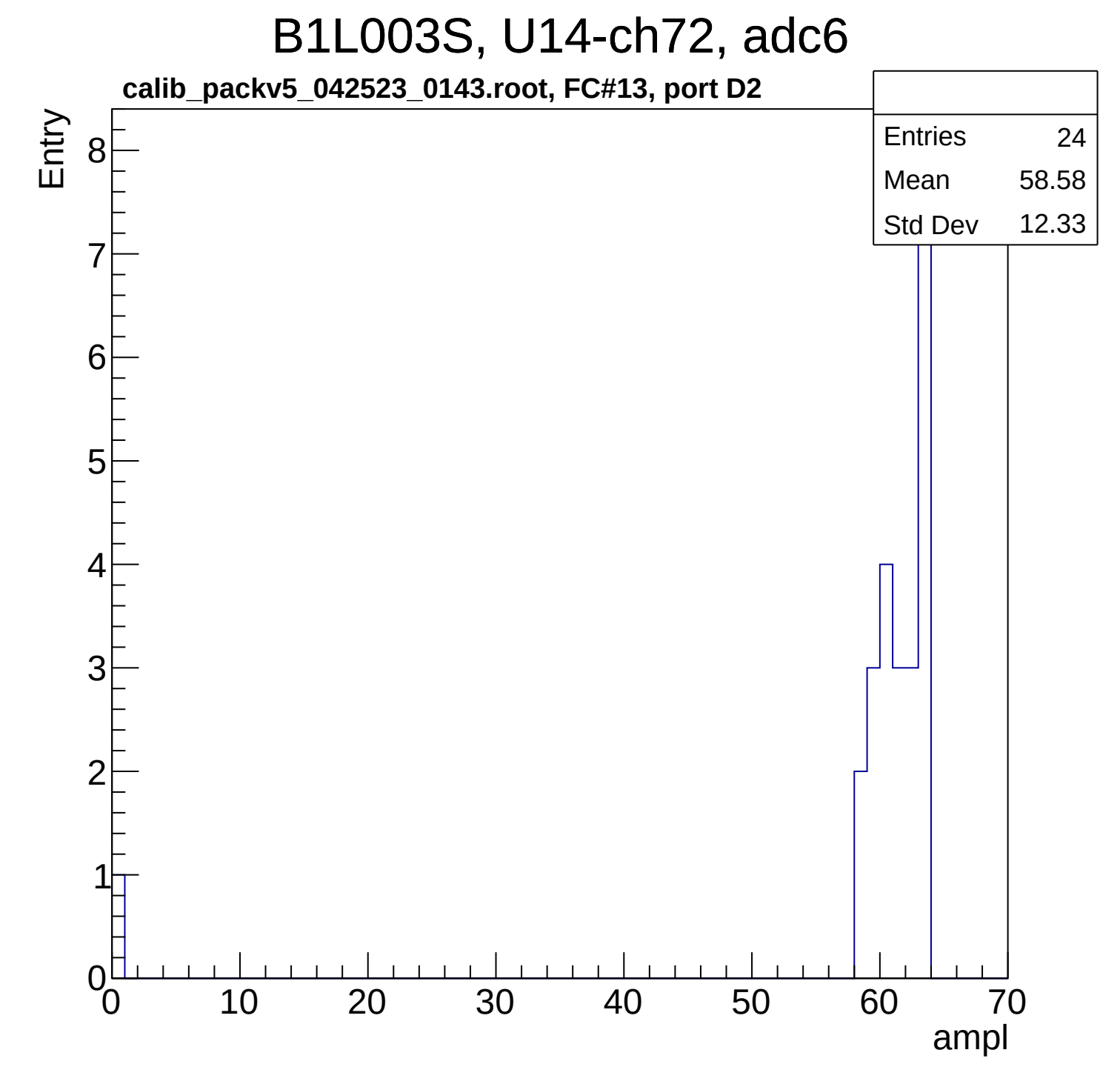

B1L003S, U14-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.84
Std Dev 3.658
Gaus mean : 36.7700
Gaus Width: 3.4991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

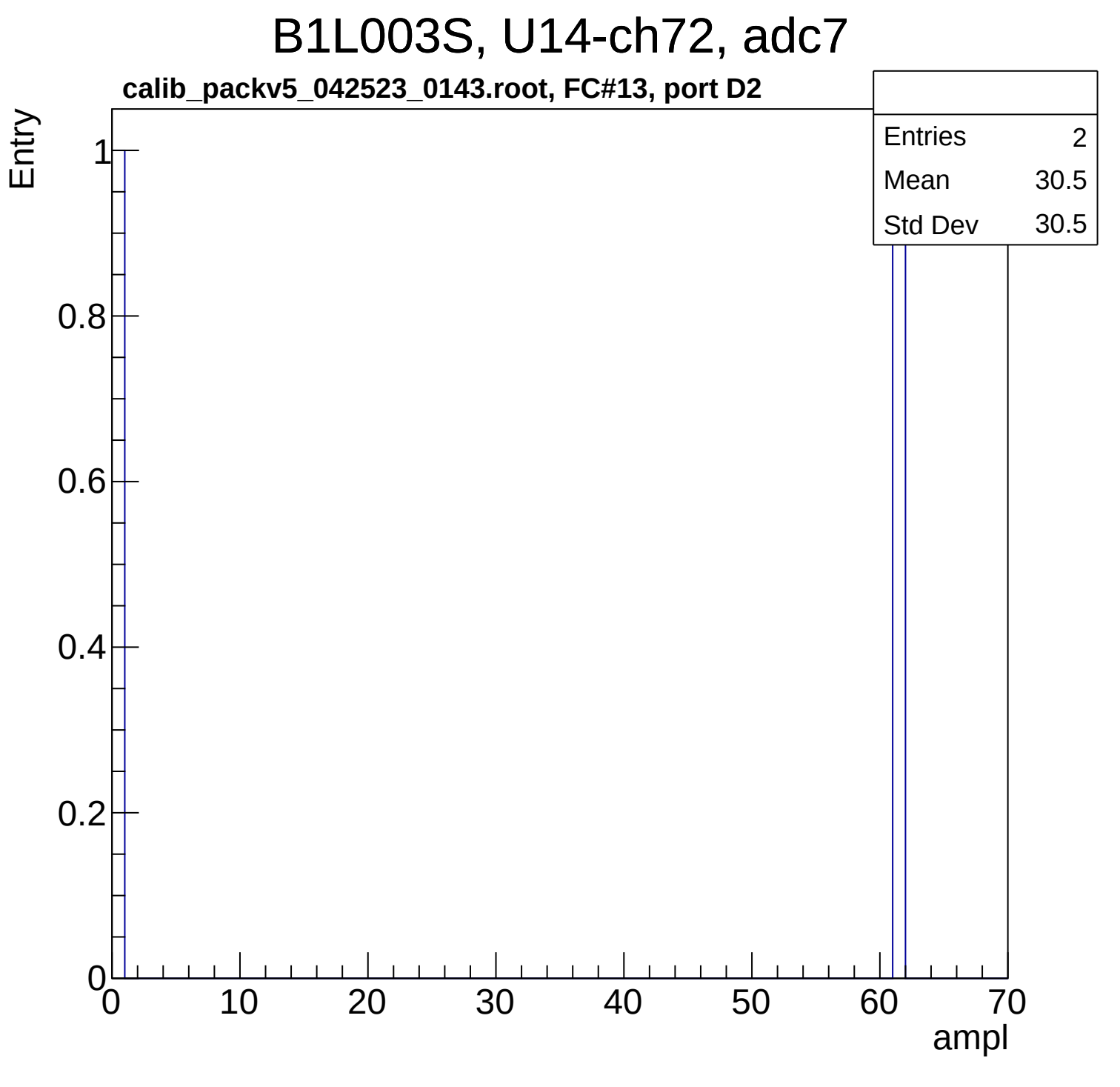

B1L003S, U14-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 42.4
Std Dev 3.229
Gaus mean : 43.2354
Gaus Width: 3.6316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

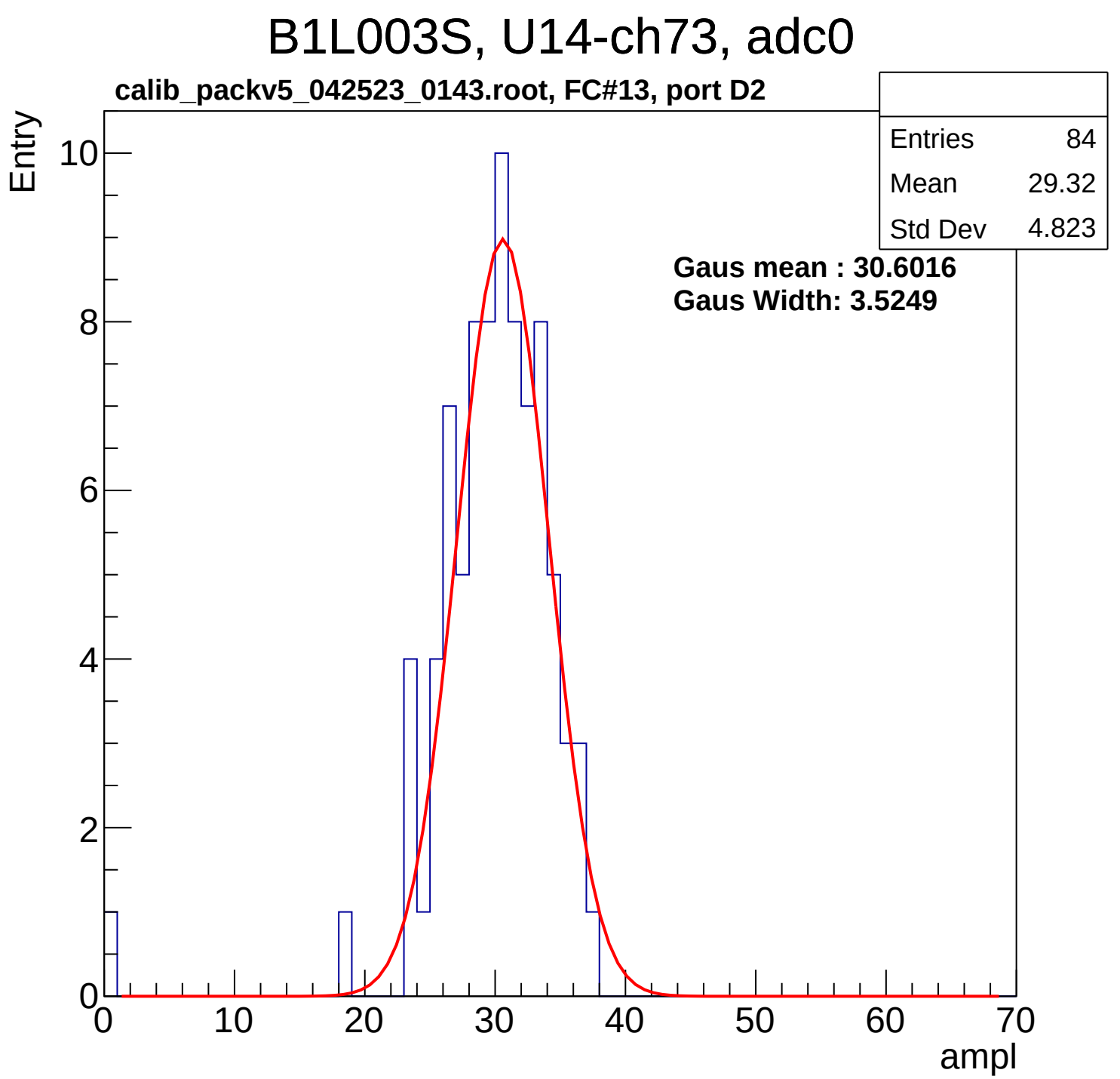

B1L003S, U14-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.11
Std Dev 3.501
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

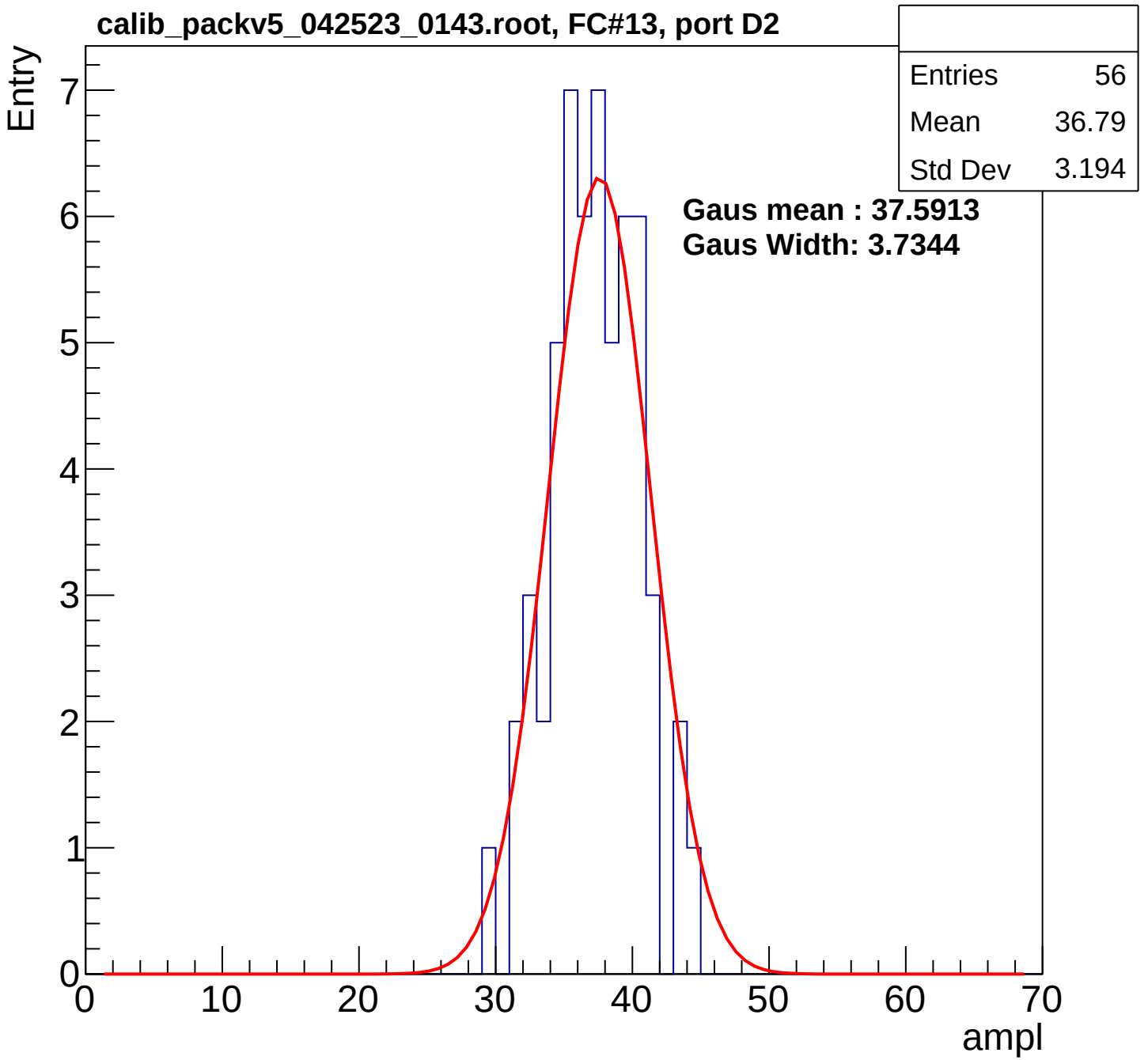

B1L003S, U14-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 54.33
Std Dev 2.671
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

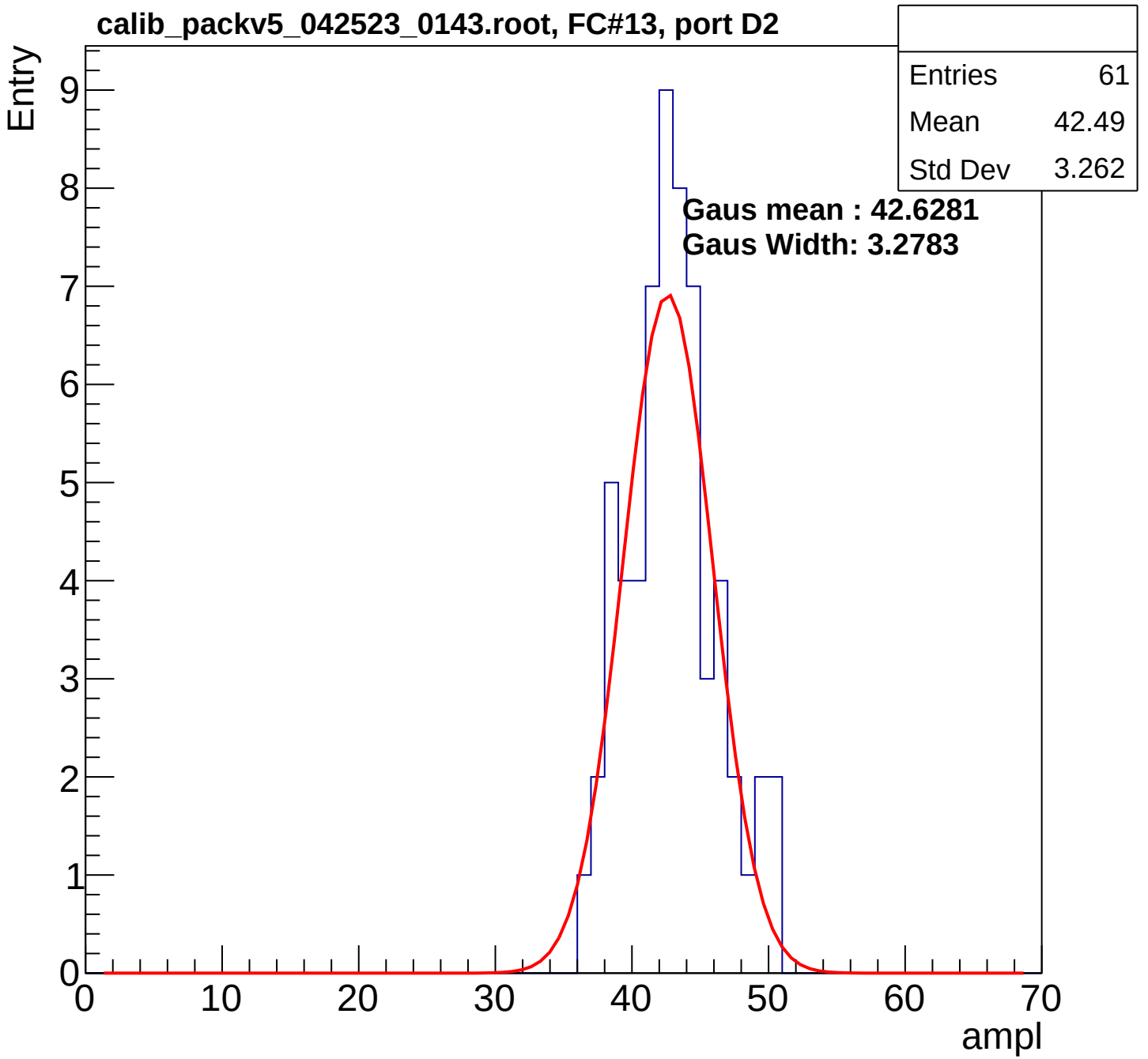

B1L003S, U14-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.78
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

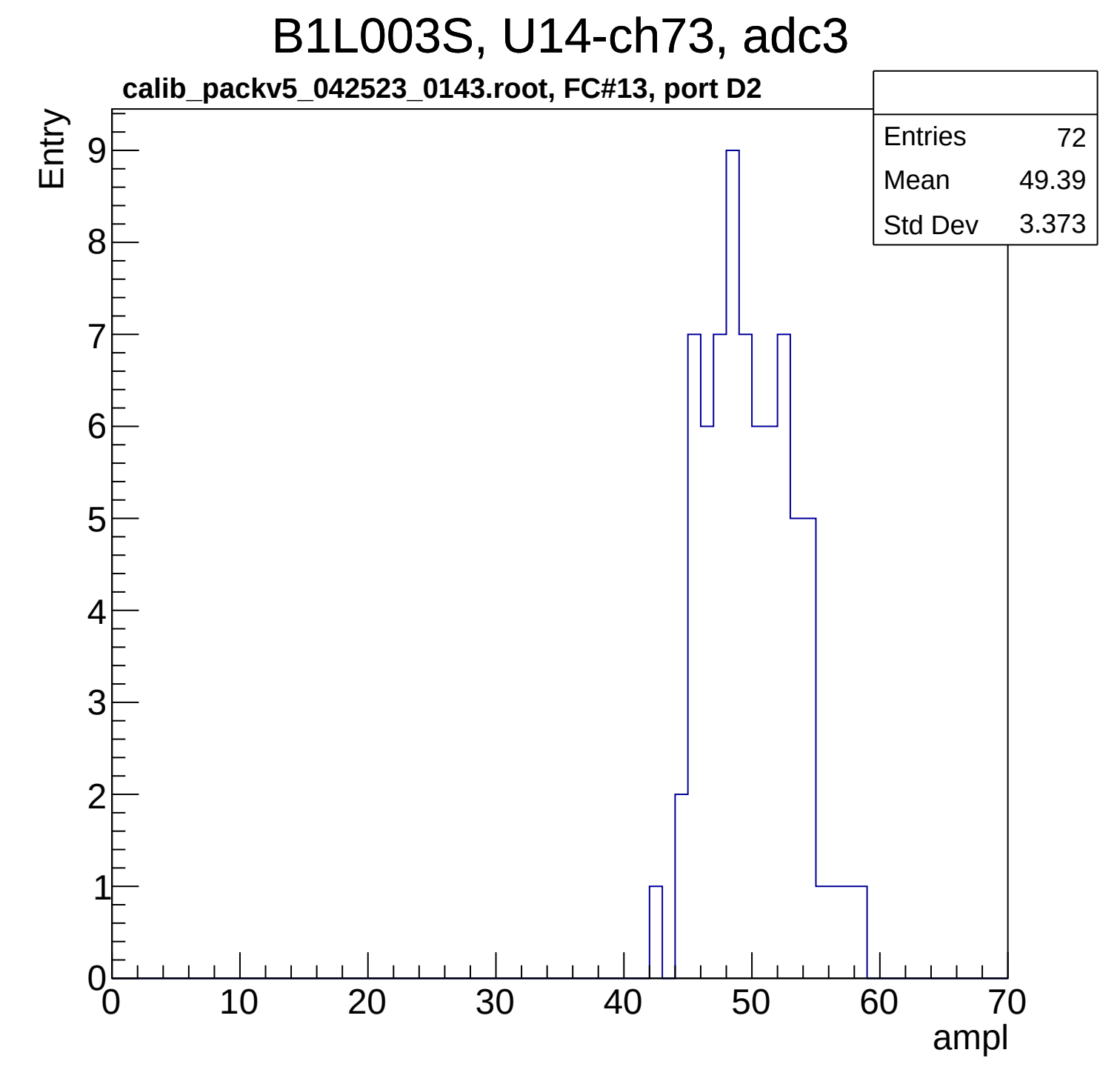

B1L003S, U14-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.58
Std Dev 12.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

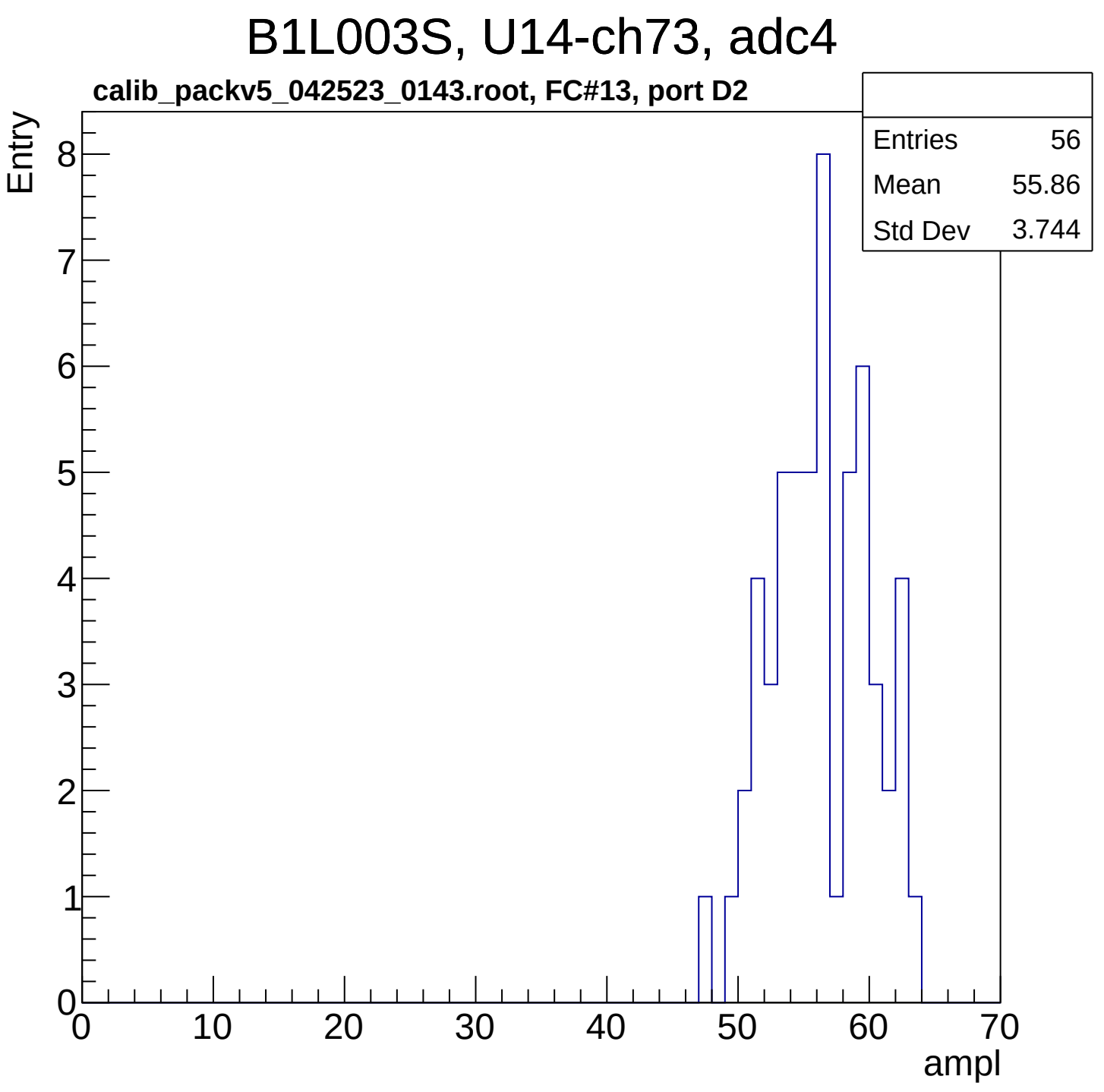

B1L003S, U14-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

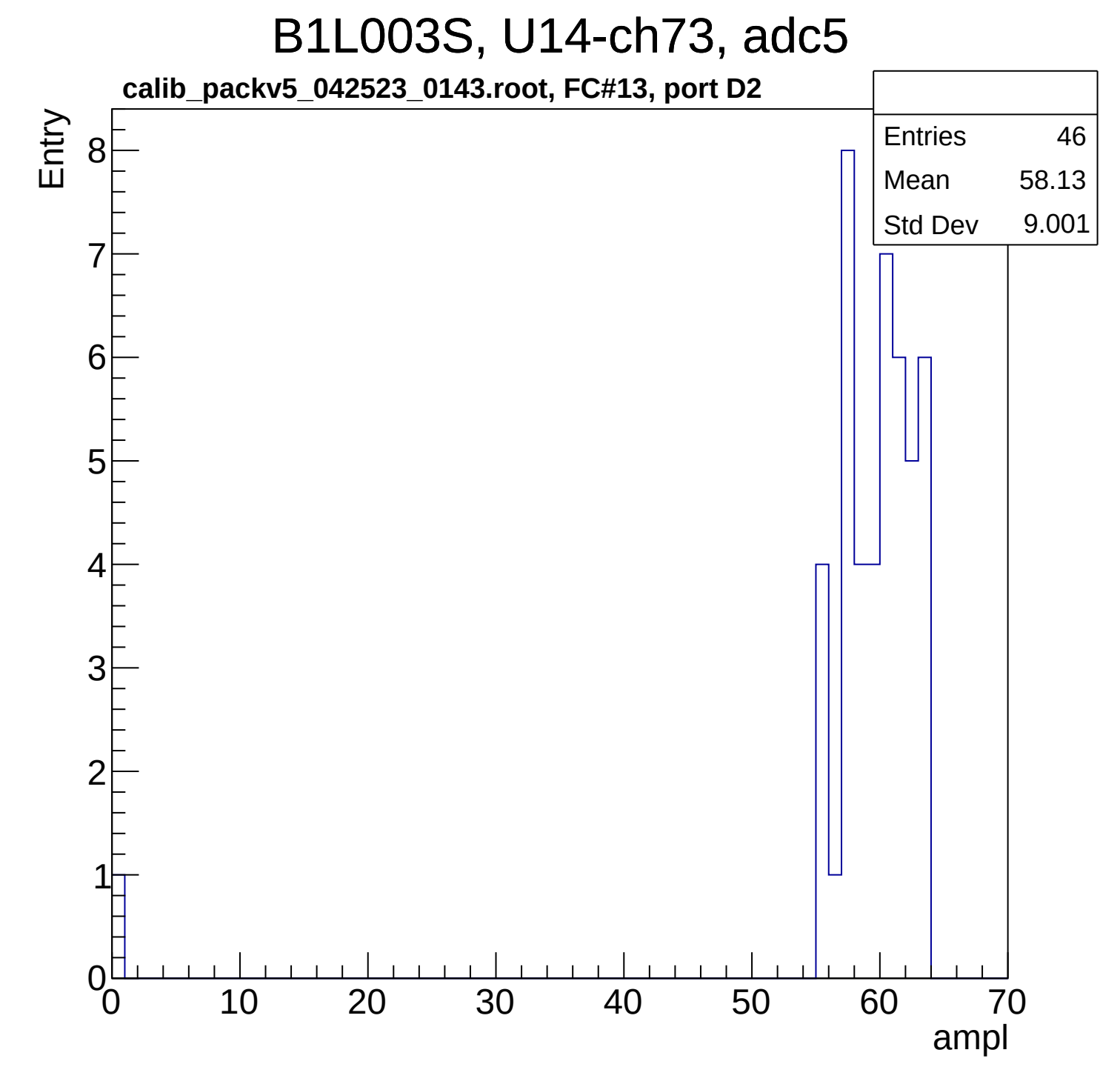

B1L003S, U14-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 29.32
Std Dev 4.823
Gaus mean : 30.6016
Gaus Width: 3.5249
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

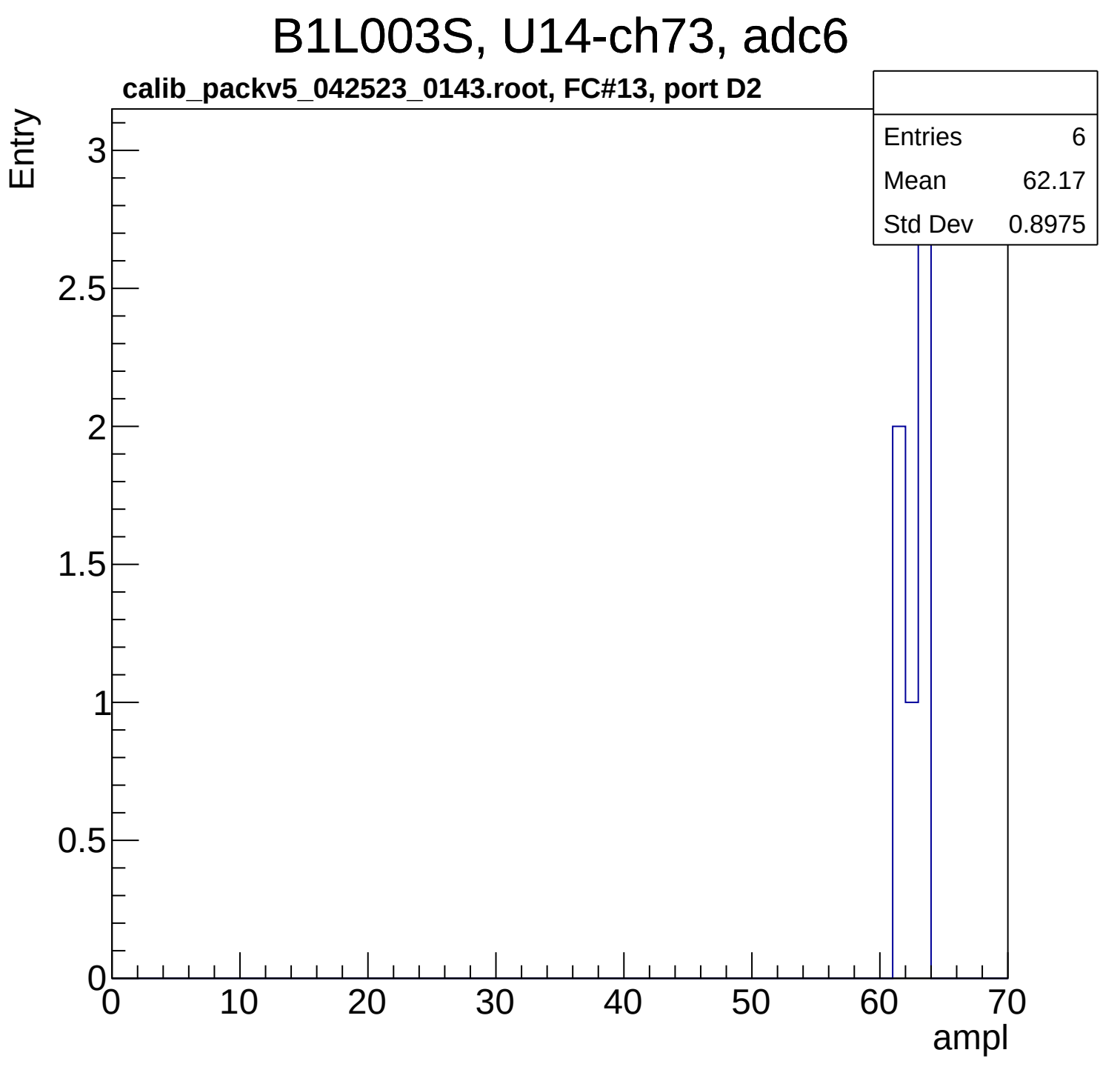

B1L003S, U14-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.79
Std Dev 3.194
Gaus mean : 37.5913
Gaus Width: 3.7344
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

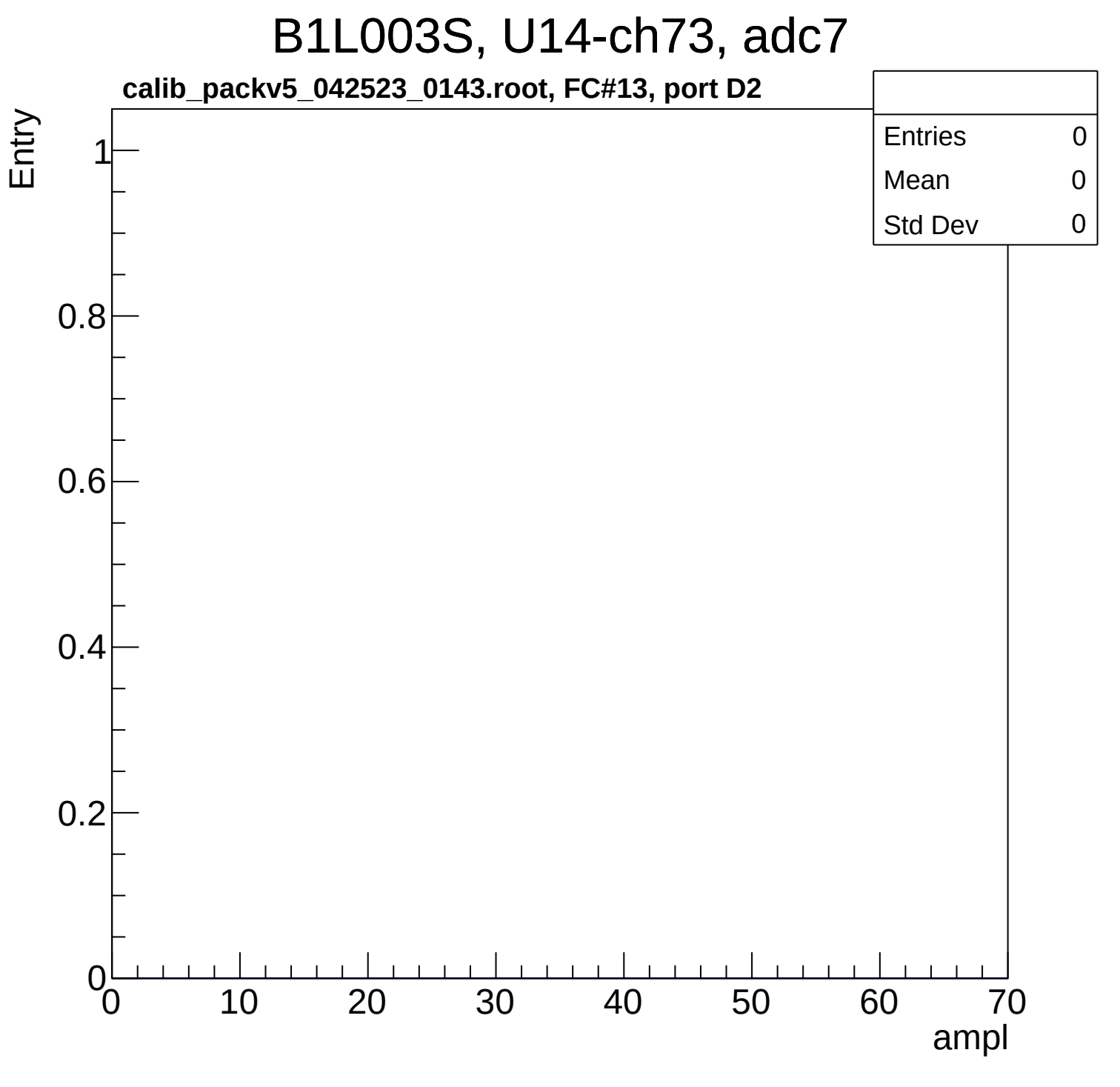

B1L003S, U14-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.49
Std Dev 3.262
Gaus mean : 42.6281
Gaus Width: 3.2783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

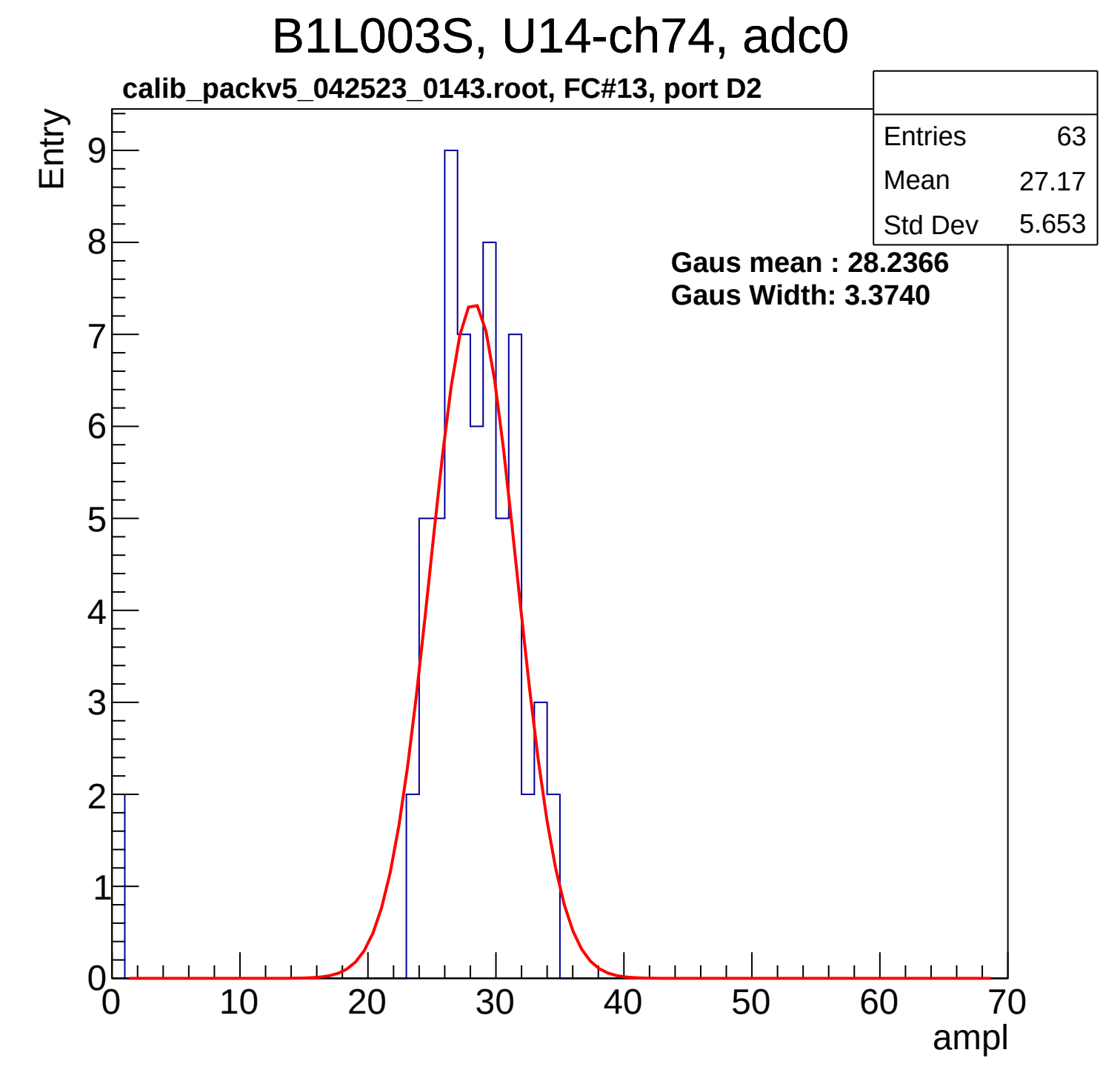

B1L003S, U14-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.39
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

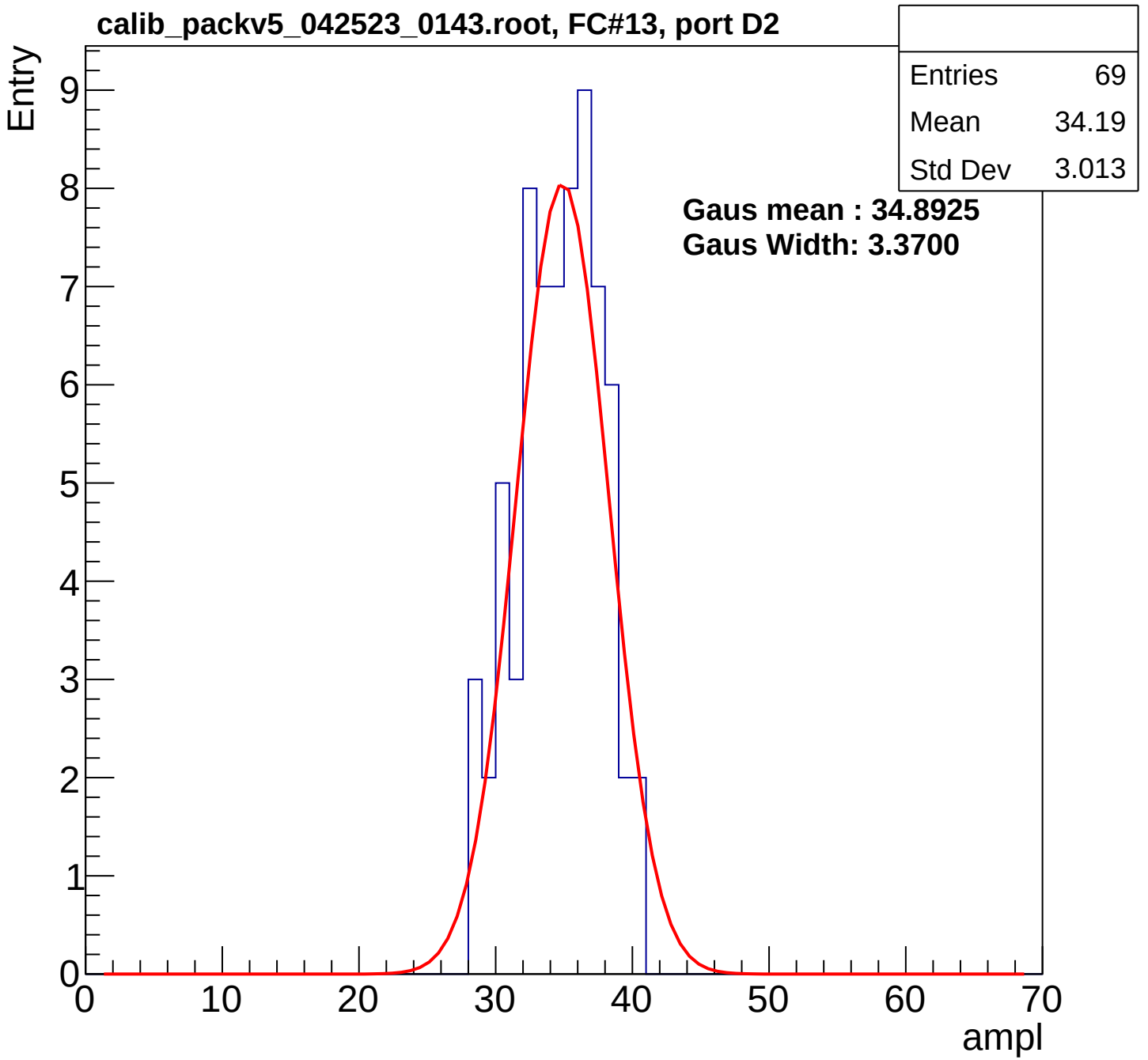

B1L003S, U14-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.86
Std Dev 3.744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

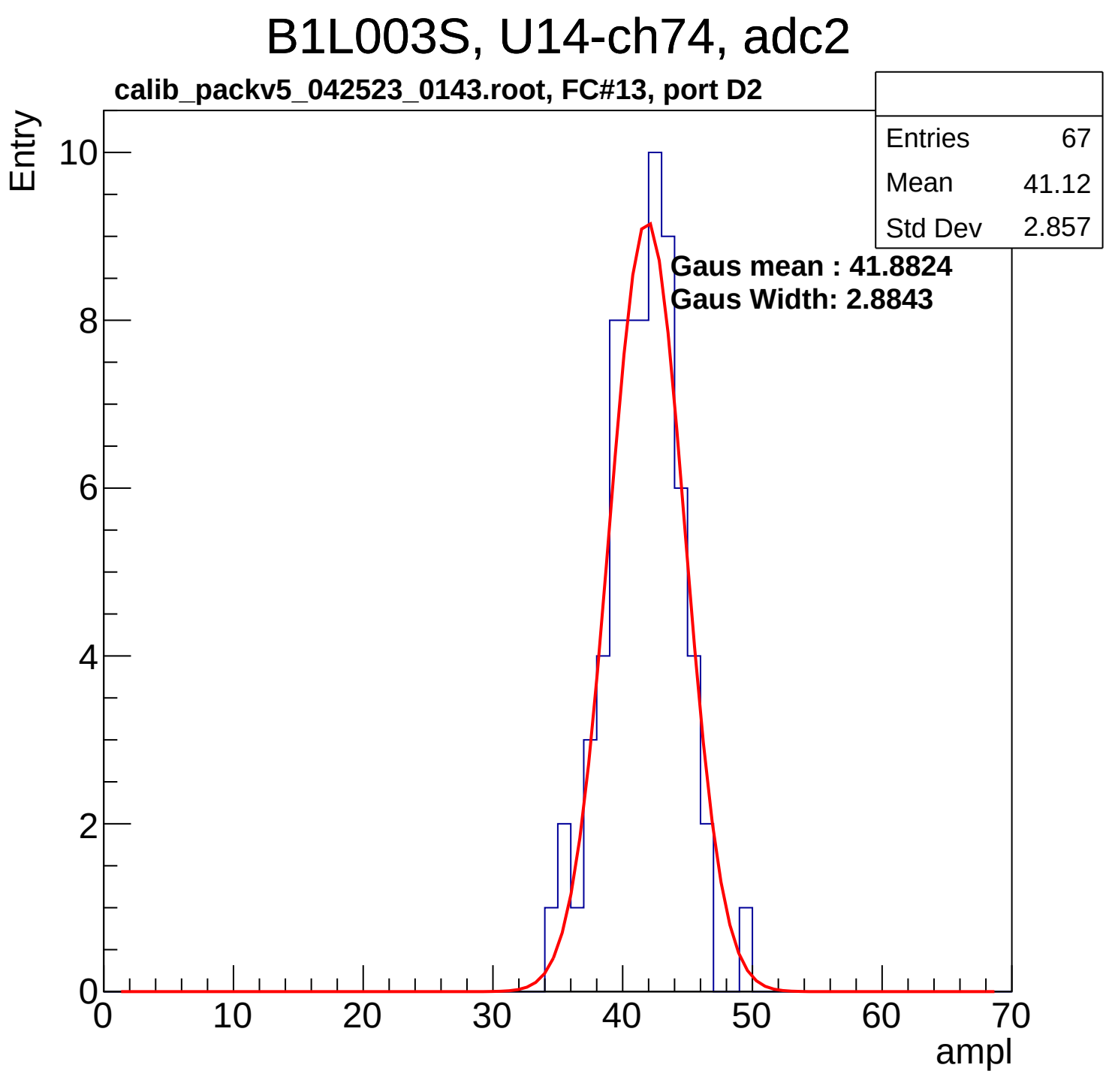

B1L003S, U14-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.13
Std Dev 9.001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

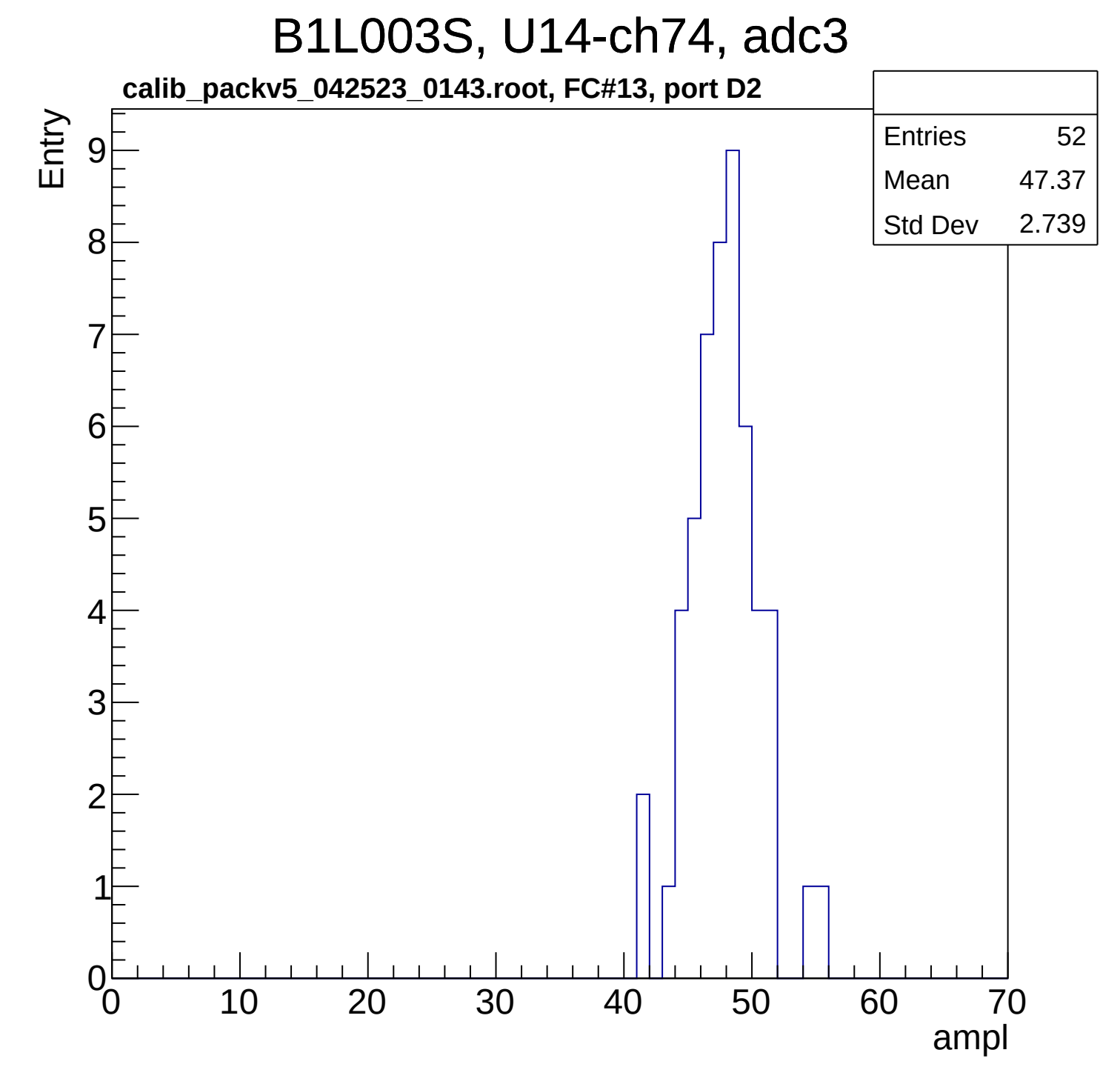

B1L003S, U14-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

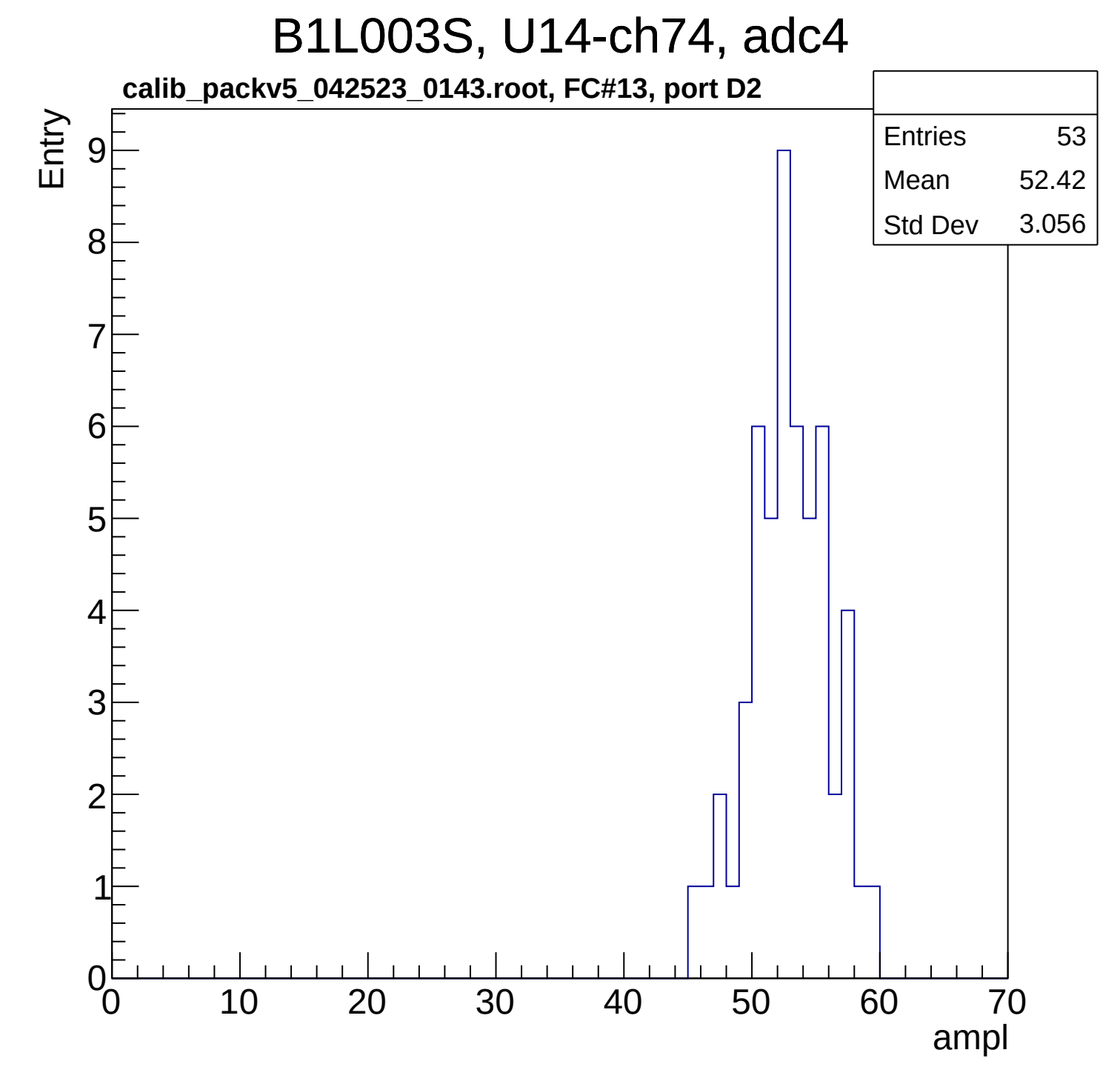

B1L003S, U14-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

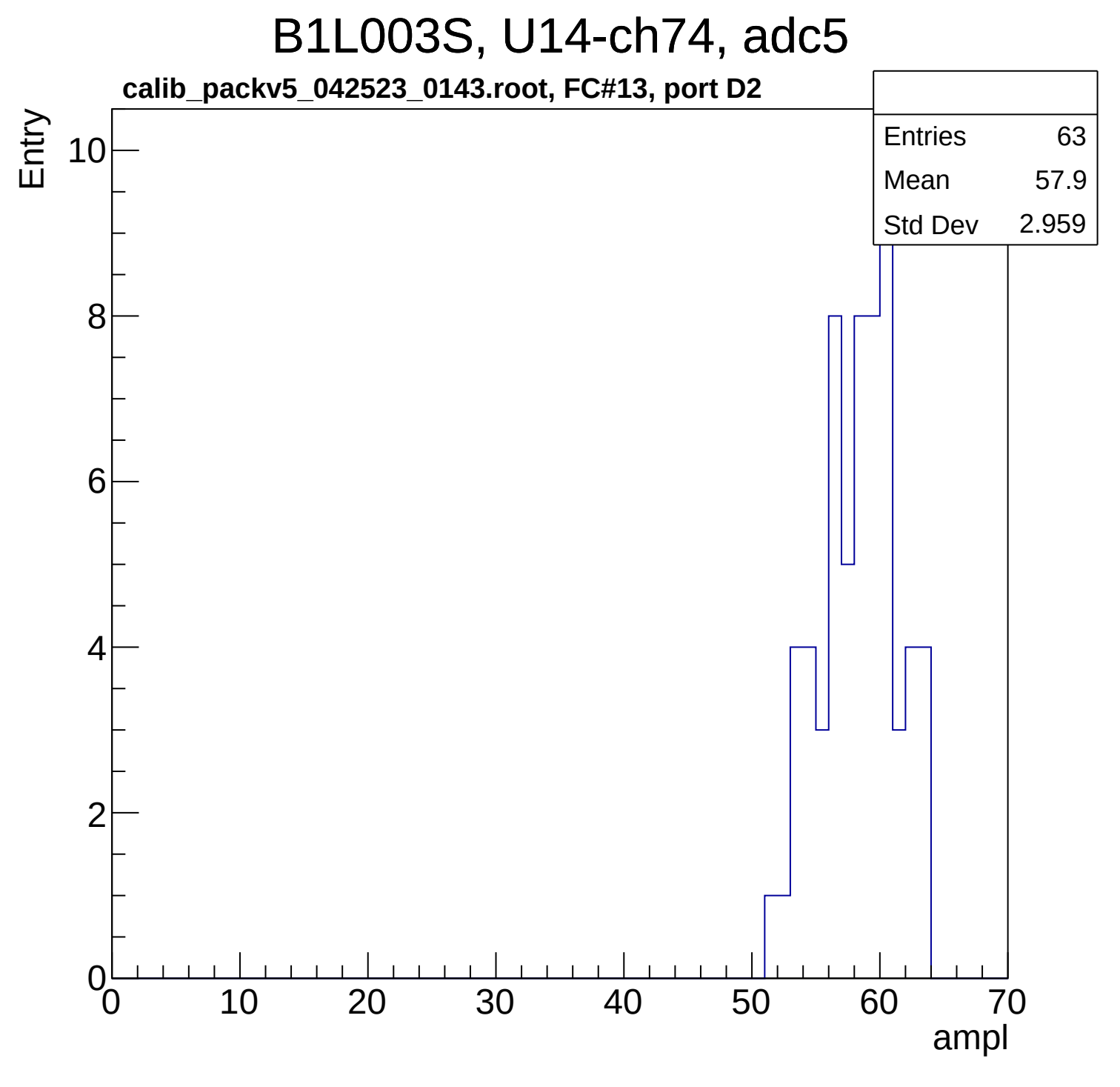

B1L003S, U14-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 27.17
Std Dev 5.653
Gaus mean : 28.2366
Gaus Width: 3.3740
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

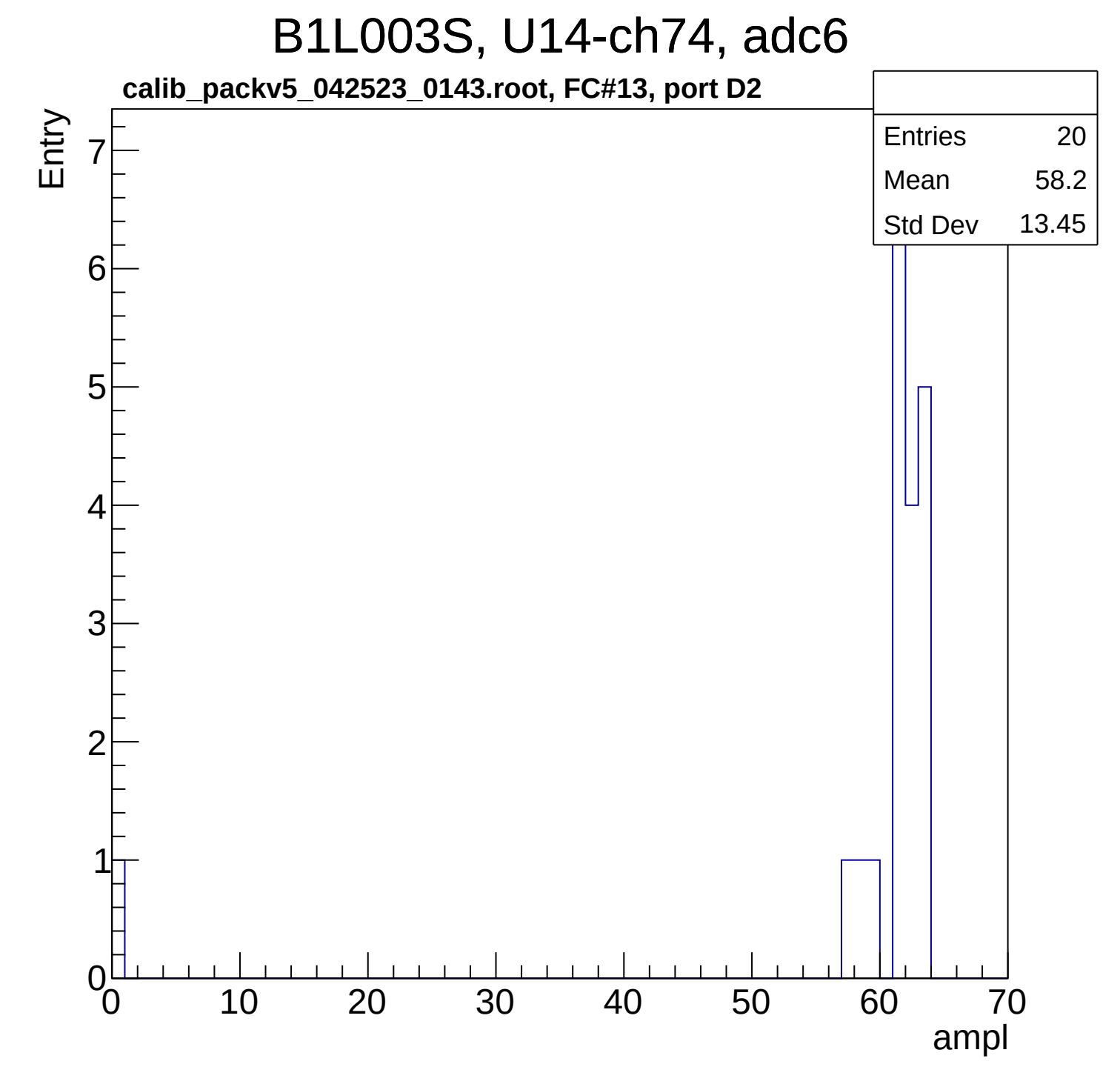

B1L003S, U14-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 34.19
Std Dev 3.013
Gaus mean : 34.8925
Gaus Width: 3.3700
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

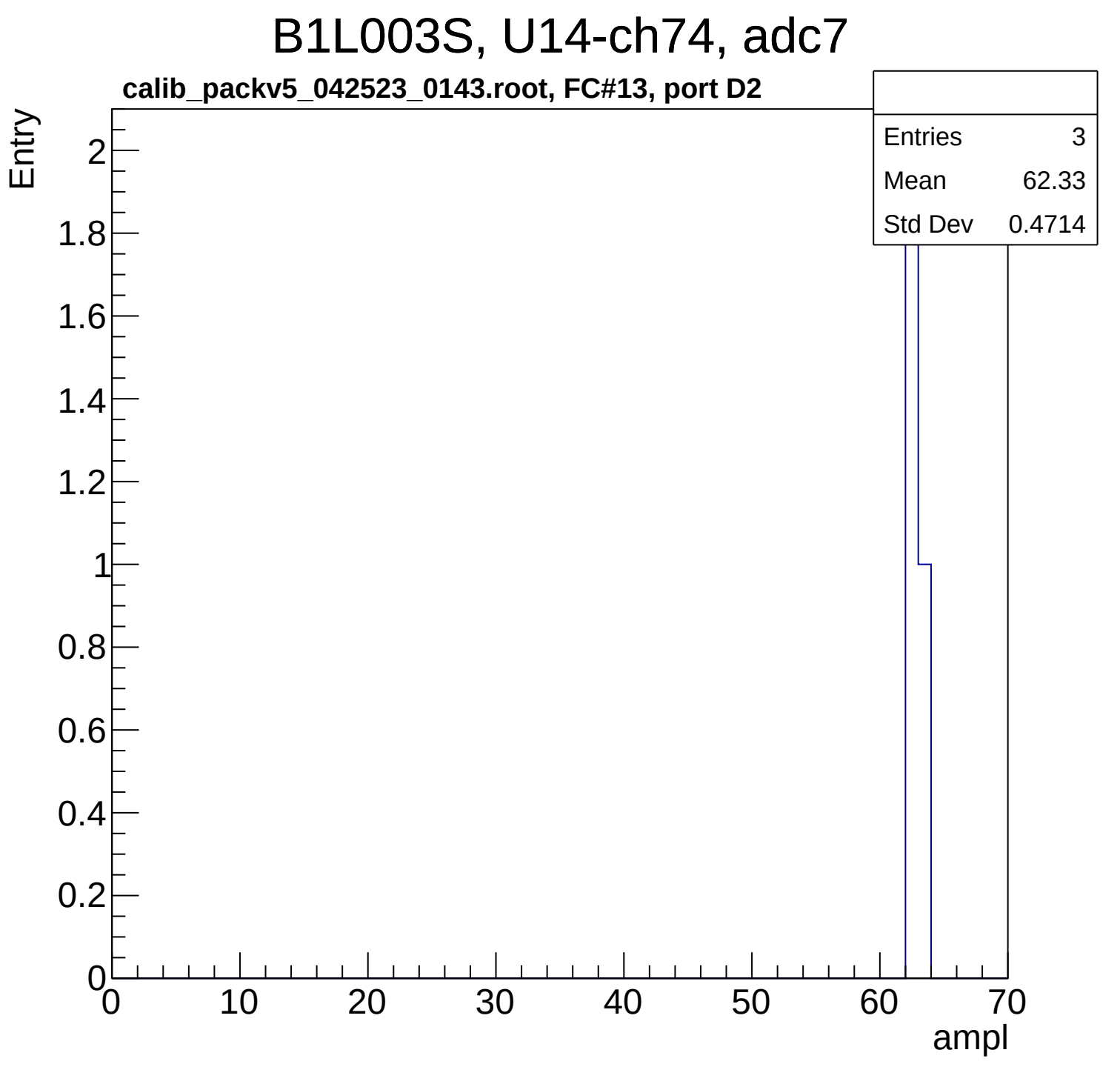

B1L003S, U14-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.12
Std Dev 2.857
Gaus mean : 41.8824
Gaus Width: 2.8843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

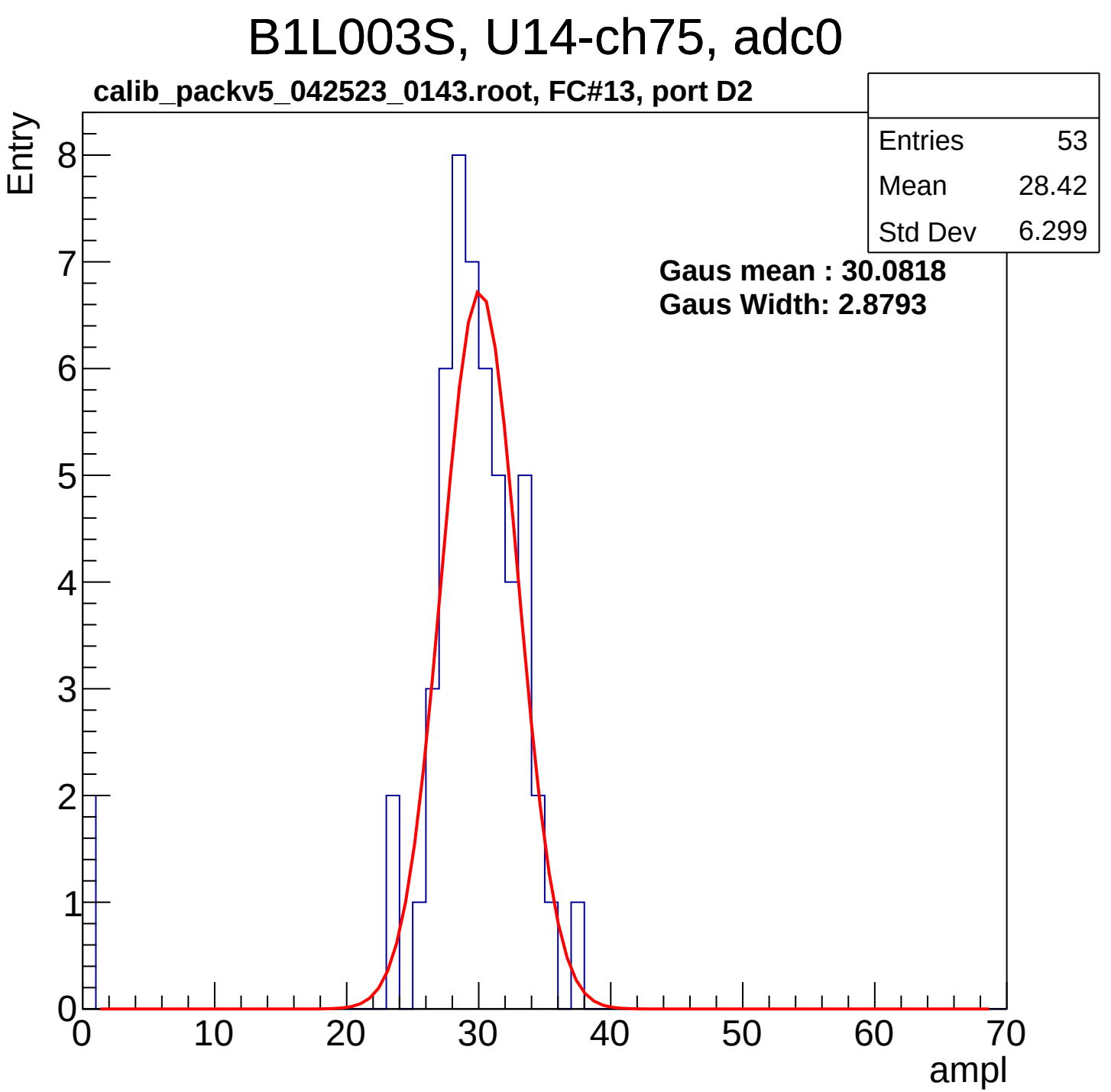

B1L003S, U14-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 47.37
Std Dev 2.739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

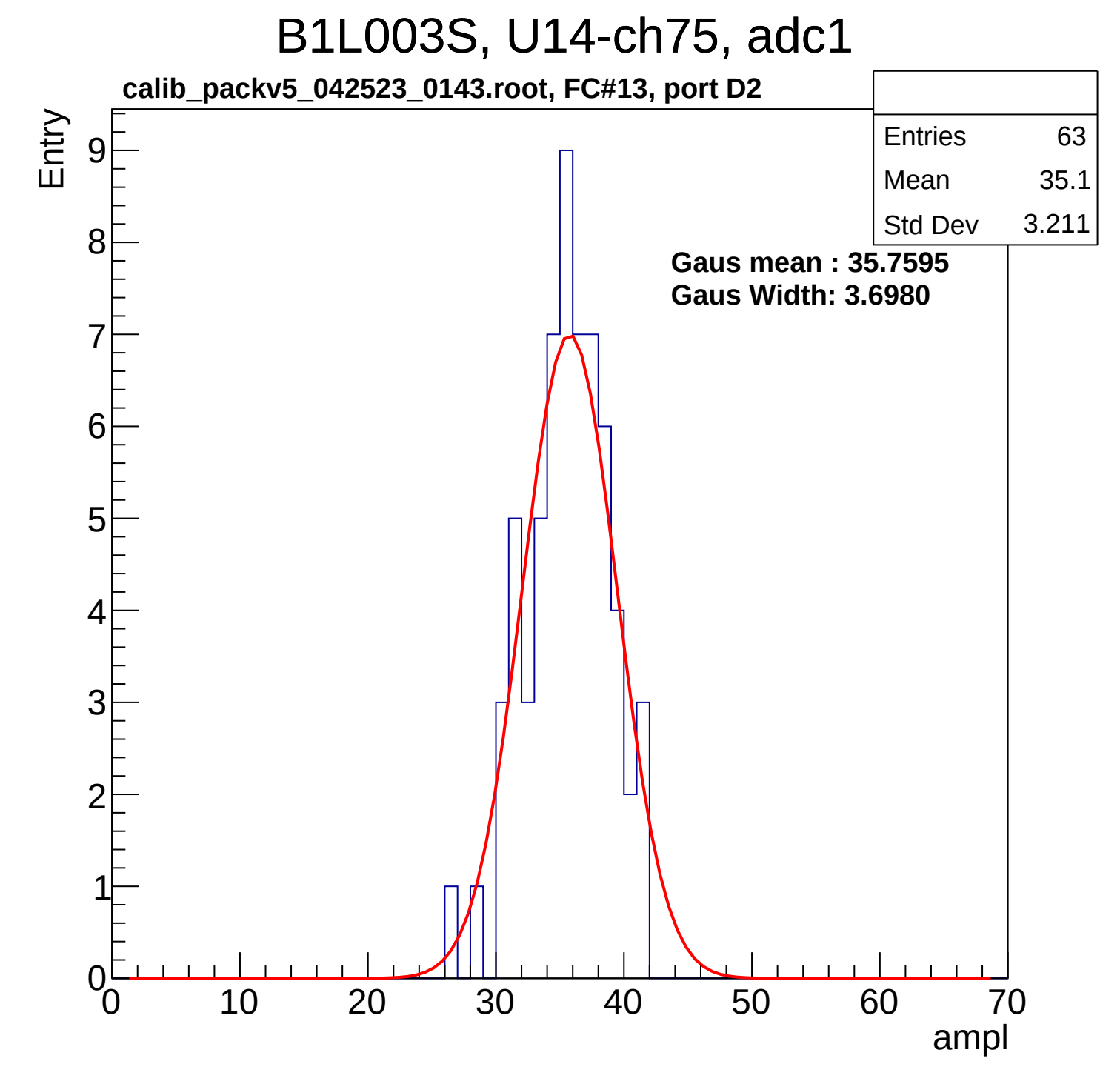

B1L003S, U14-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 52.42
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

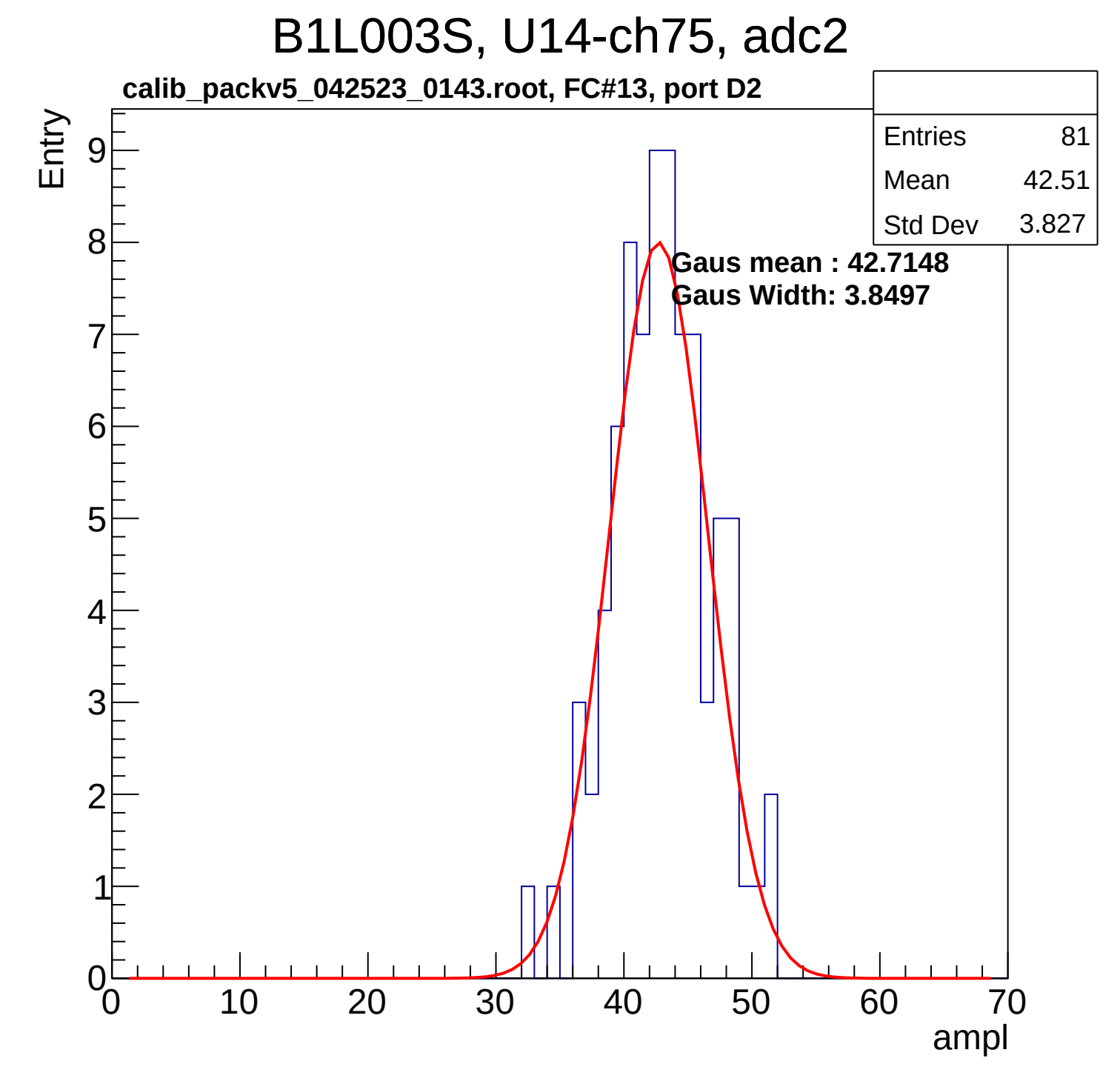

B1L003S, U14-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 57.9
Std Dev 2.959
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

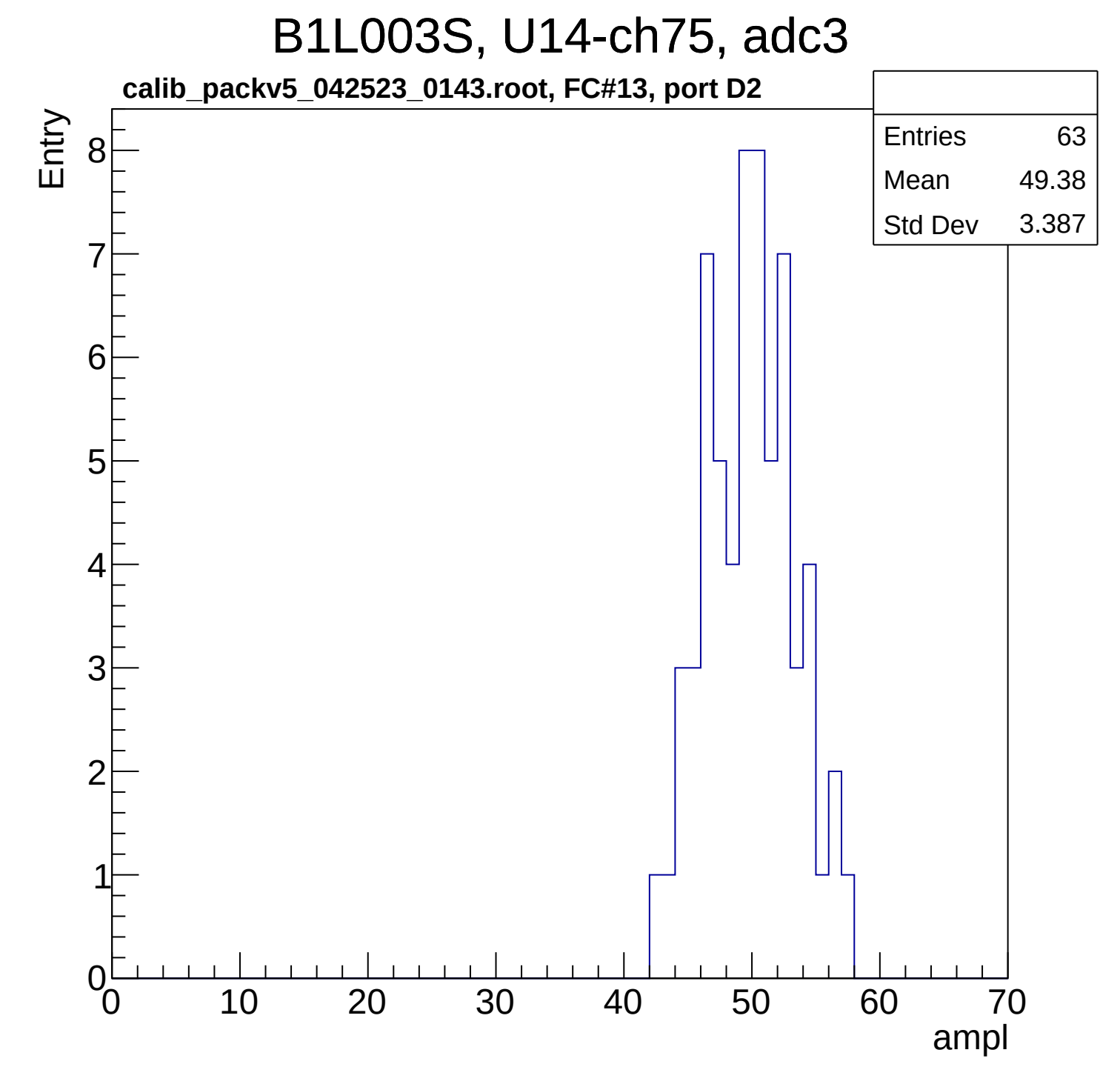

B1L003S, U14-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.2
Std Dev 13.45
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

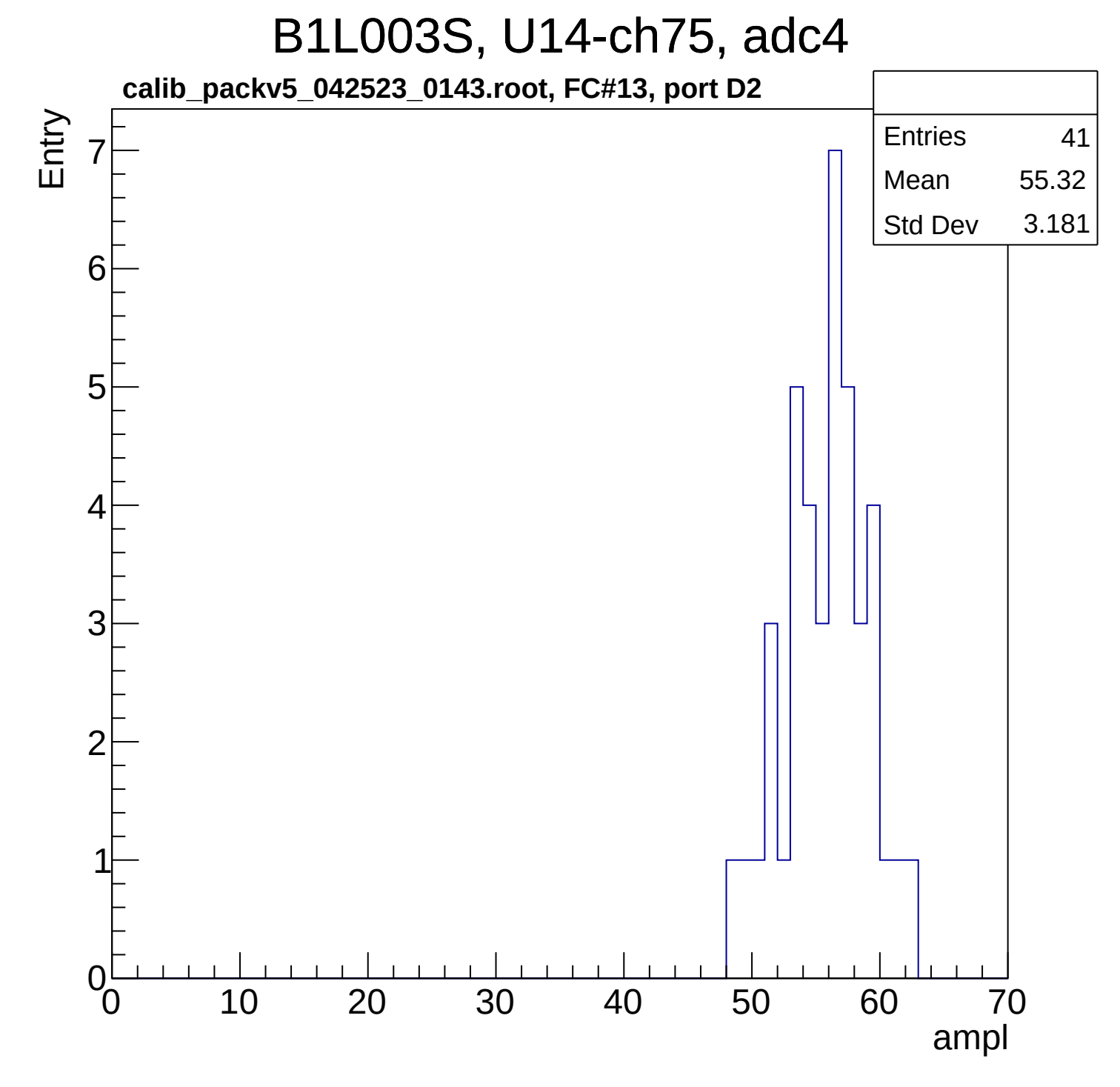

B1L003S, U14-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

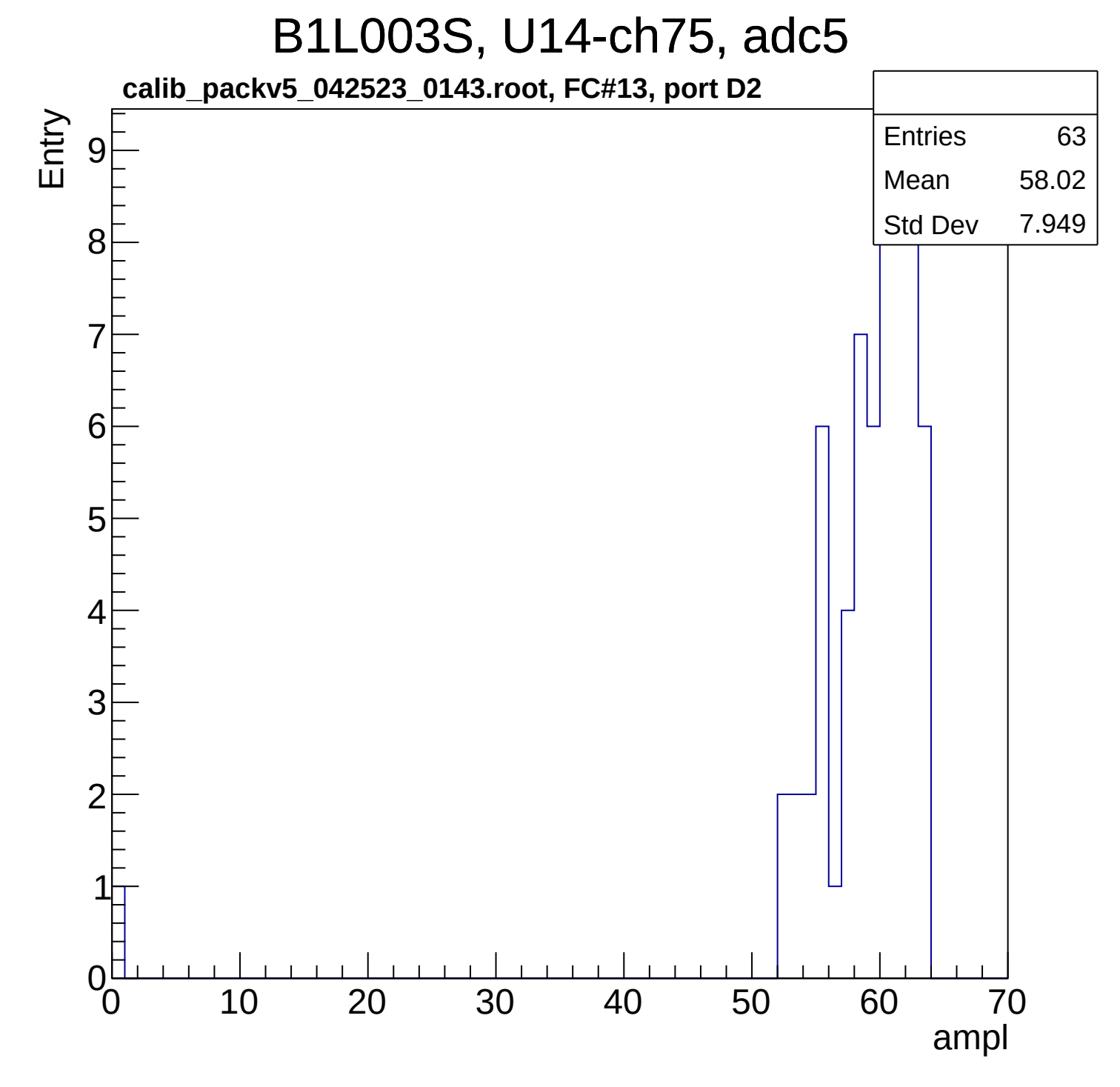

B1L003S, U14-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 28.42
Std Dev 6.299
Gaus mean : 30.0818
Gaus Width: 2.8793
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

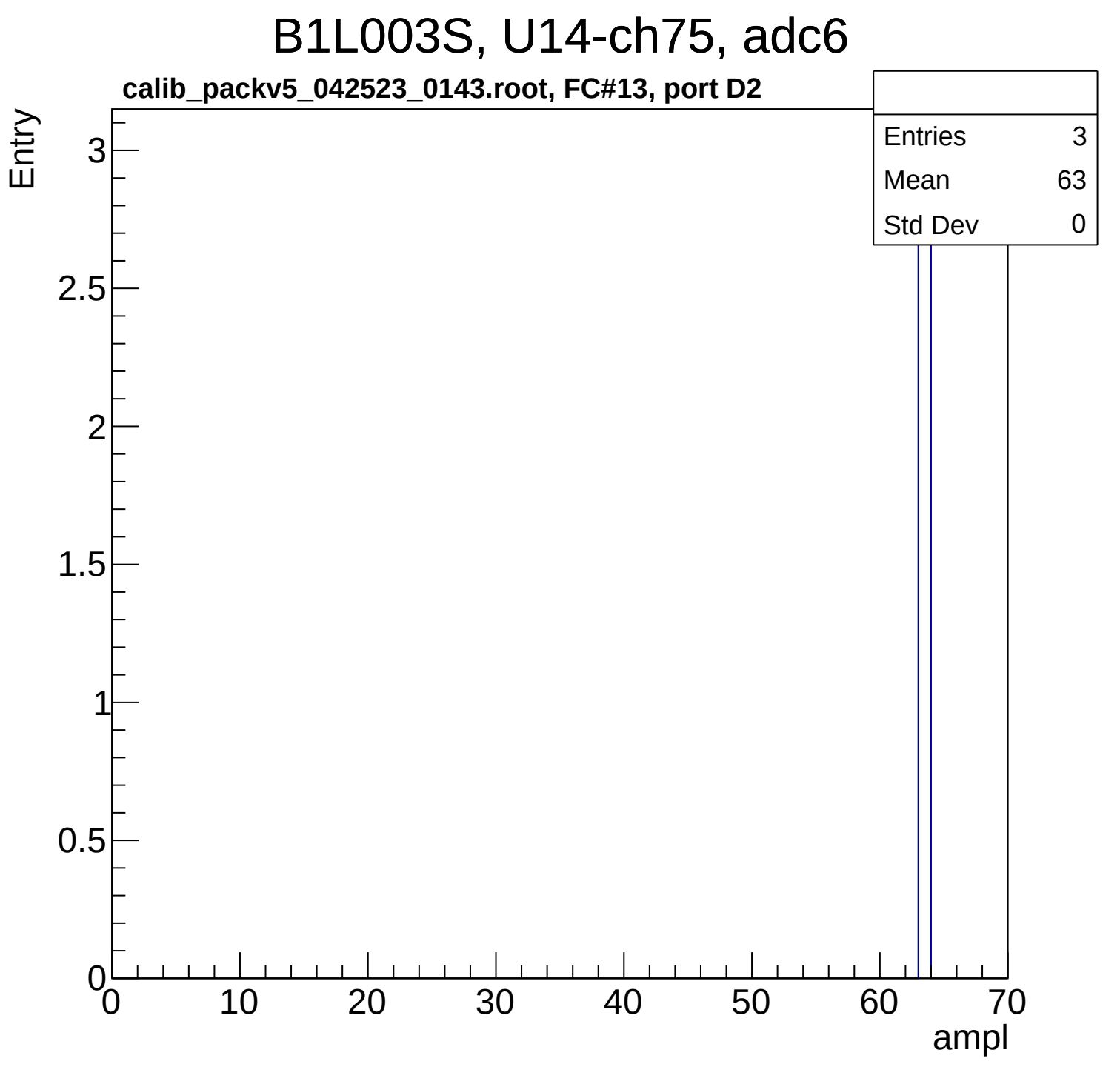

B1L003S, U14-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.1
Std Dev 3.211
Gaus mean : 35.7595
Gaus Width: 3.6980
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

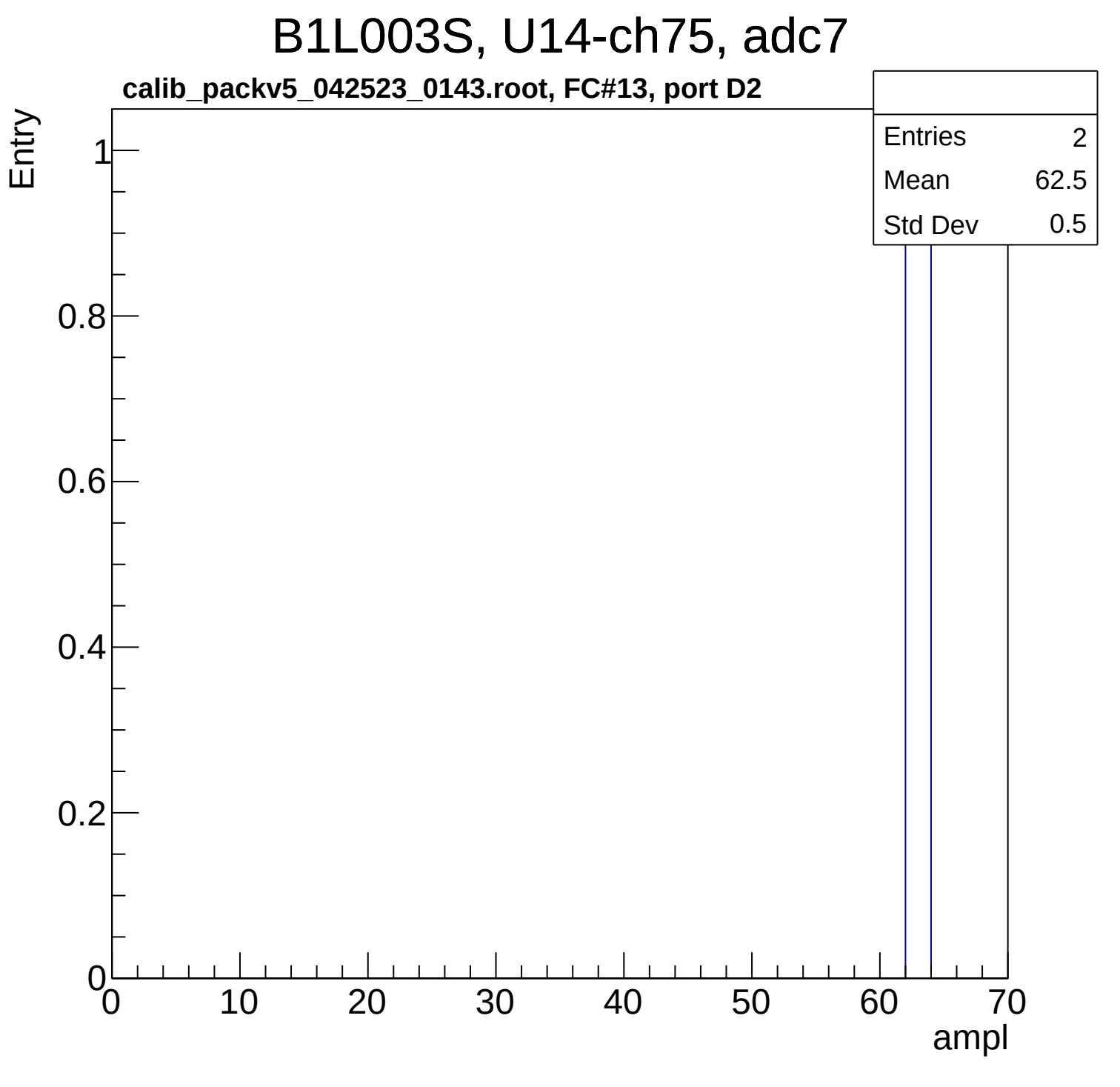

B1L003S, U14-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 42.51
Std Dev 3.827
Gaus mean : 42.7148
Gaus Width: 3.8497
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

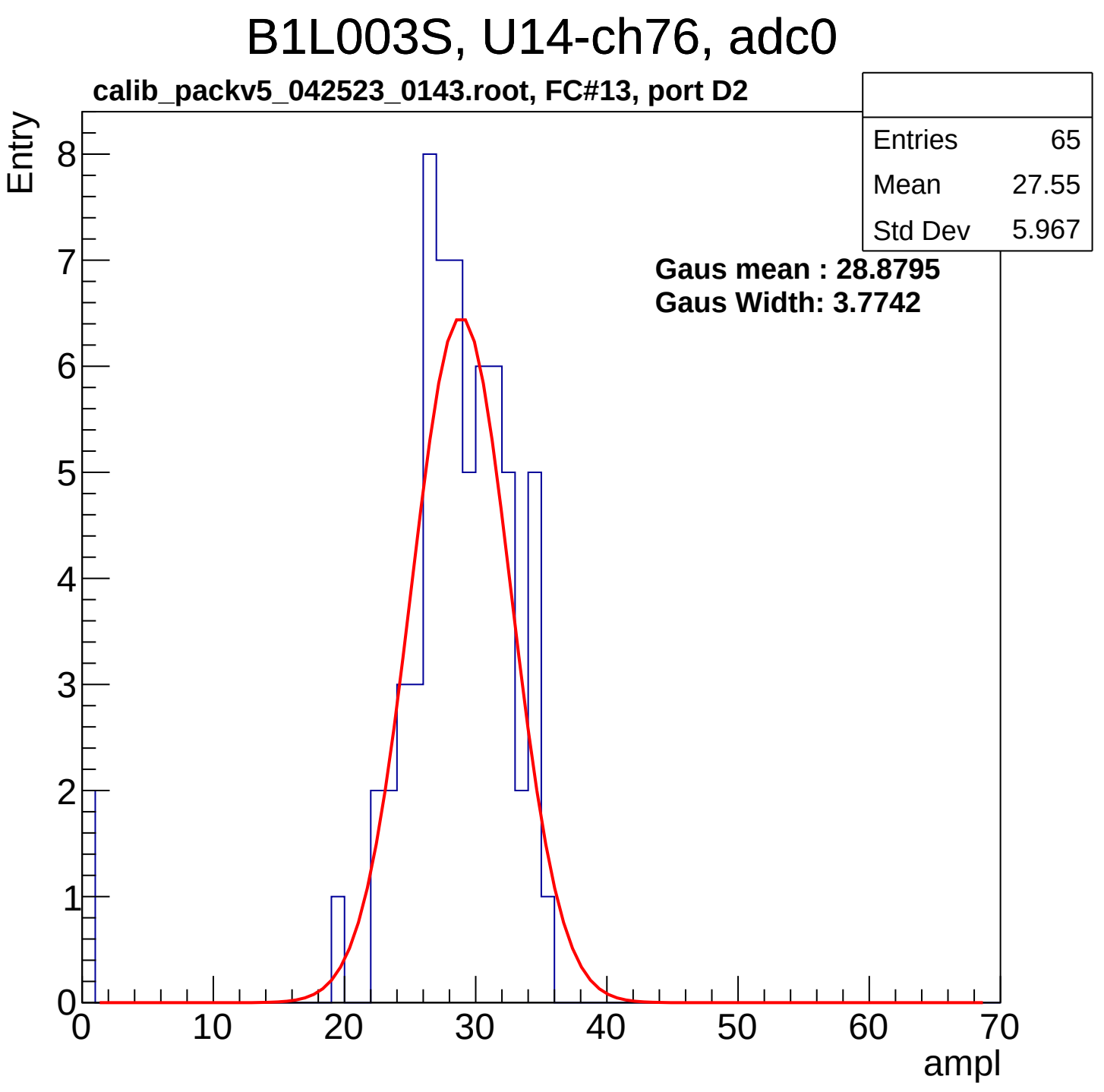

B1L003S, U14-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.38
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

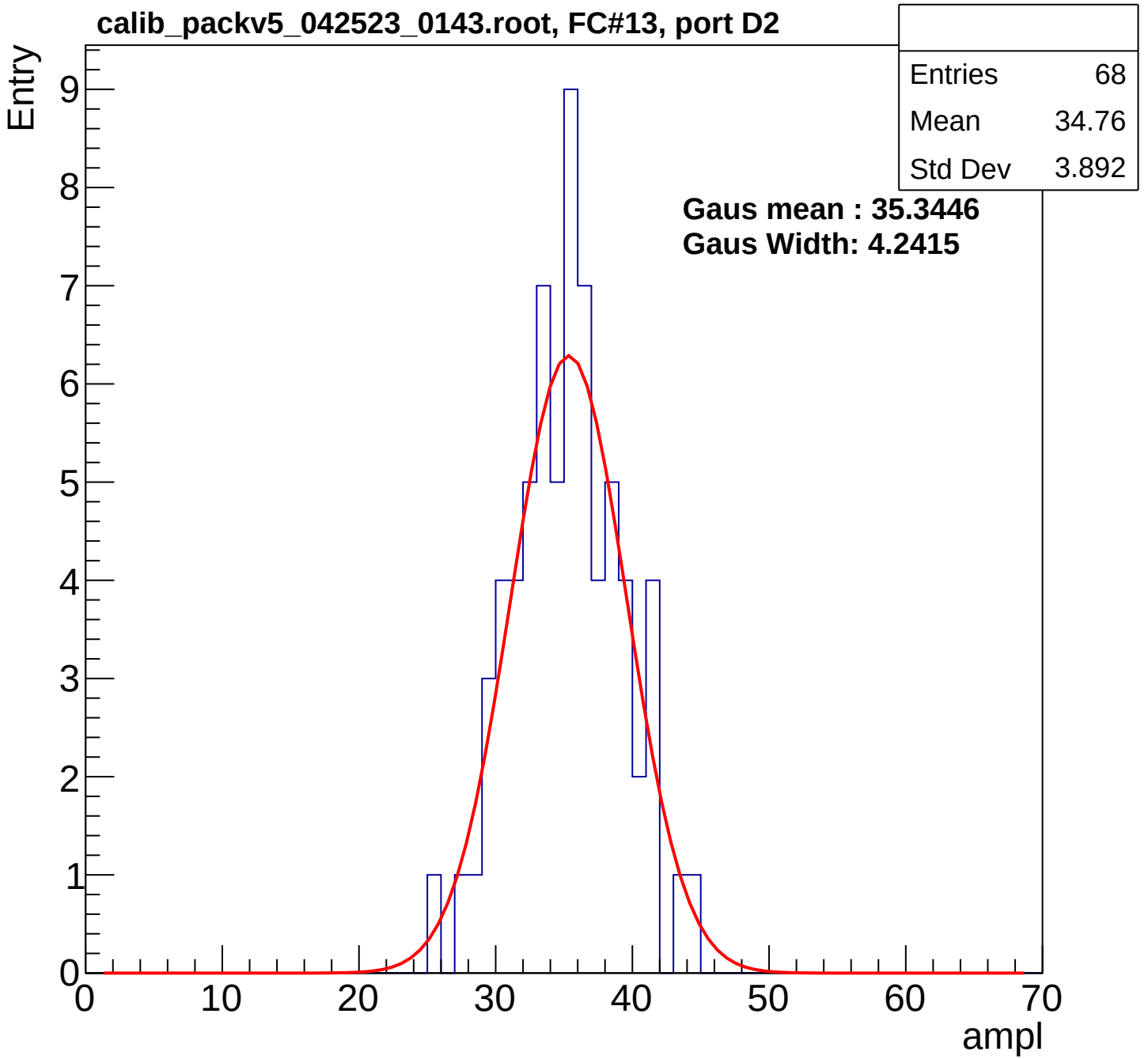

B1L003S, U14-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 55.32
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

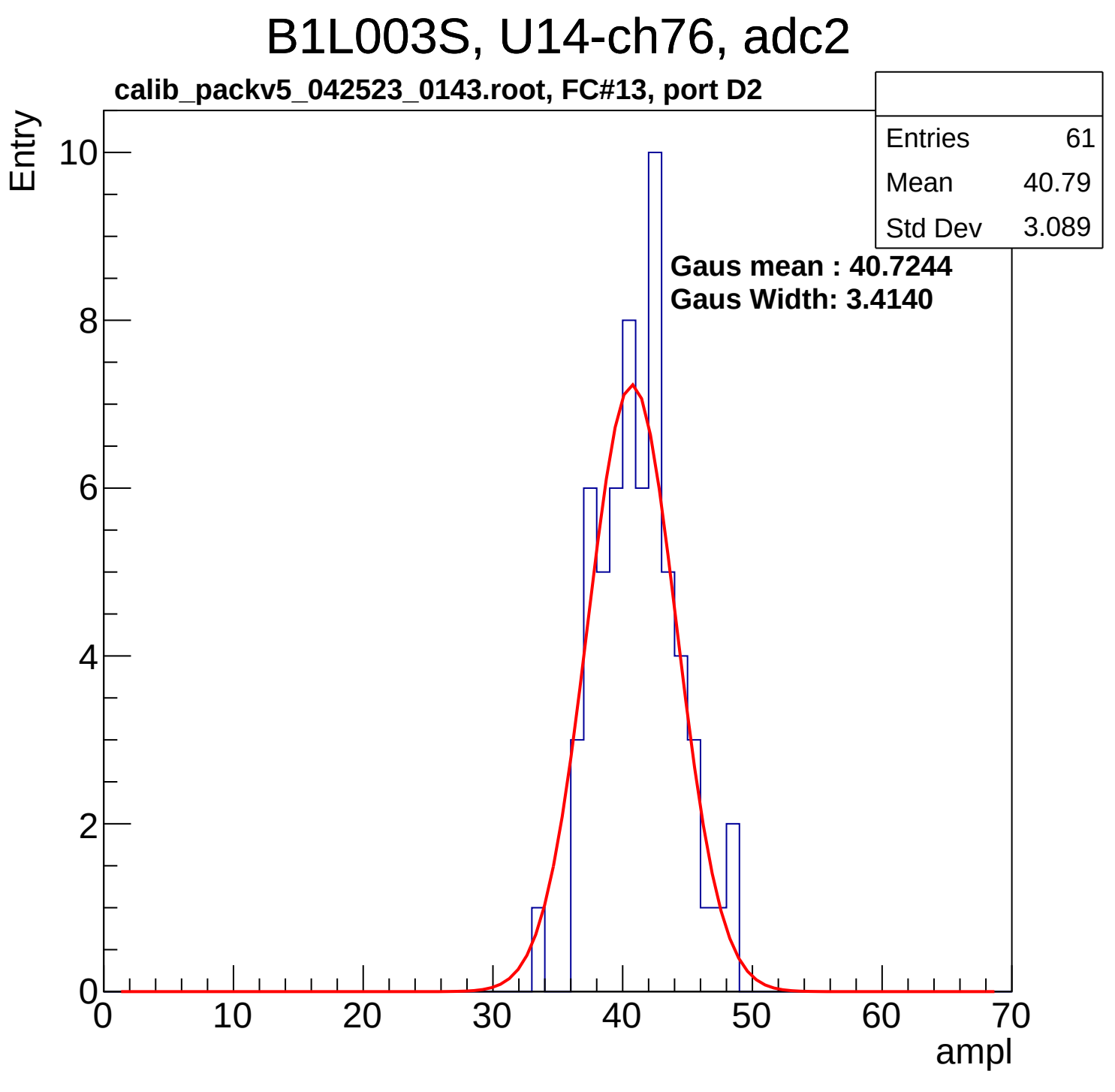

B1L003S, U14-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.02
Std Dev 7.949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

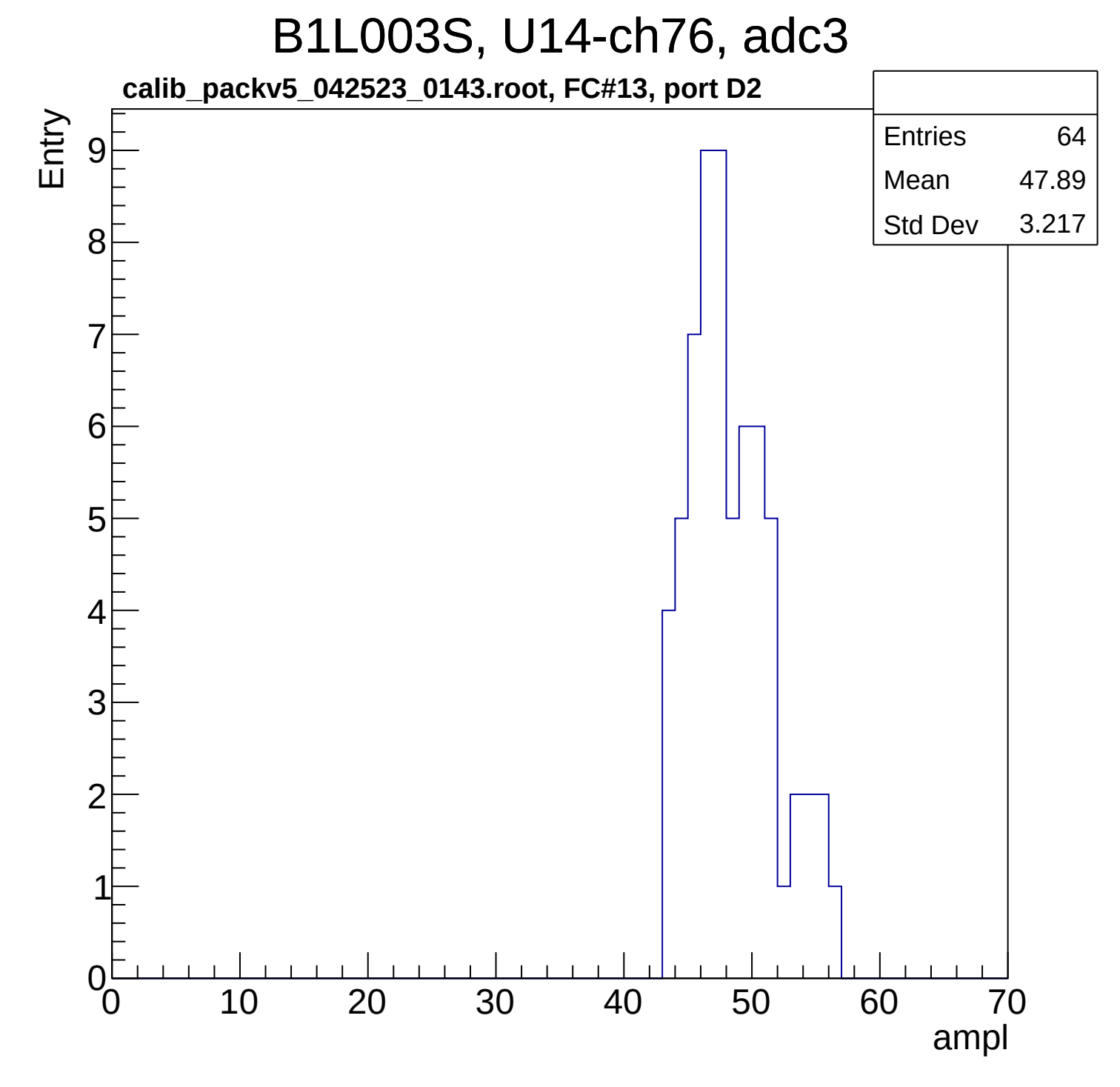

B1L003S, U14-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

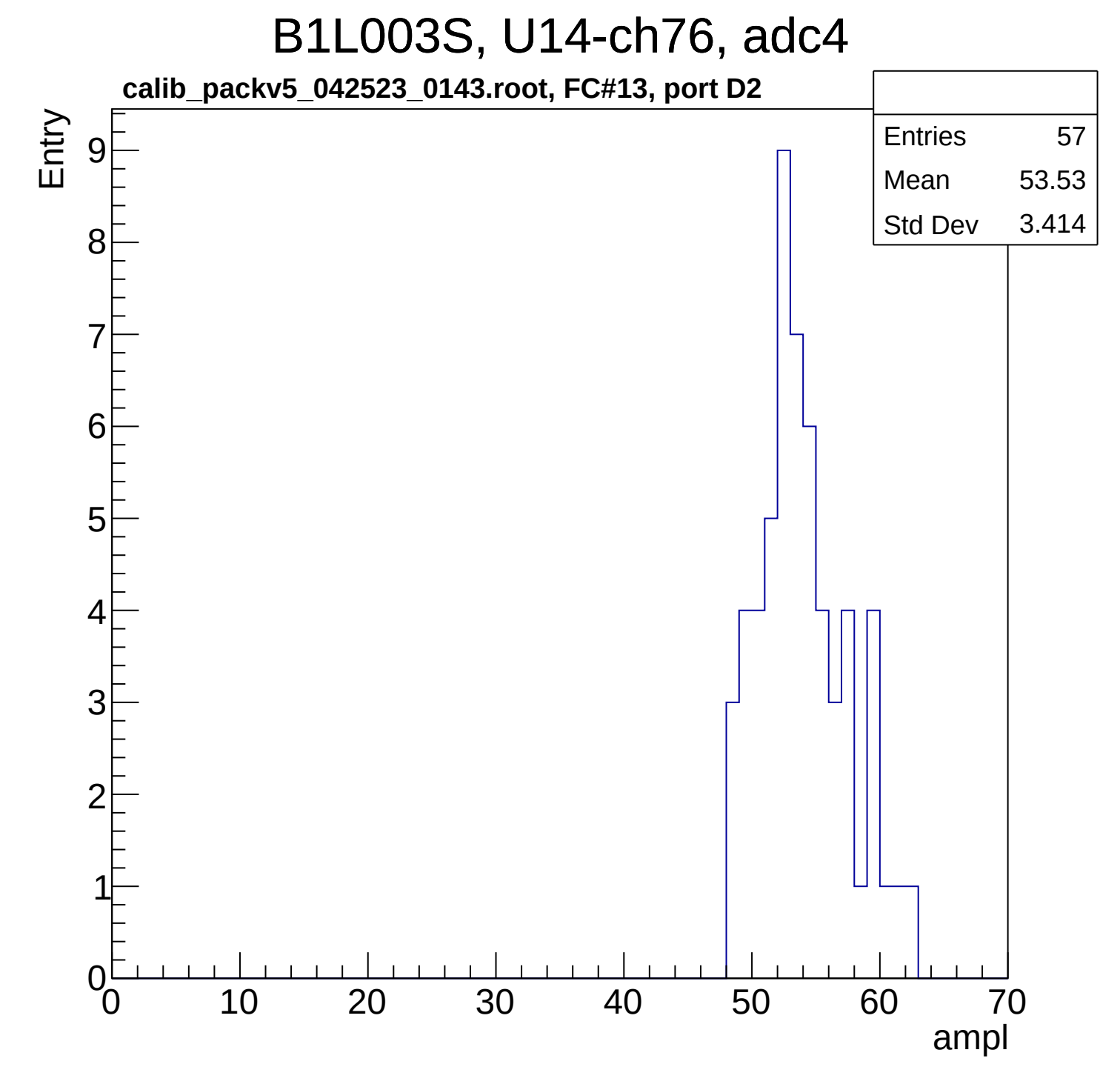

B1L003S, U14-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

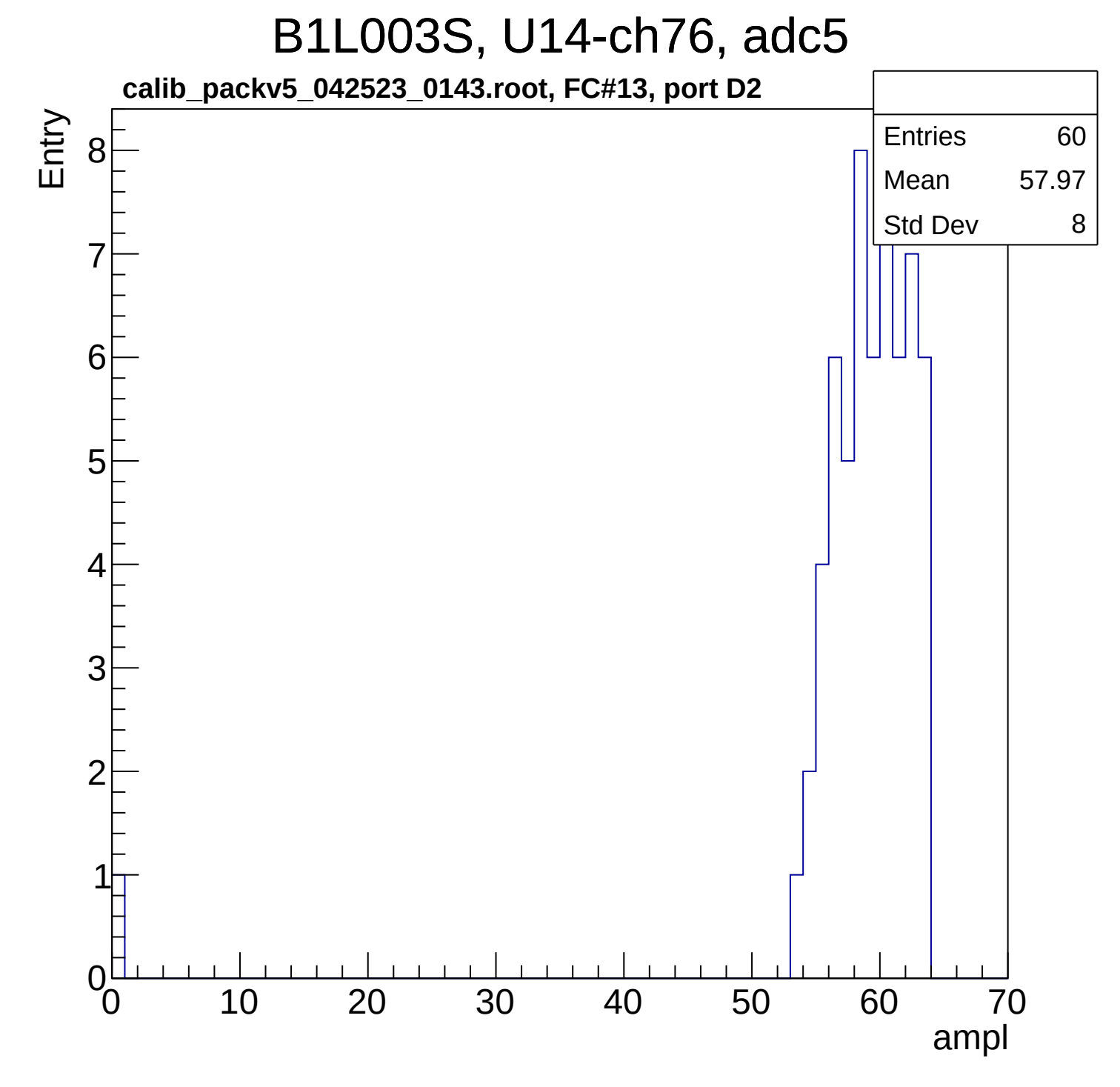

B1L003S, U14-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 27.55
Std Dev 5.967
Gaus mean : 28.8795
Gaus Width: 3.7742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

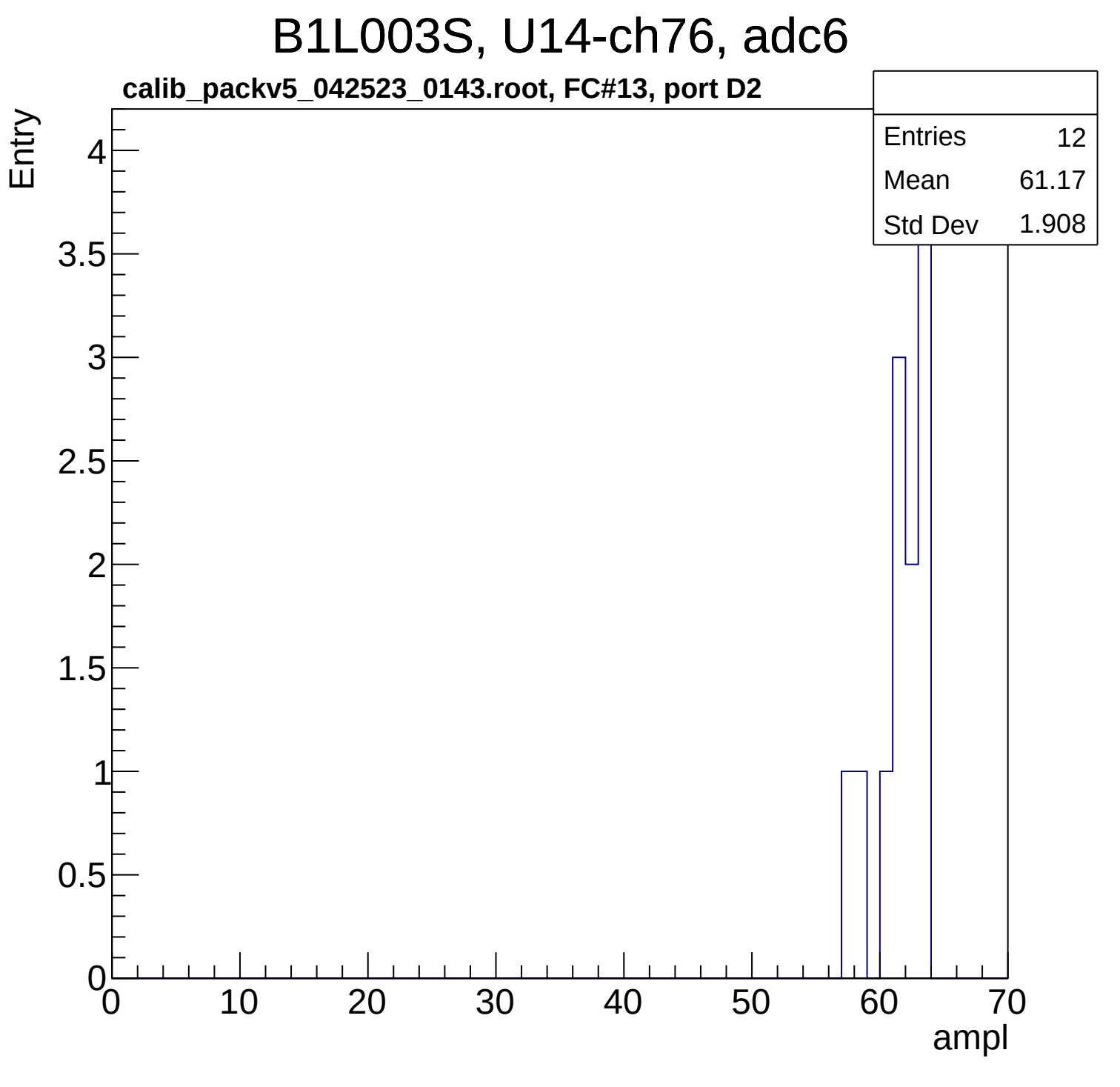

B1L003S, U14-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 34.76
Std Dev 3.892
Gaus mean : 35.3446
Gaus Width: 4.2415
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

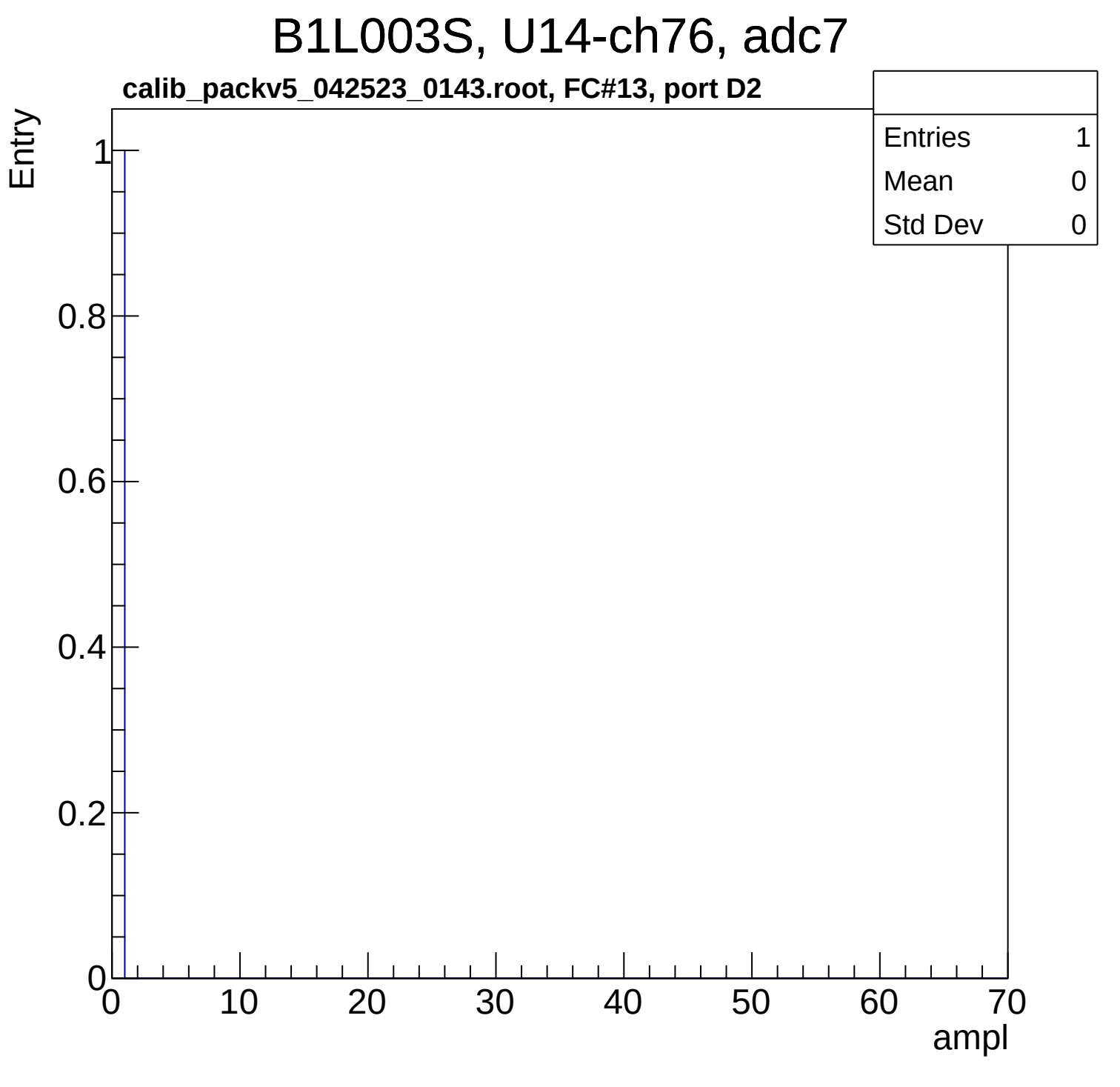

B1L003S, U14-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 40.79
Std Dev 3.089
Gaus mean : 40.7244
Gaus Width: 3.4140
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

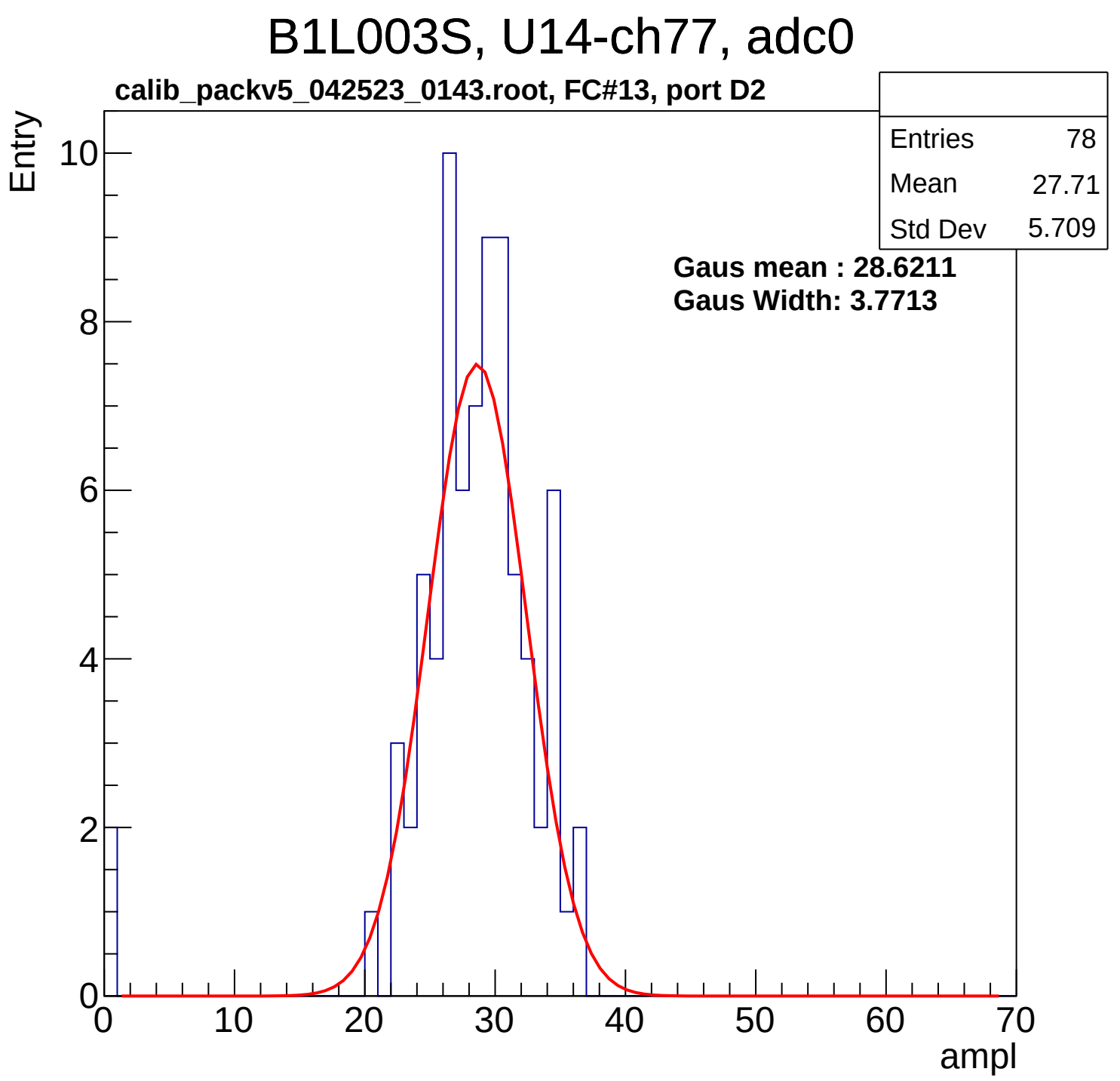

B1L003S, U14-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.89
Std Dev 3.217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

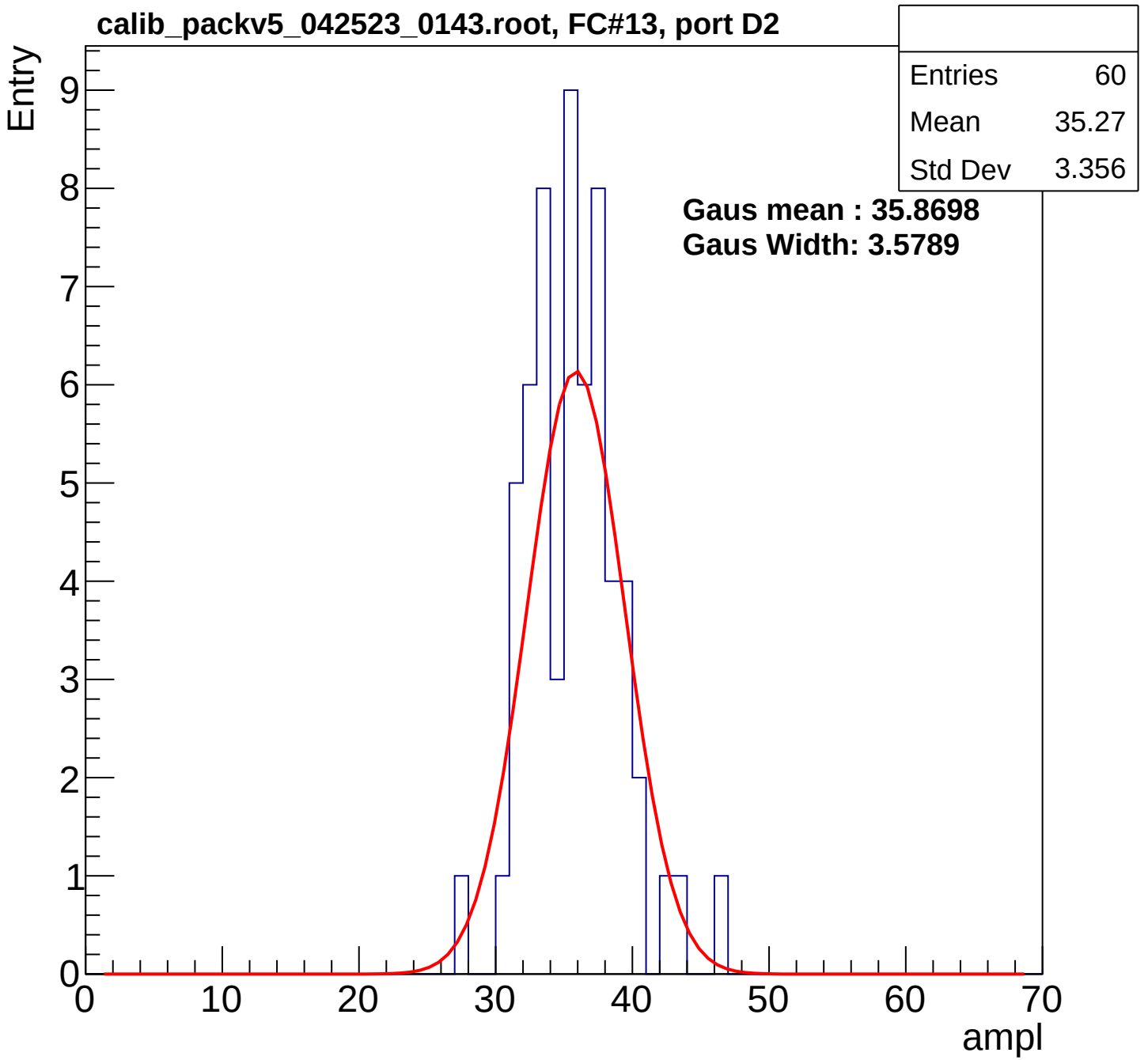

B1L003S, U14-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.53
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

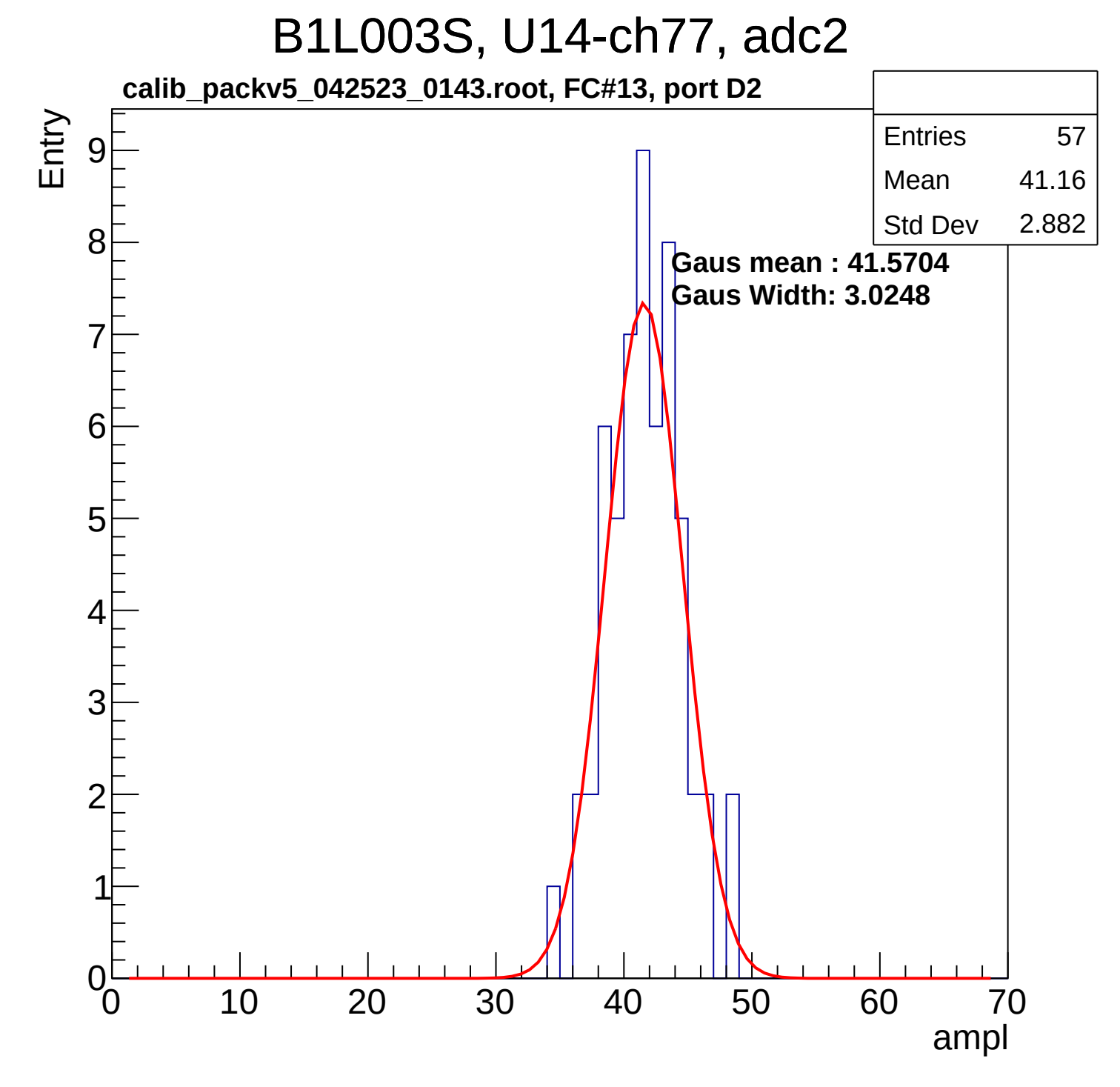

B1L003S, U14-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.97
Std Dev 8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

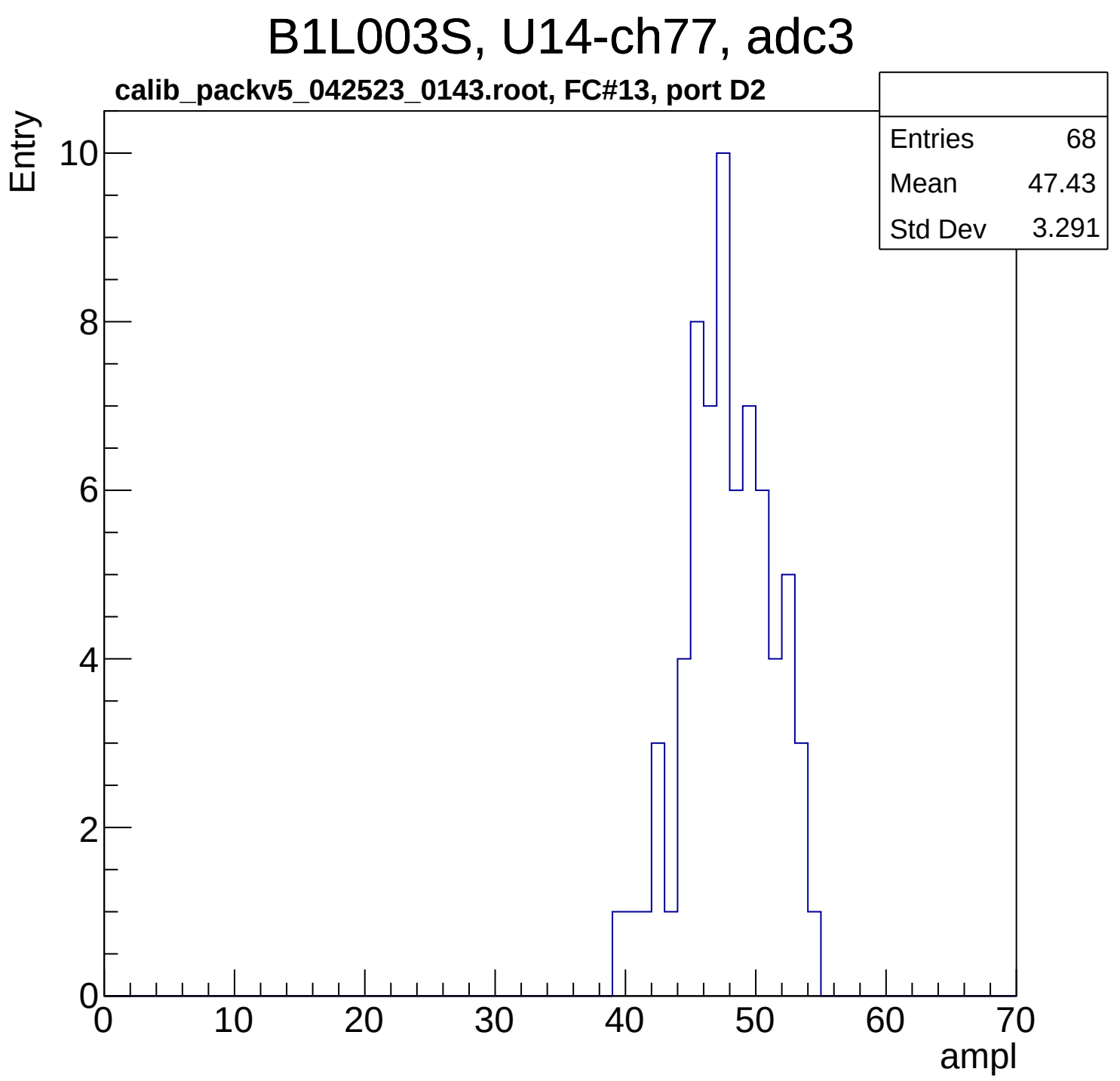

B1L003S, U14-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.17
Std Dev 1.908
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

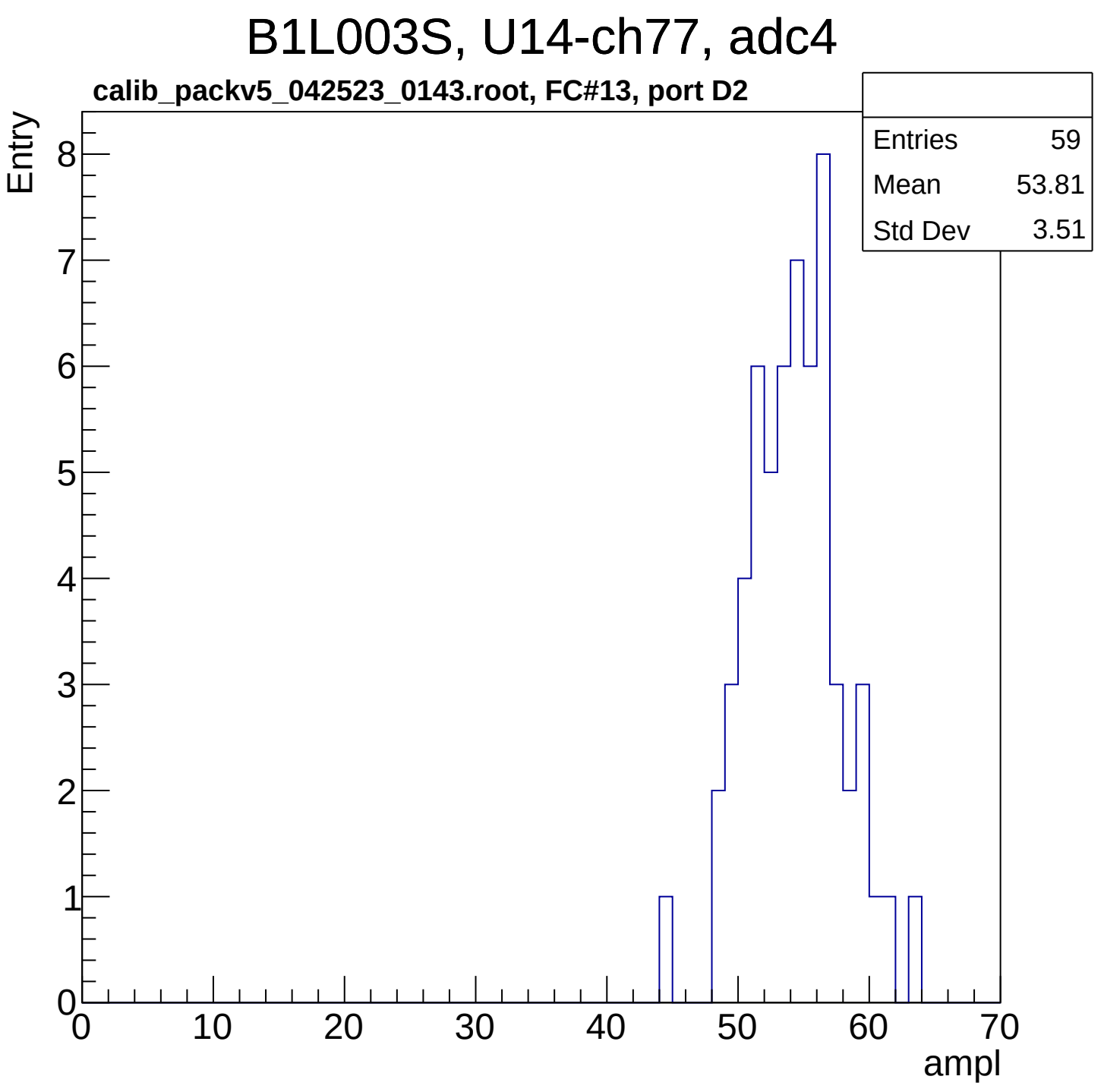

B1L003S, U14-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

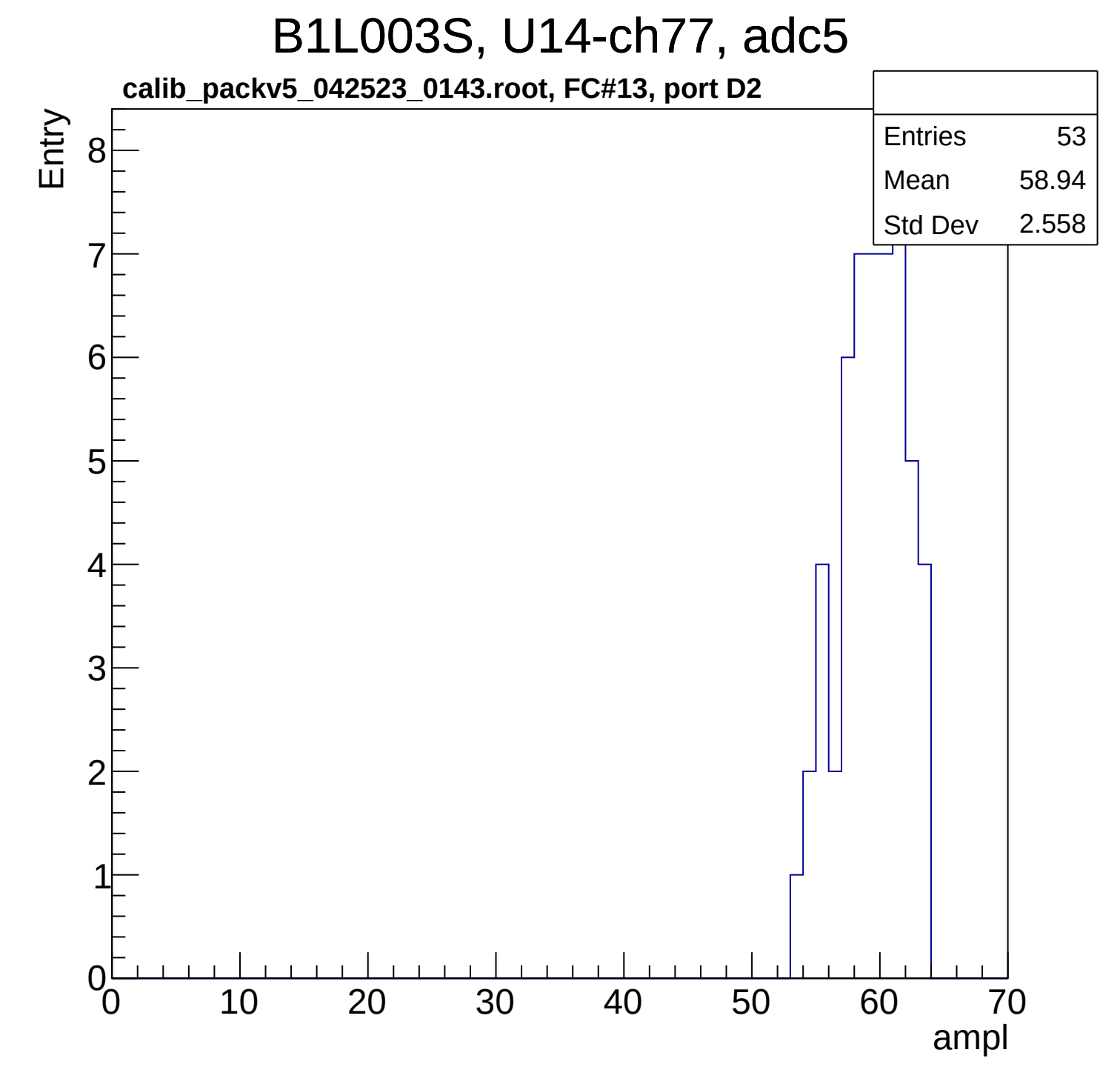

B1L003S, U14-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 27.71
Std Dev 5.709
Gaus mean : 28.6211
Gaus Width: 3.7713
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

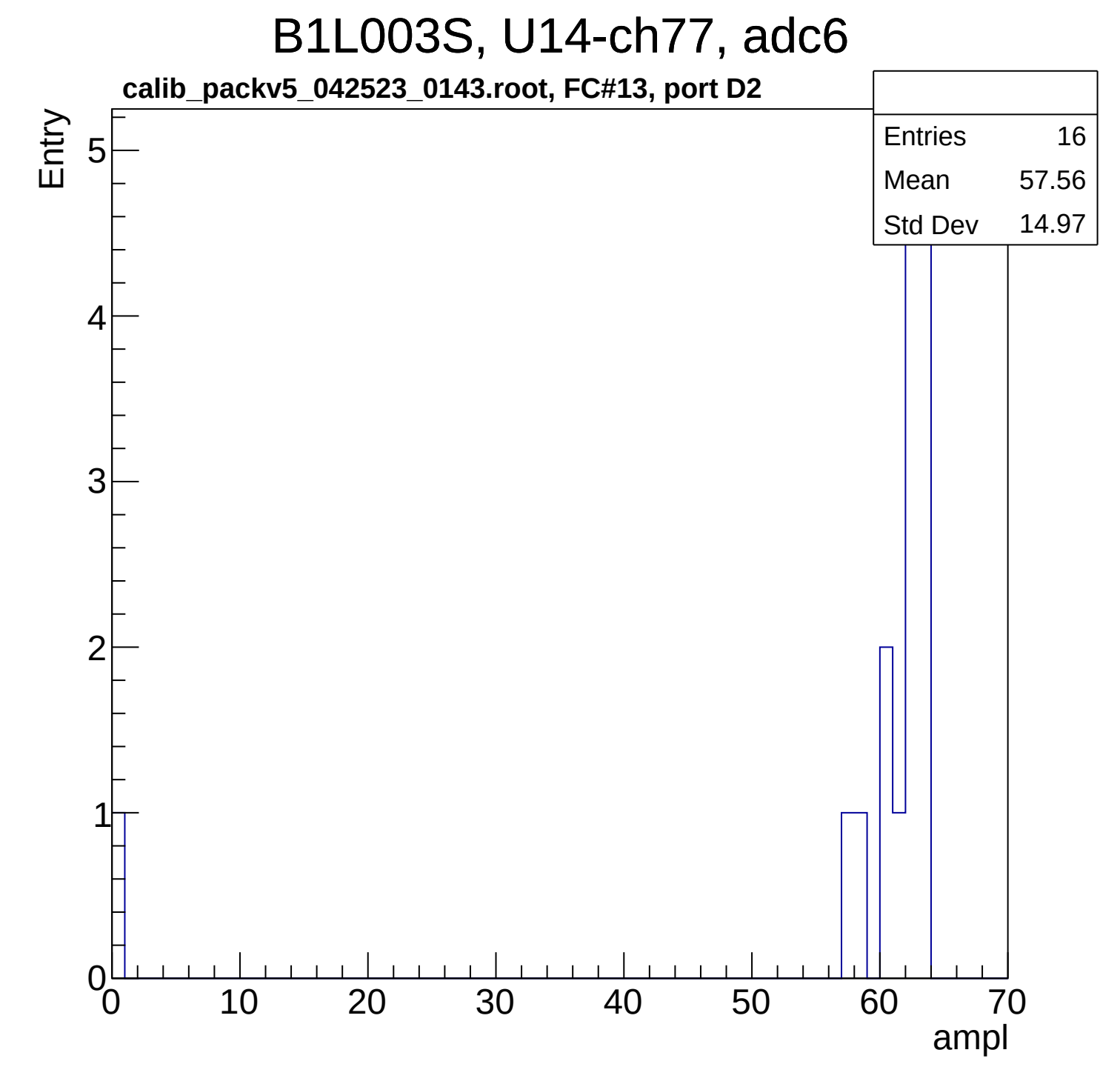

B1L003S, U14-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.27
Std Dev 3.356
Gaus mean : 35.8698
Gaus Width: 3.5789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

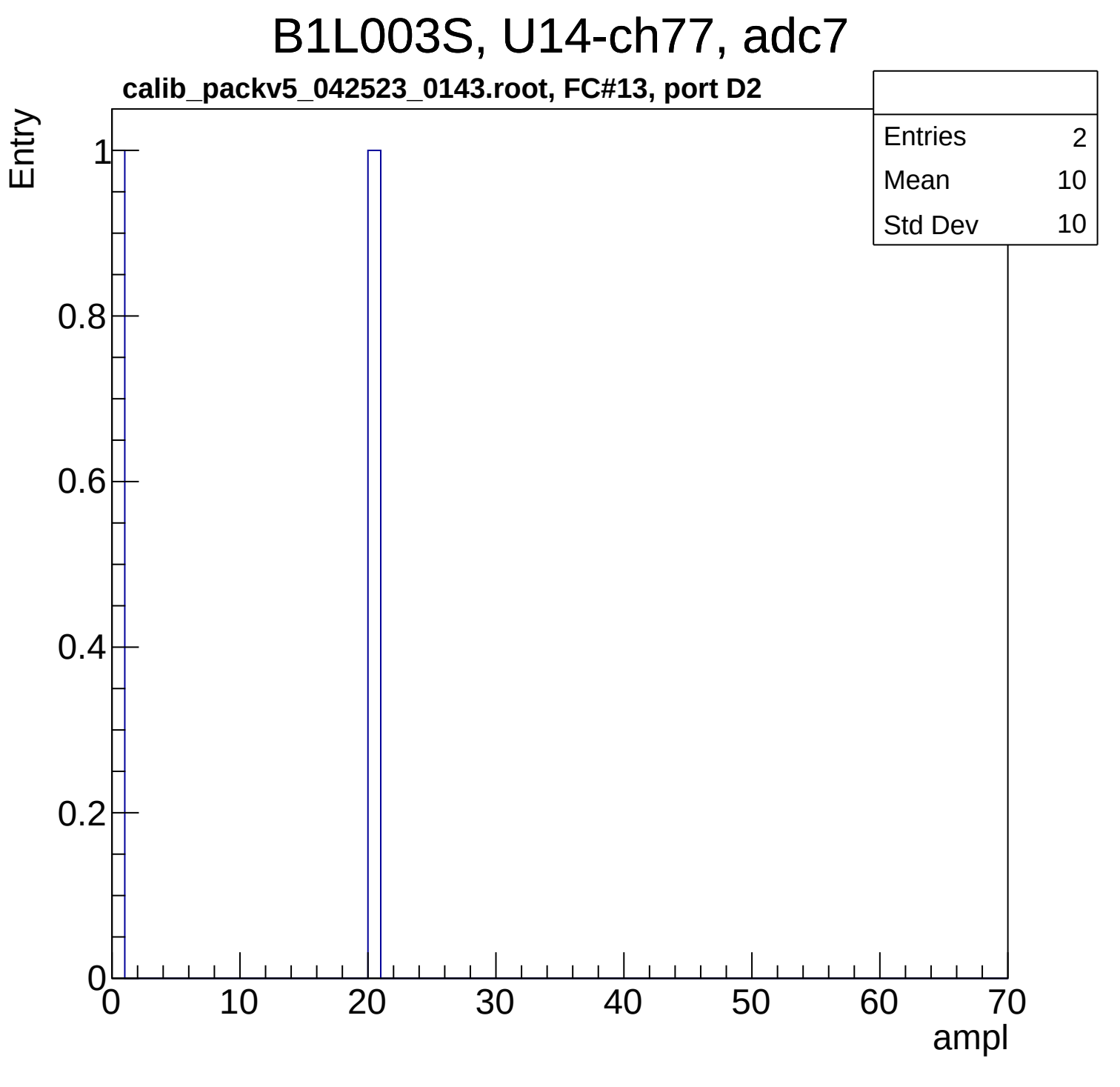

B1L003S, U14-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.16
Std Dev 2.882
Gaus mean : 41.5704
Gaus Width: 3.0248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

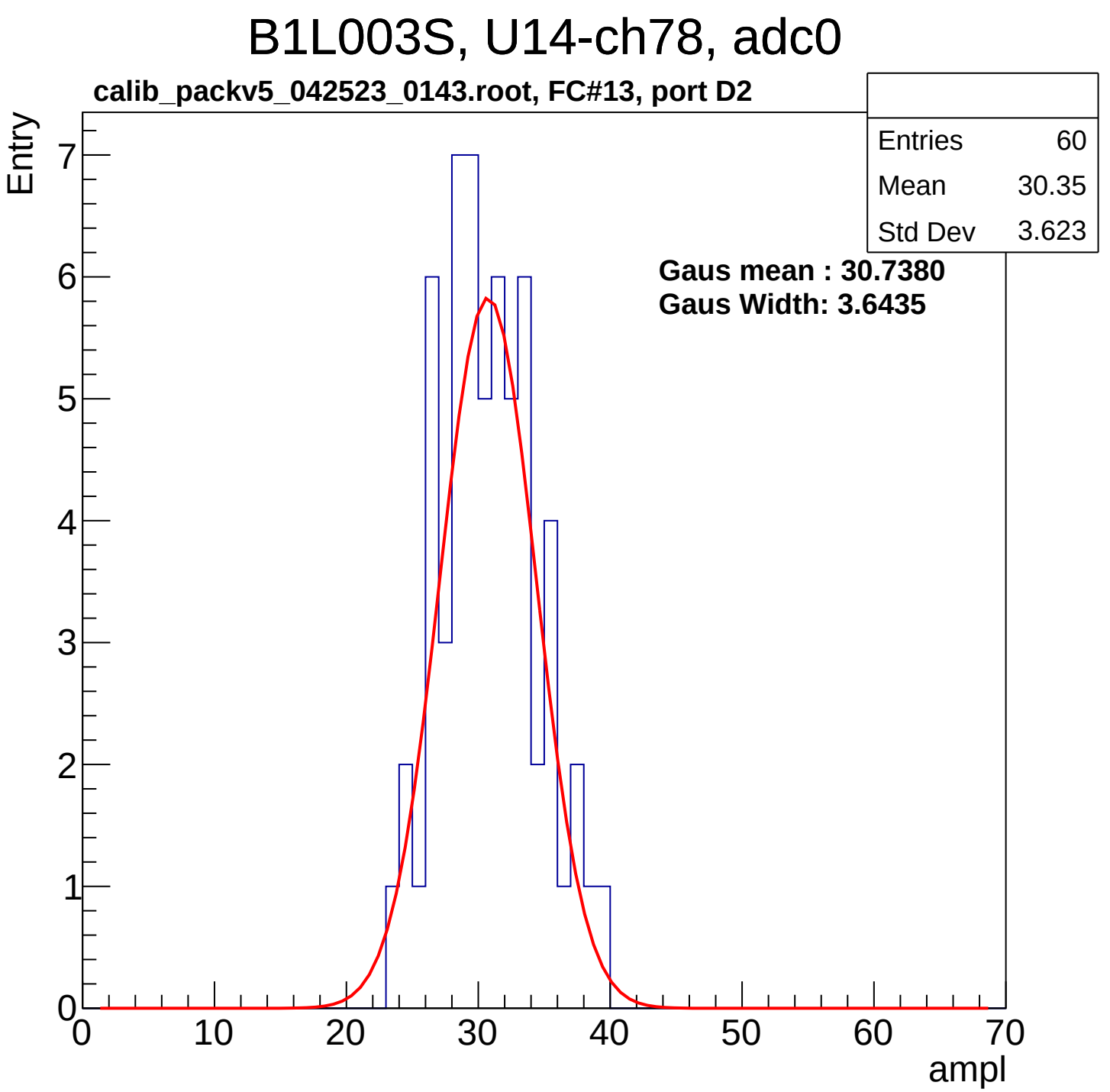

B1L003S, U14-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 47.43
Std Dev 3.291
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

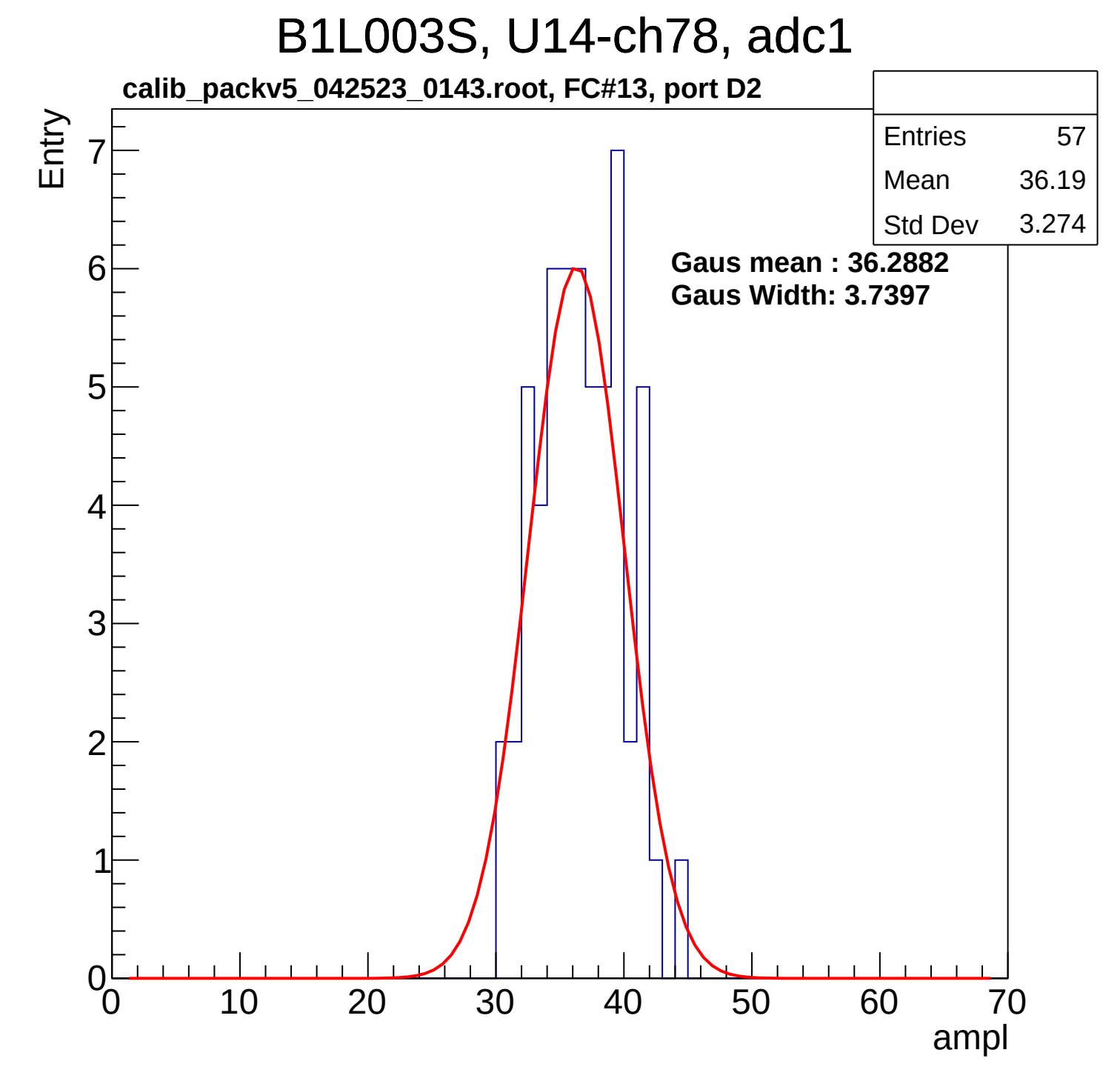

B1L003S, U14-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 53.81
Std Dev 3.51
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

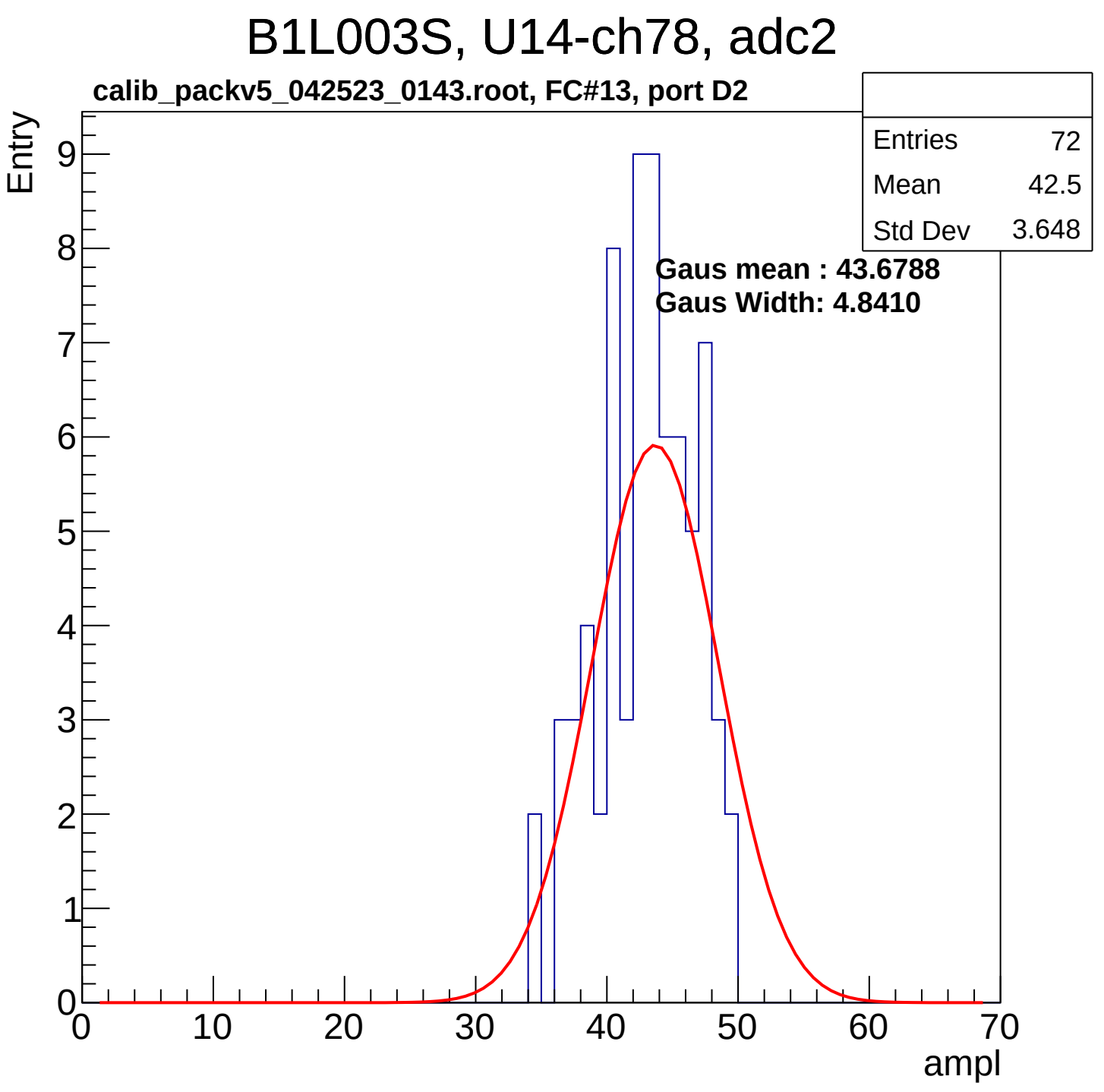

B1L003S, U14-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.94
Std Dev 2.558
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

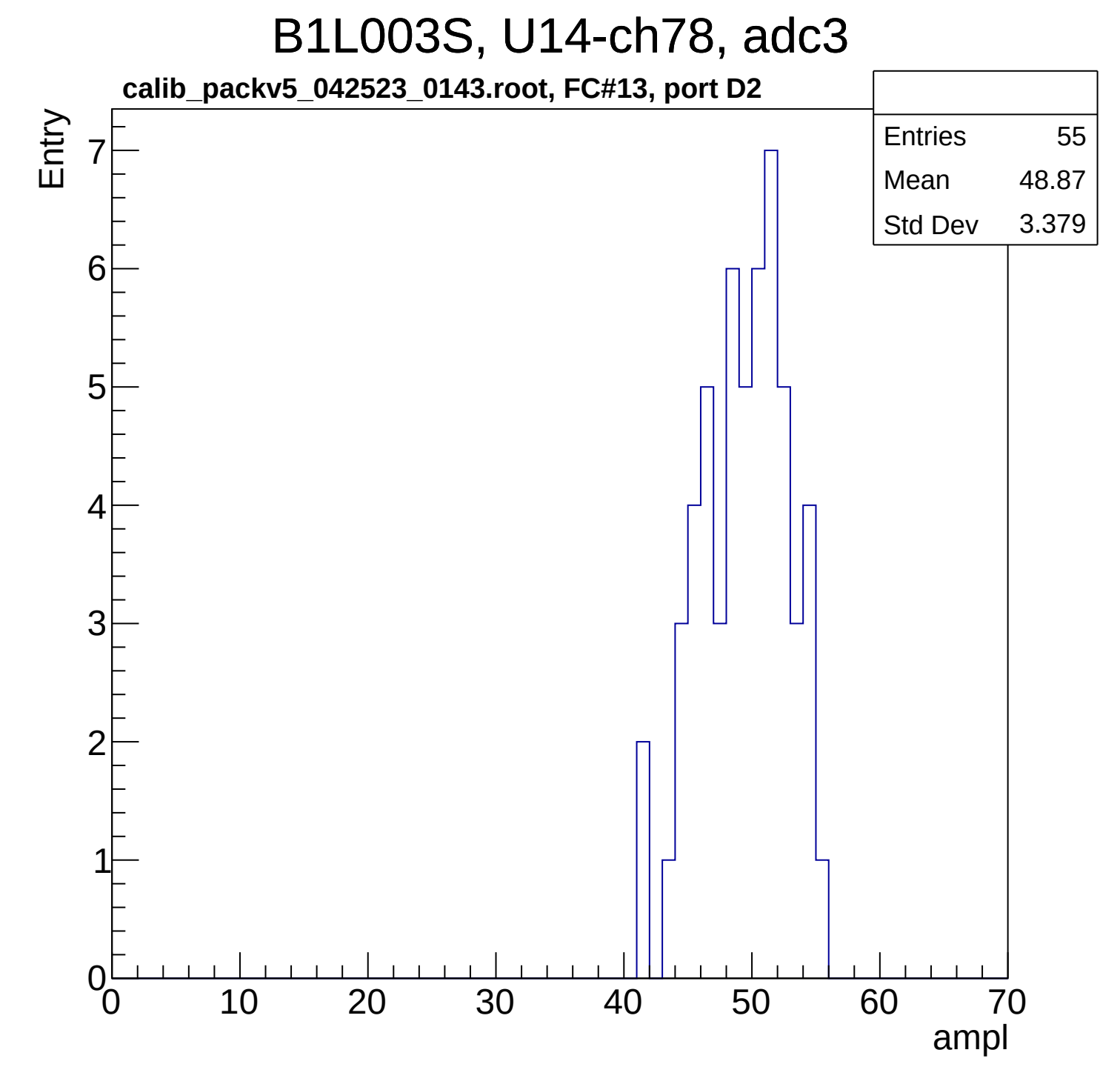

B1L003S, U14-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.56
Std Dev 14.97
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

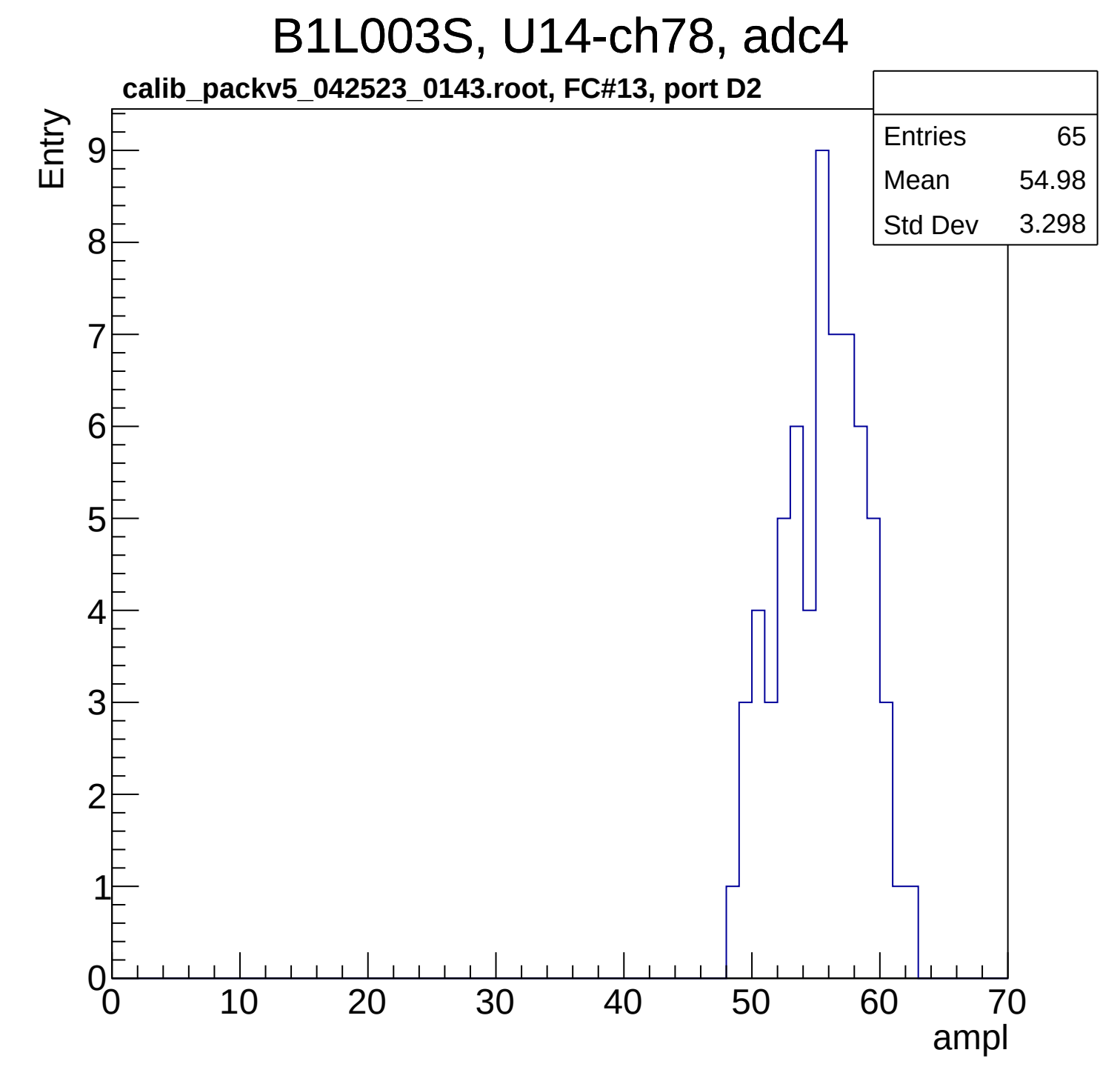

B1L003S, U14-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

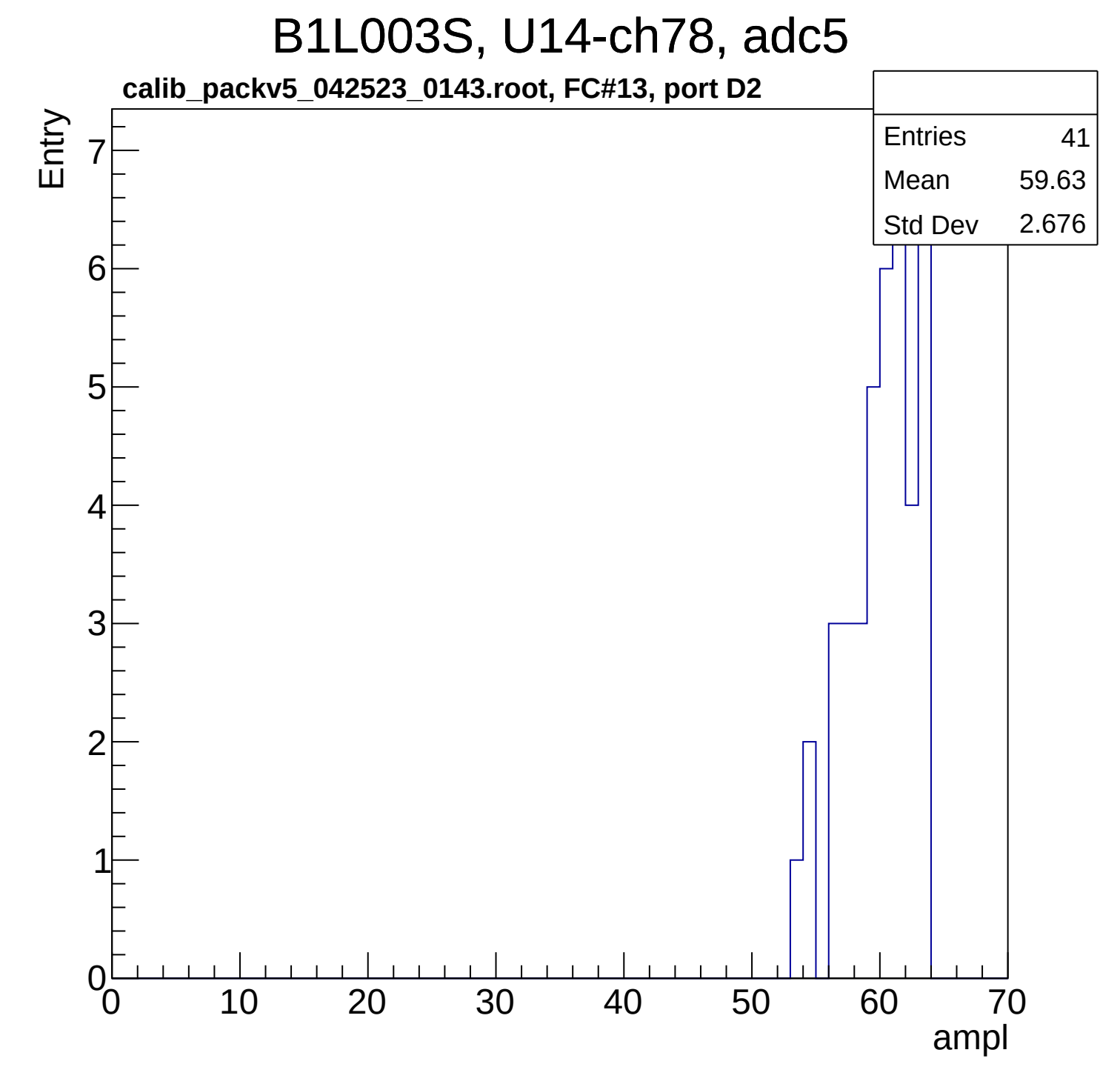

B1L003S, U14-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.35
Std Dev 3.623
Gaus mean : 30.7380
Gaus Width: 3.6435
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

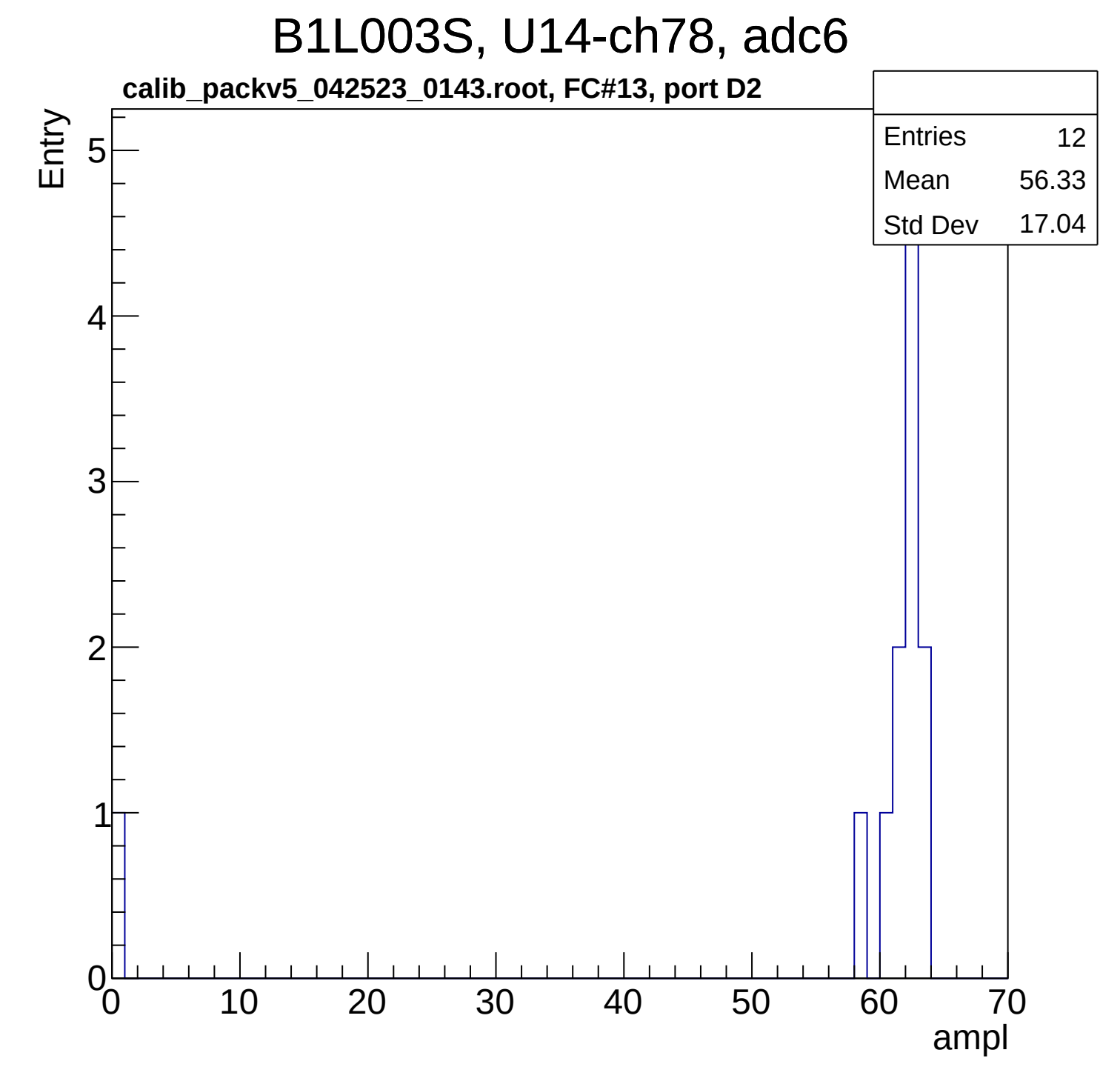

B1L003S, U14-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.19
Std Dev 3.274
Gaus mean : 36.2882
Gaus Width: 3.7397
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

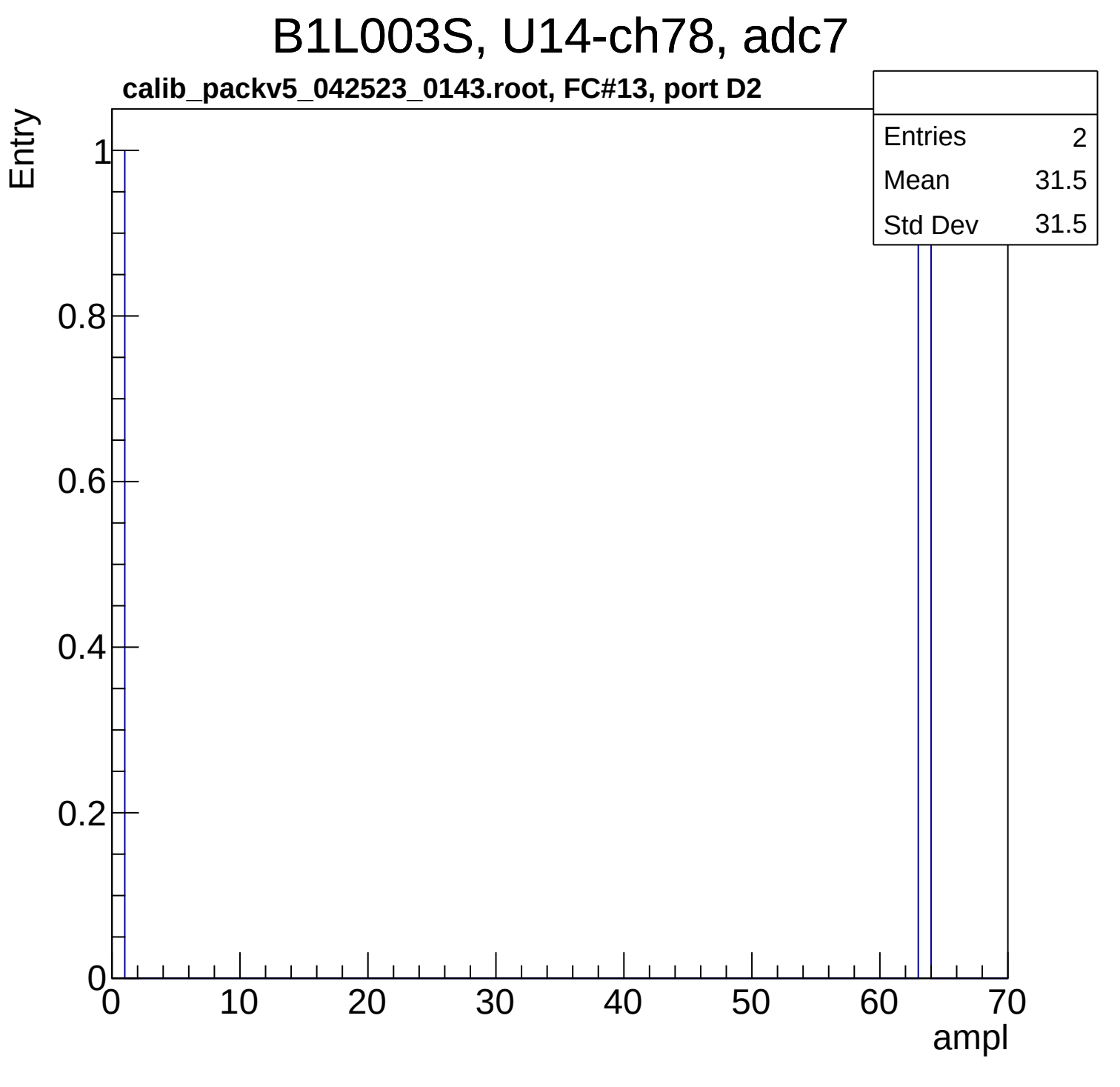

B1L003S, U14-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.5
Std Dev 3.648
Gaus mean : 43.6788
Gaus Width: 4.8410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

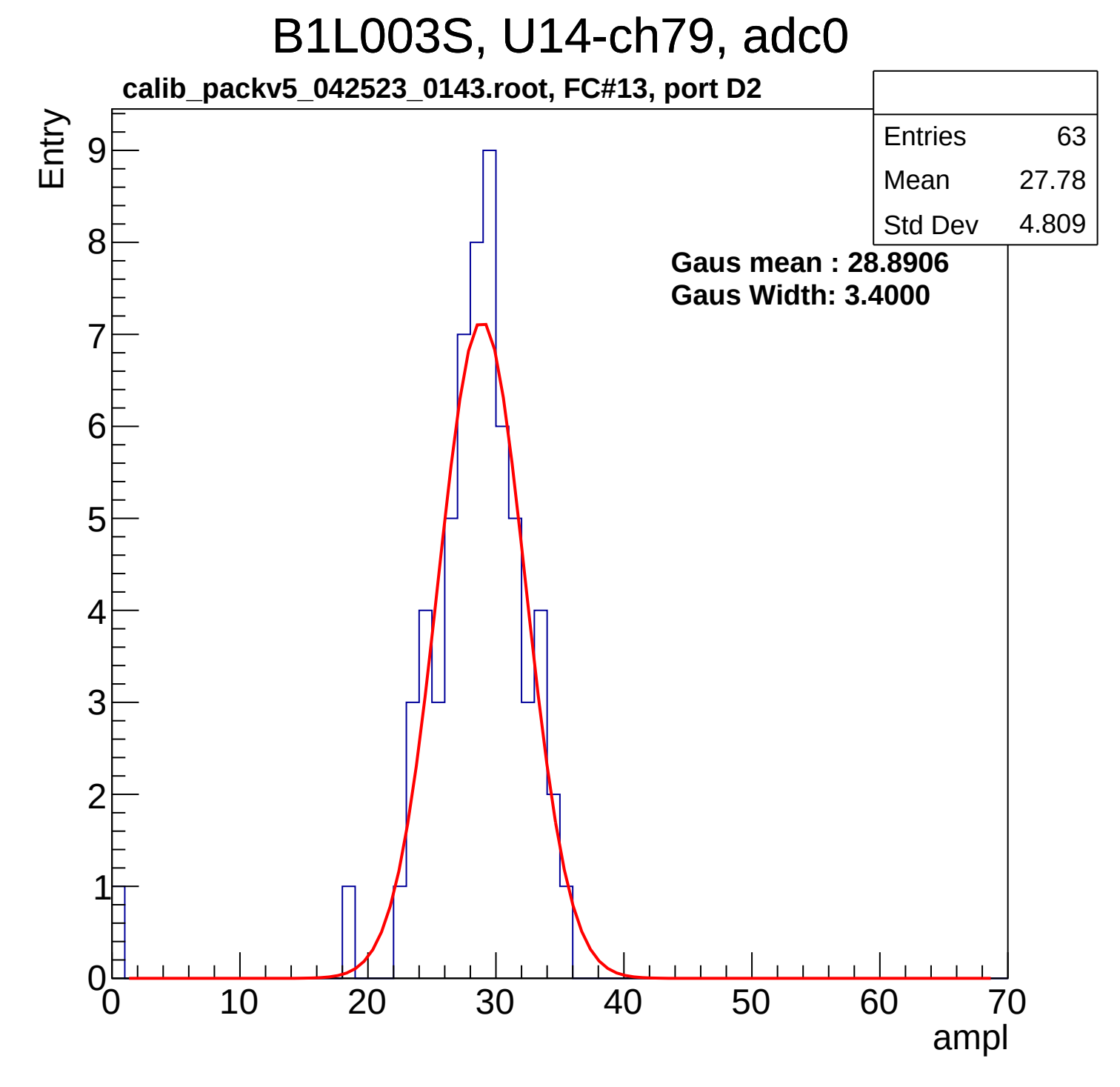

B1L003S, U14-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.87
Std Dev 3.379
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

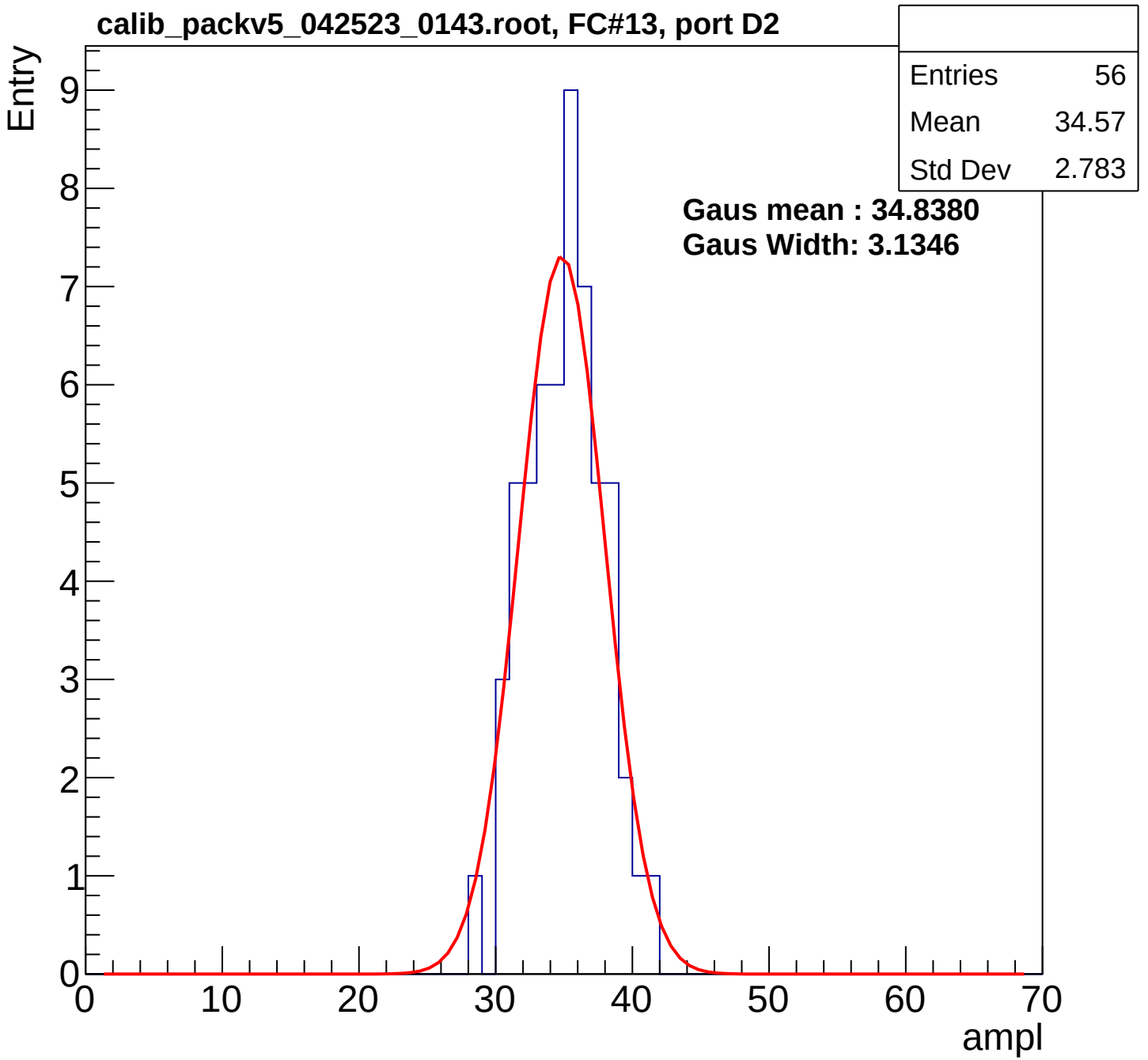

B1L003S, U14-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.98
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

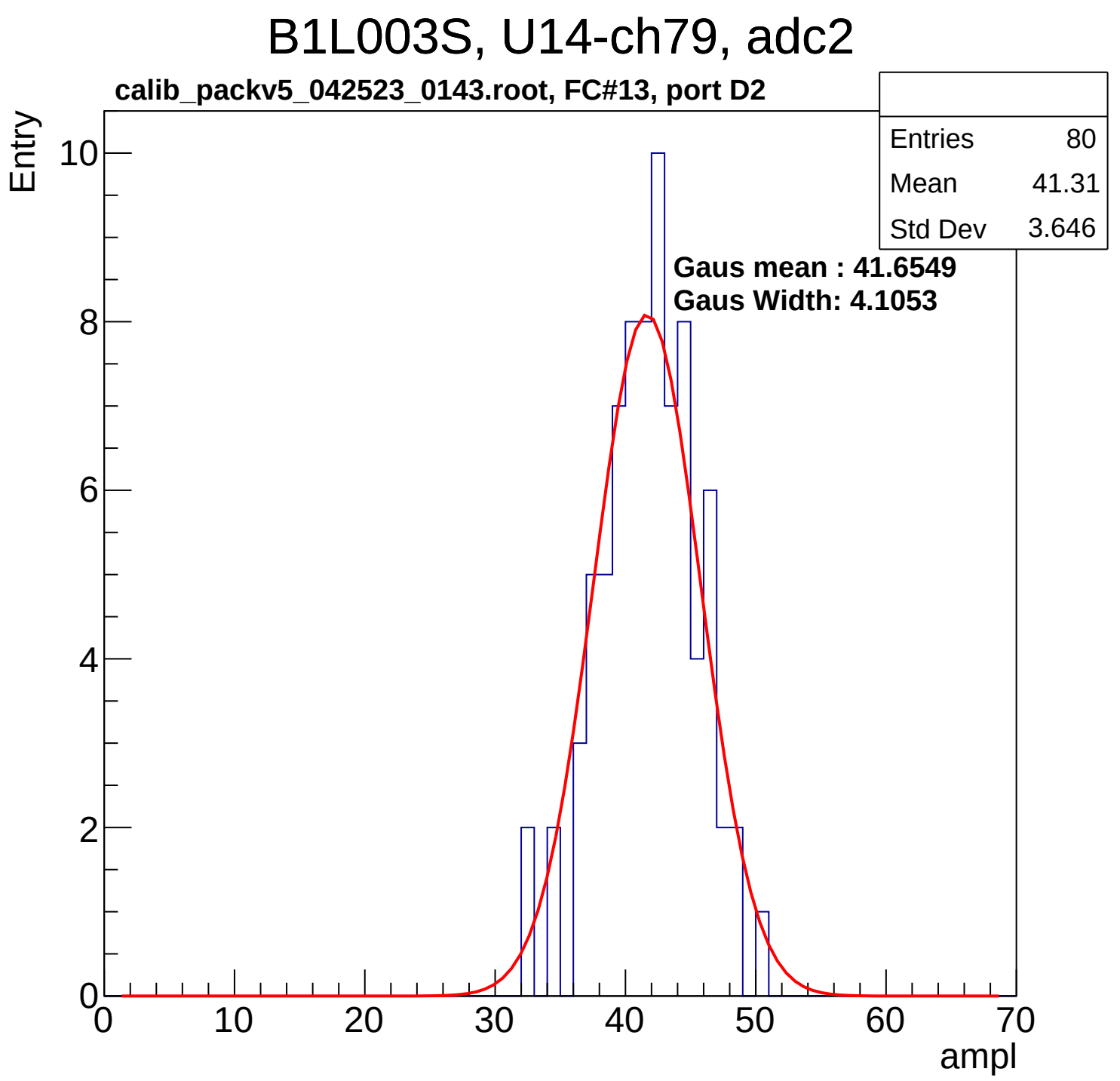

B1L003S, U14-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.63
Std Dev 2.676
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

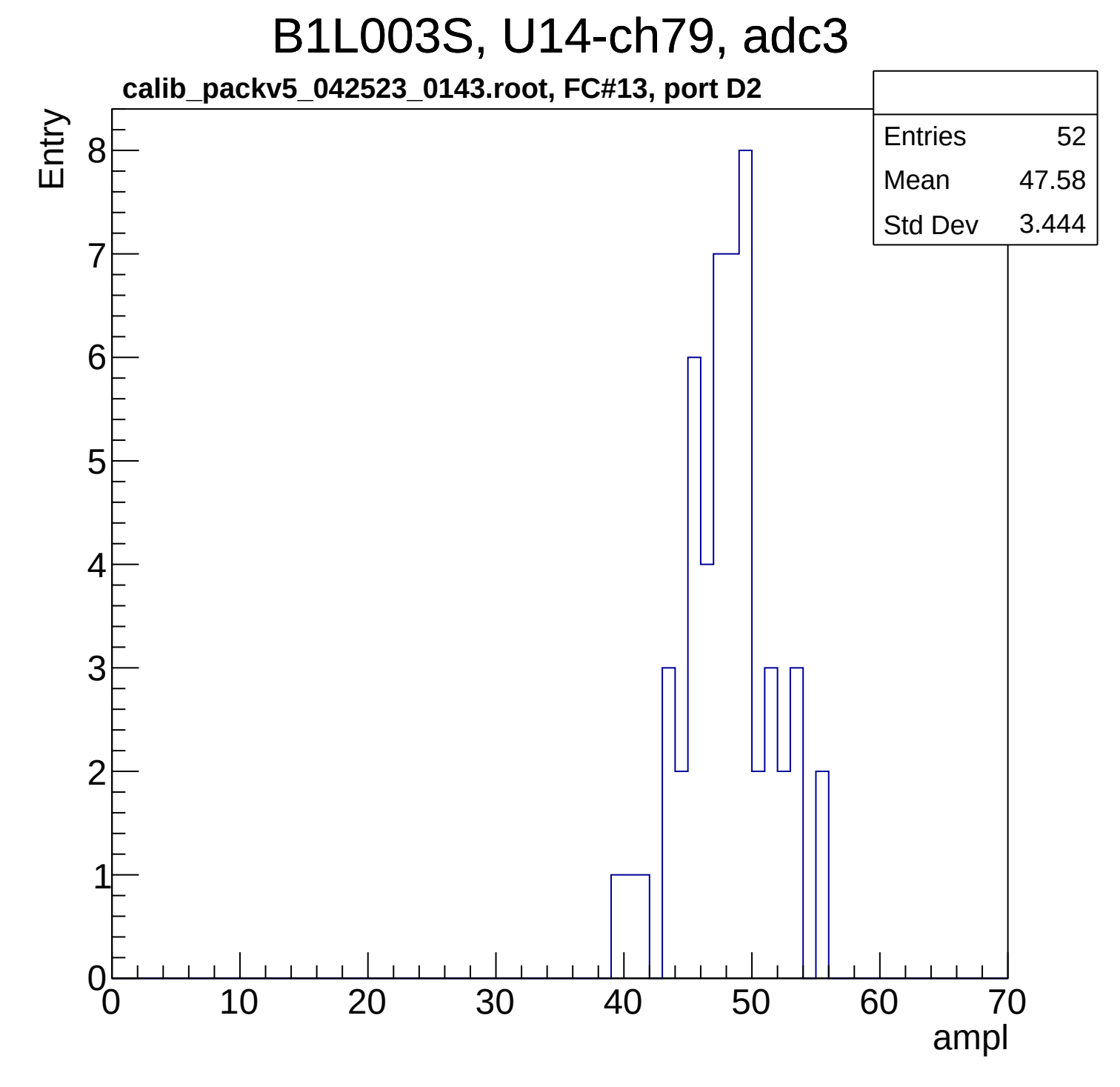

B1L003S, U14-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.33
Std Dev 17.04
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

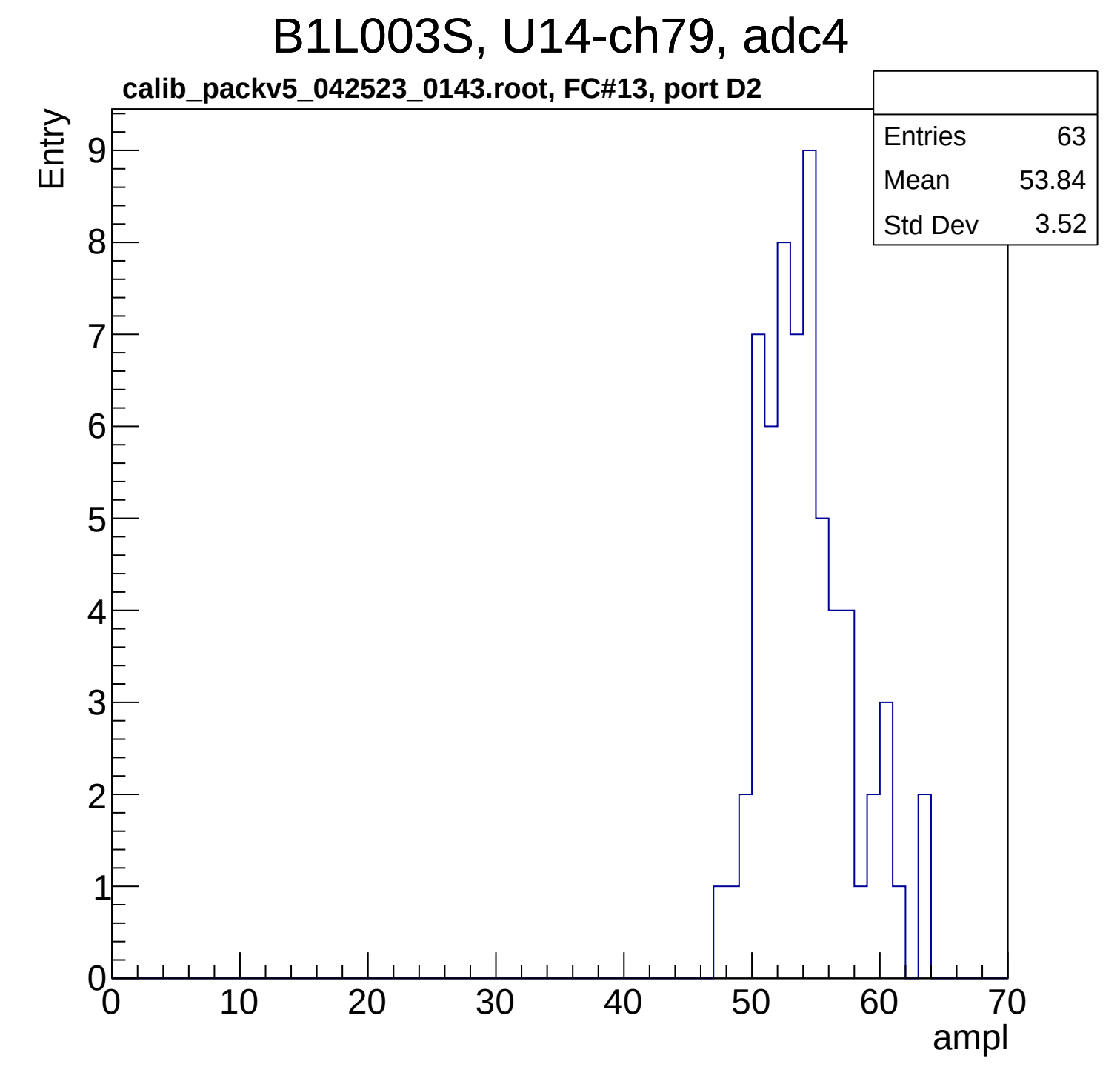

B1L003S, U14-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

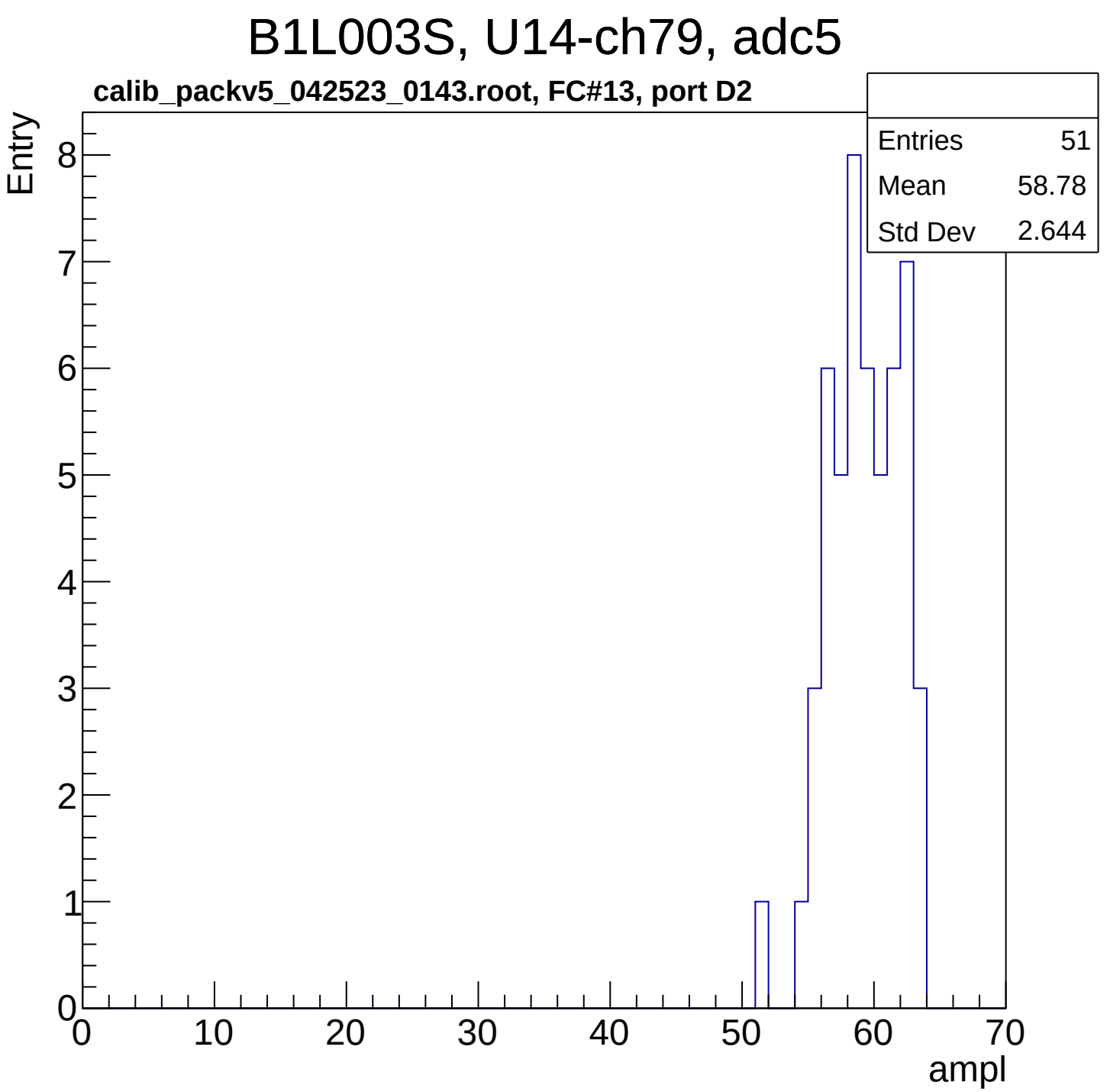

B1L003S, U14-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 27.78
Std Dev 4.809
Gaus mean : 28.8906
Gaus Width: 3.4000
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

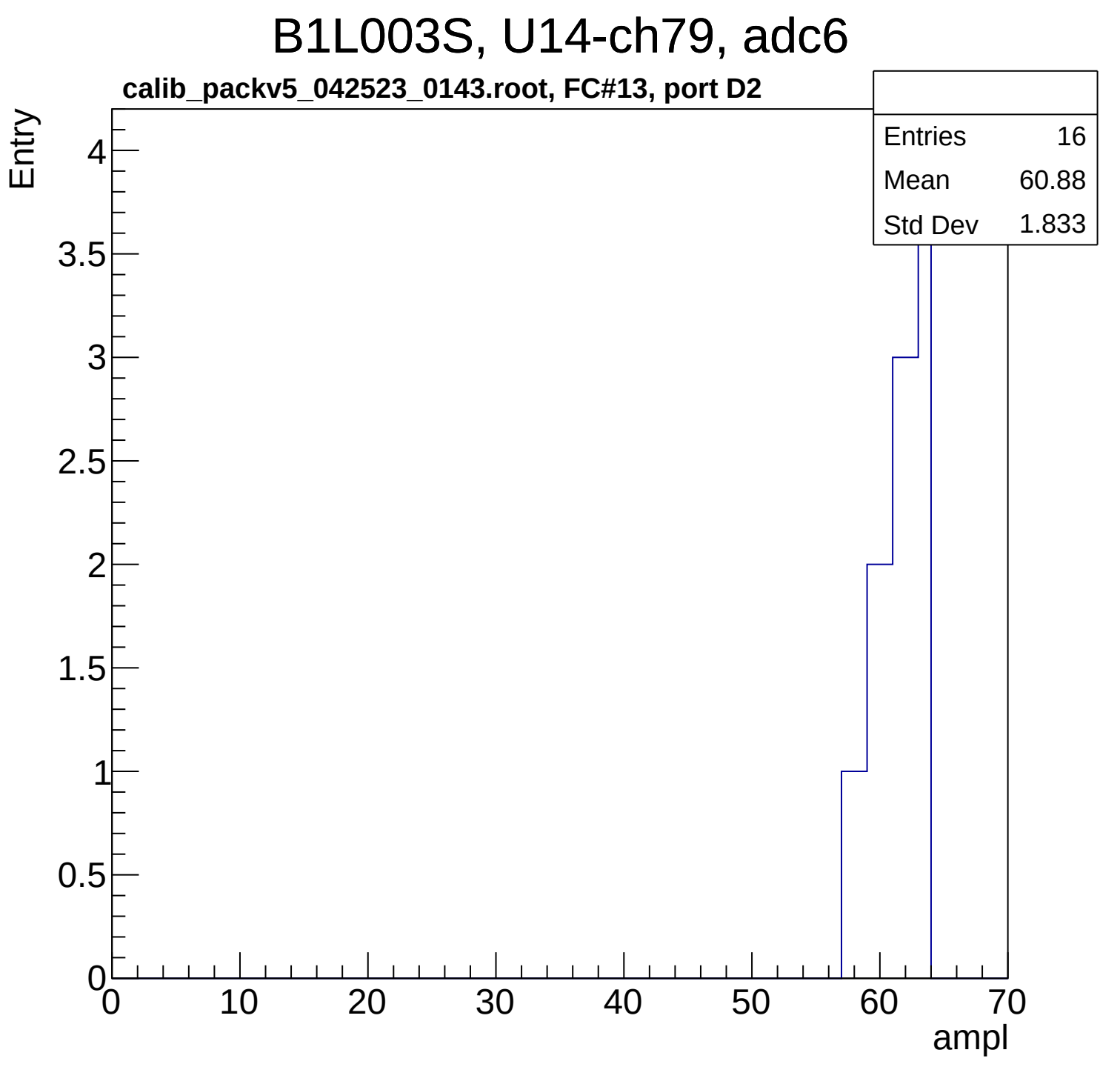

B1L003S, U14-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 34.57
Std Dev 2.783
Gaus mean : 34.8380
Gaus Width: 3.1346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

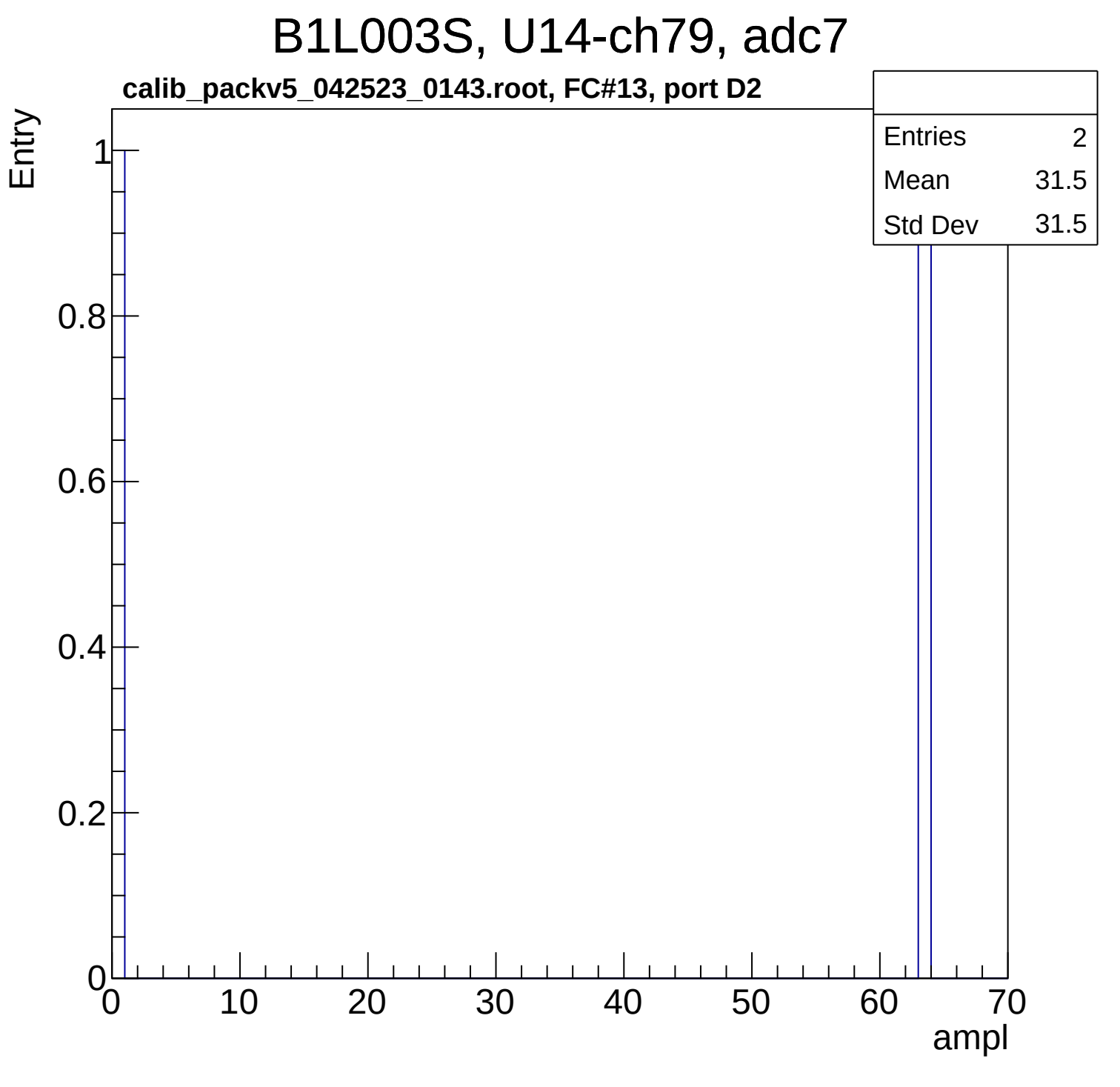

B1L003S, U14-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 41.31
Std Dev 3.646
Gaus mean : 41.6549
Gaus Width: 4.1053
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

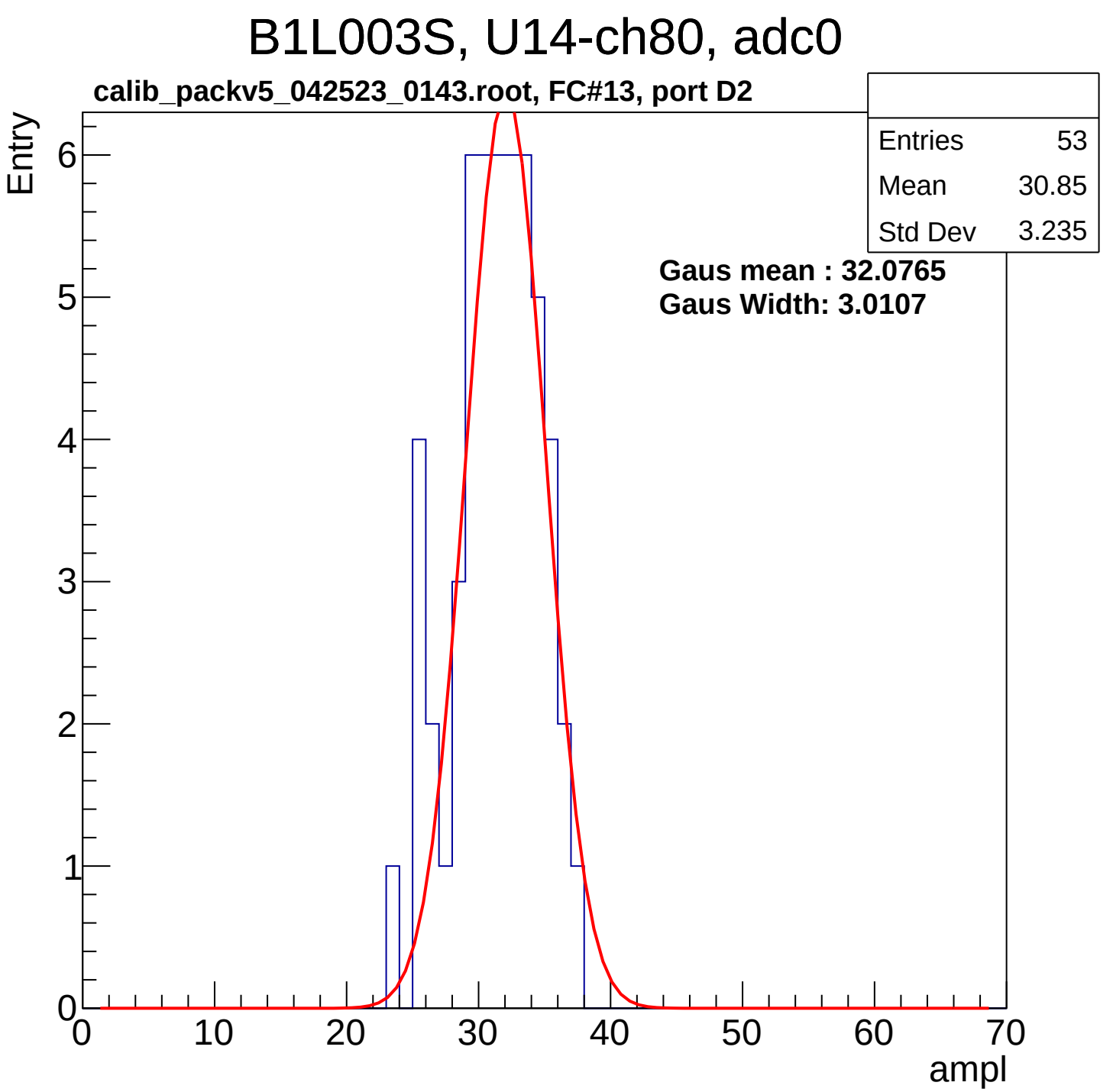

B1L003S, U14-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 47.58
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

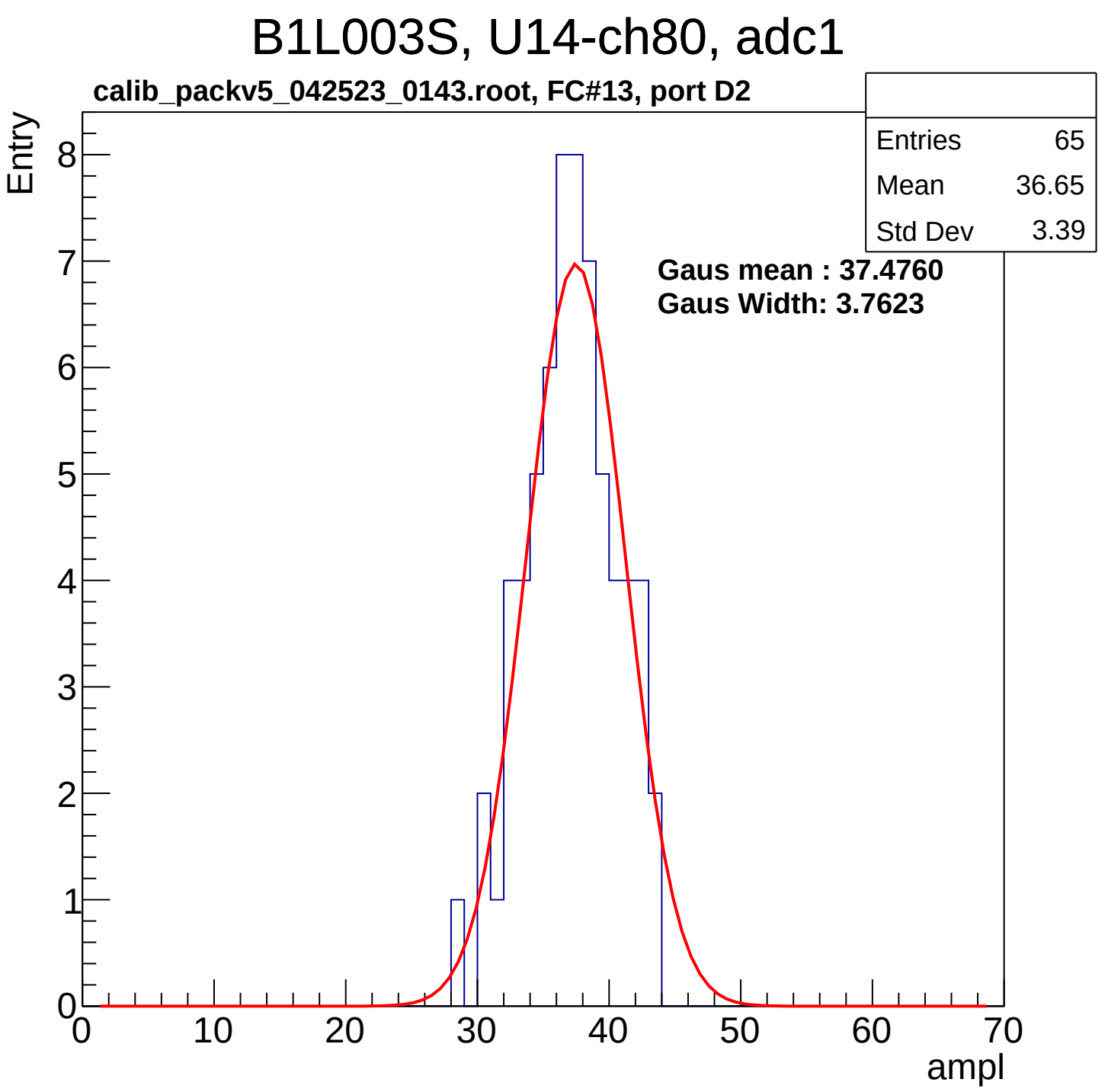

B1L003S, U14-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 53.84
Std Dev 3.52
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

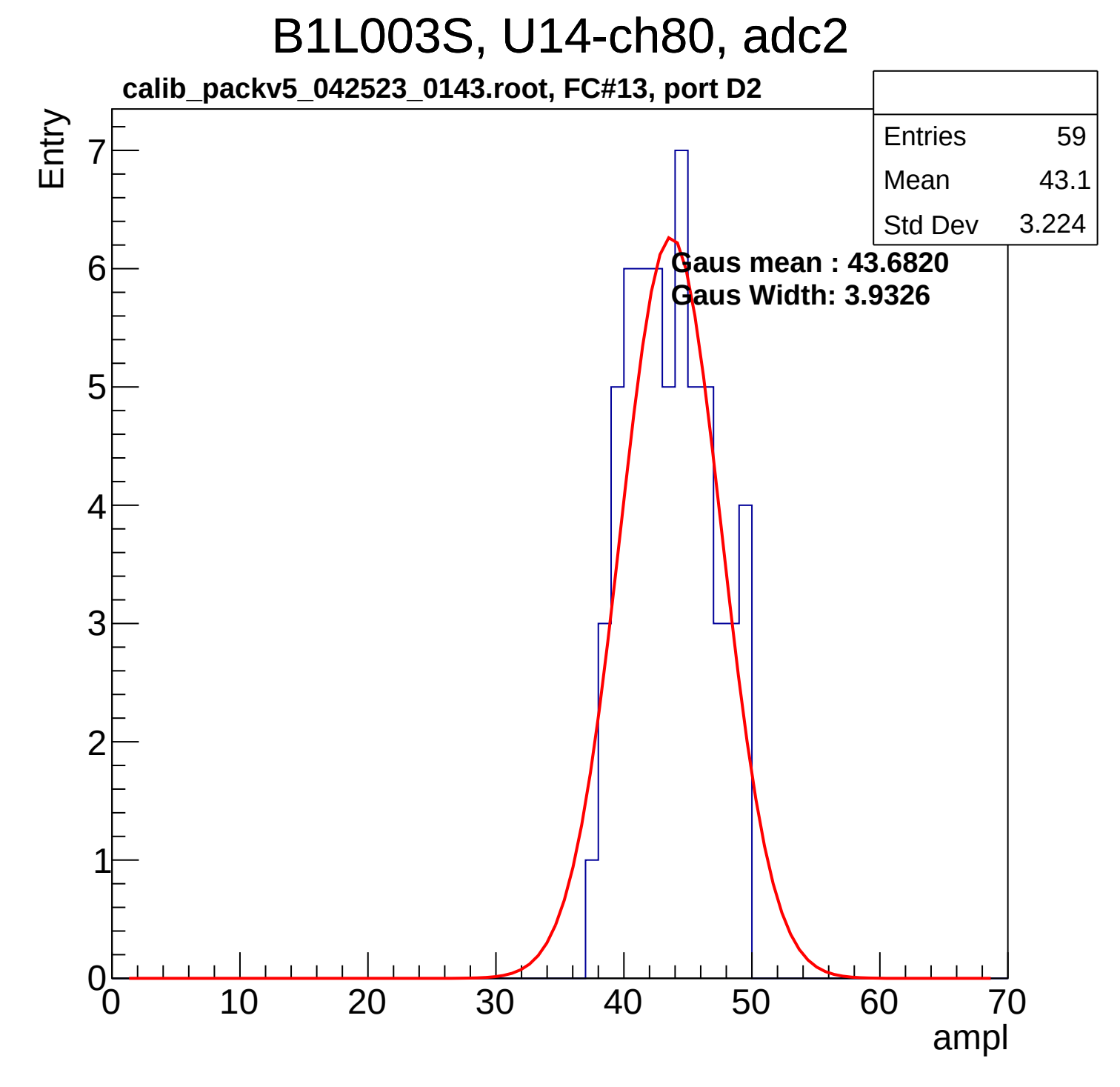

B1L003S, U14-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.78
Std Dev 2.644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

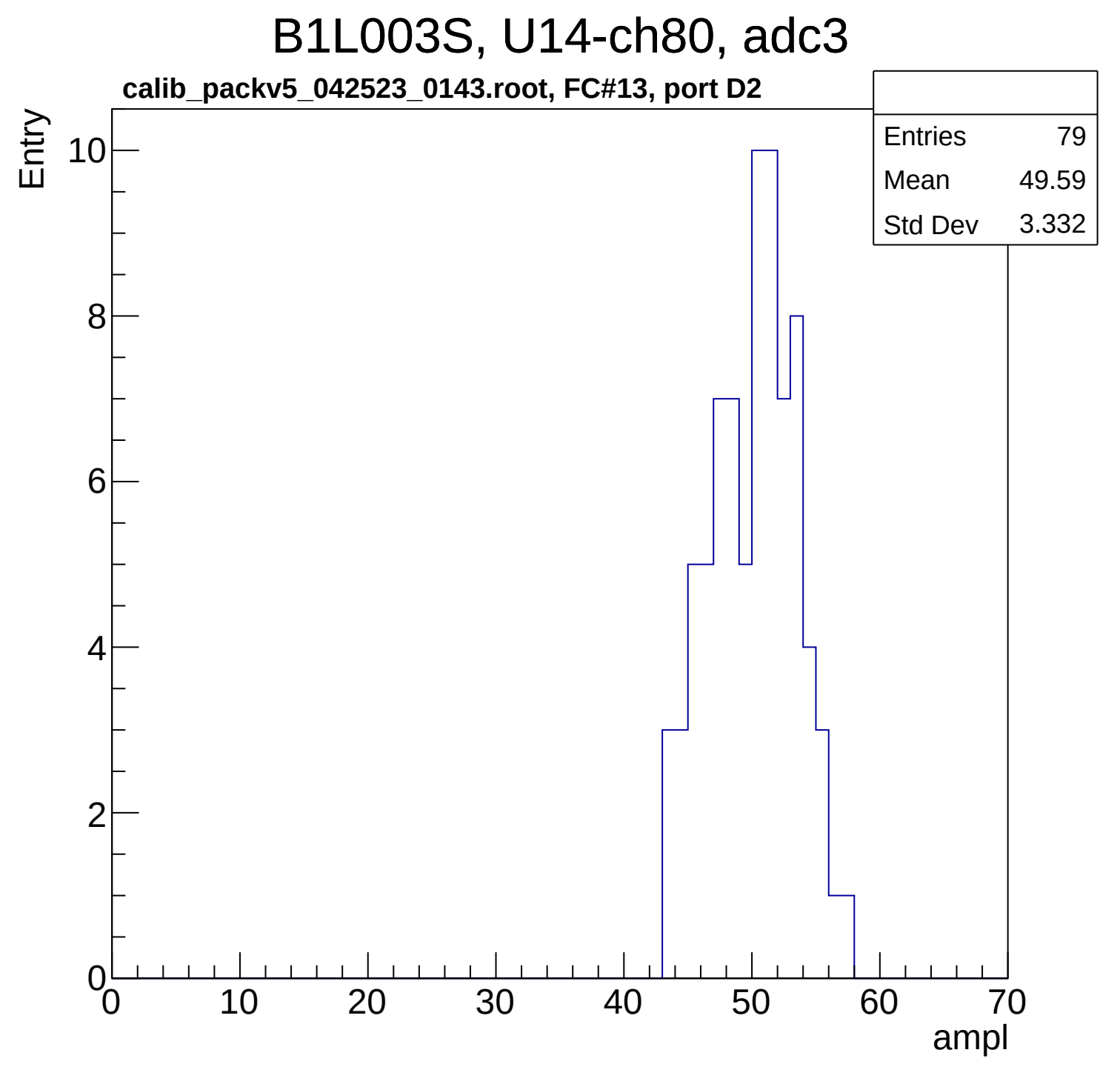

B1L003S, U14-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 60.88
Std Dev 1.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

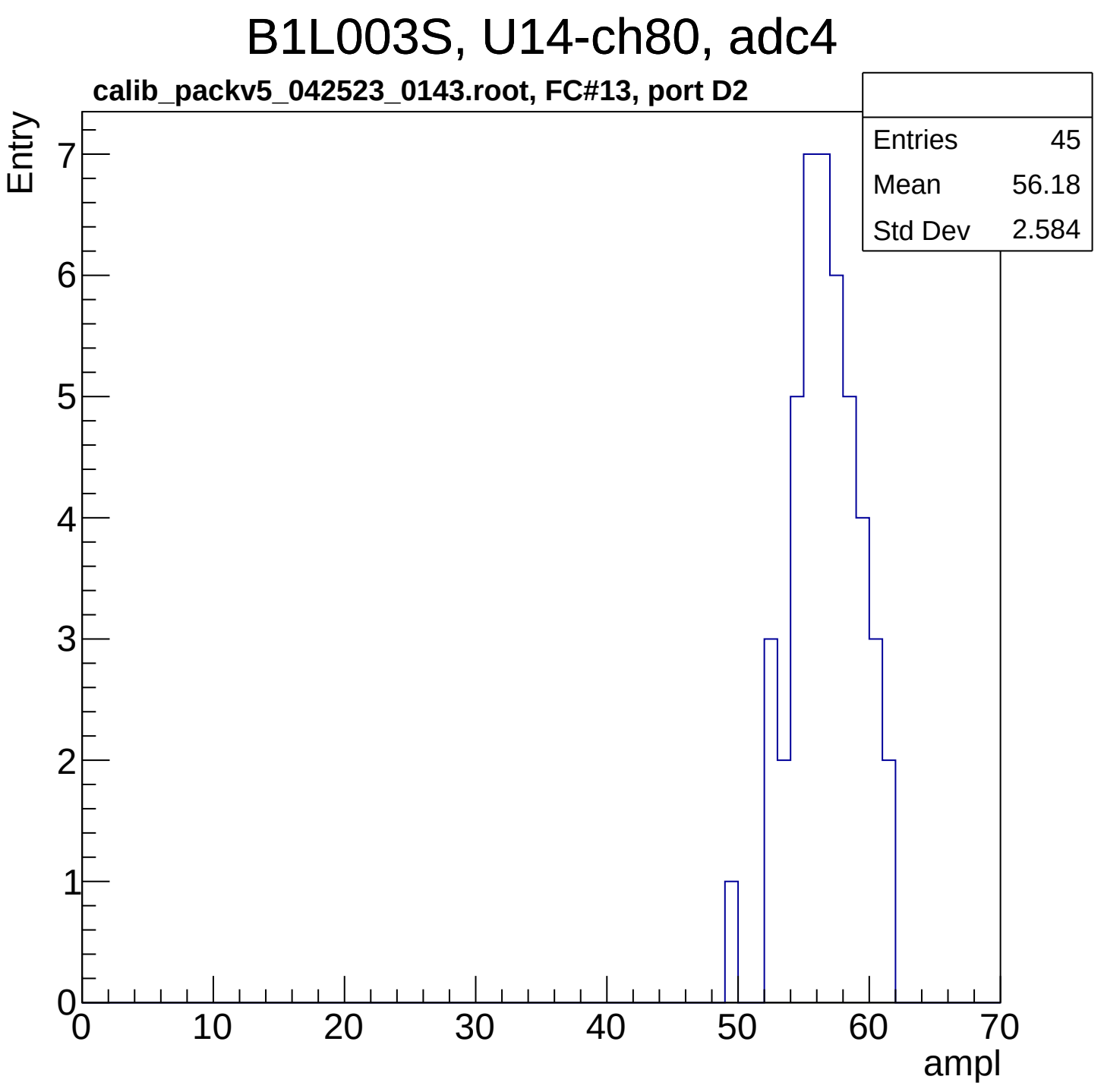

B1L003S, U14-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

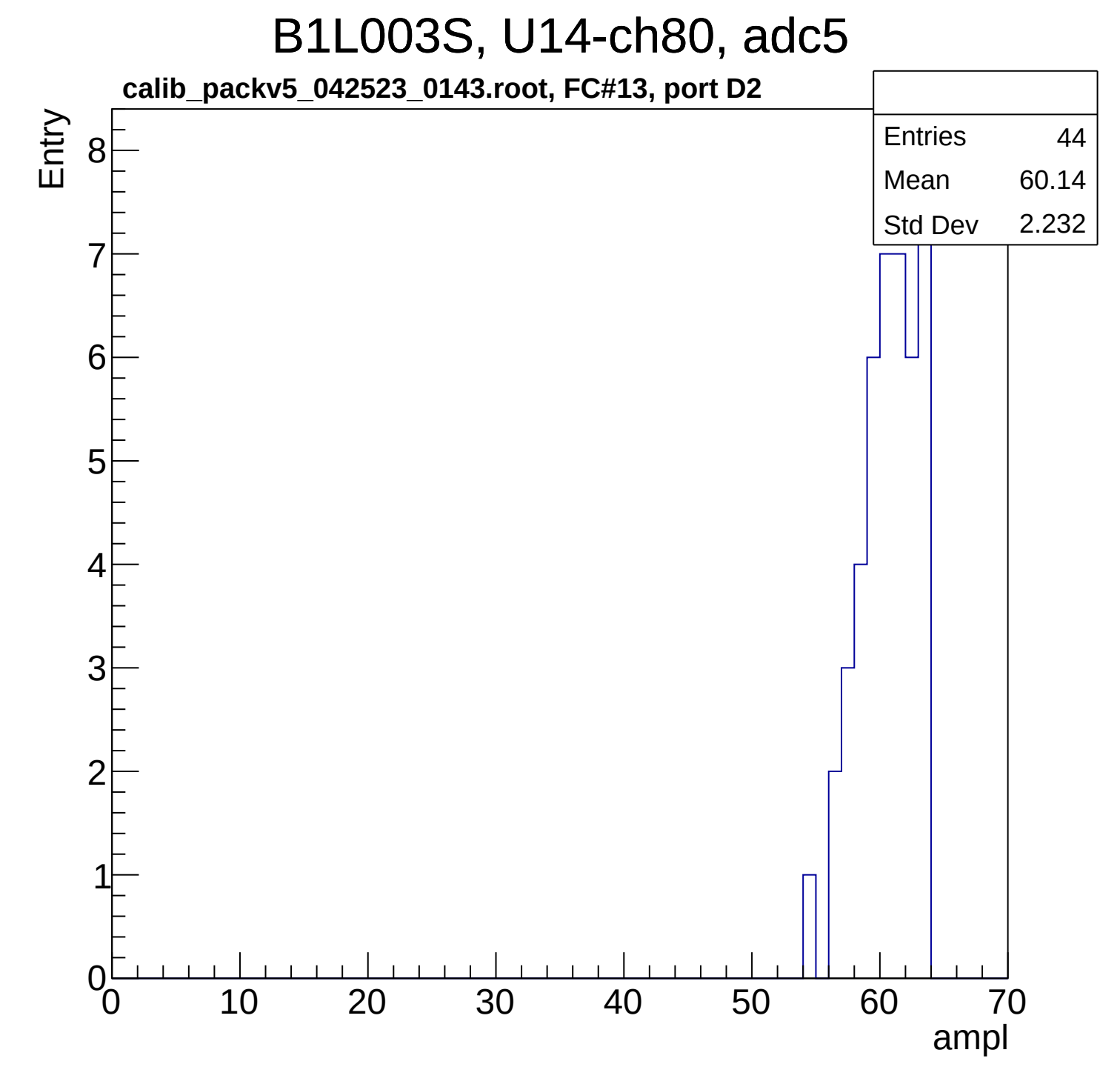

B1L003S, U14-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 30.85
Std Dev 3.235
Gaus mean : 32.0765
Gaus Width: 3.0107
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

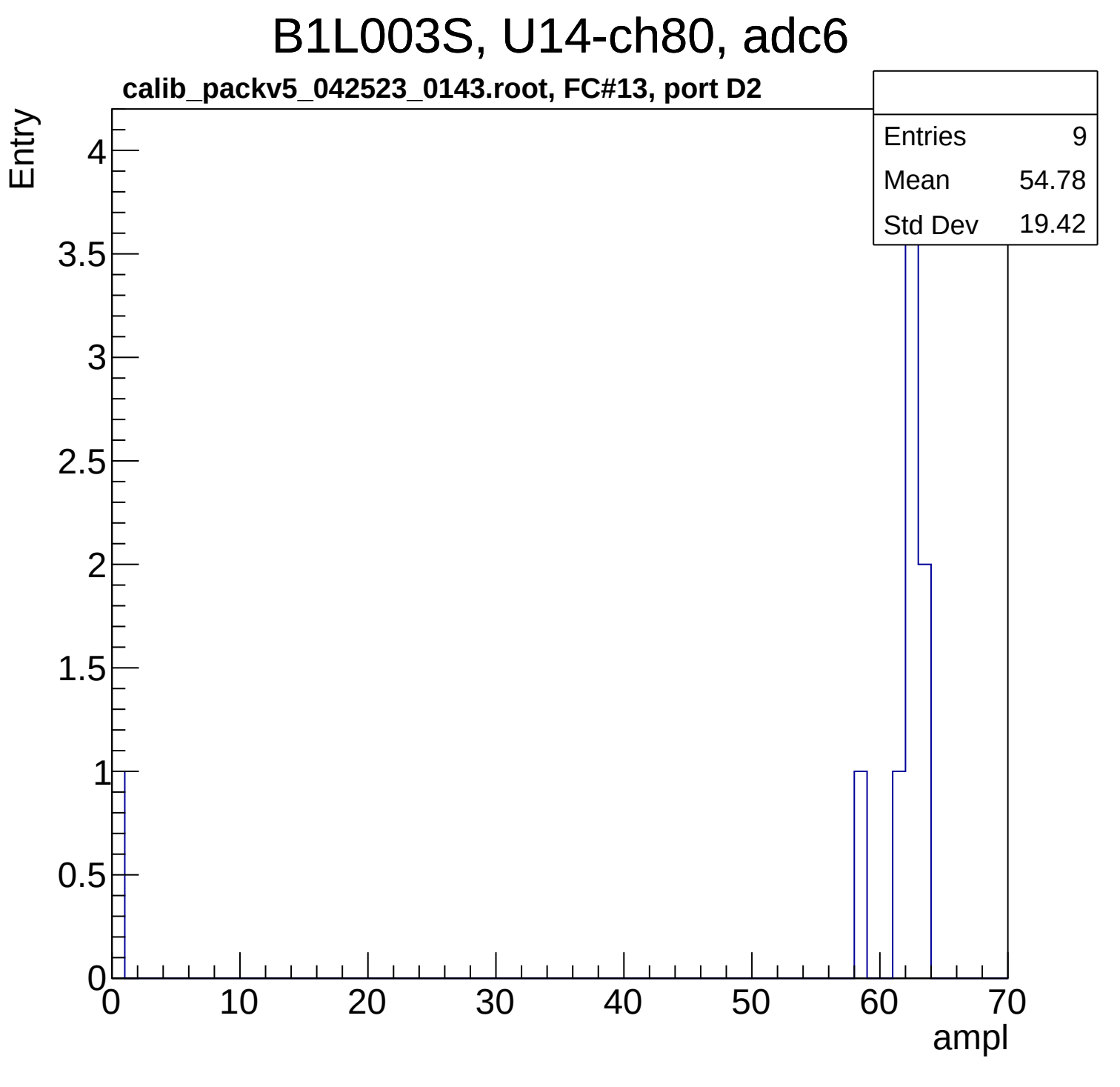

B1L003S, U14-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 36.65
Std Dev 3.39
Gaus mean : 37.4760
Gaus Width: 3.7623
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

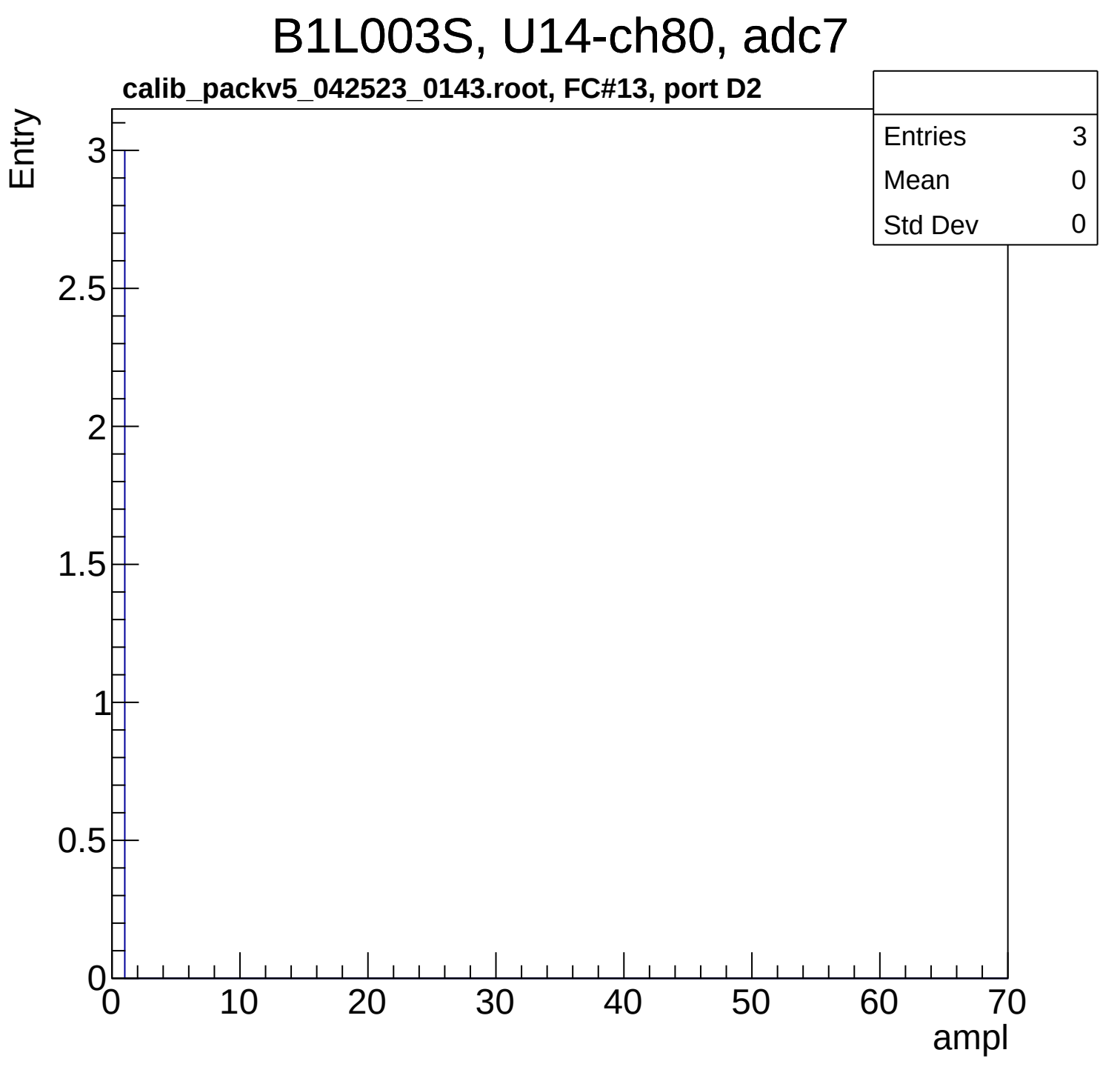

B1L003S, U14-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.1
Std Dev 3.224
Gaus mean : 43.6820
Gaus Width: 3.9326
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

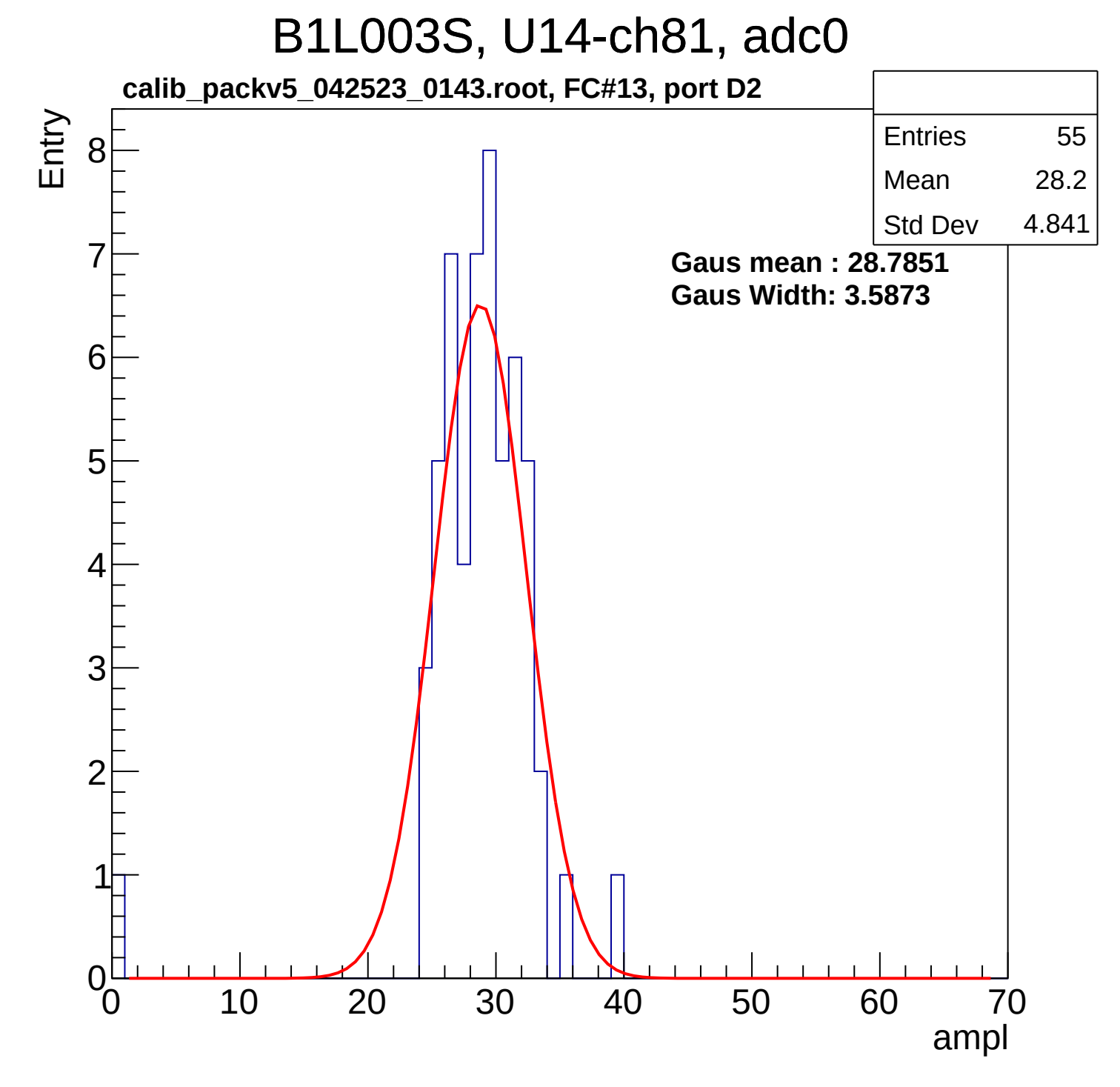

B1L003S, U14-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.59
Std Dev 3.332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

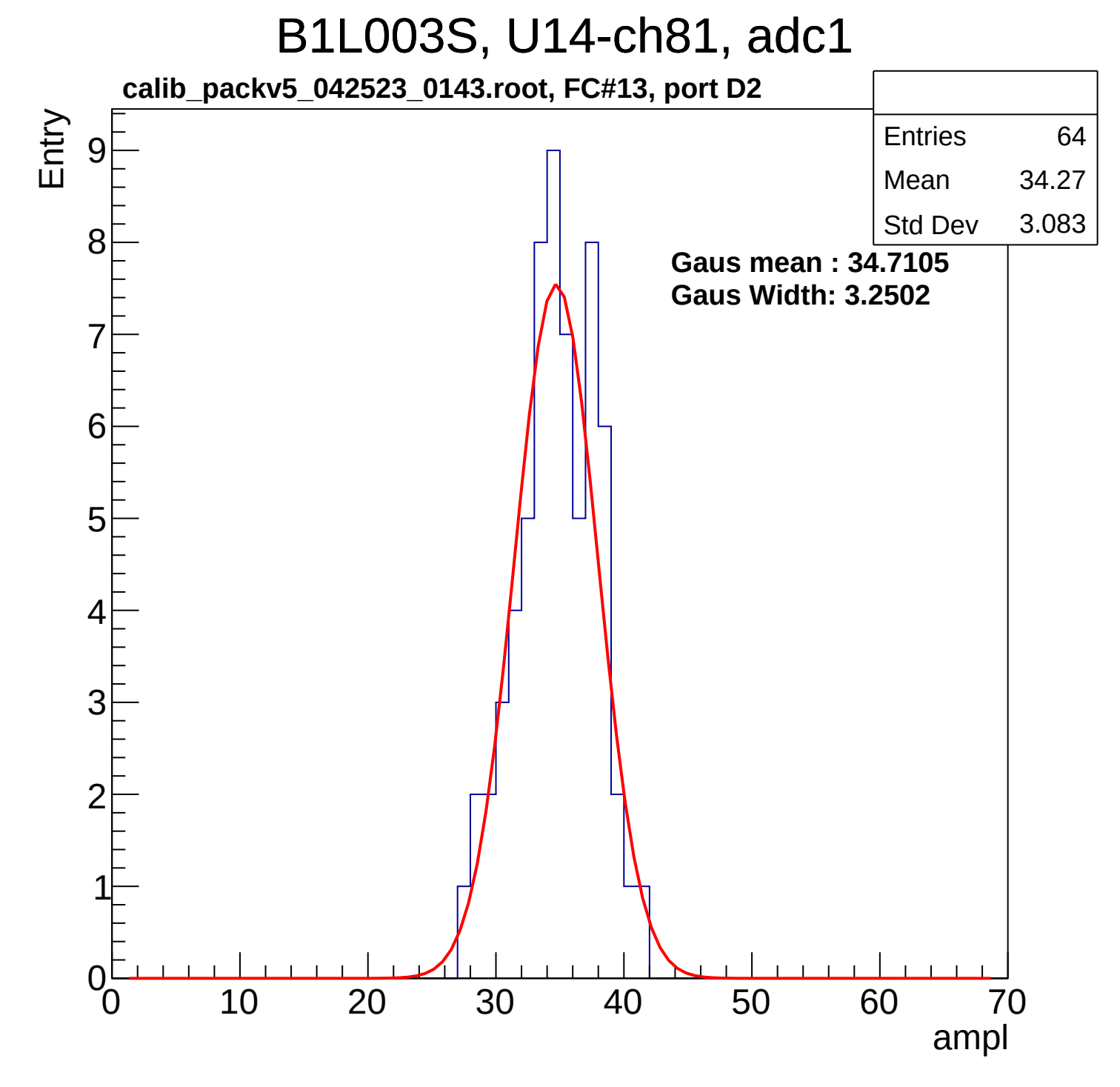

B1L003S, U14-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 56.18
Std Dev 2.584
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

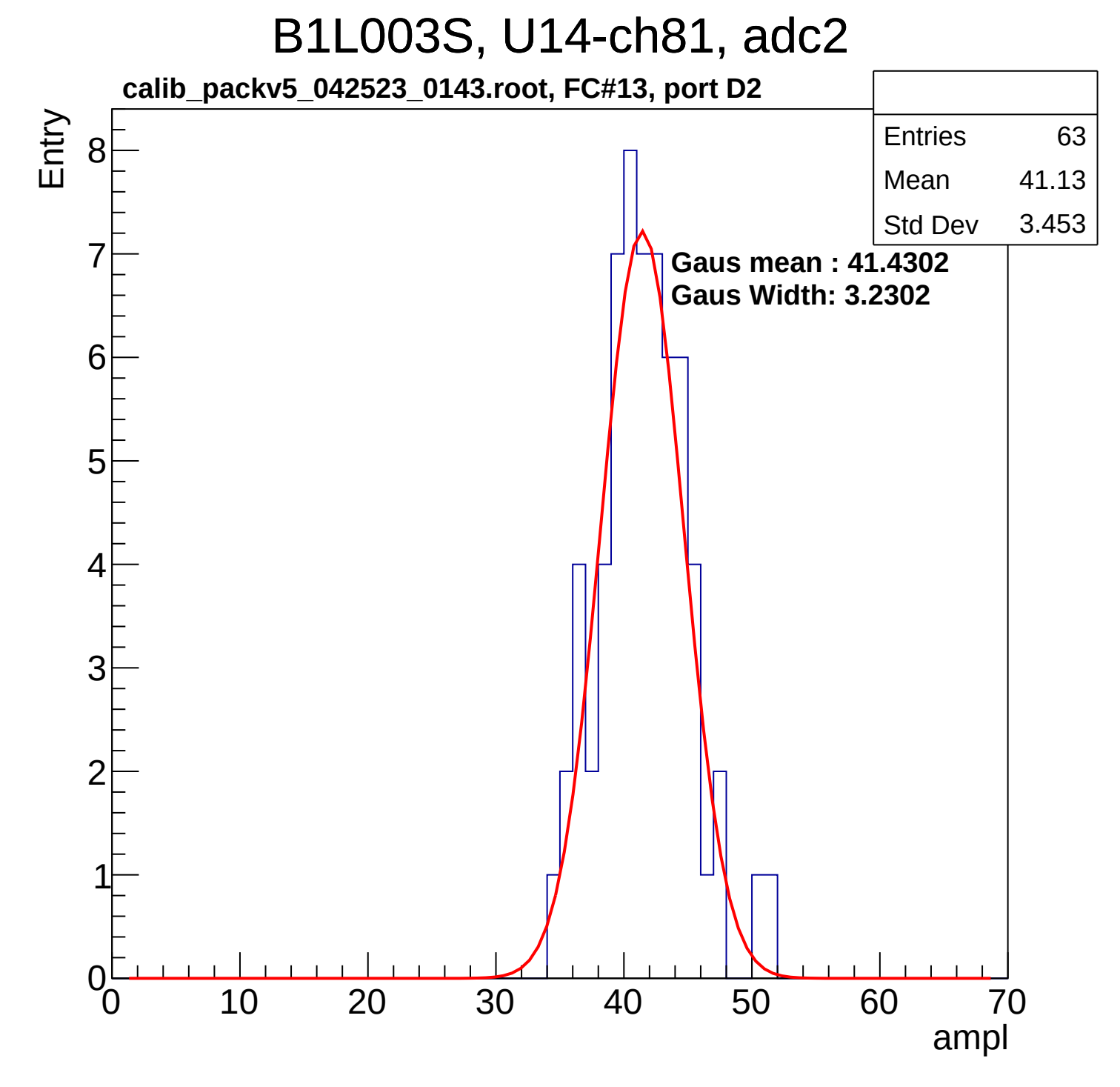

B1L003S, U14-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 60.14
Std Dev 2.232
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

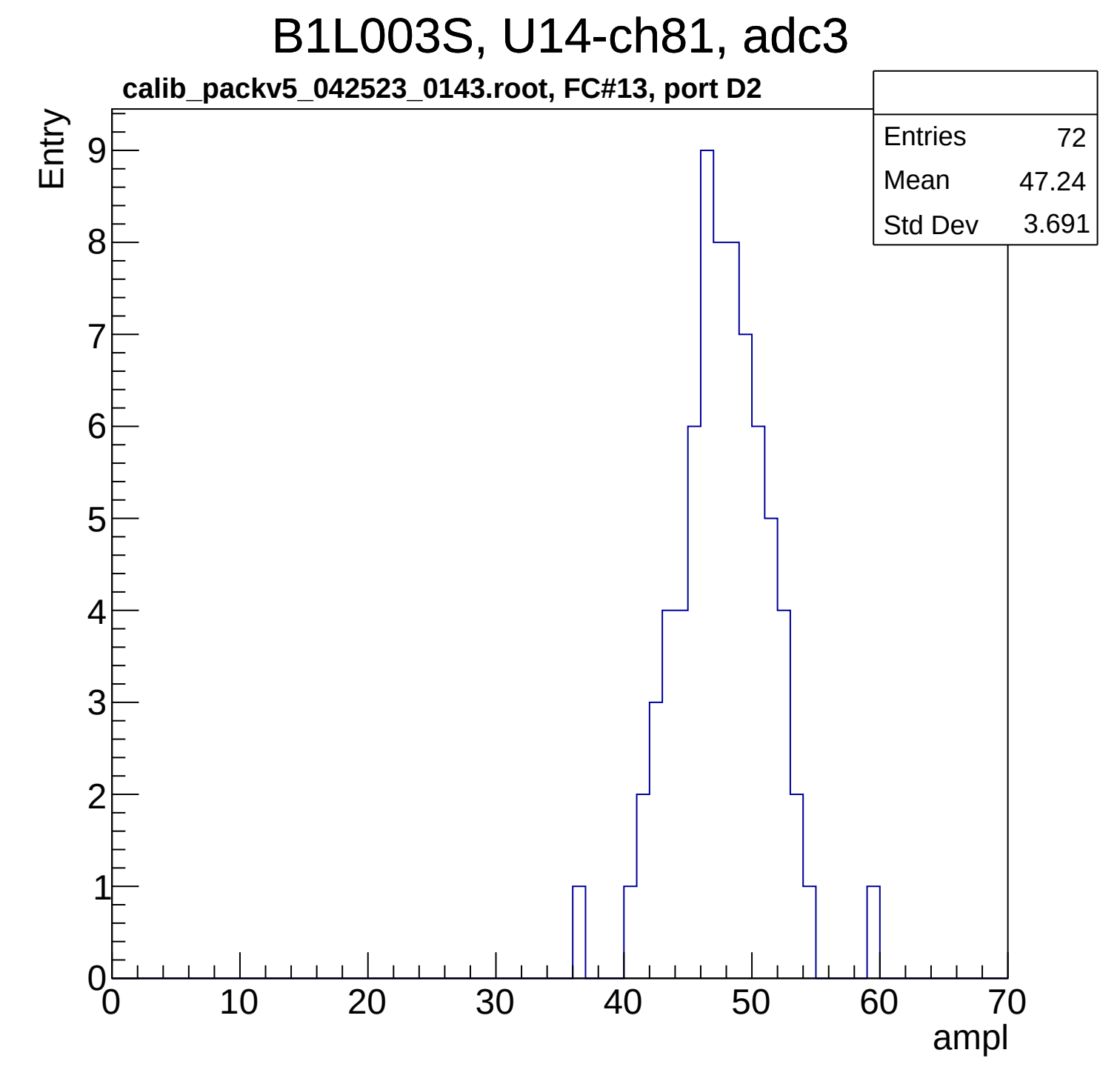

B1L003S, U14-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.78
Std Dev 19.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

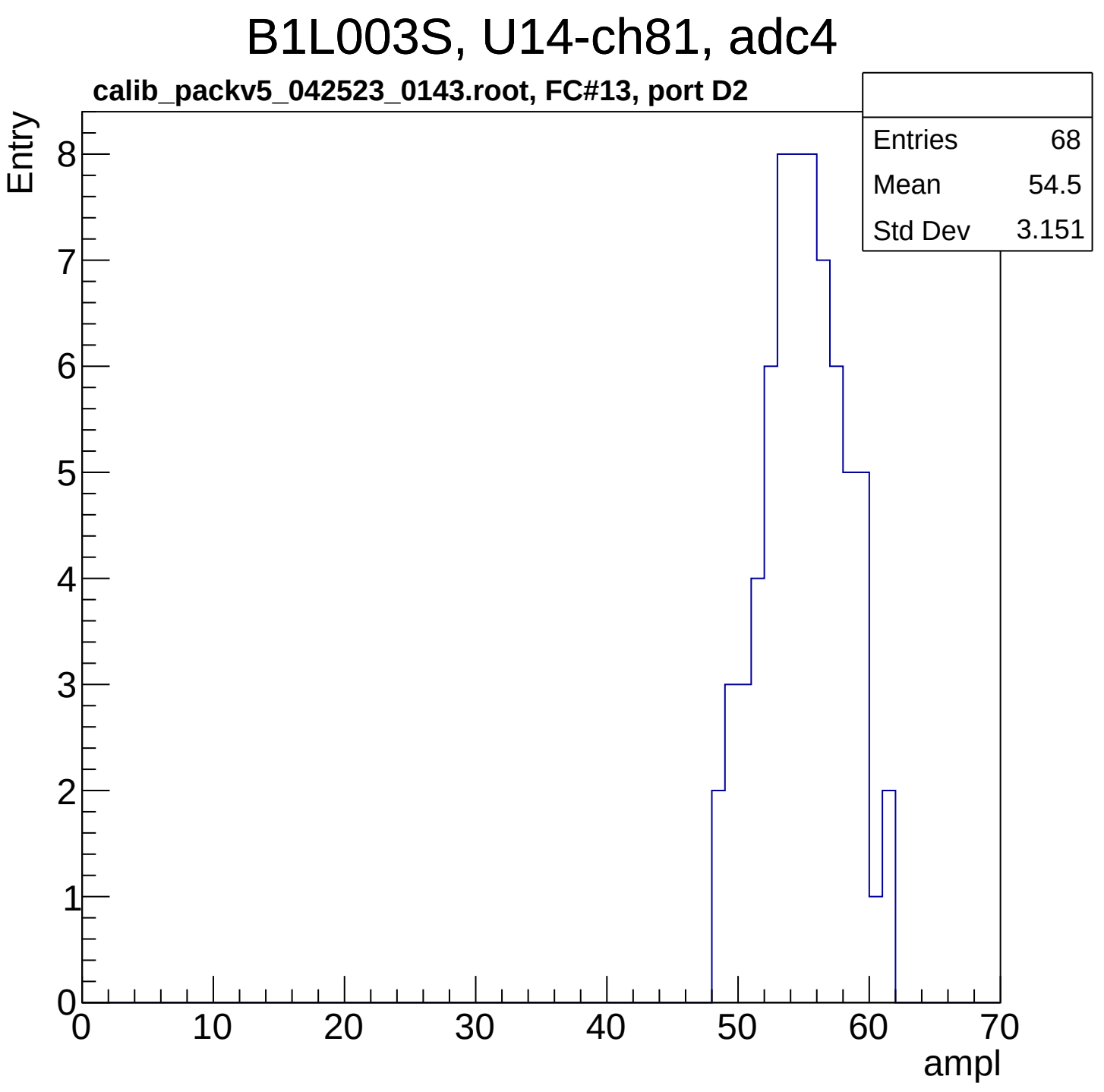

B1L003S, U14-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

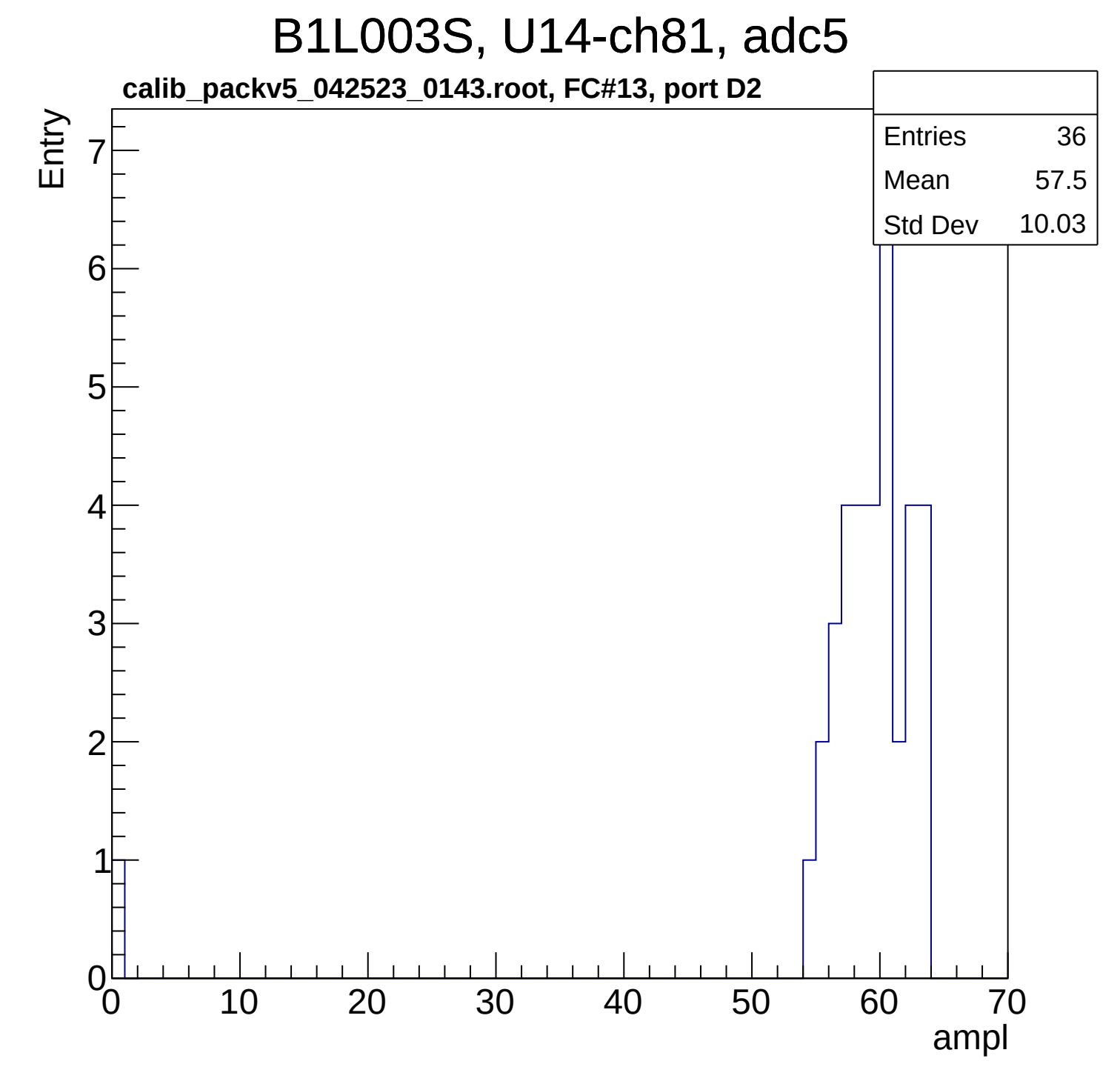

B1L003S, U14-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 28.2
Std Dev 4.841
Gaus mean : 28.7851
Gaus Width: 3.5873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

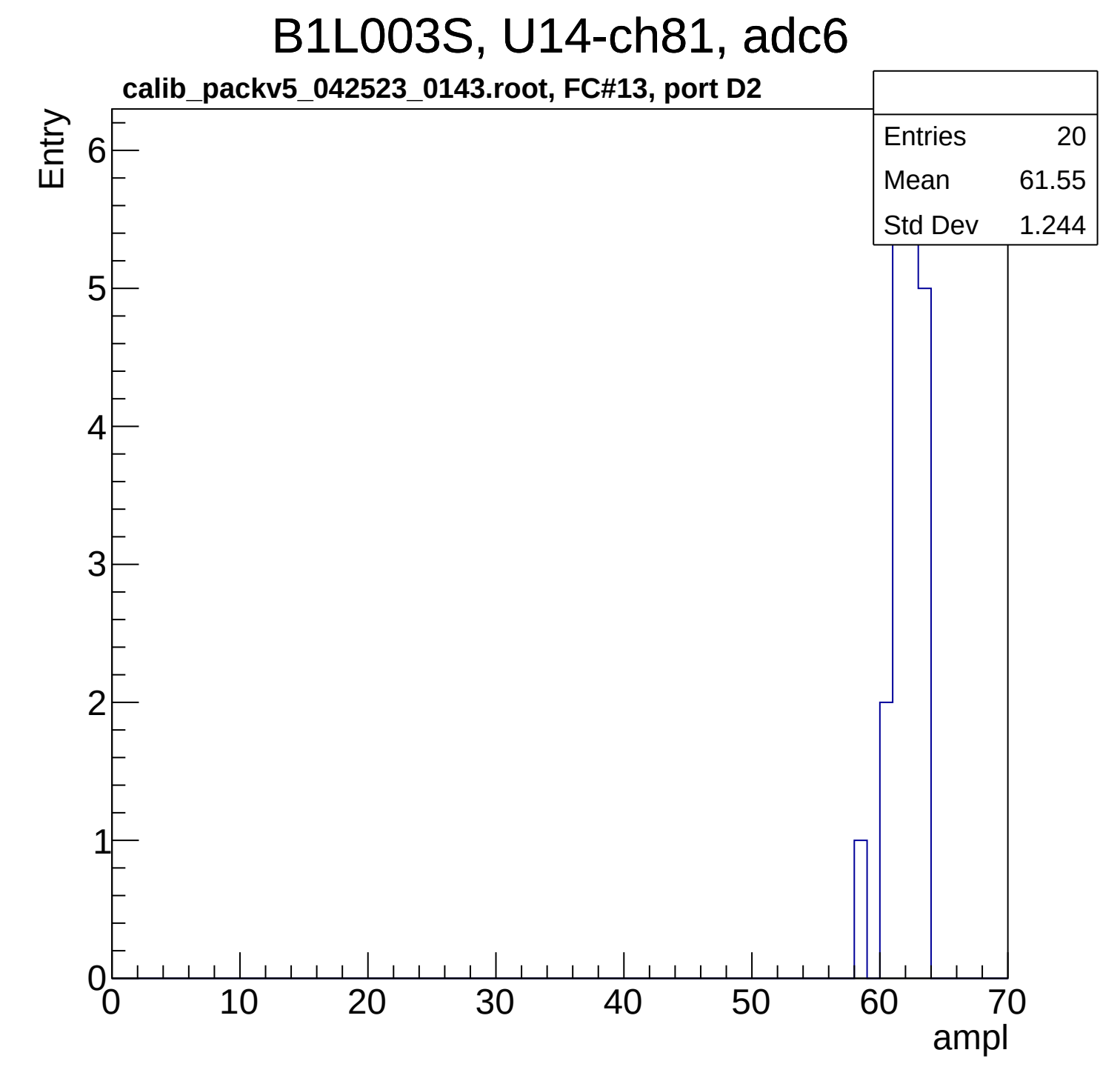

B1L003S, U14-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 34.27
Std Dev 3.083
Gaus mean : 34.7105
Gaus Width: 3.2502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

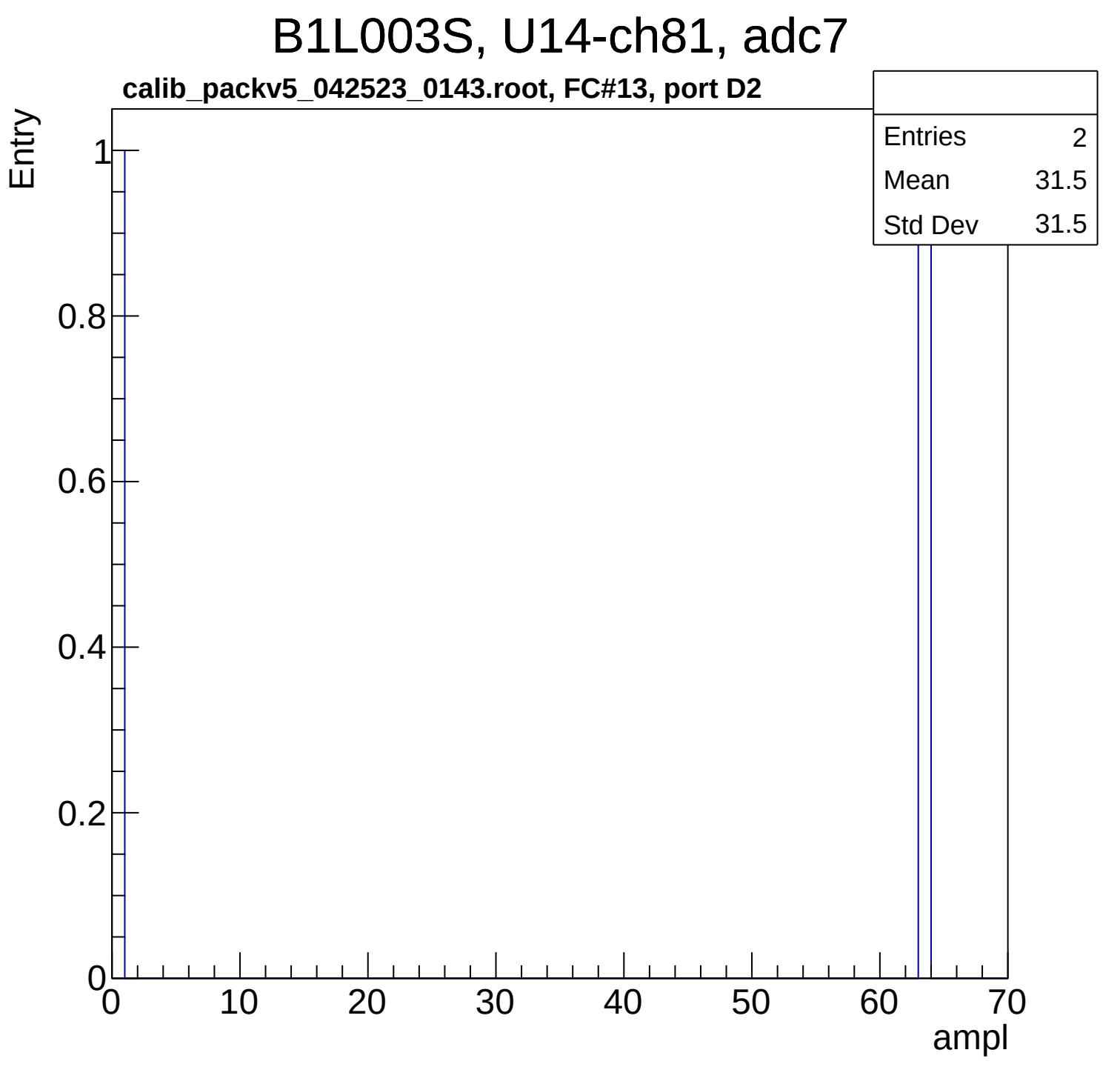

B1L003S, U14-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.13
Std Dev 3.453
Gaus mean : 41.4302
Gaus Width: 3.2302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

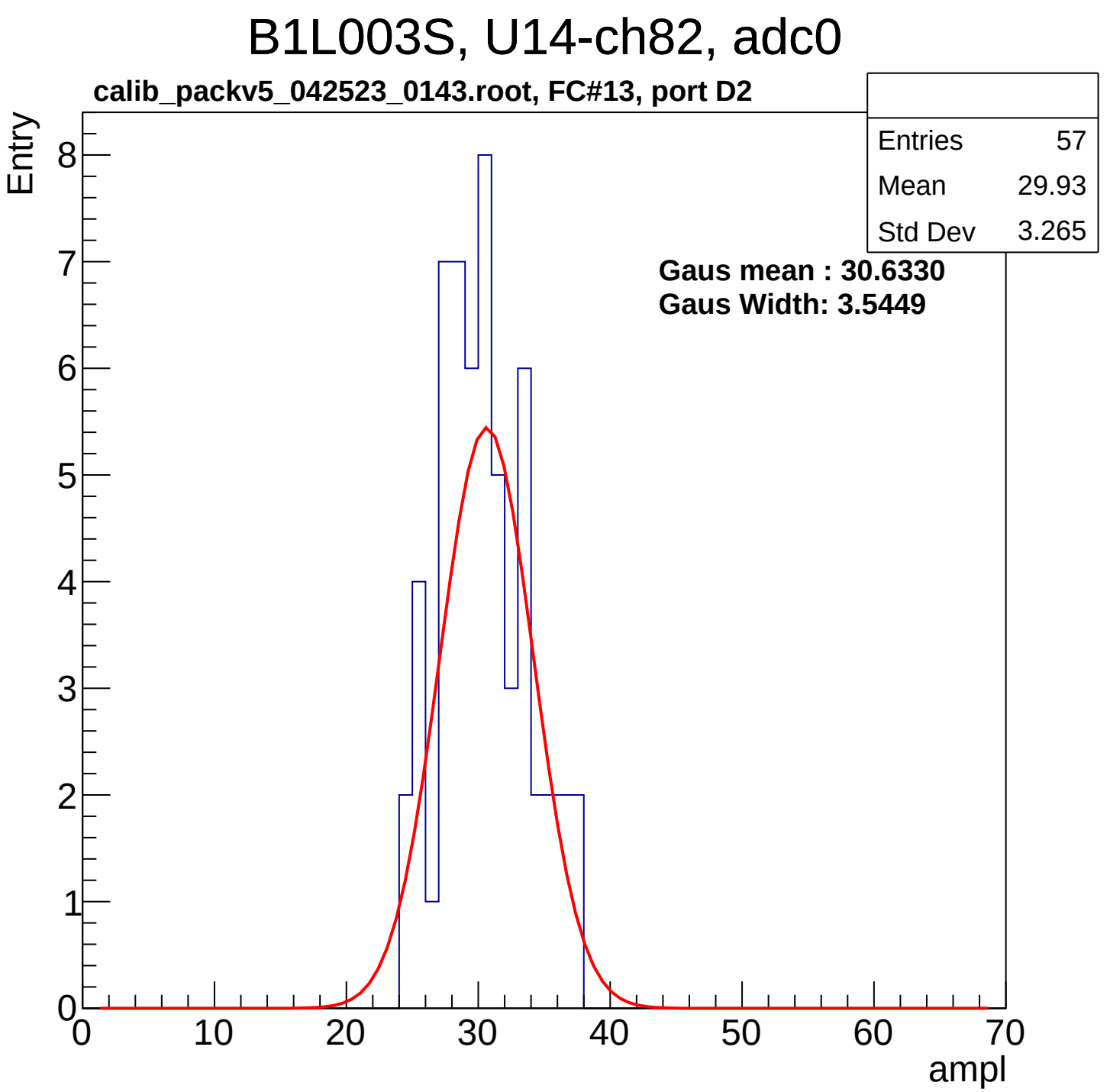

B1L003S, U14-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 47.24
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

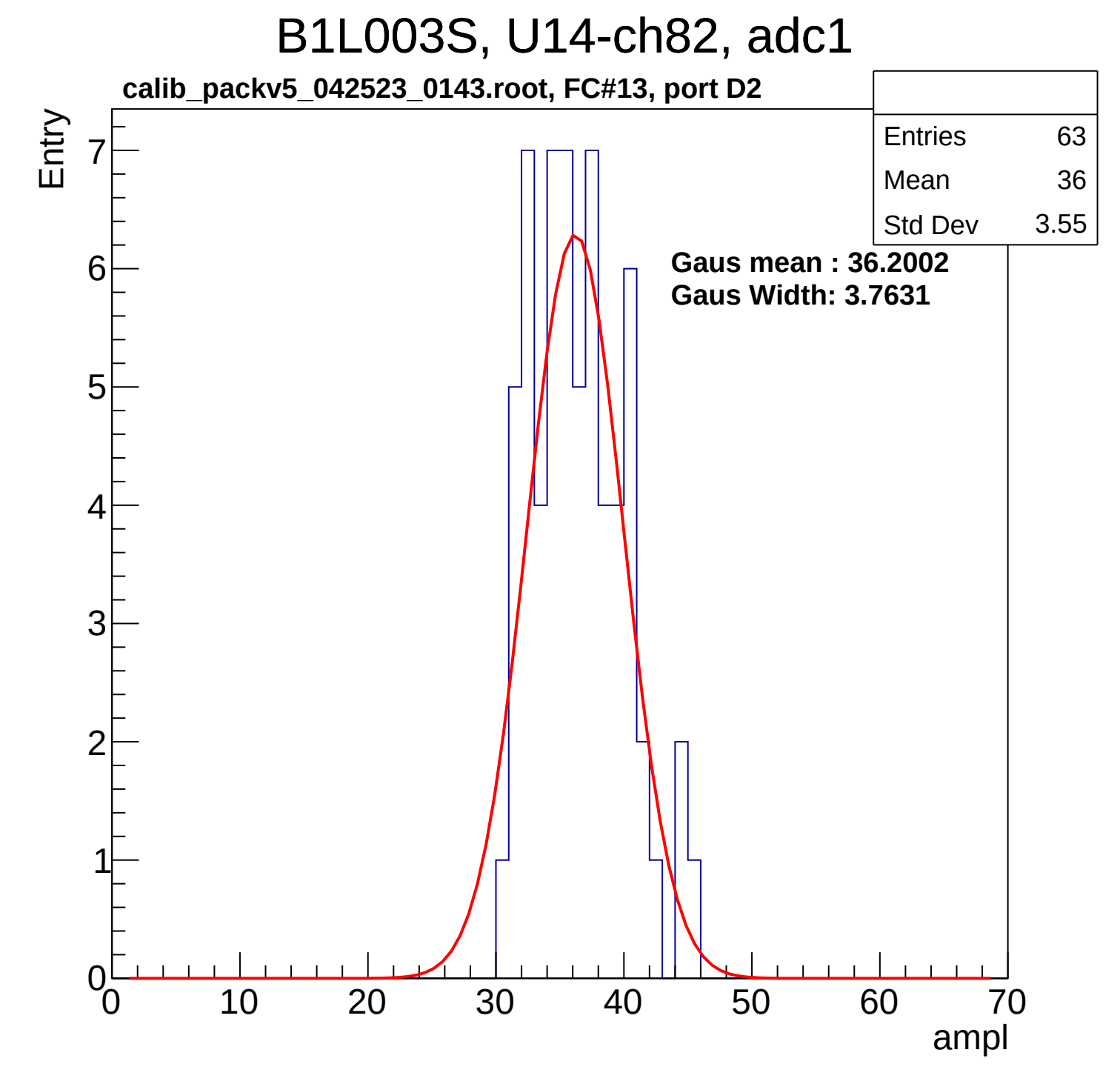

B1L003S, U14-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 54.5
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

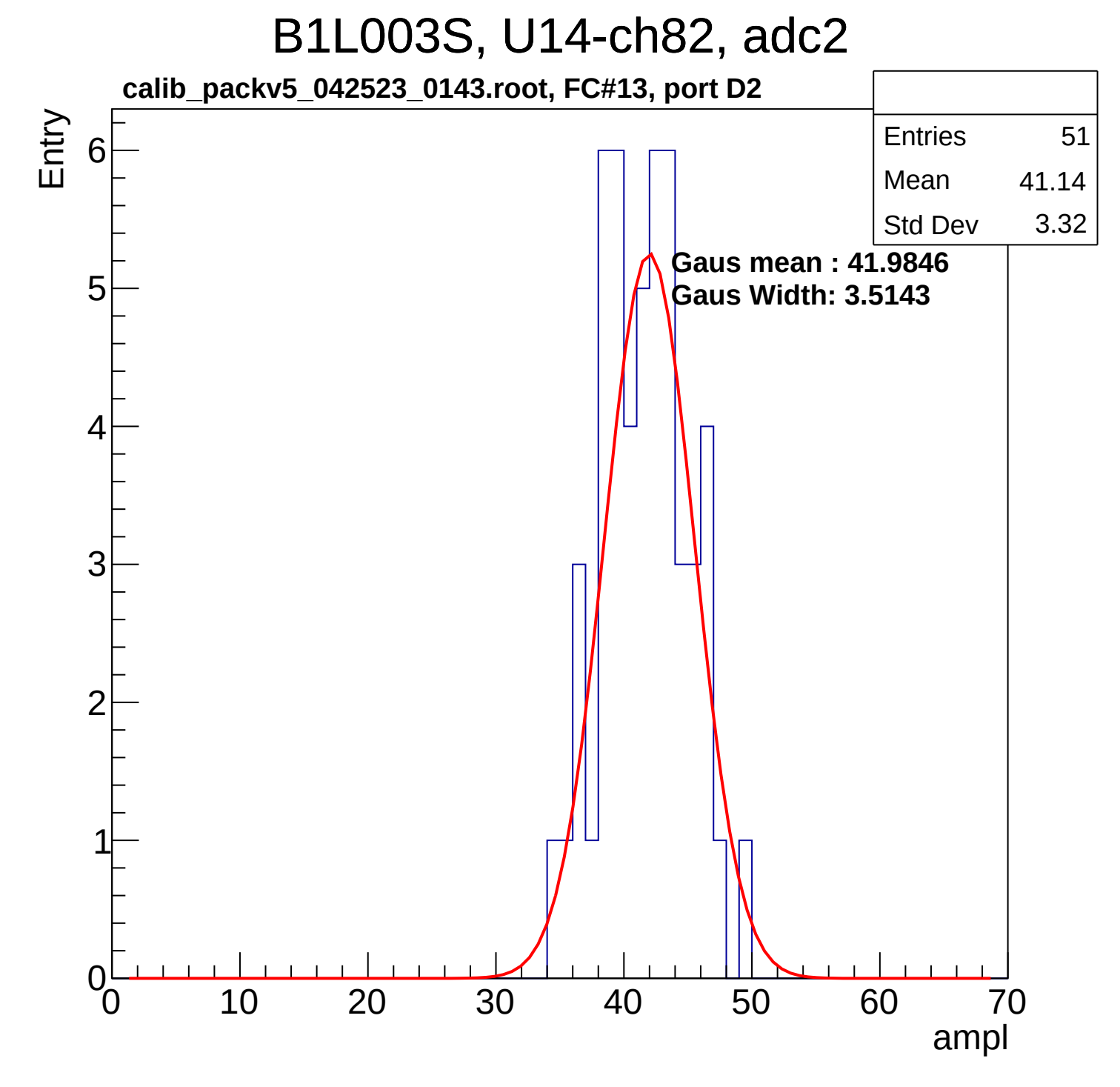

B1L003S, U14-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 57.5
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

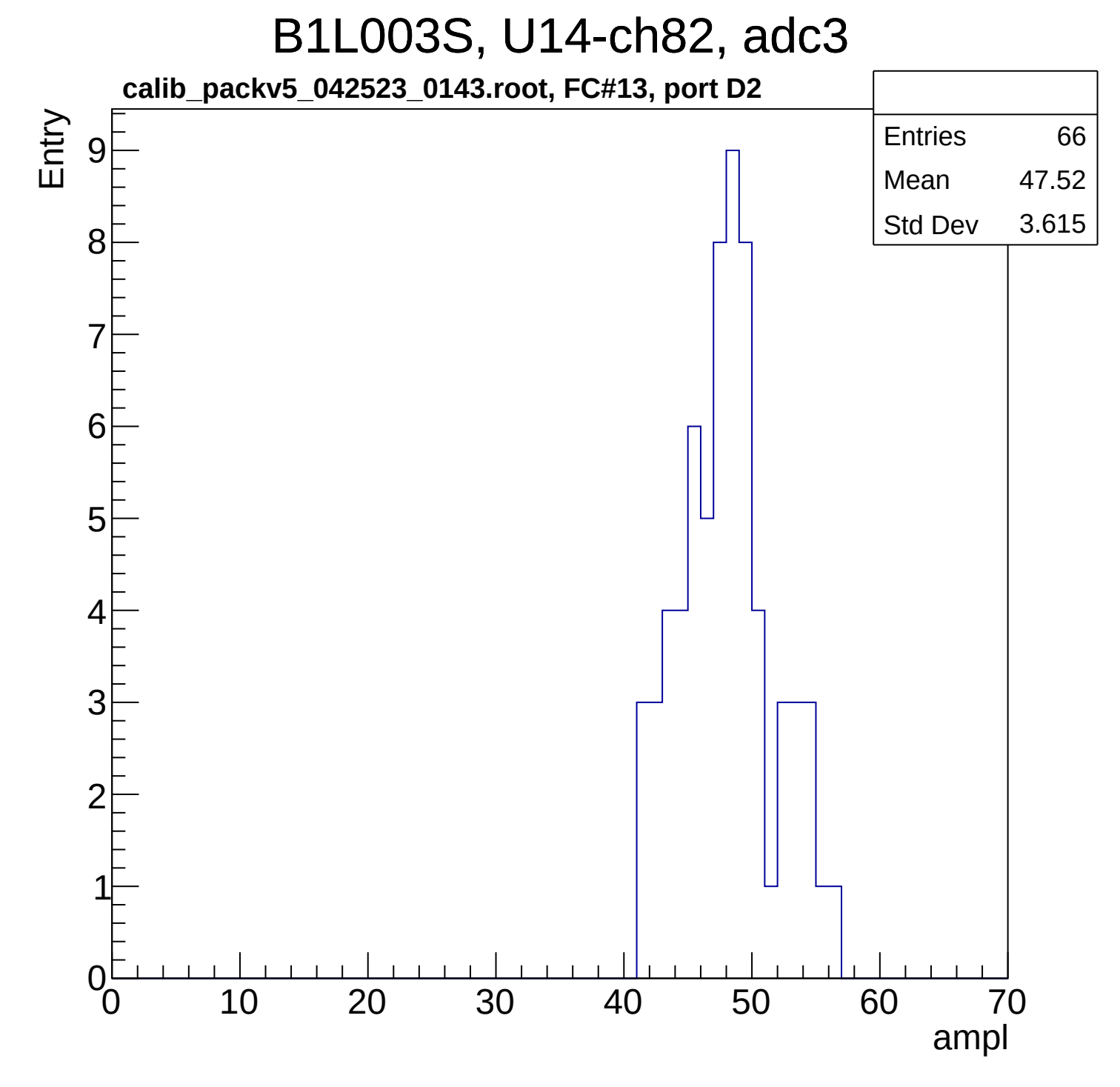

B1L003S, U14-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 61.55
Std Dev 1.244
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

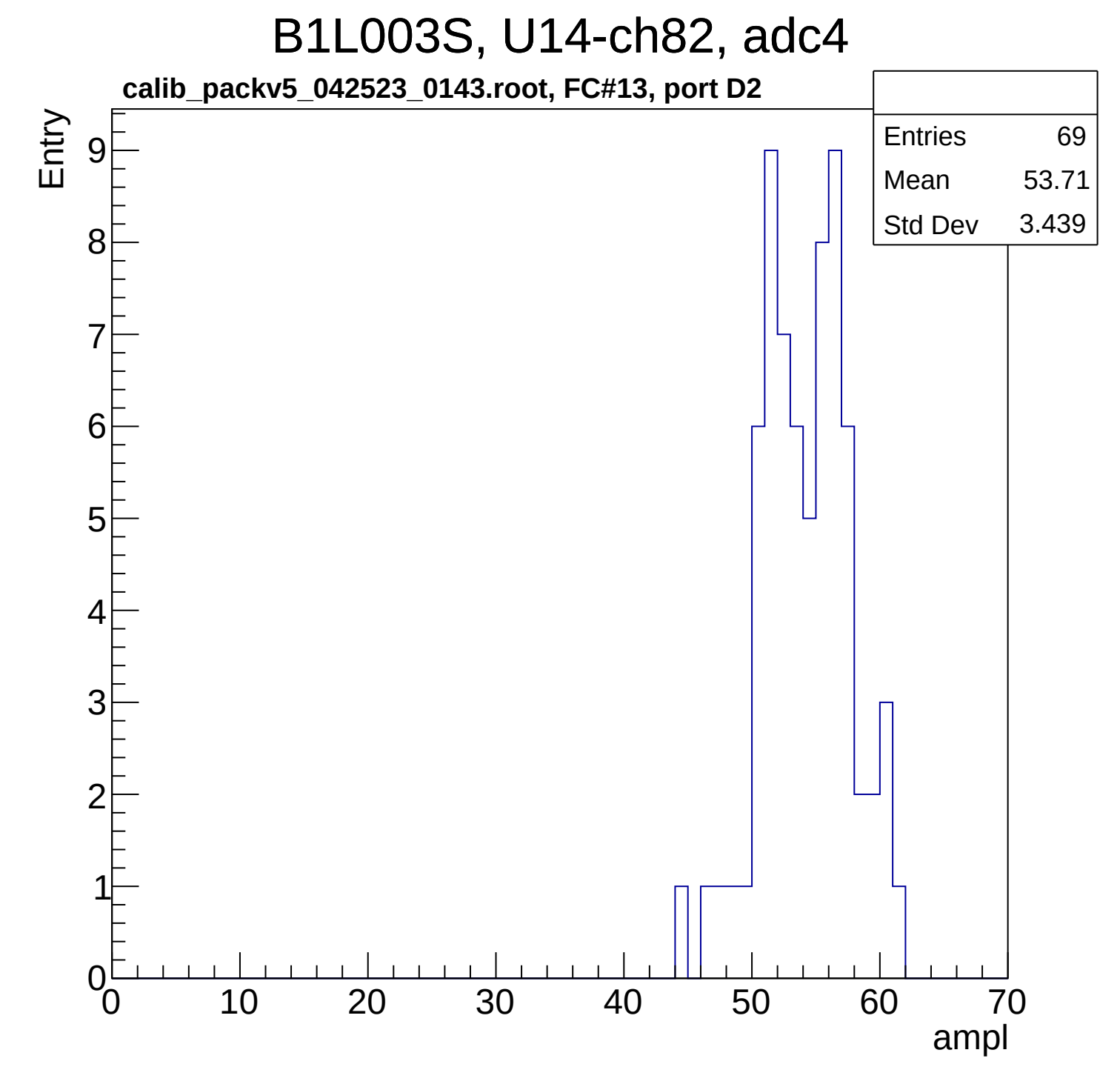

B1L003S, U14-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

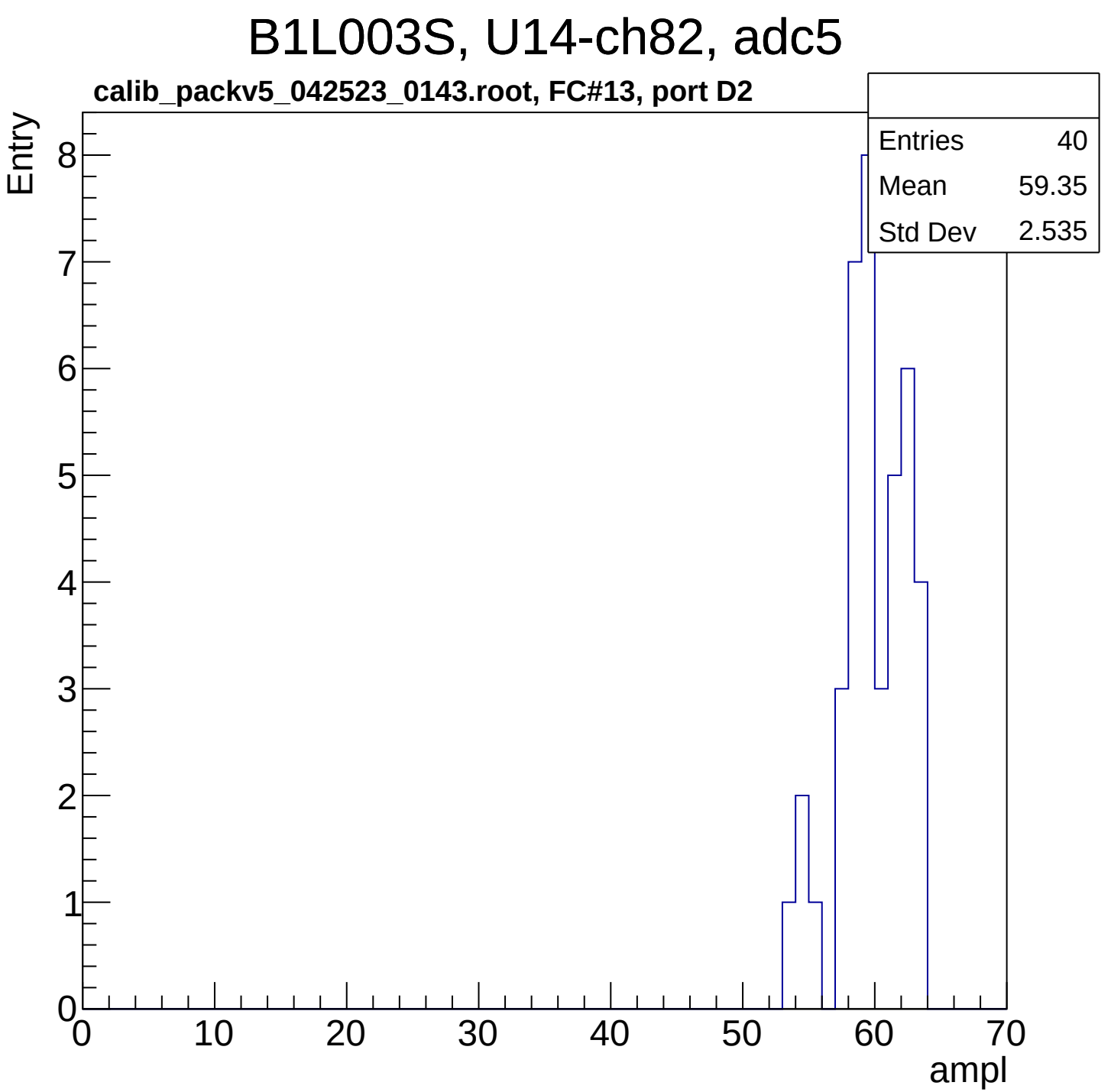

B1L003S, U14-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.93
Std Dev 3.265
Gaus mean : 30.6330
Gaus Width: 3.5449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

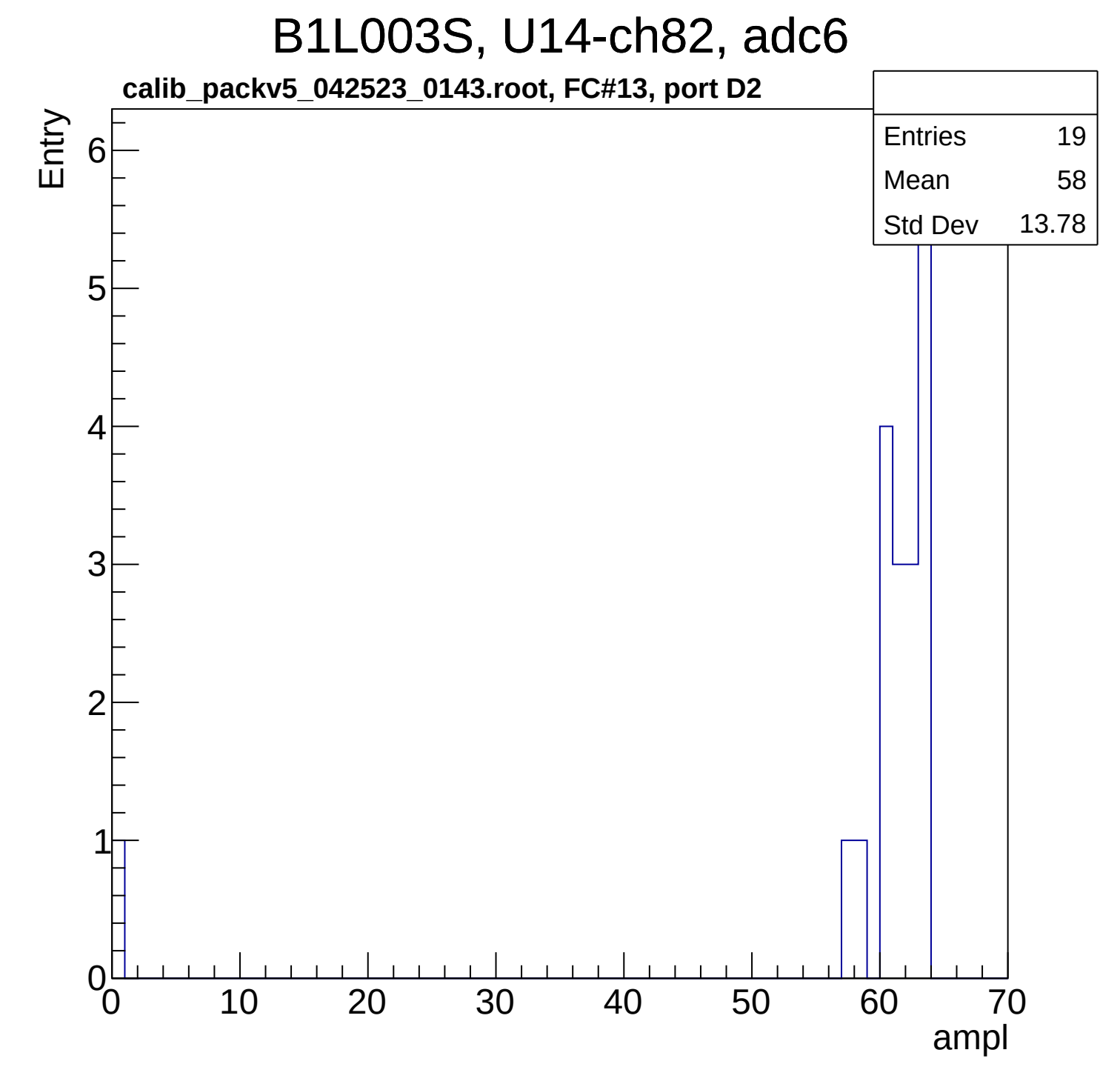

B1L003S, U14-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36
Std Dev 3.55
Gaus mean : 36.2002
Gaus Width: 3.7631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

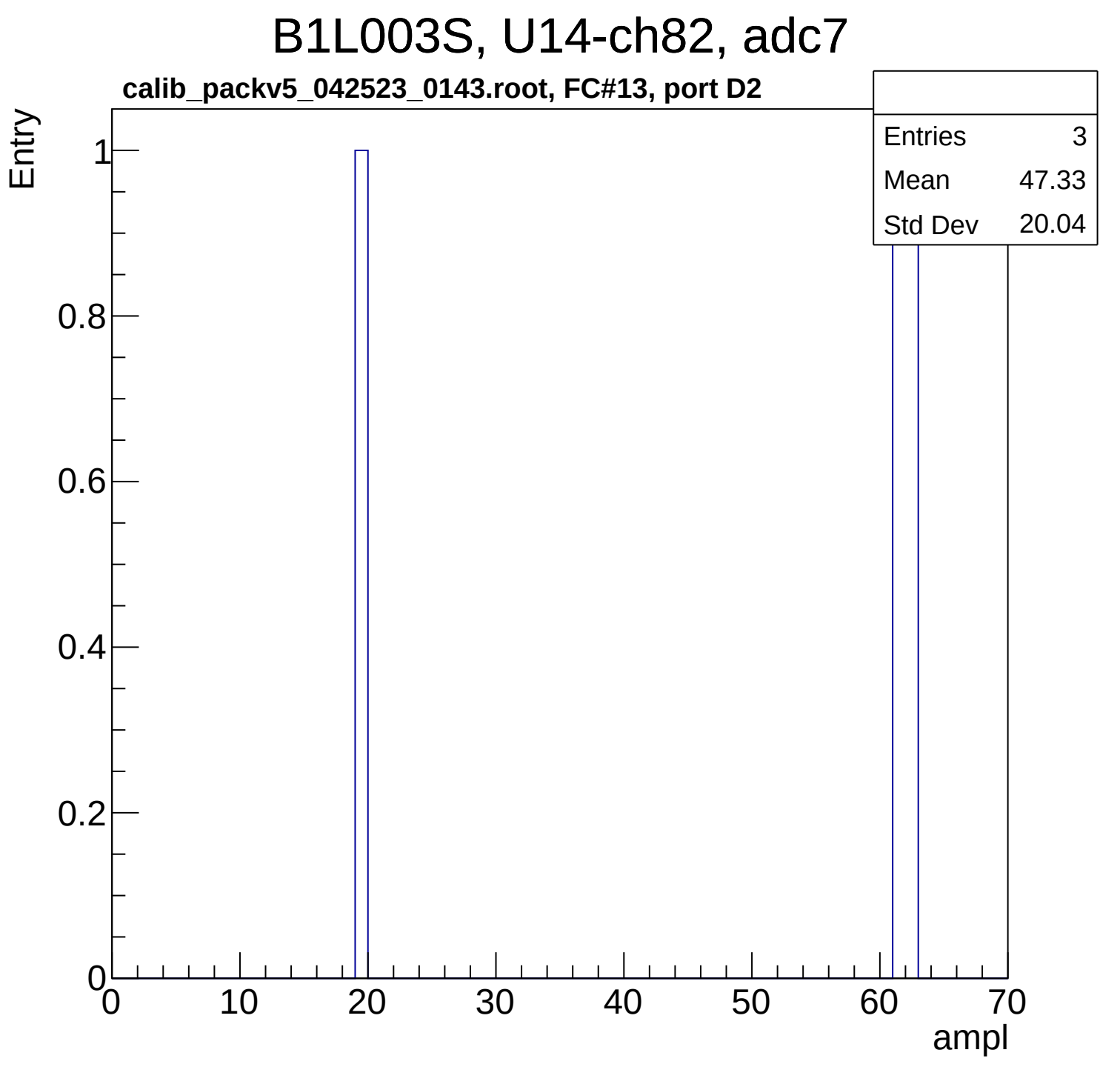

B1L003S, U14-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.14
Std Dev 3.32
Gaus mean : 41.9846
Gaus Width: 3.5143
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

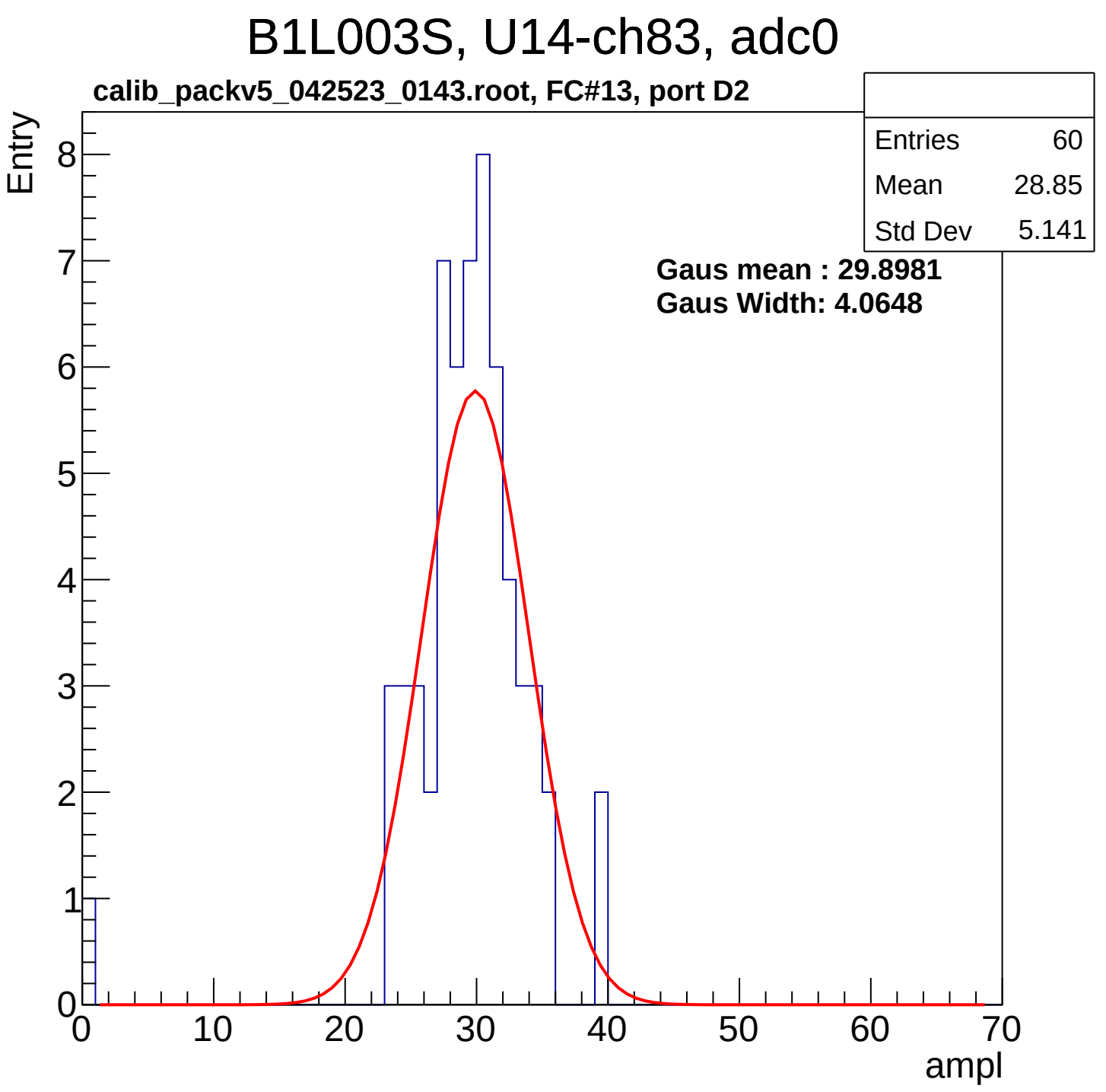

B1L003S, U14-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 47.52
Std Dev 3.615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

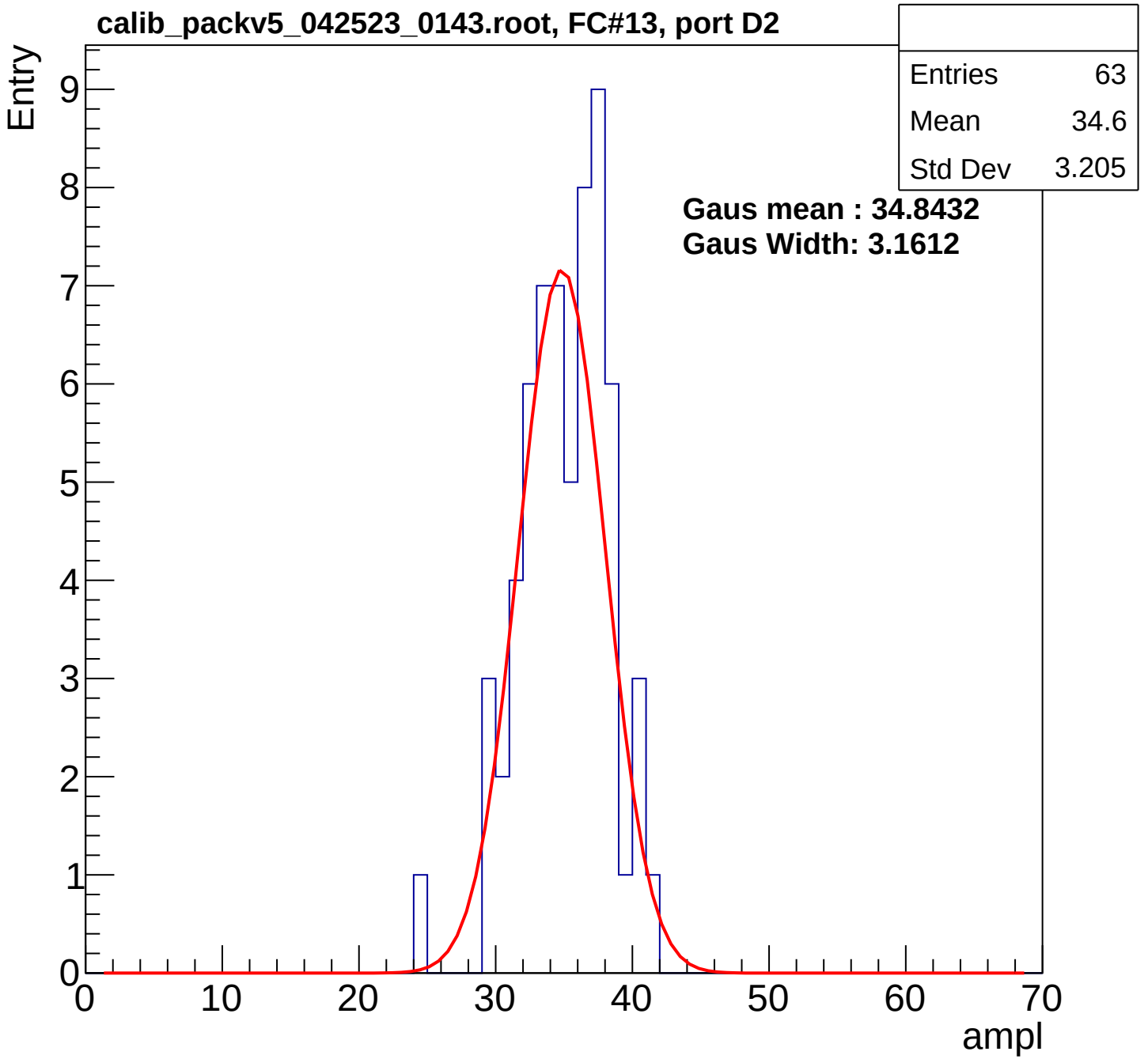

B1L003S, U14-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 53.71
Std Dev 3.439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

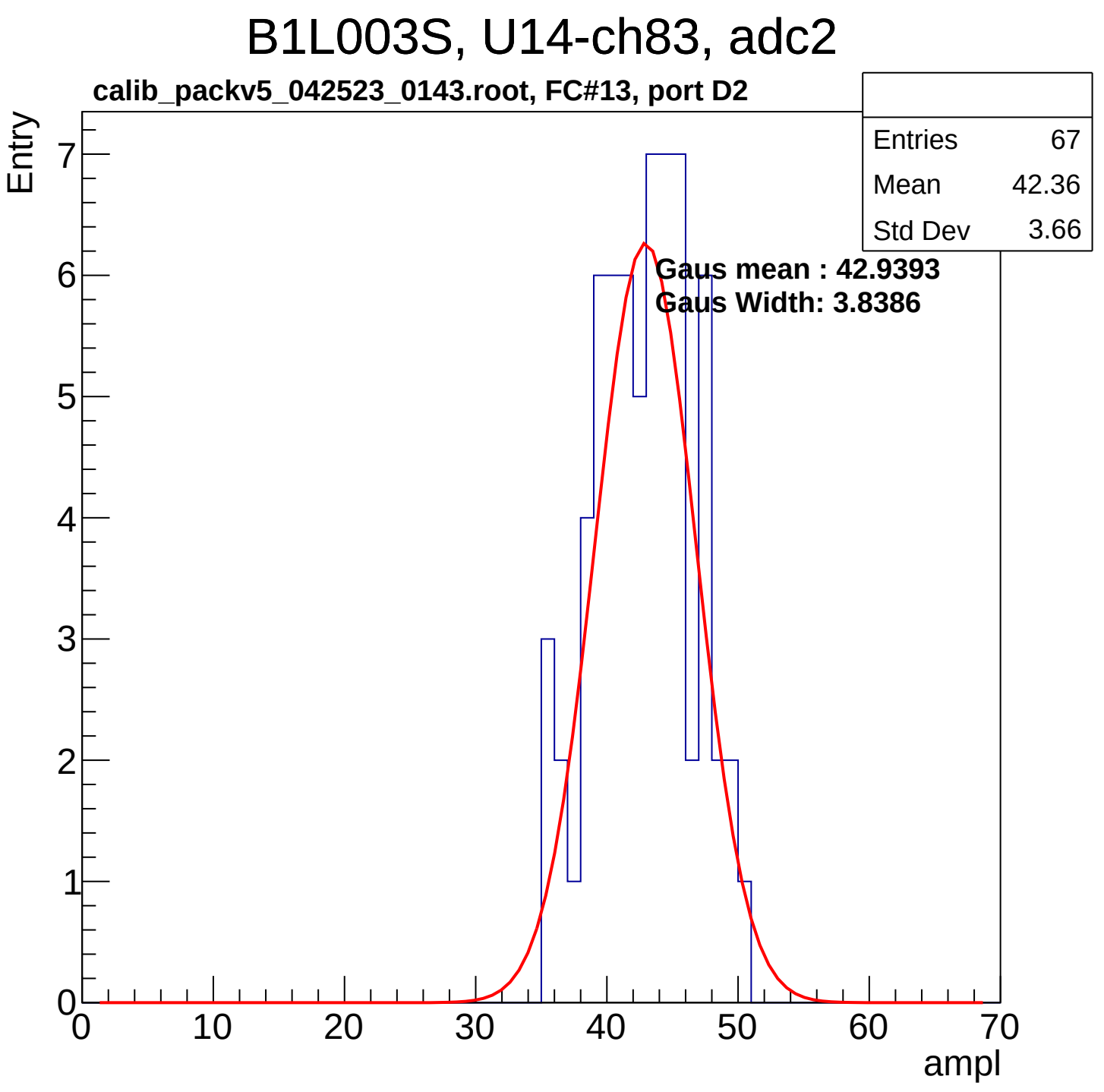

B1L003S, U14-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.35
Std Dev 2.535
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

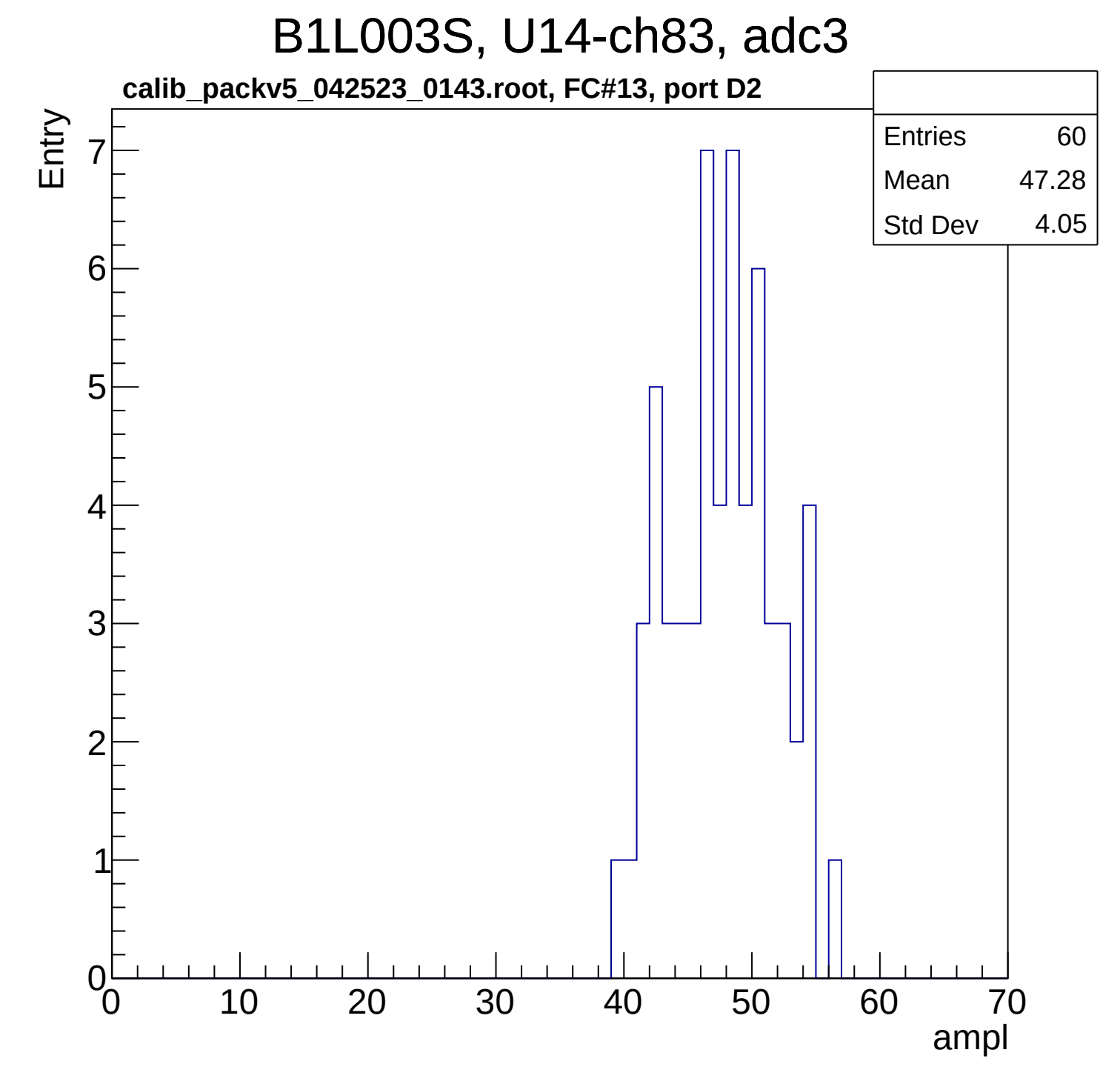

B1L003S, U14-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58
Std Dev 13.78
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

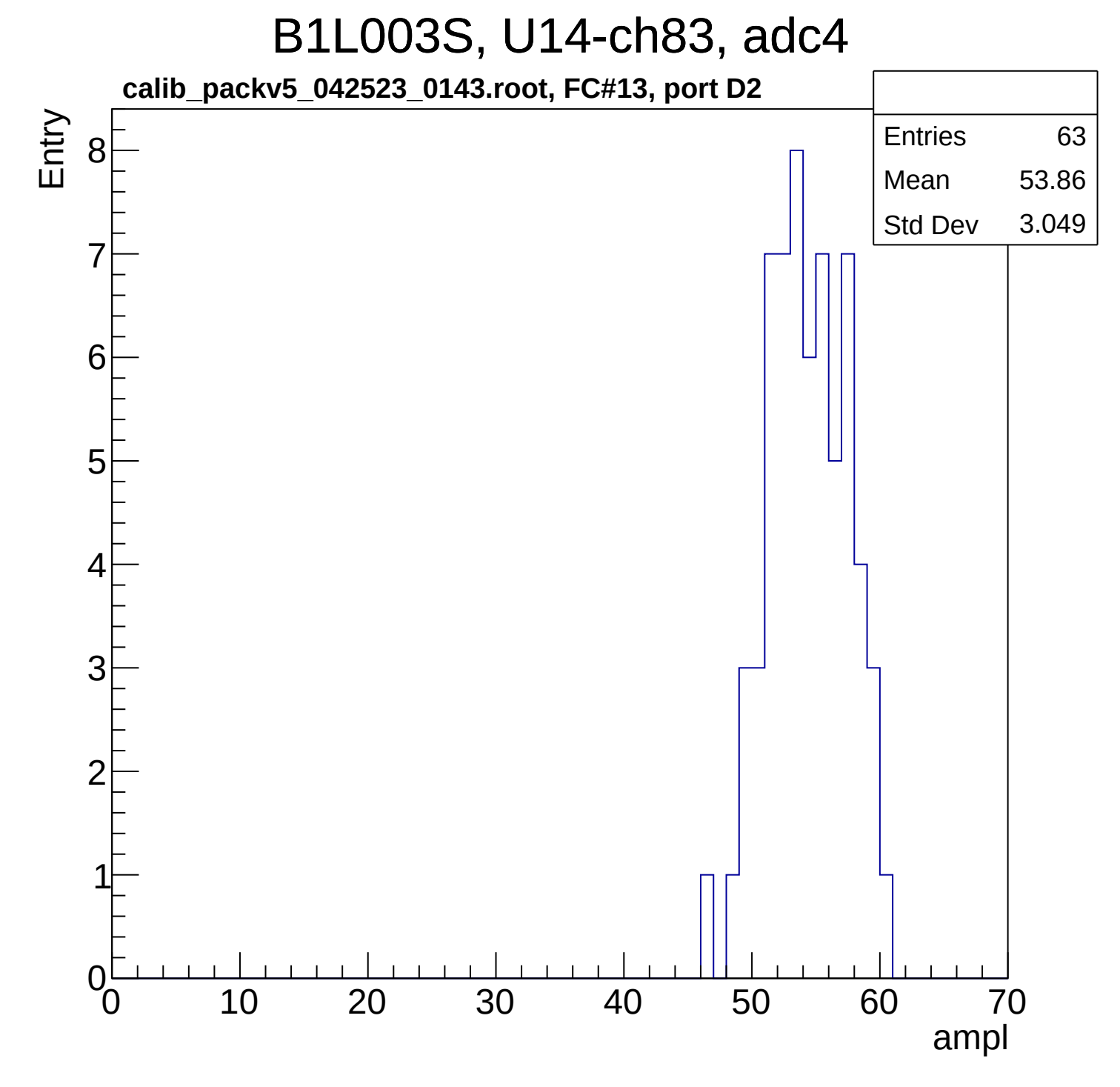

B1L003S, U14-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 47.33
Std Dev 20.04
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

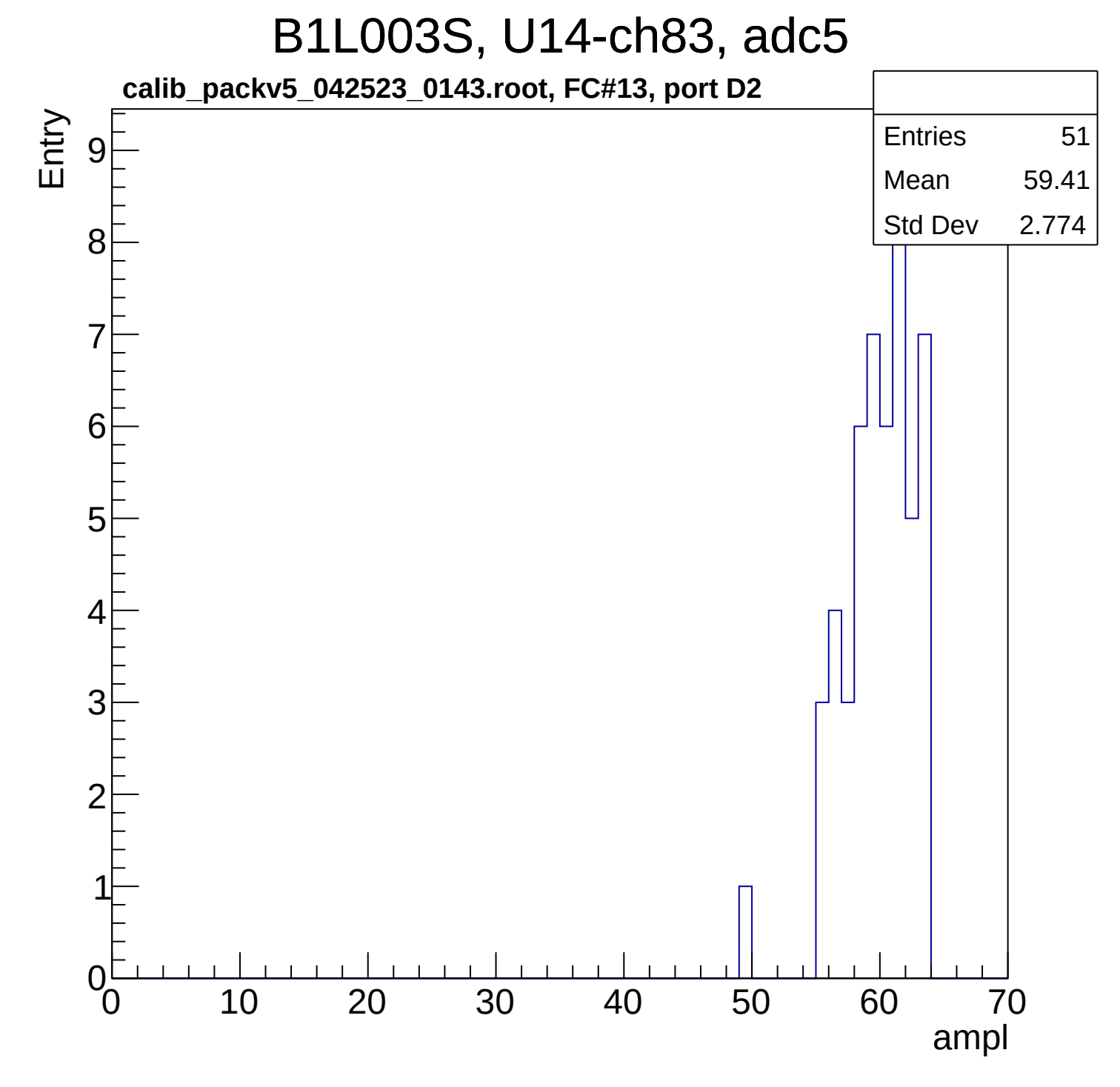

B1L003S, U14-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.85
Std Dev 5.141
Gaus mean : 29.8981
Gaus Width: 4.0648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

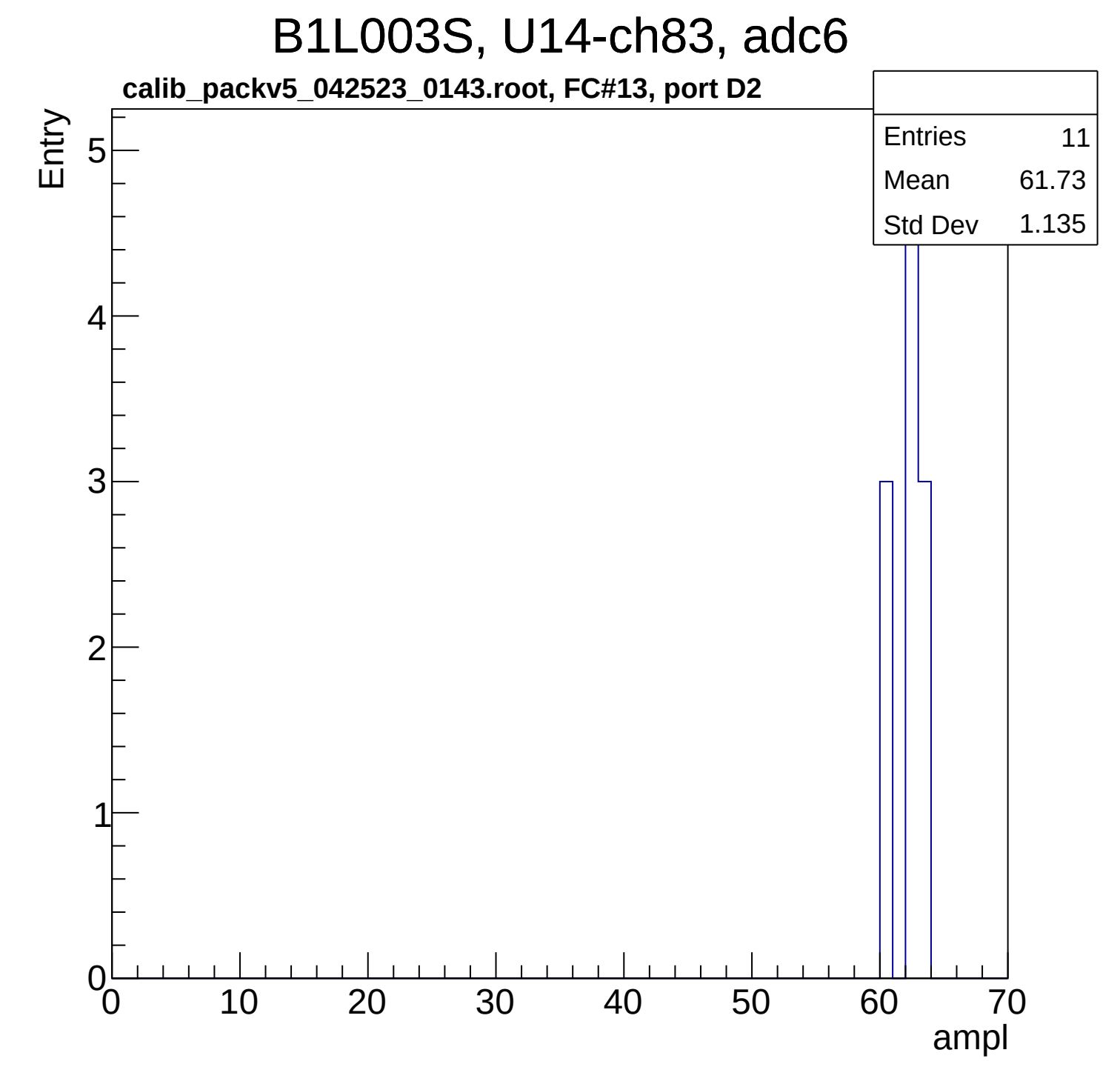

B1L003S, U14-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 34.6
Std Dev 3.205
Gaus mean : 34.8432
Gaus Width: 3.1612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

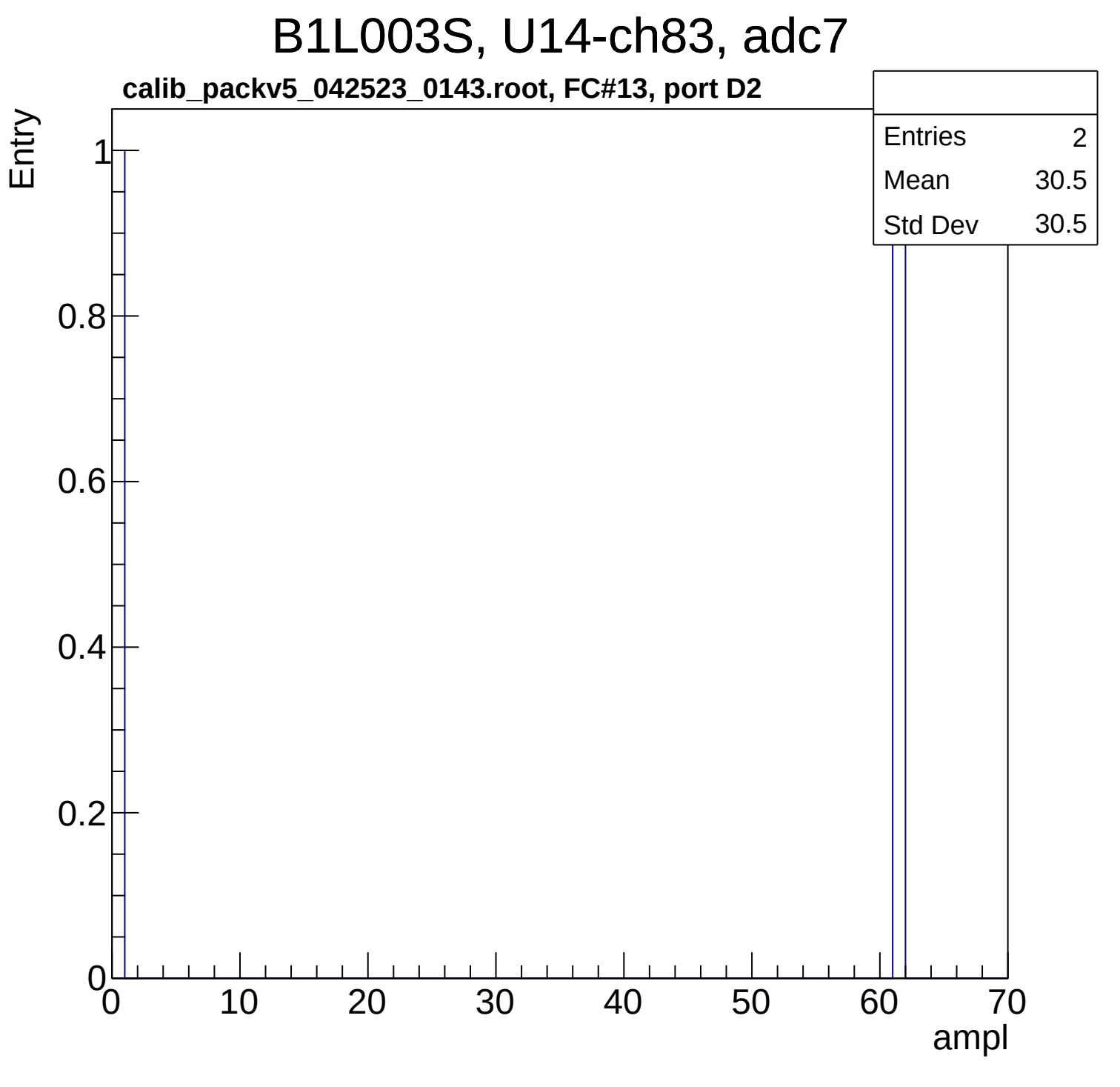

B1L003S, U14-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.36
Std Dev 3.66
Gaus mean : 42.9393
Gaus Width: 3.8386
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

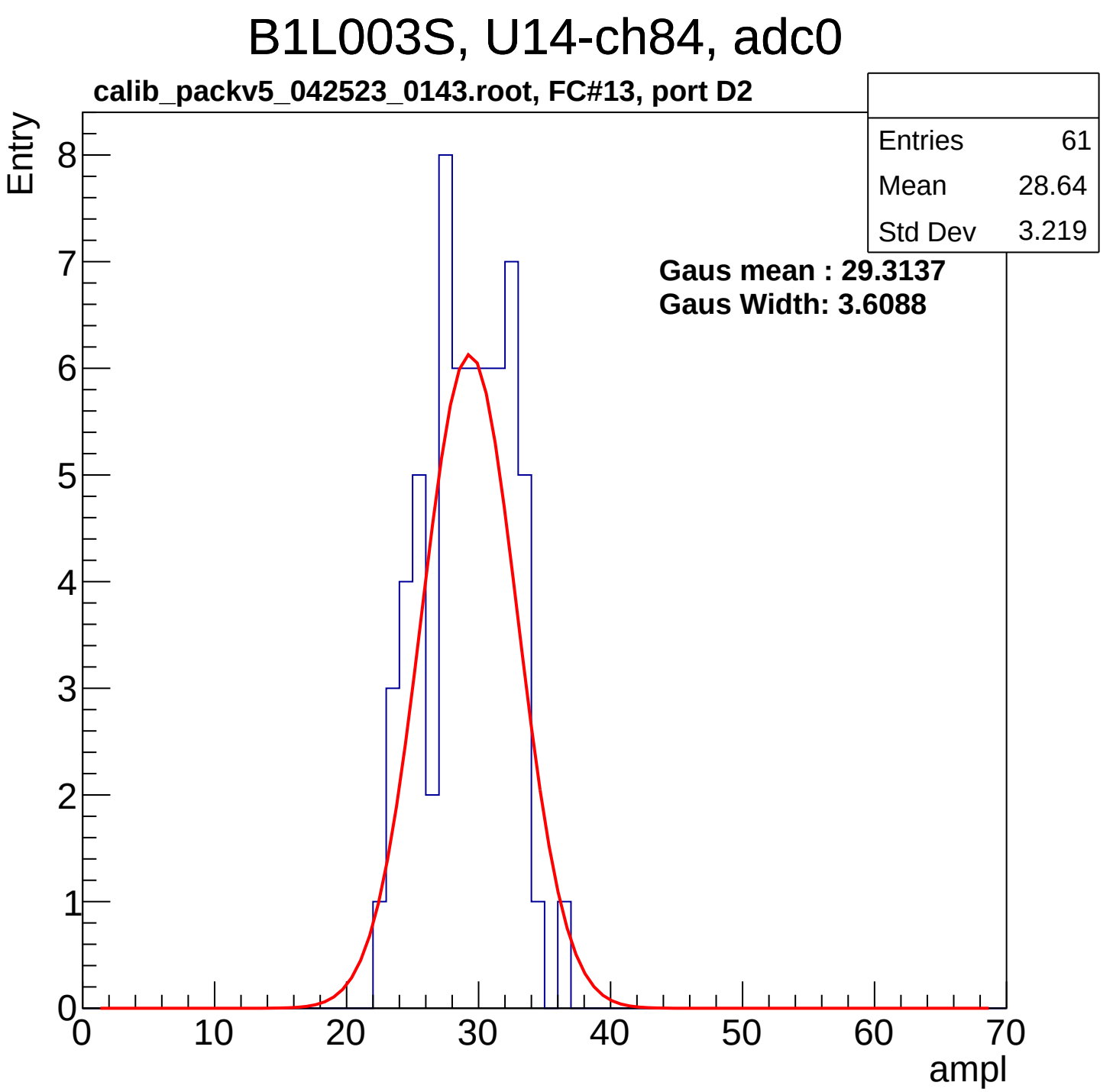

B1L003S, U14-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 47.28
Std Dev 4.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

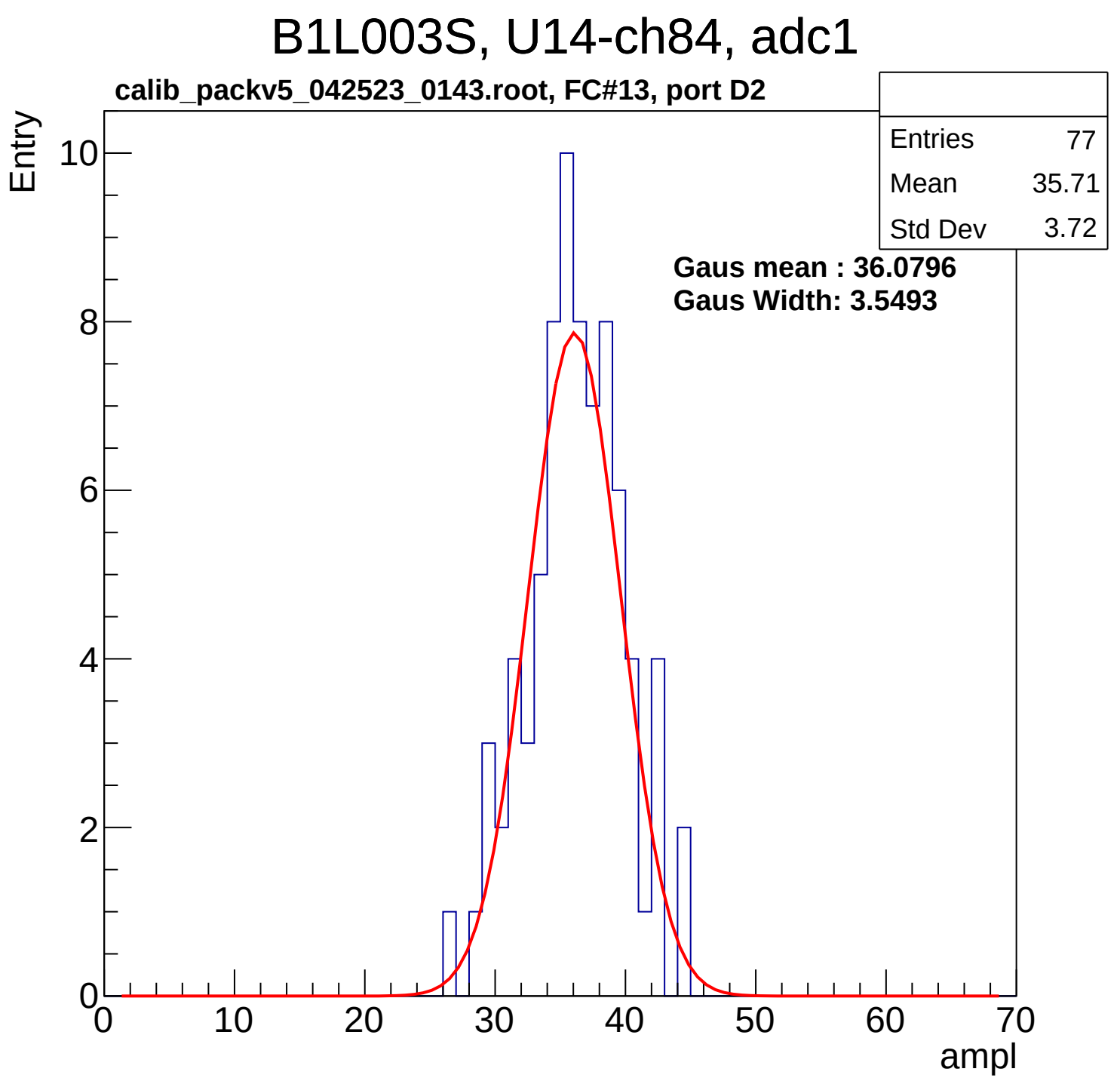

B1L003S, U14-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 53.86
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

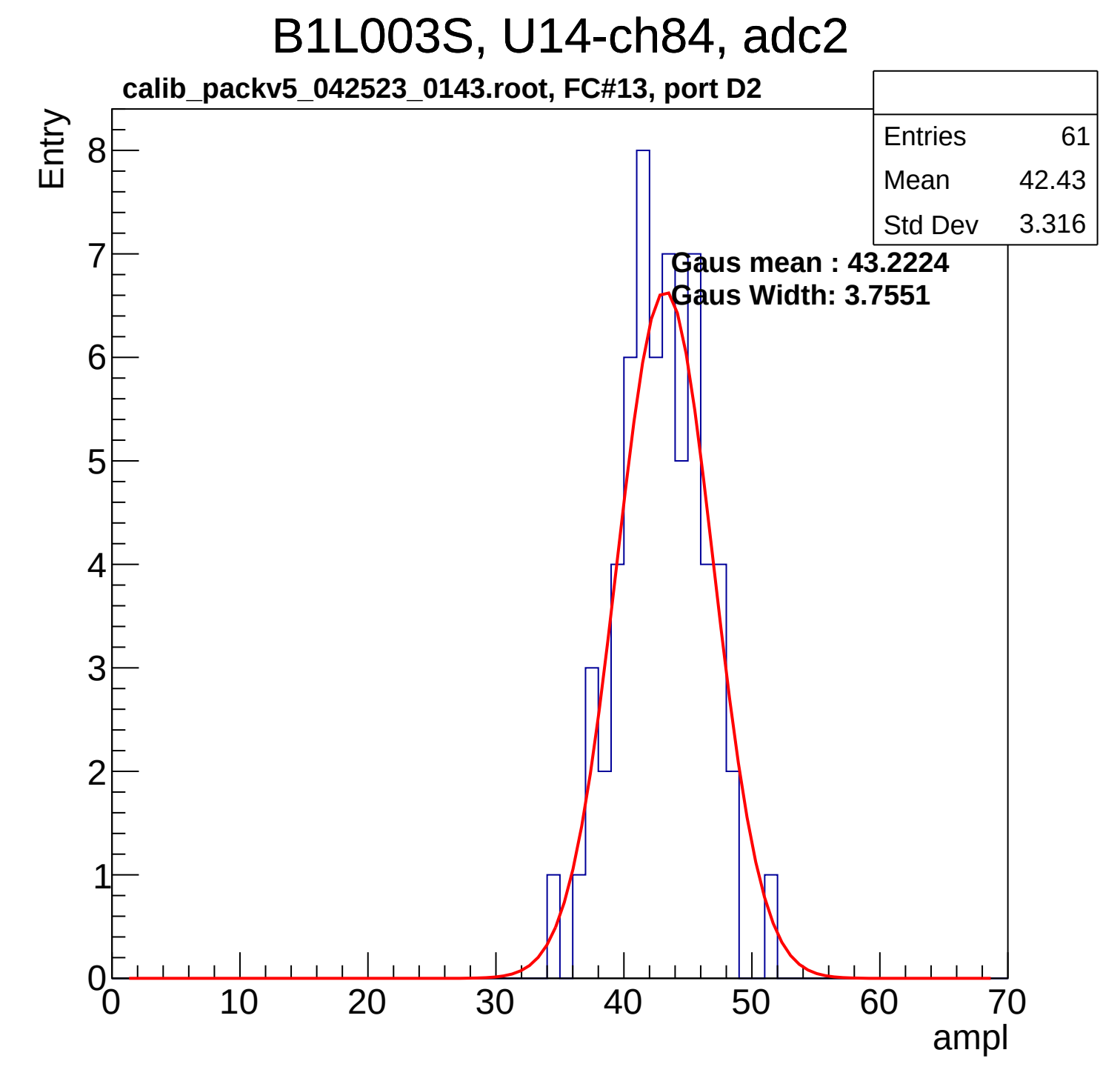

B1L003S, U14-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.41
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

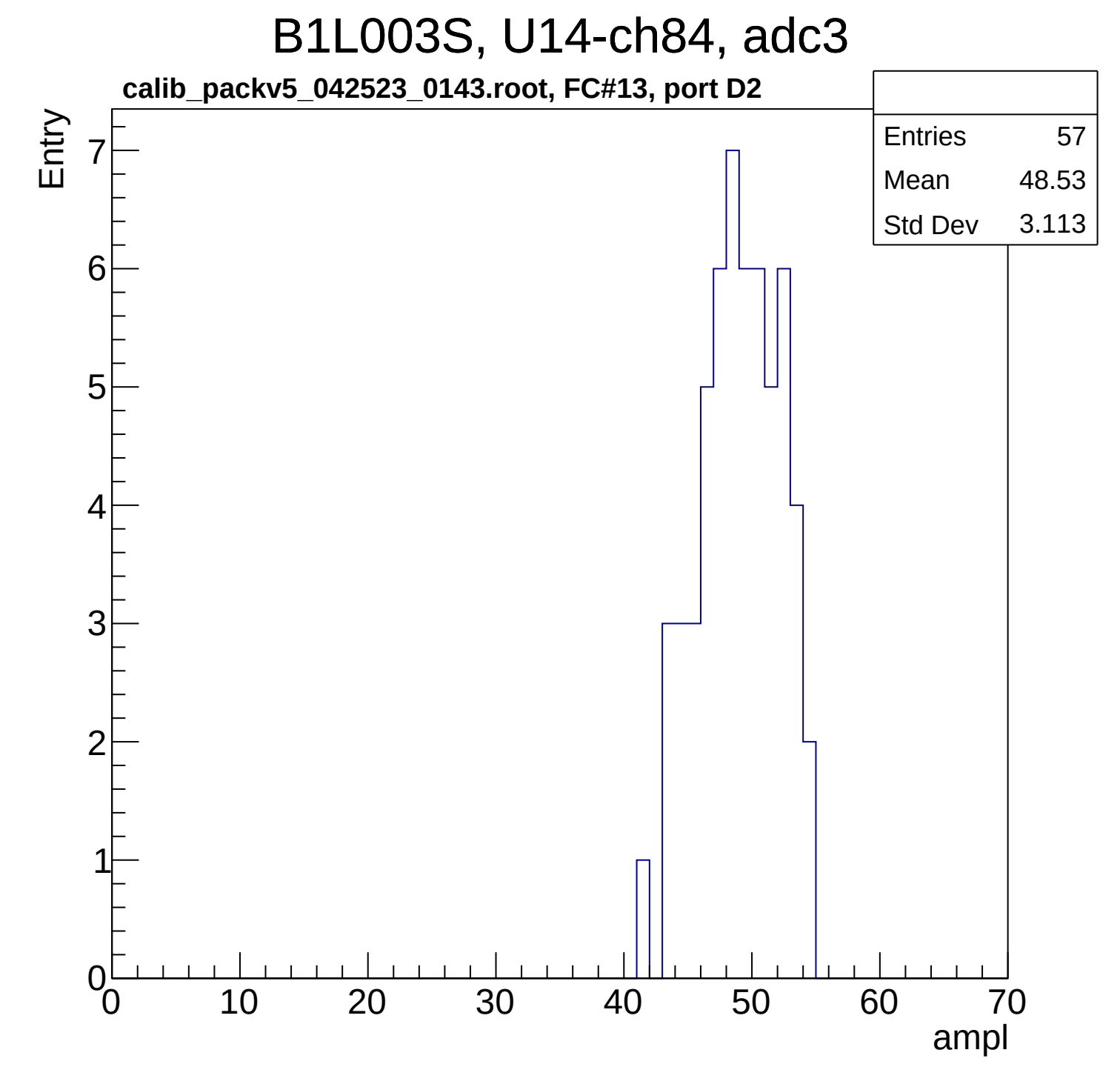

B1L003S, U14-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.73
Std Dev 1.135
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

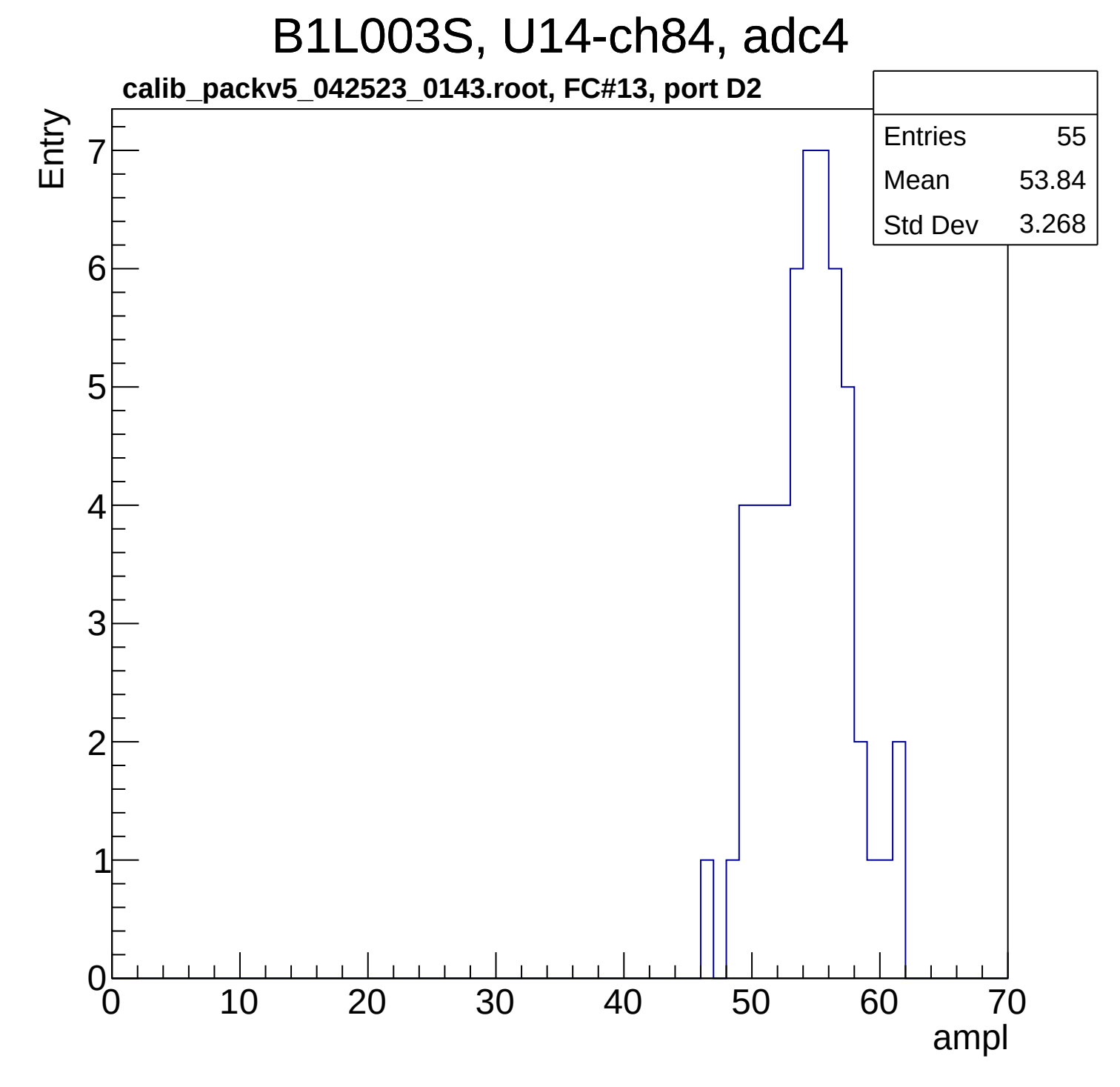

B1L003S, U14-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

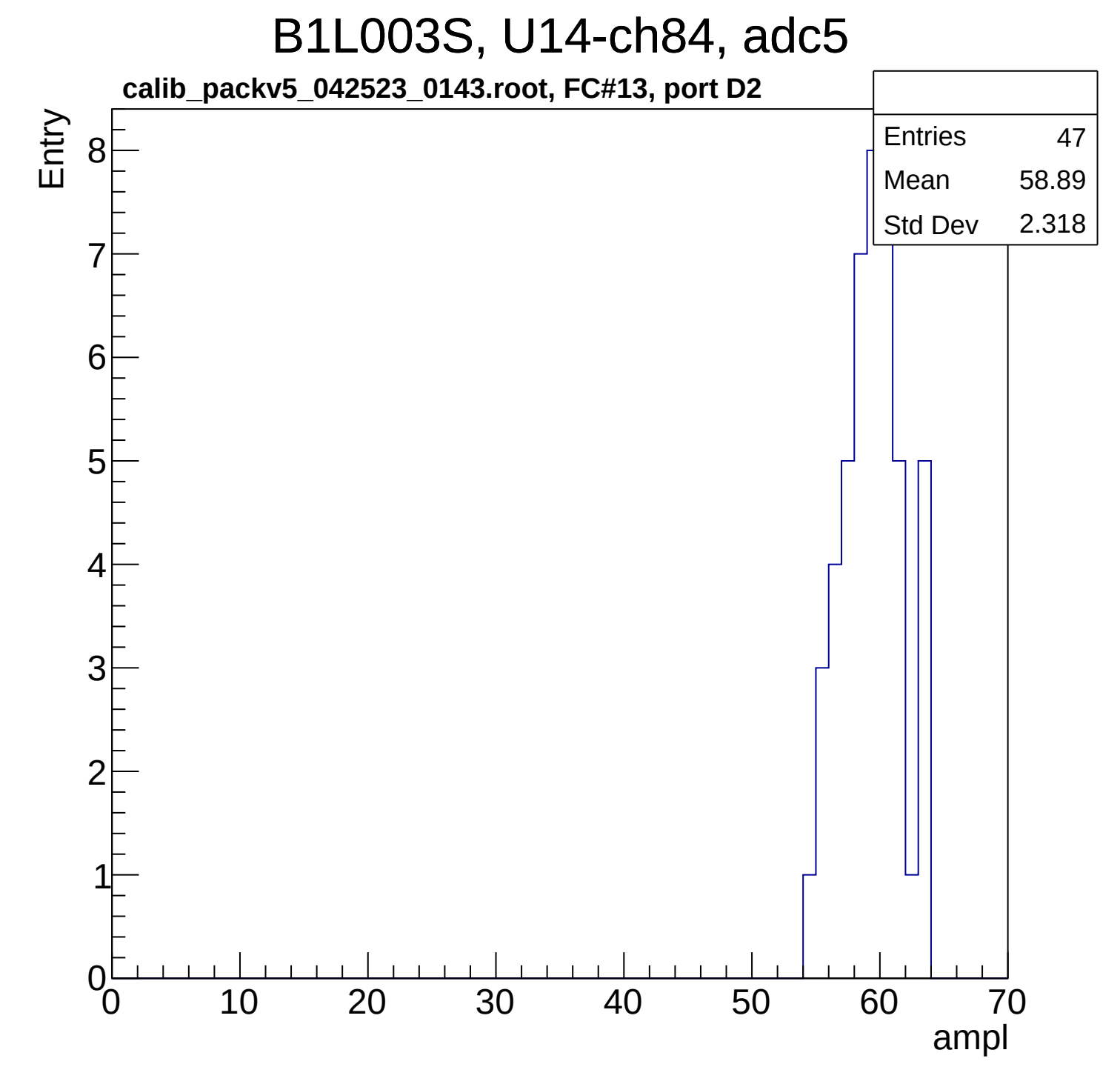

B1L003S, U14-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 28.64
Std Dev 3.219
Gaus mean : 29.3137
Gaus Width: 3.6088
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

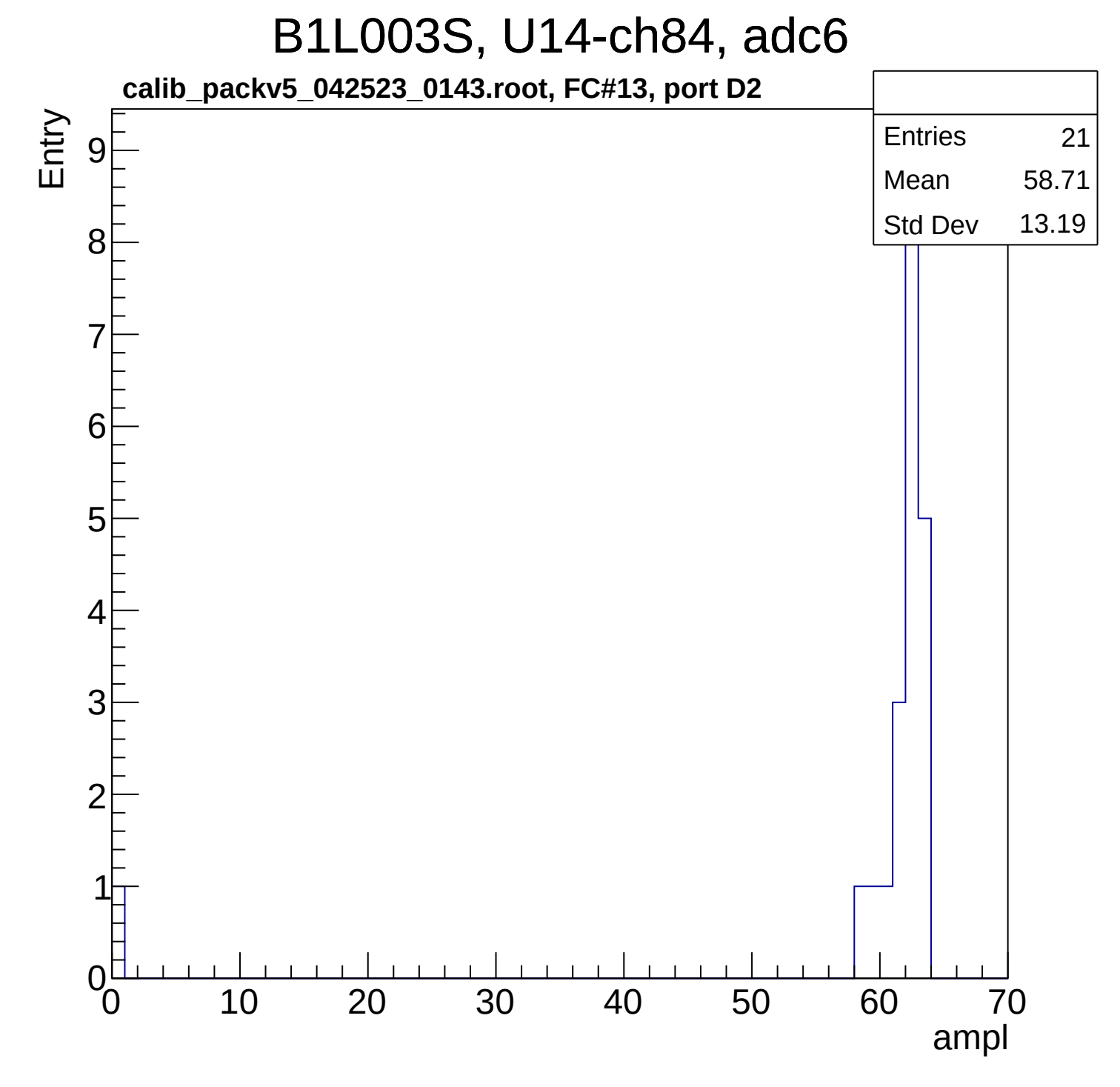

B1L003S, U14-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.71
Std Dev 3.72
Gaus mean : 36.0796
Gaus Width: 3.5493
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

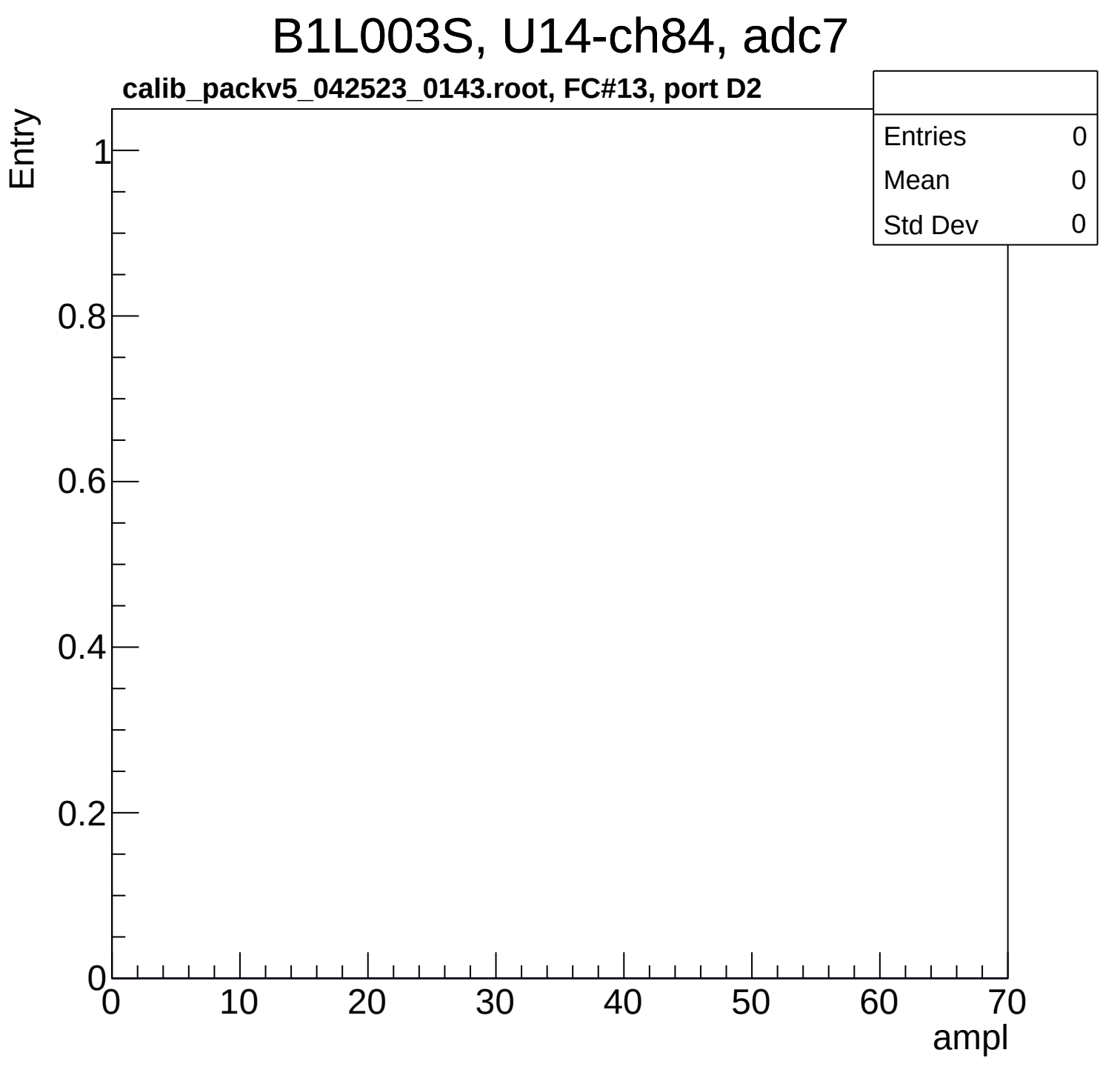

B1L003S, U14-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.43
Std Dev 3.316
Gaus mean : 43.2224
Gaus Width: 3.7551
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

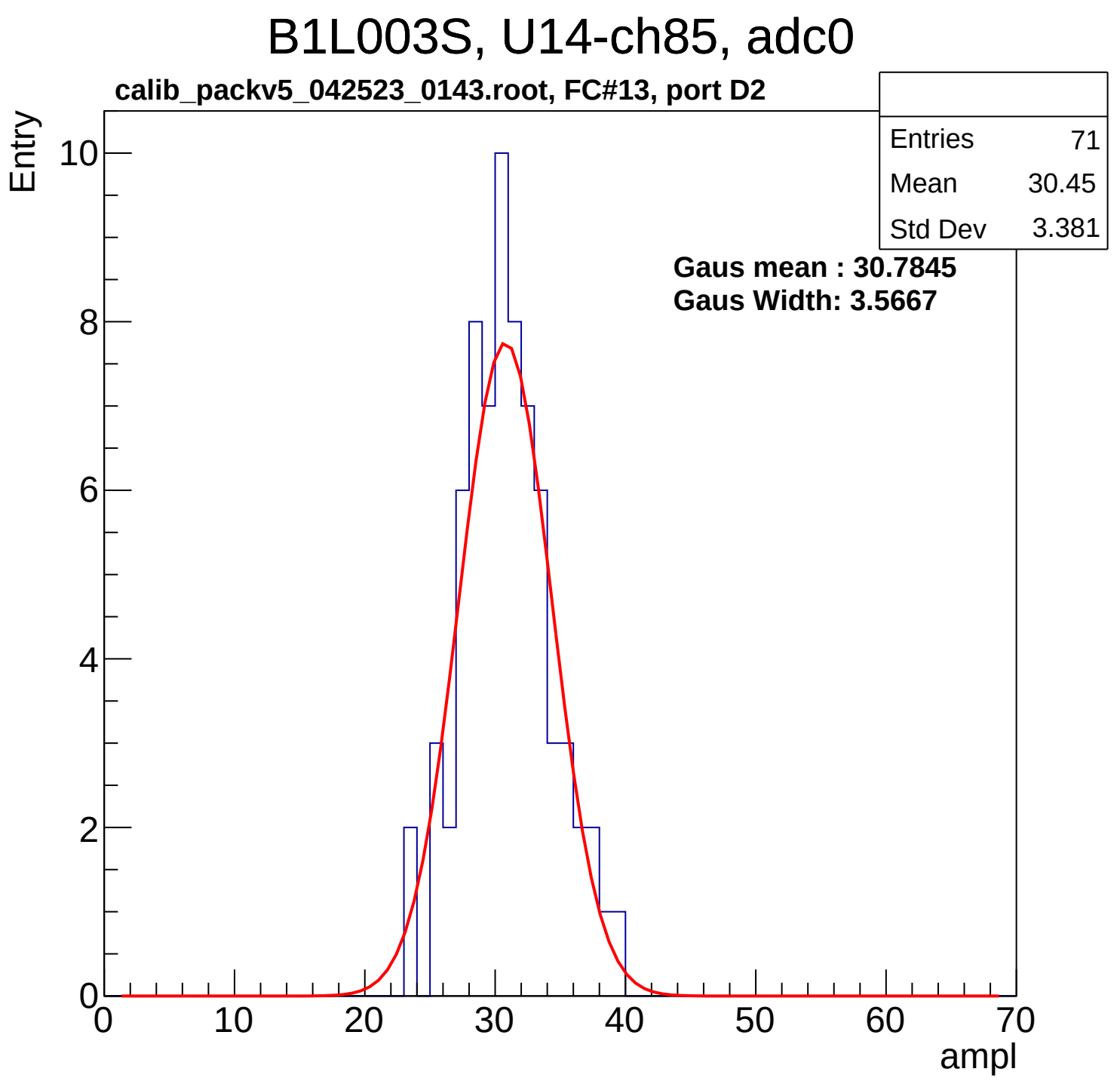

B1L003S, U14-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.53
Std Dev 3.113
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

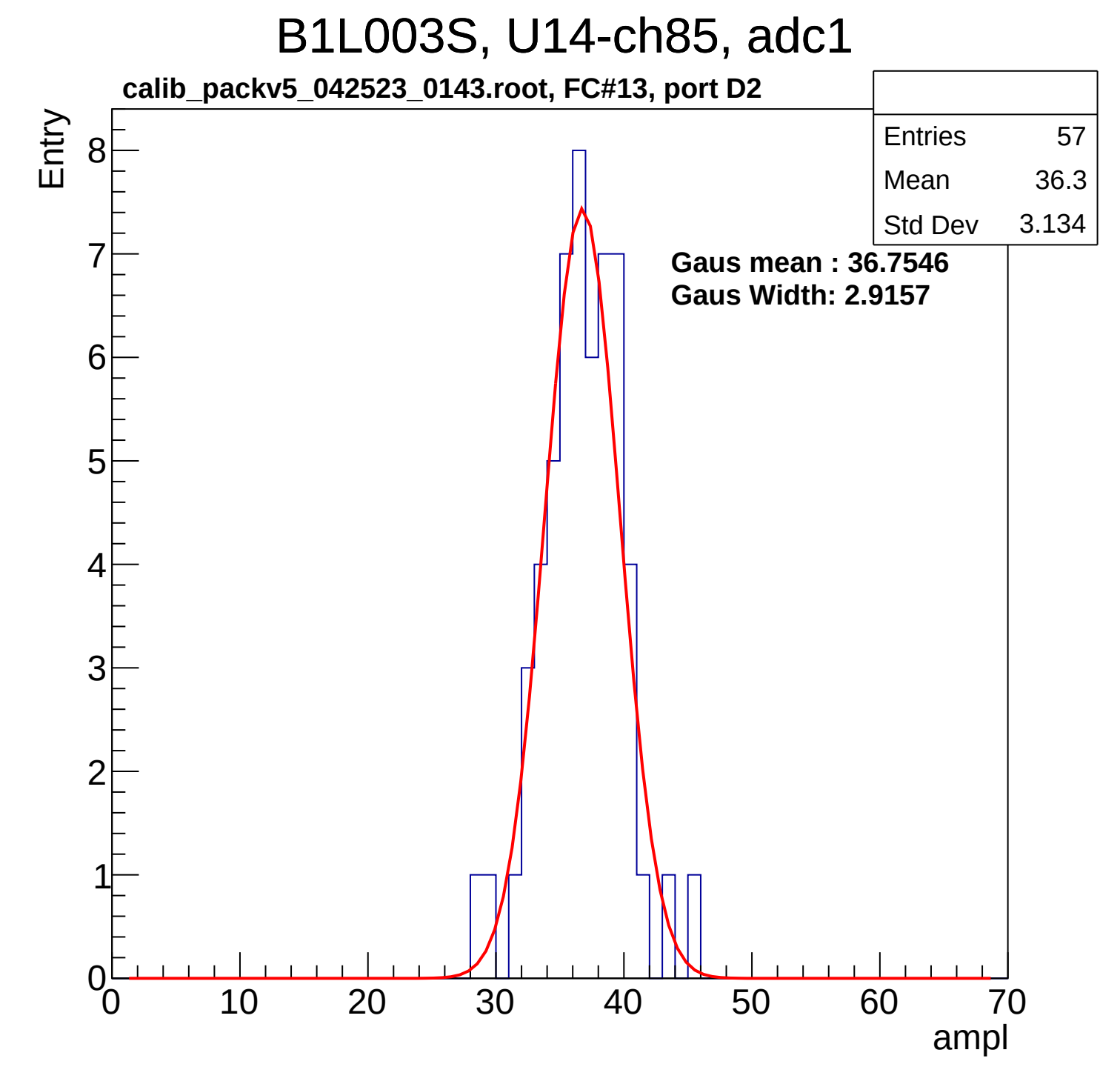

B1L003S, U14-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.84
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

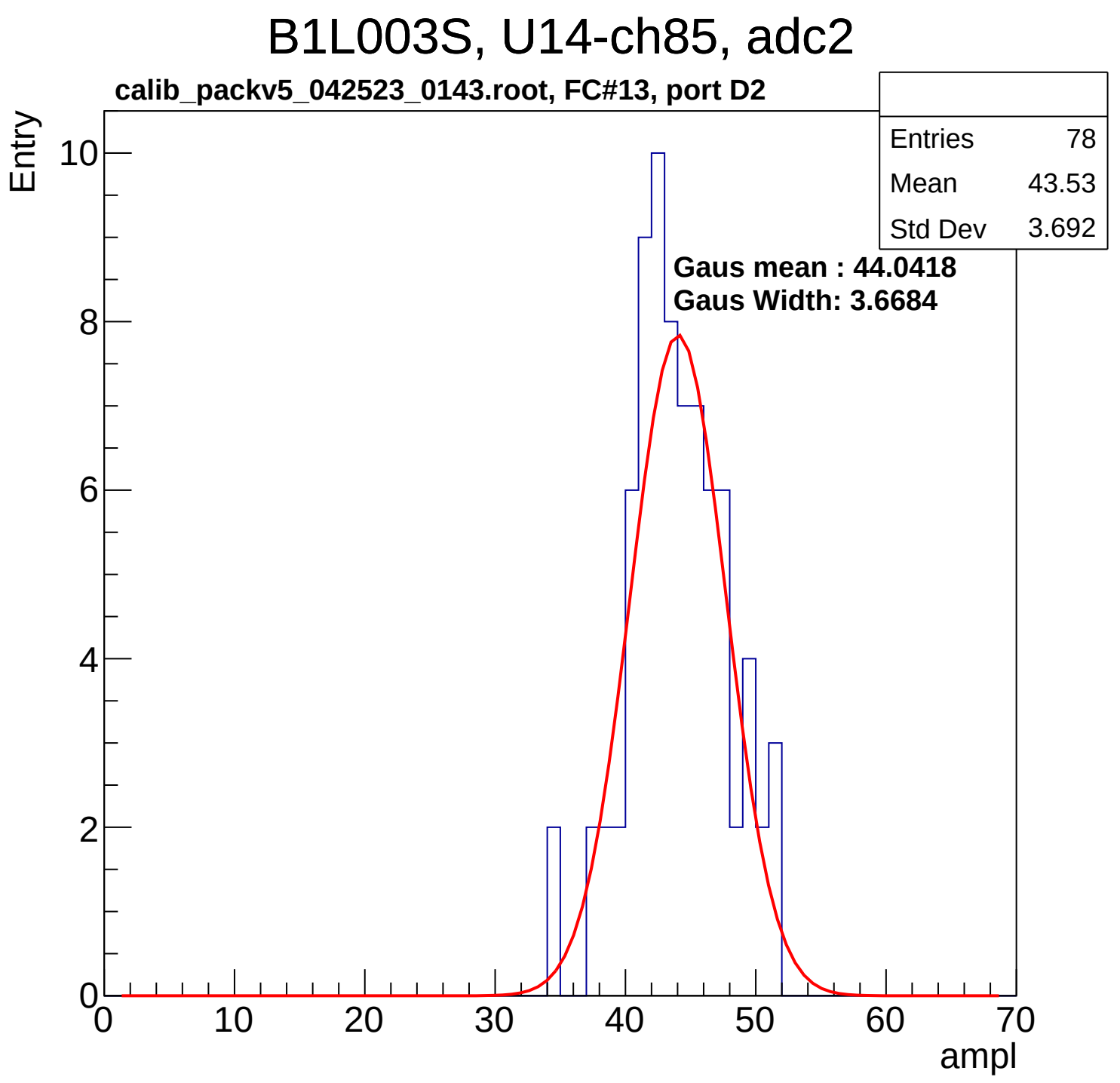

B1L003S, U14-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.89
Std Dev 2.318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

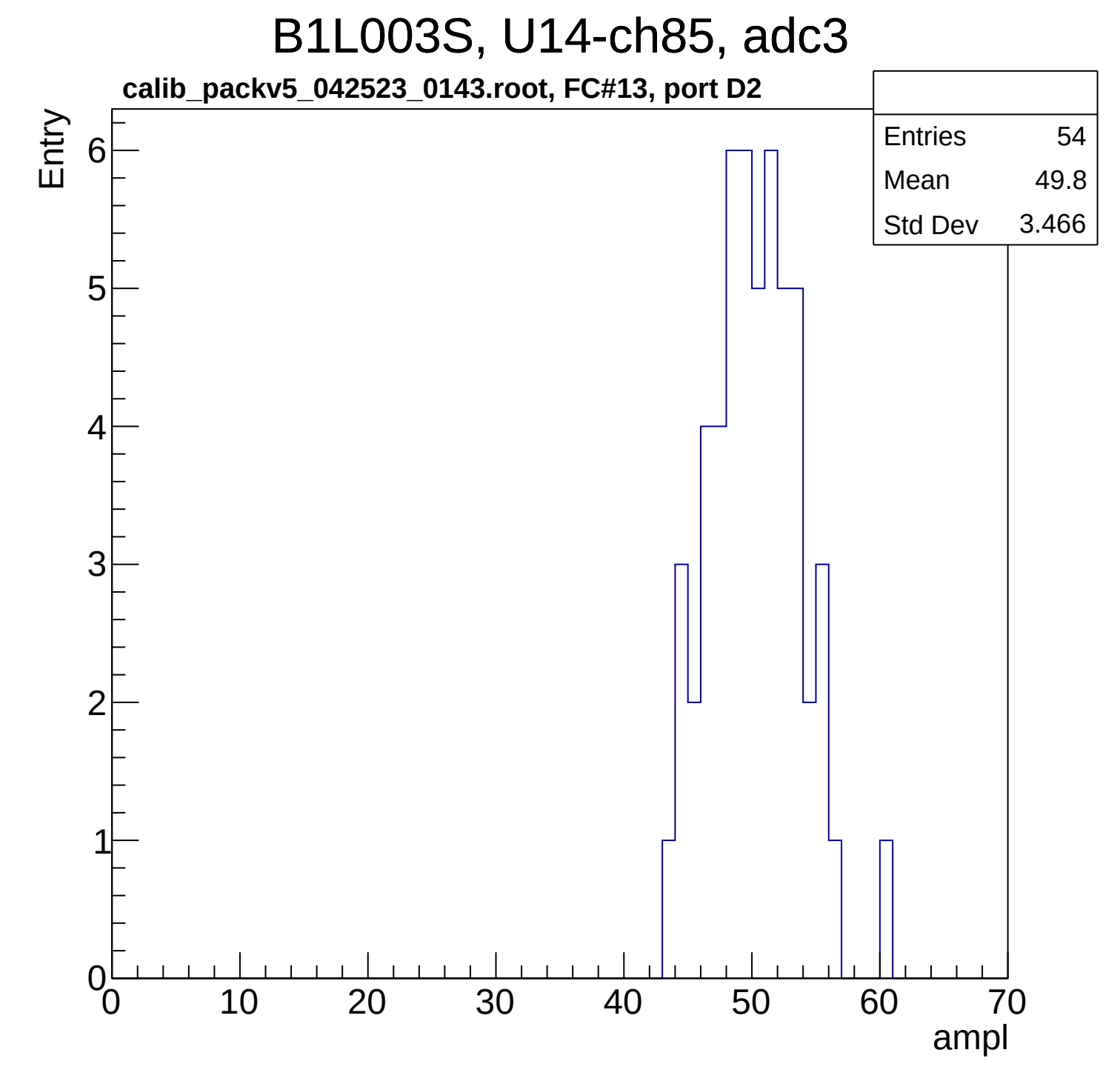

B1L003S, U14-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.71
Std Dev 13.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

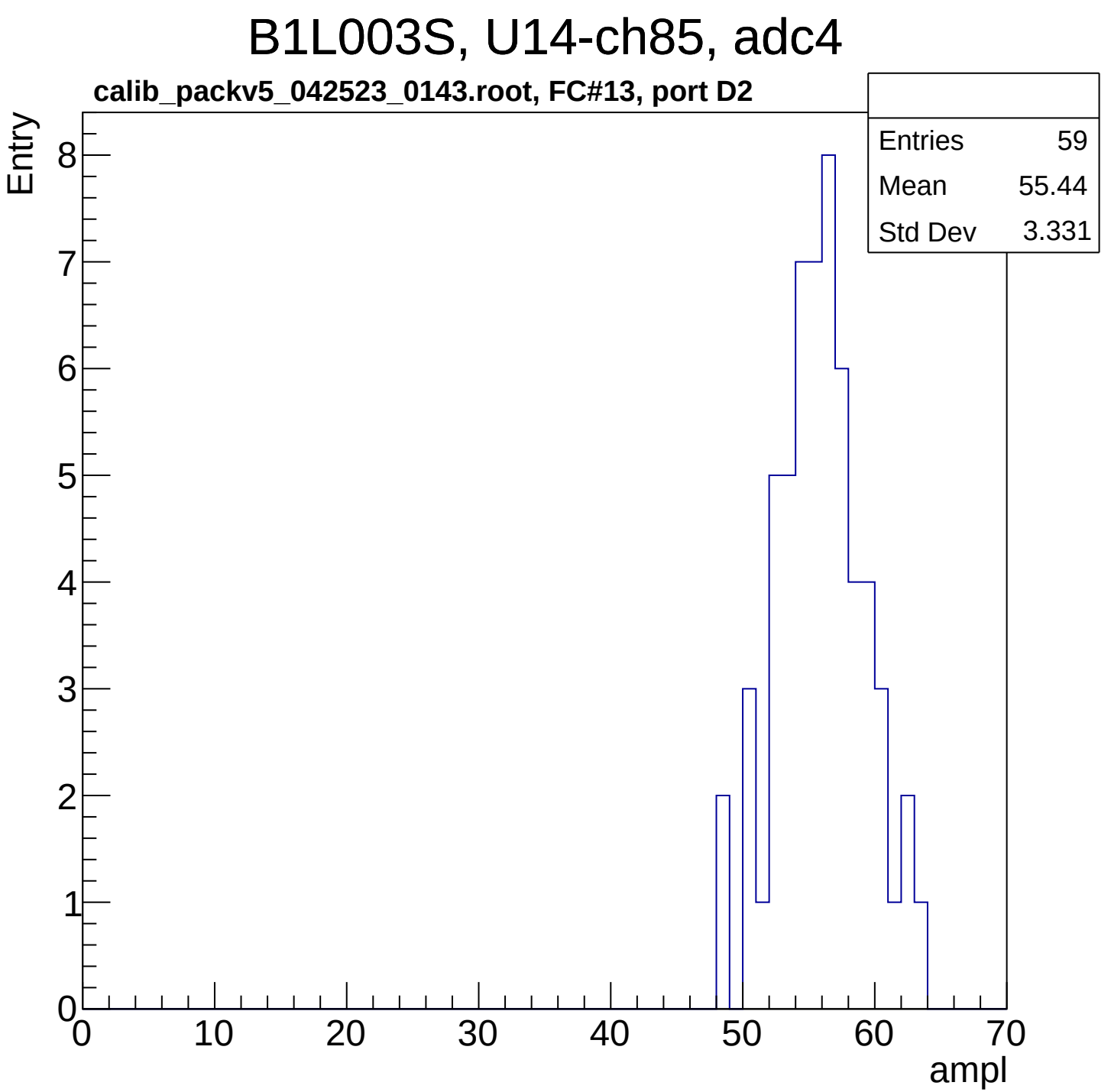

B1L003S, U14-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

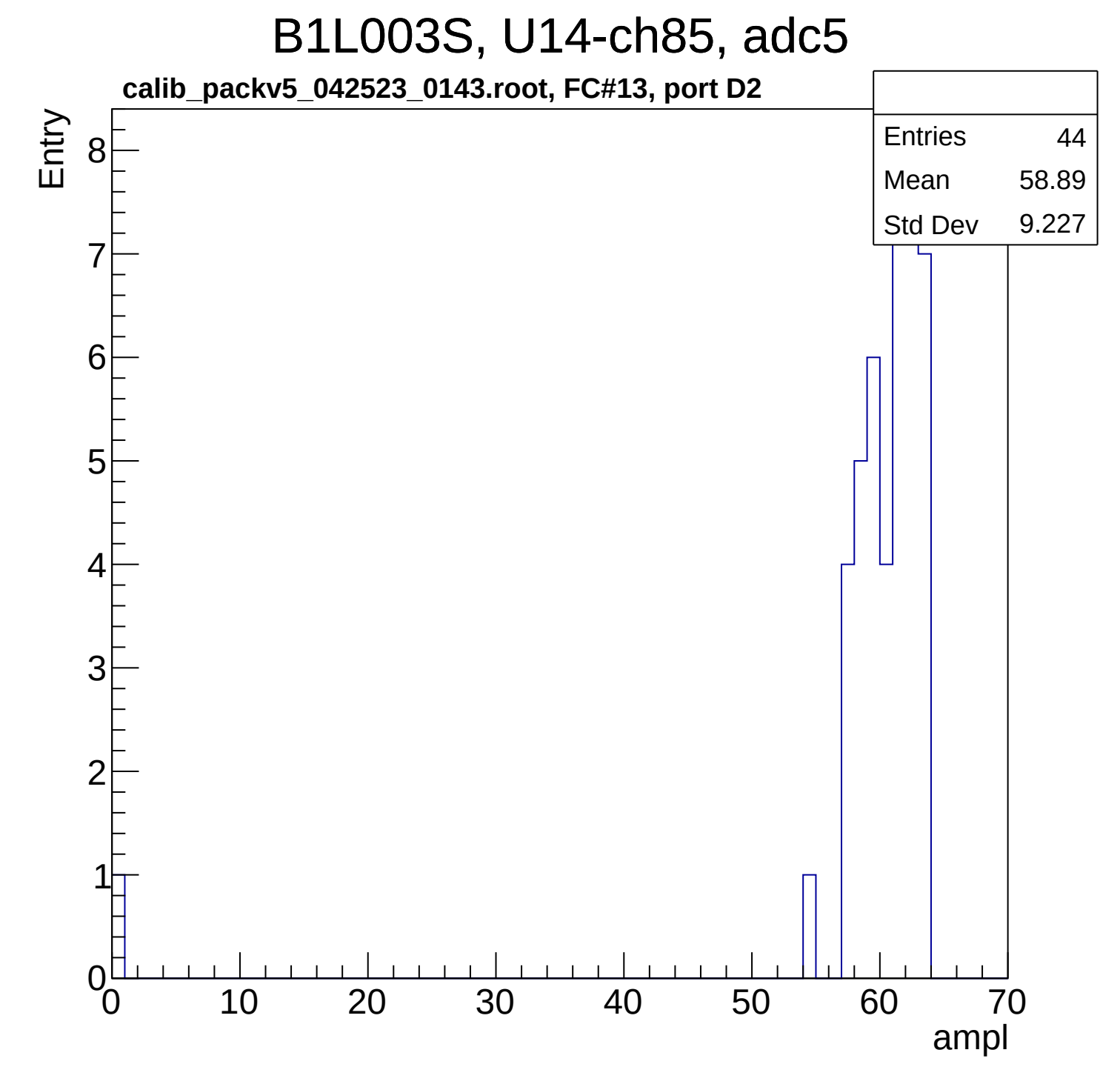

B1L003S, U14-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 30.45
Std Dev 3.381
Gaus mean : 30.7845
Gaus Width: 3.5667
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

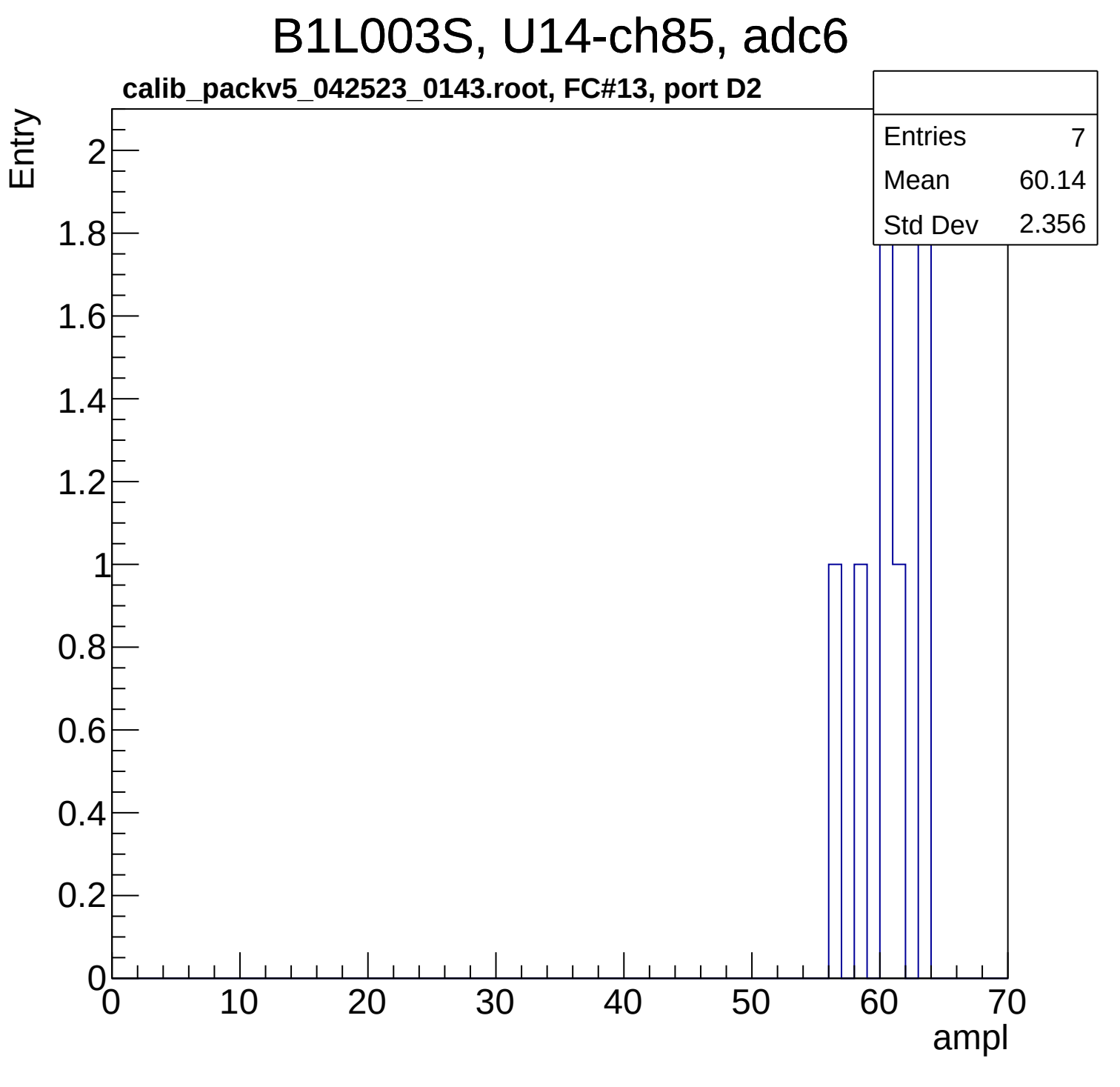

B1L003S, U14-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.3
Std Dev 3.134
Gaus mean : 36.7546
Gaus Width: 2.9157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

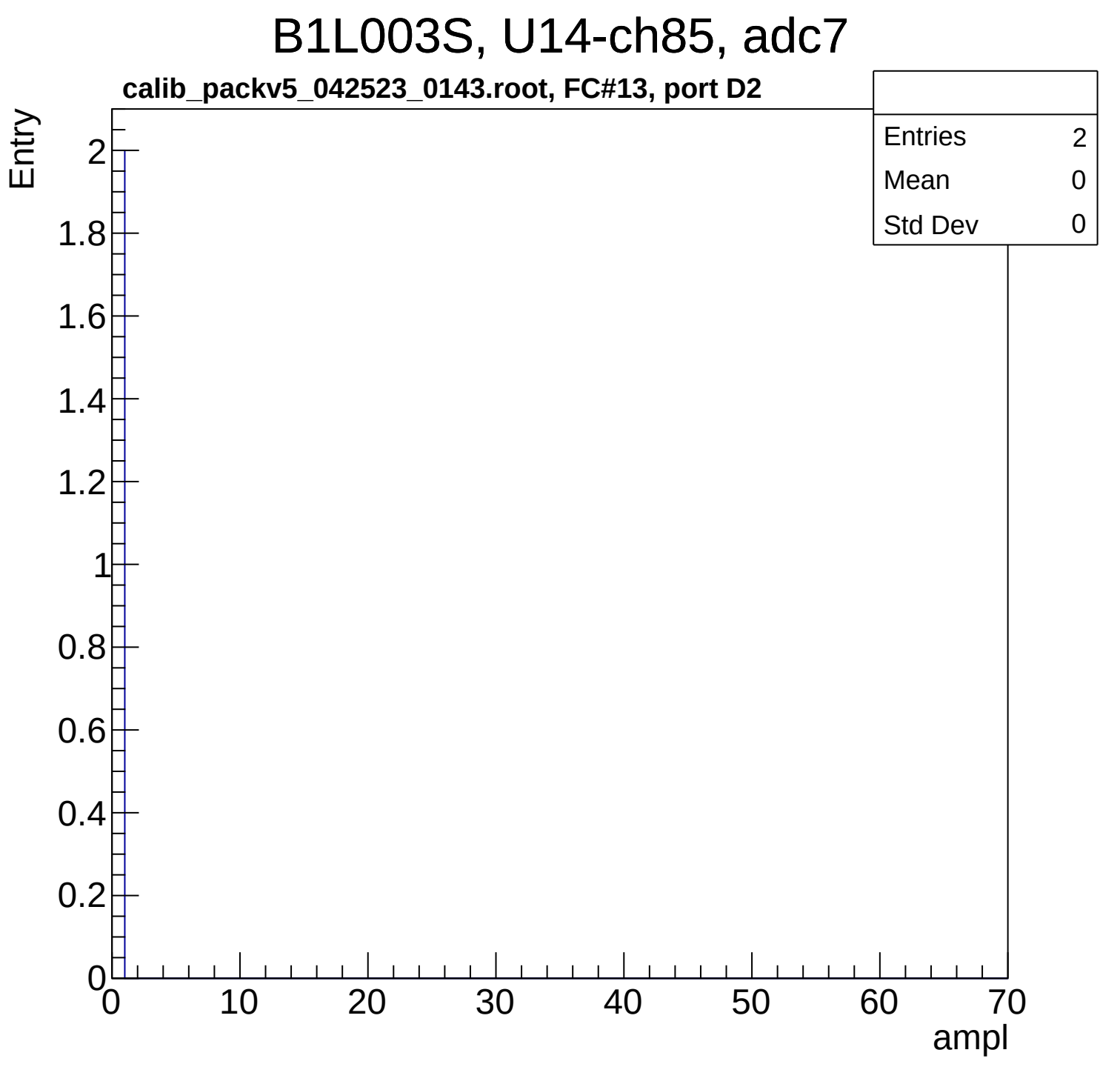

B1L003S, U14-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 43.53
Std Dev 3.692
Gaus mean : 44.0418
Gaus Width: 3.6684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

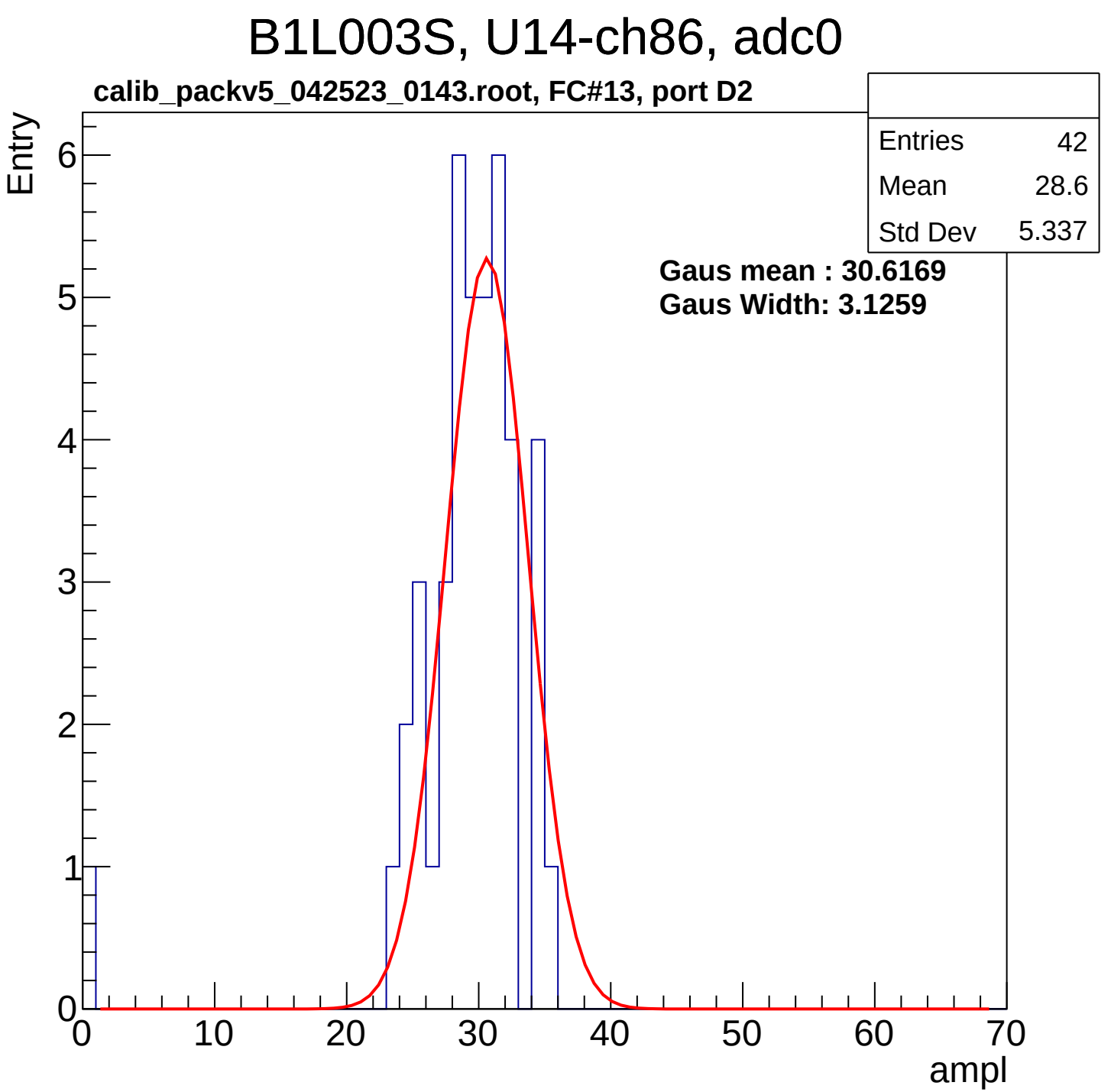

B1L003S, U14-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.8
Std Dev 3.466
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

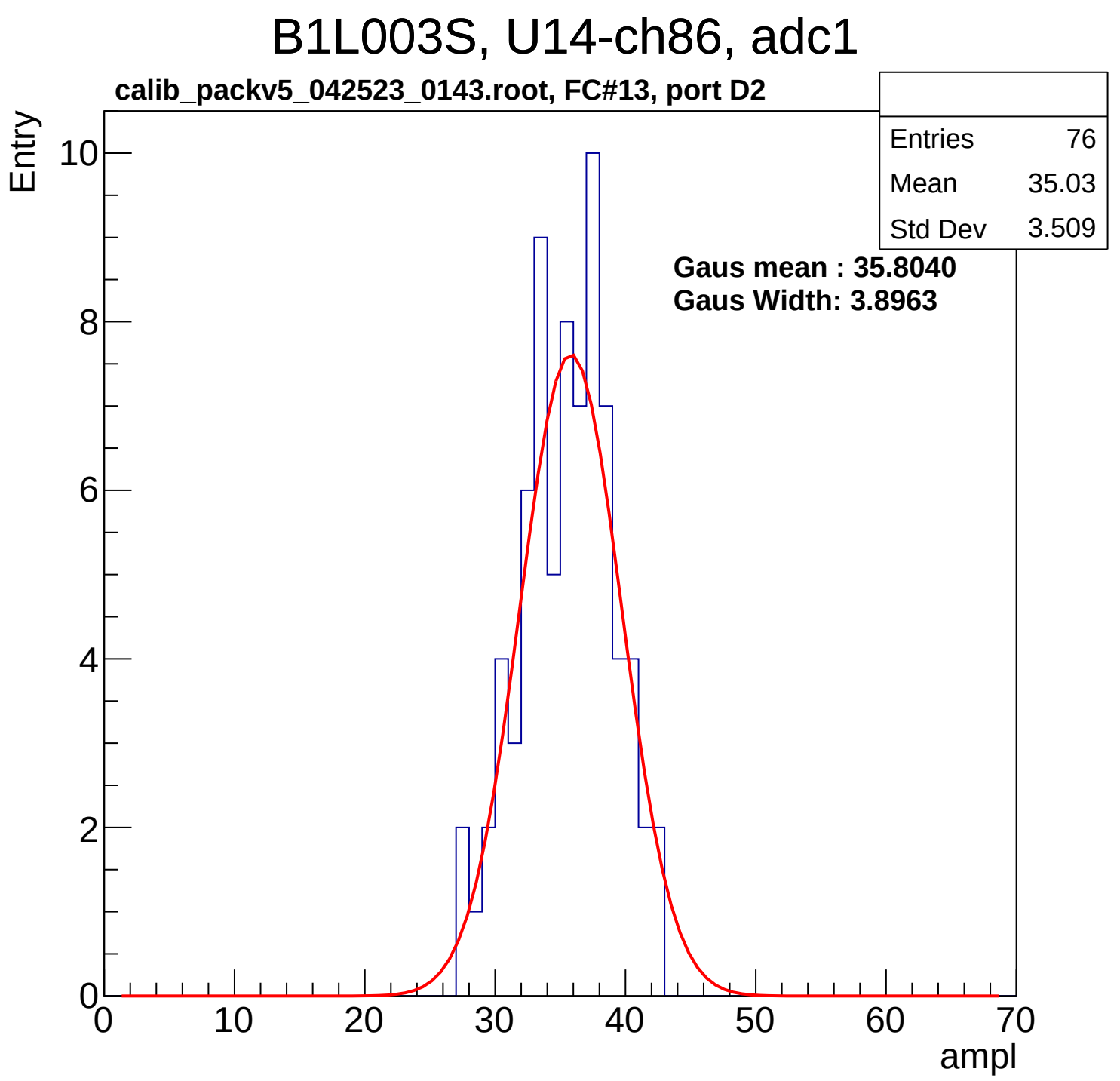

B1L003S, U14-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.44
Std Dev 3.331
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

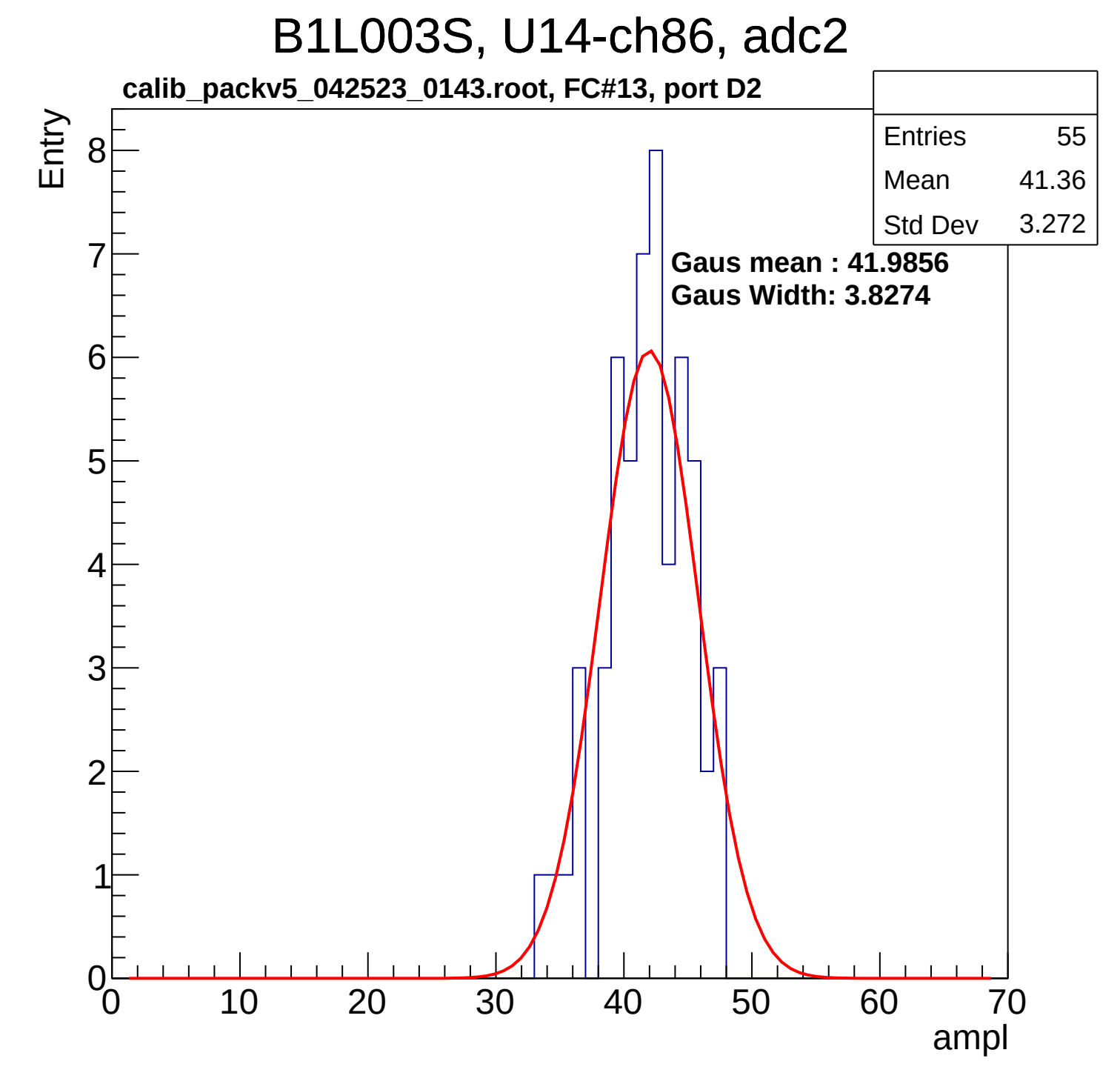

B1L003S, U14-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.89
Std Dev 9.227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

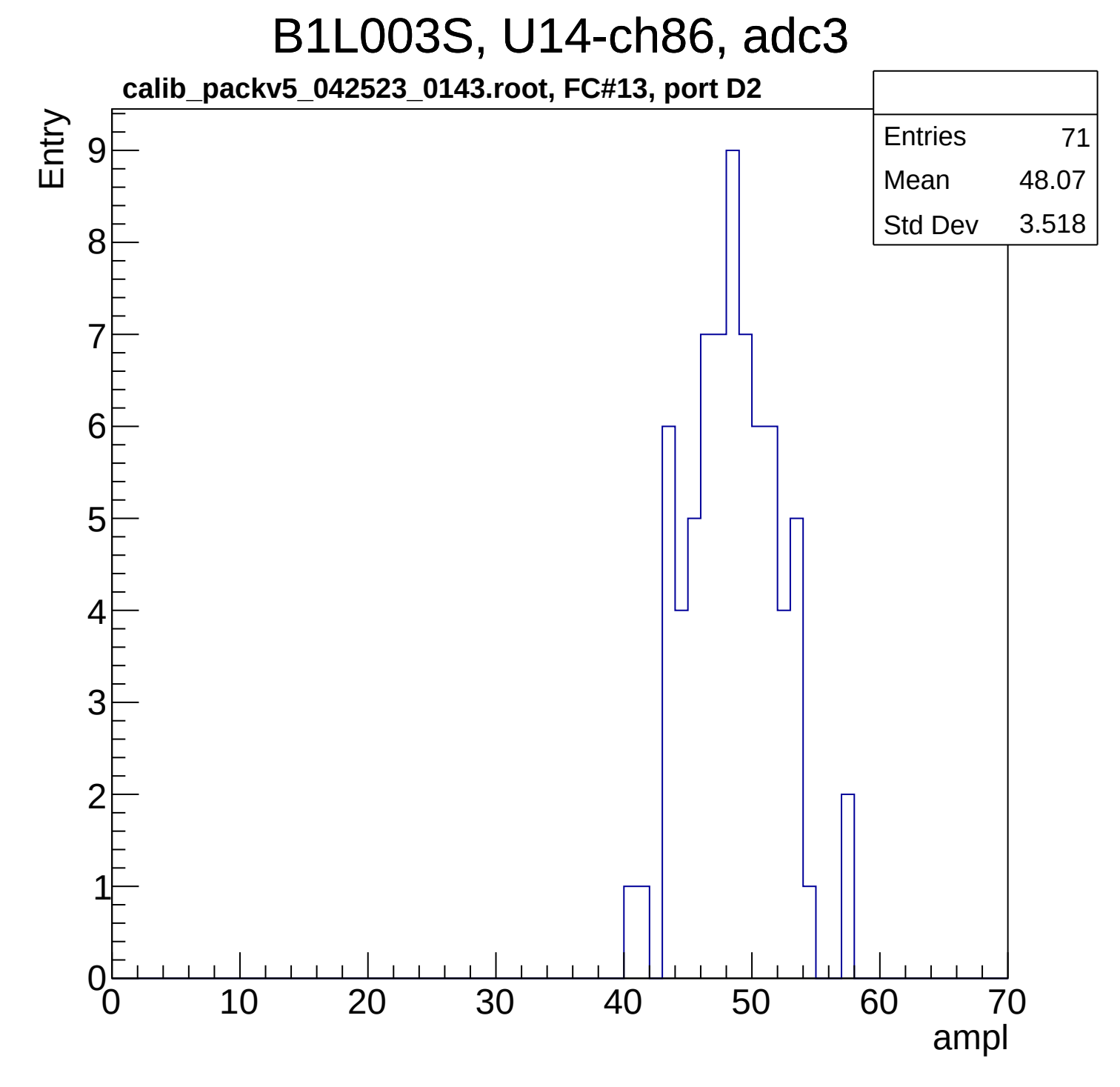

B1L003S, U14-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 60.14
Std Dev 2.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

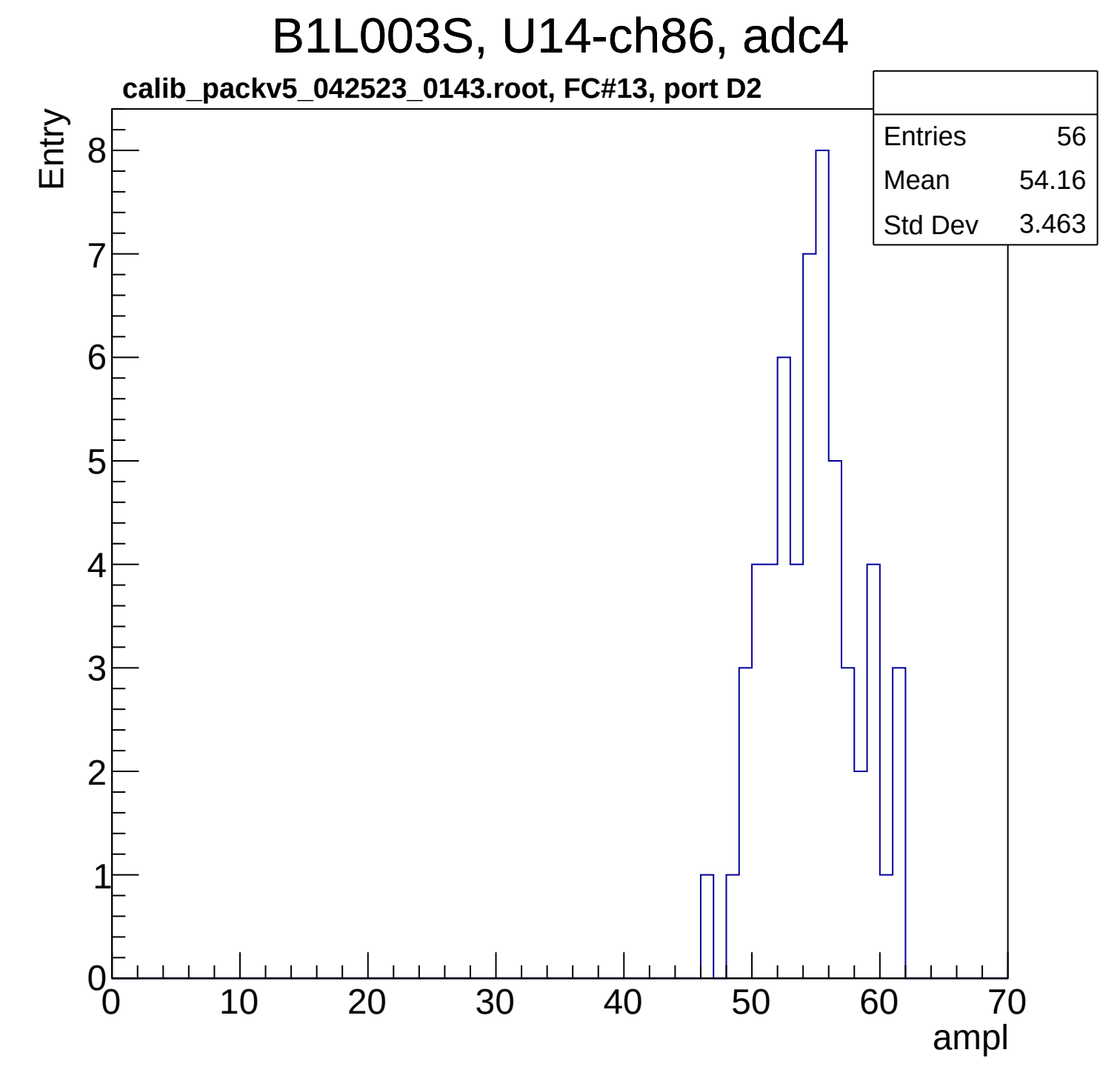

B1L003S, U14-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

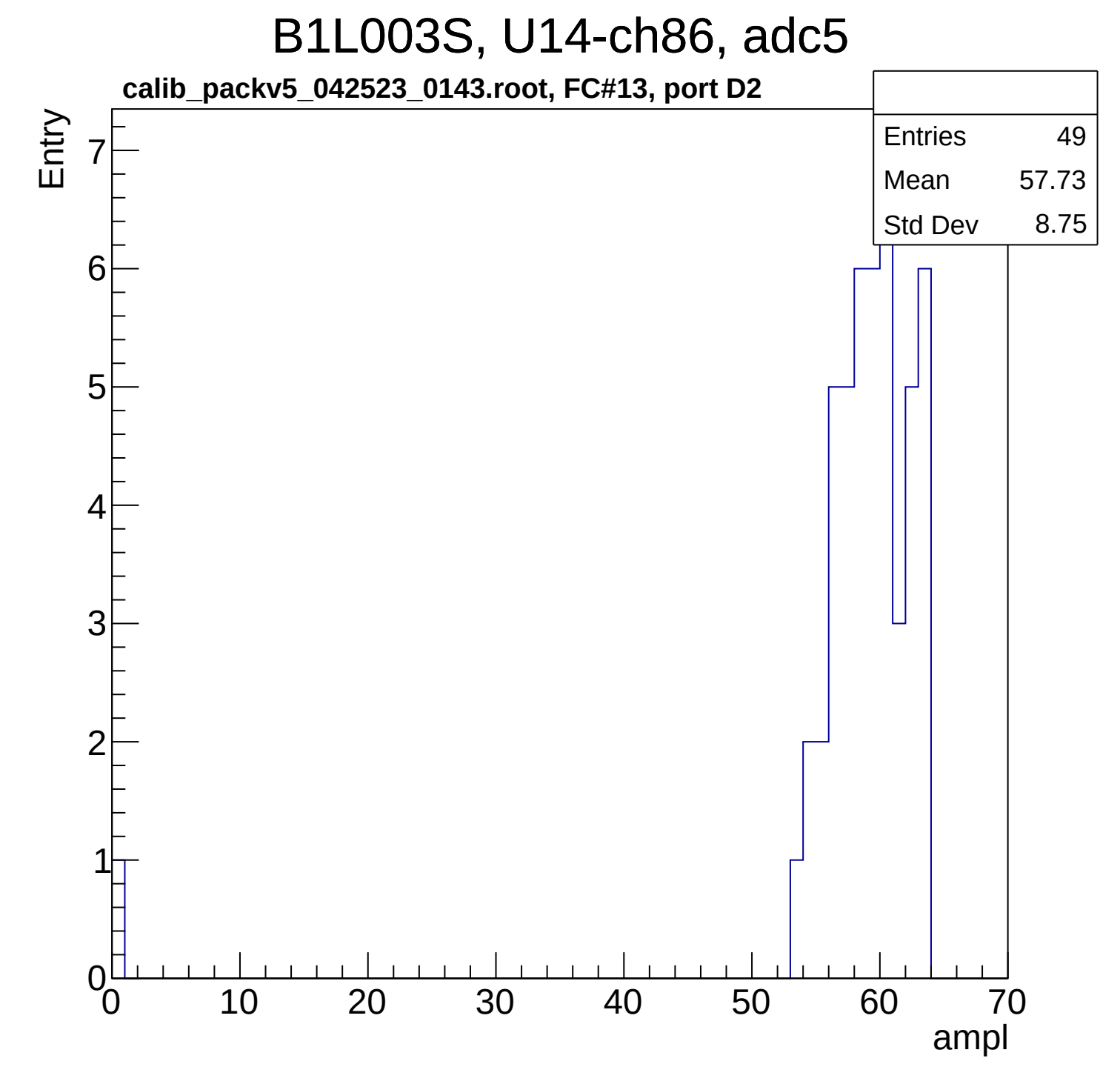

B1L003S, U14-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 28.6
Std Dev 5.337
Gaus mean : 30.6169
Gaus Width: 3.1259
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

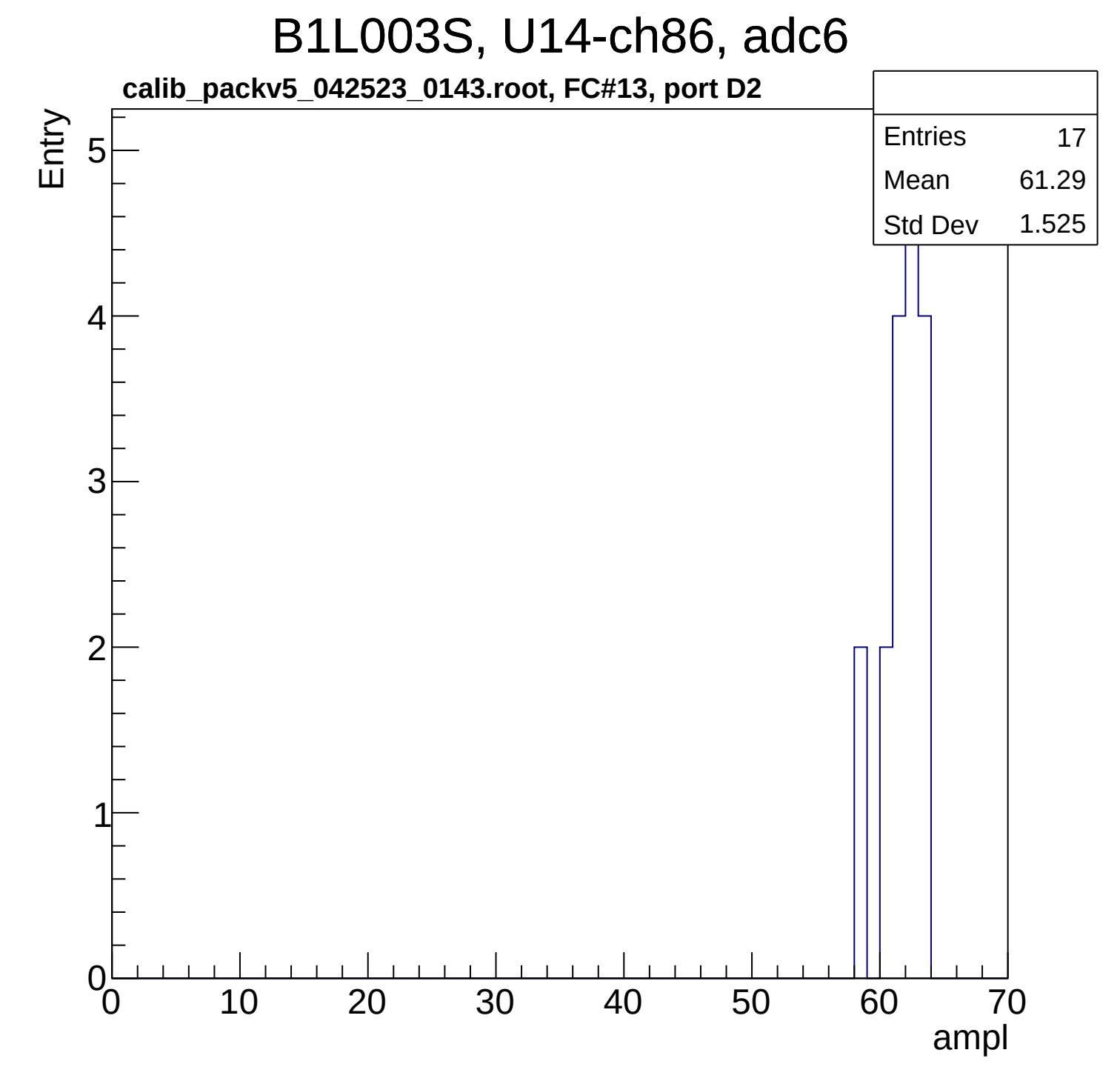

B1L003S, U14-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.03
Std Dev 3.509
Gaus mean : 35.8040
Gaus Width: 3.8963
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

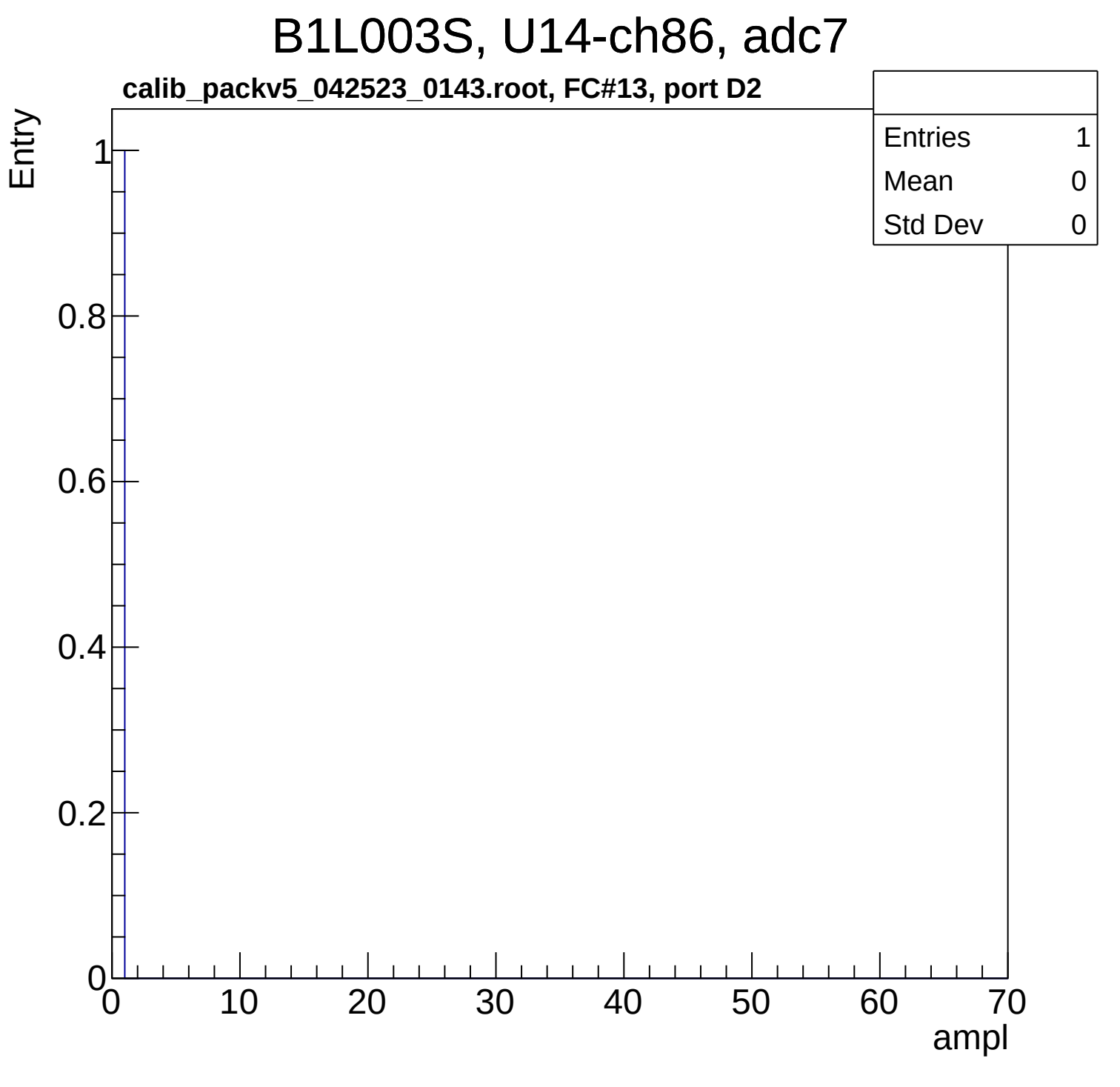

B1L003S, U14-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.36
Std Dev 3.272
Gaus mean : 41.9856
Gaus Width: 3.8274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

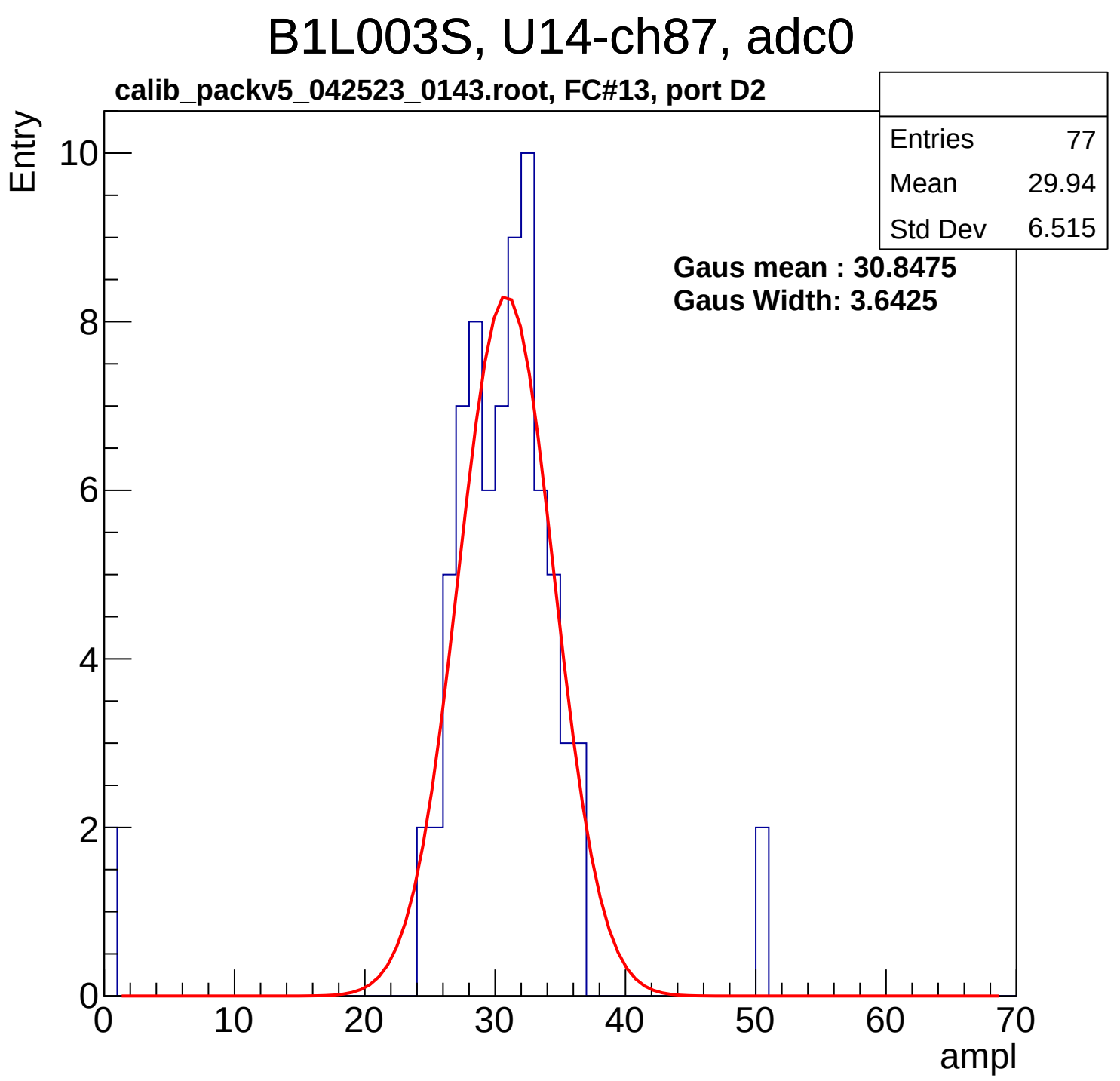

B1L003S, U14-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 48.07
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

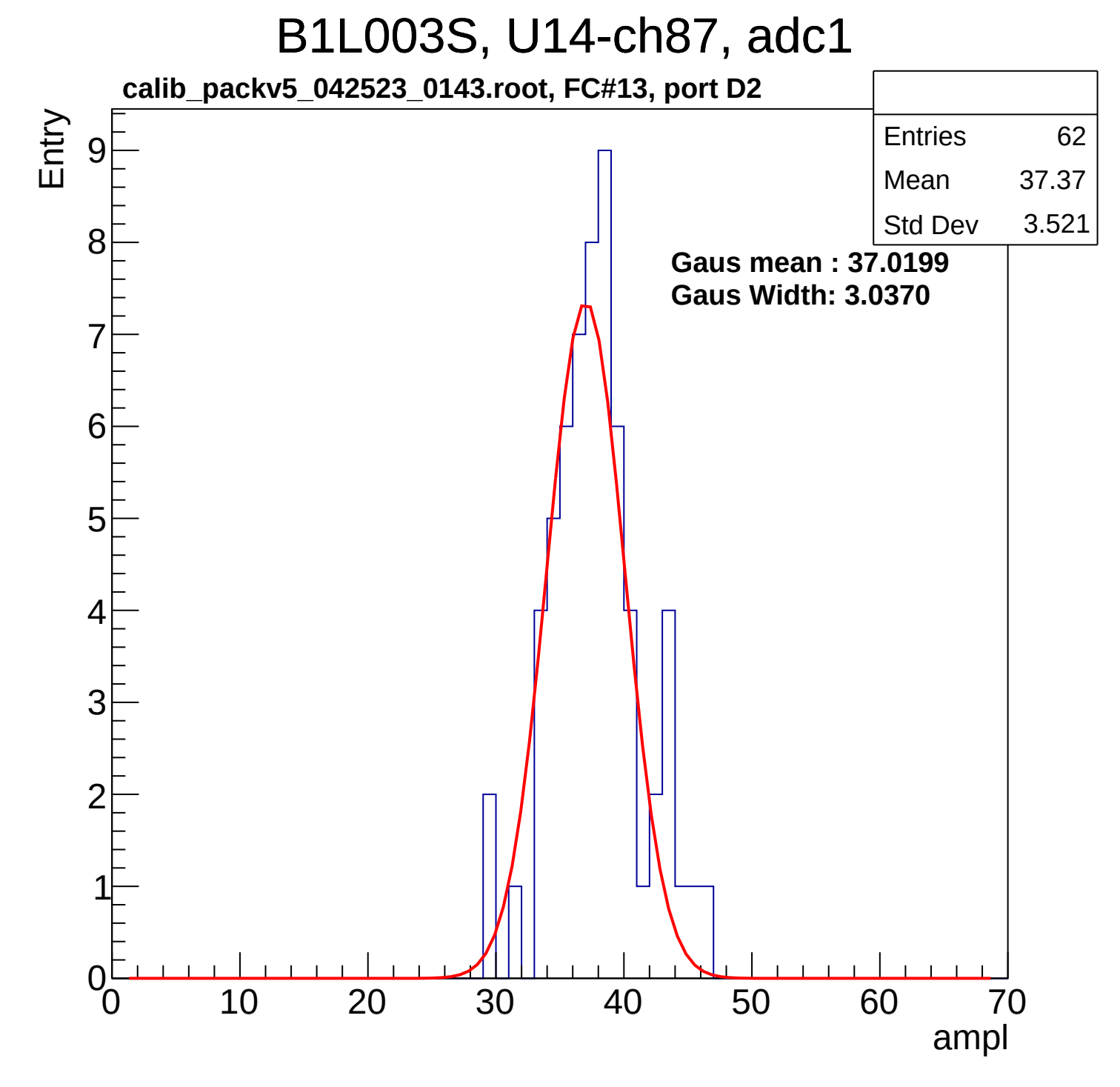

B1L003S, U14-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.16
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

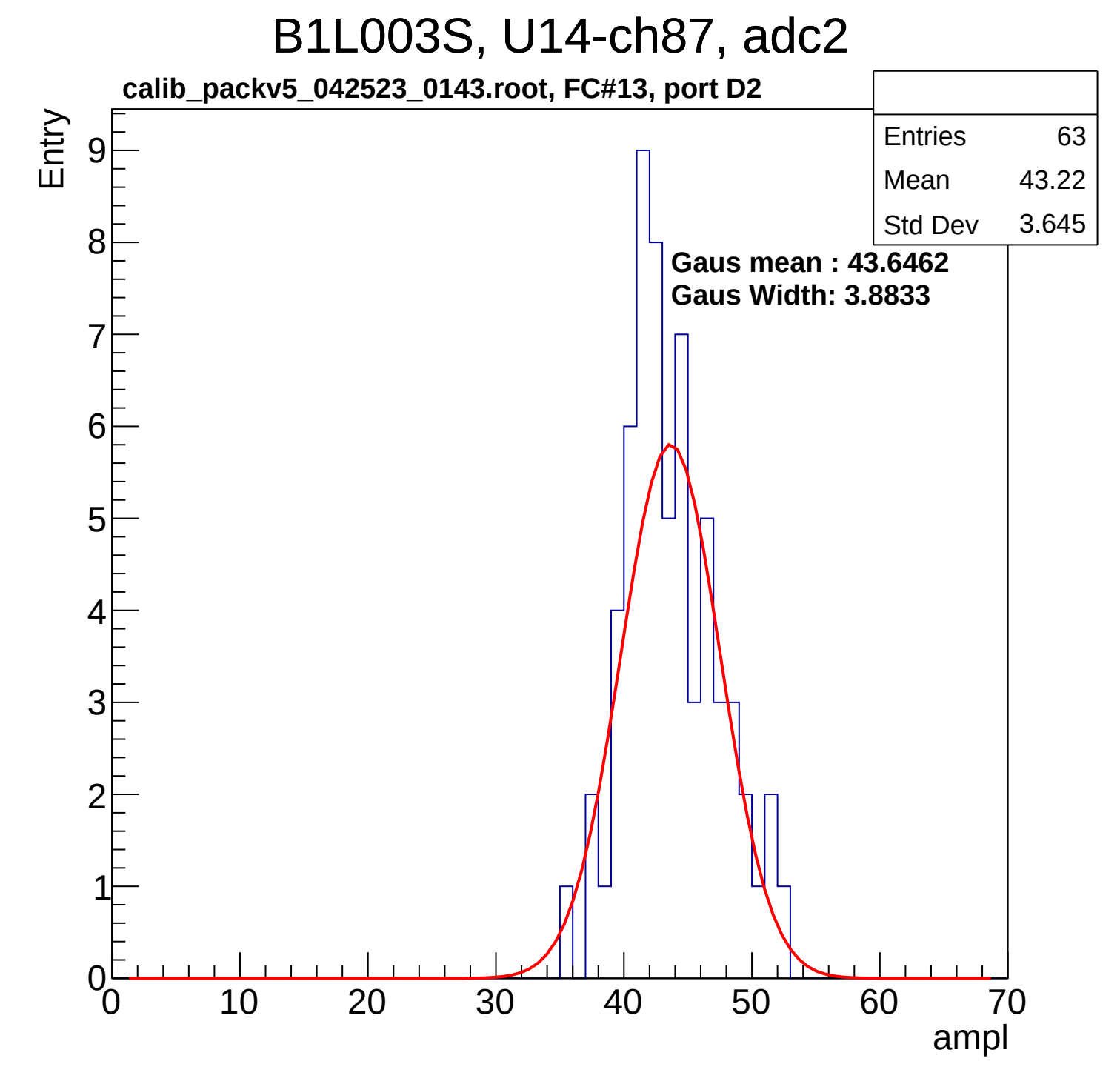

B1L003S, U14-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 57.73
Std Dev 8.75
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

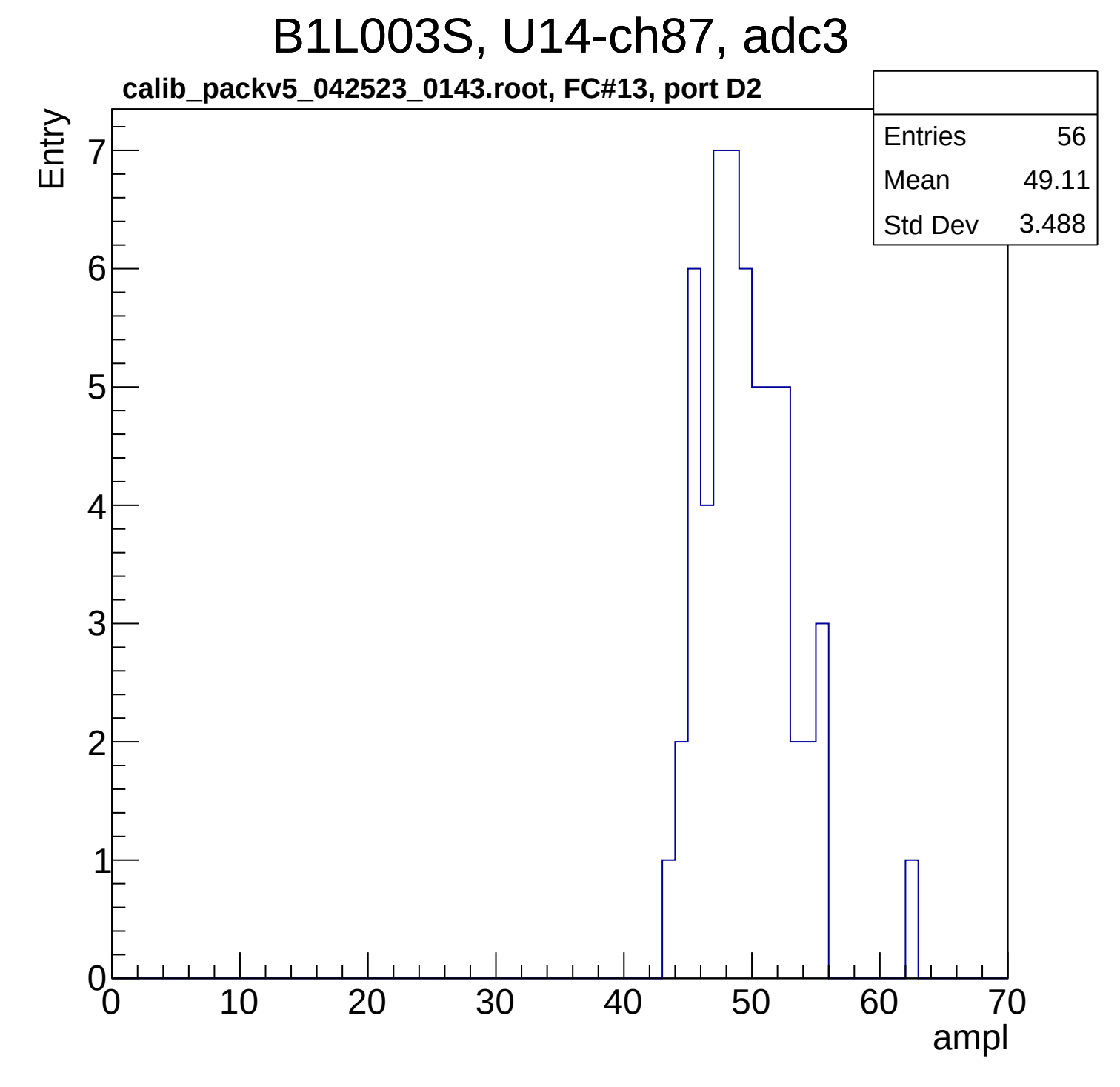

B1L003S, U14-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 61.29
Std Dev 1.525
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

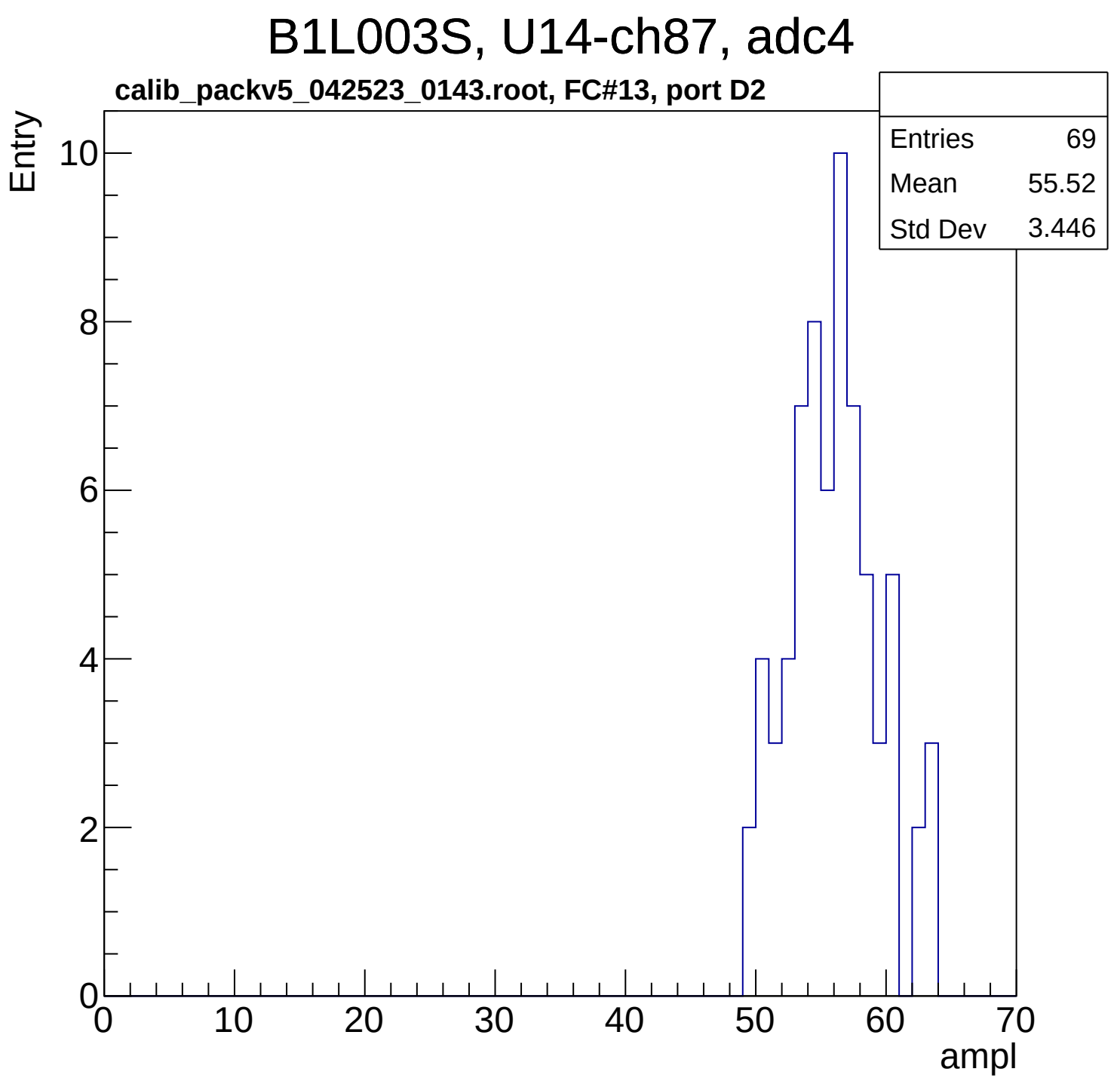

B1L003S, U14-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

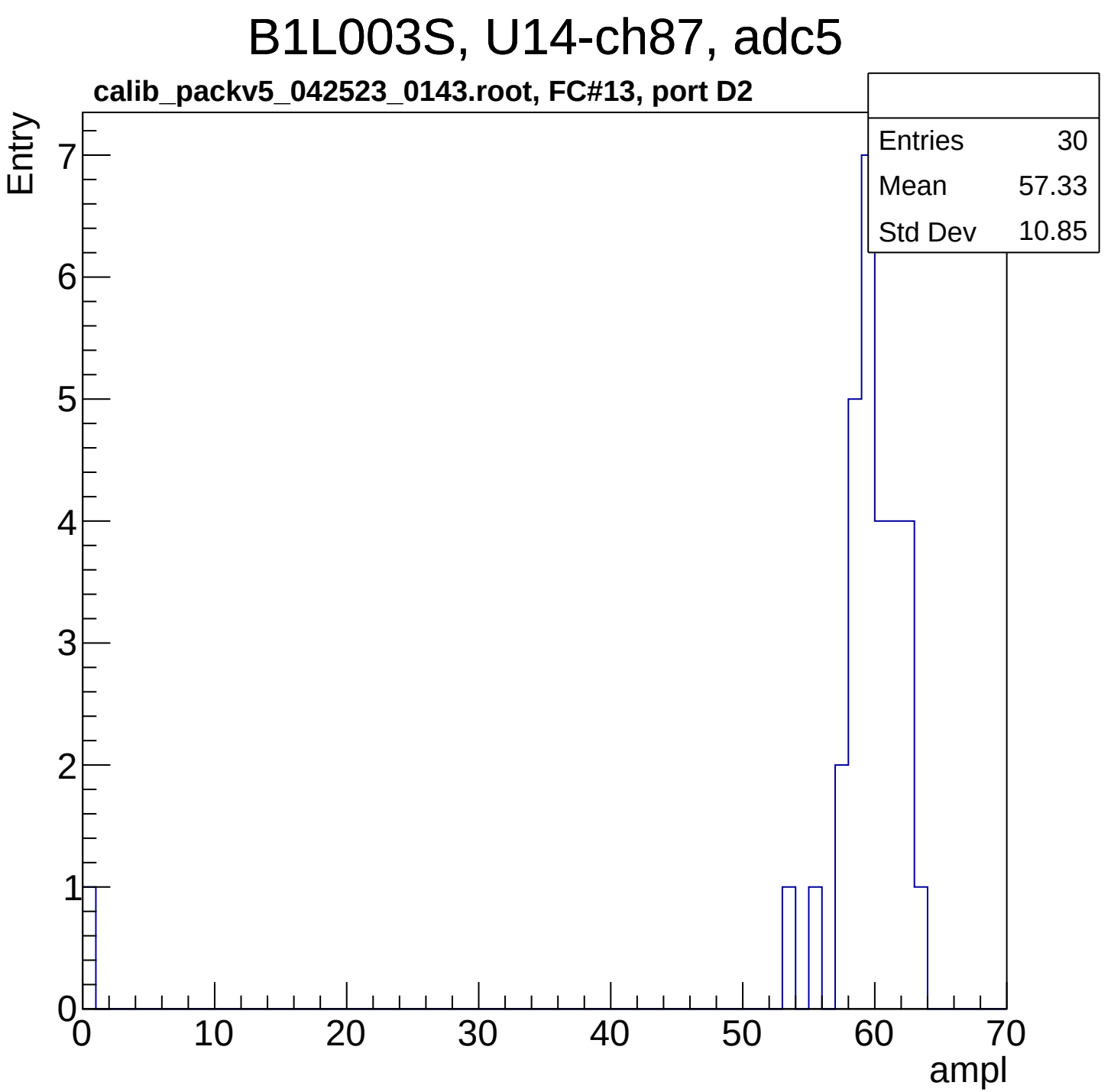

B1L003S, U14-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 29.94
Std Dev 6.515
Gaus mean : 30.8475
Gaus Width: 3.6425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

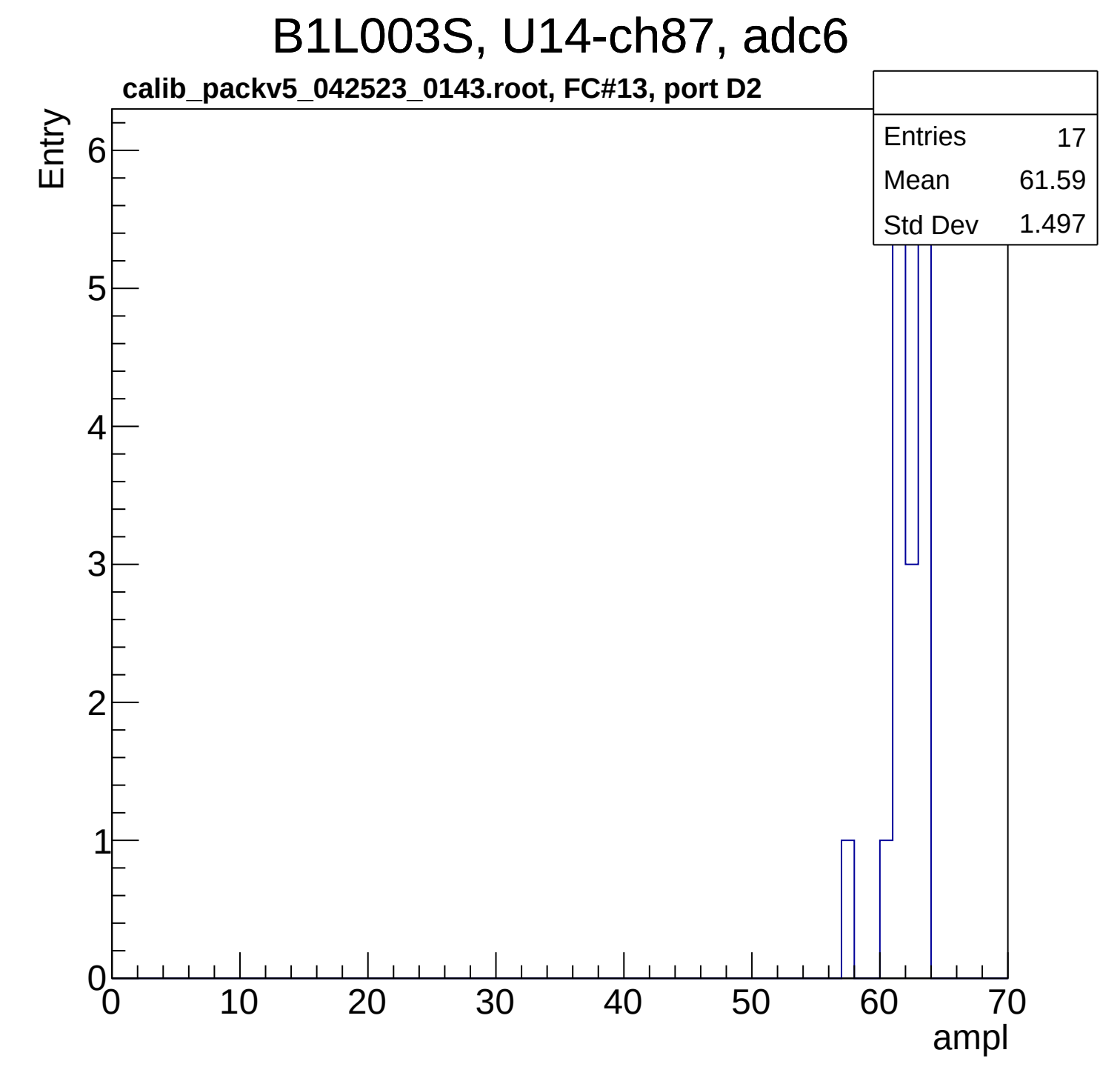

B1L003S, U14-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.37
Std Dev 3.521
Gaus mean : 37.0199
Gaus Width: 3.0370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

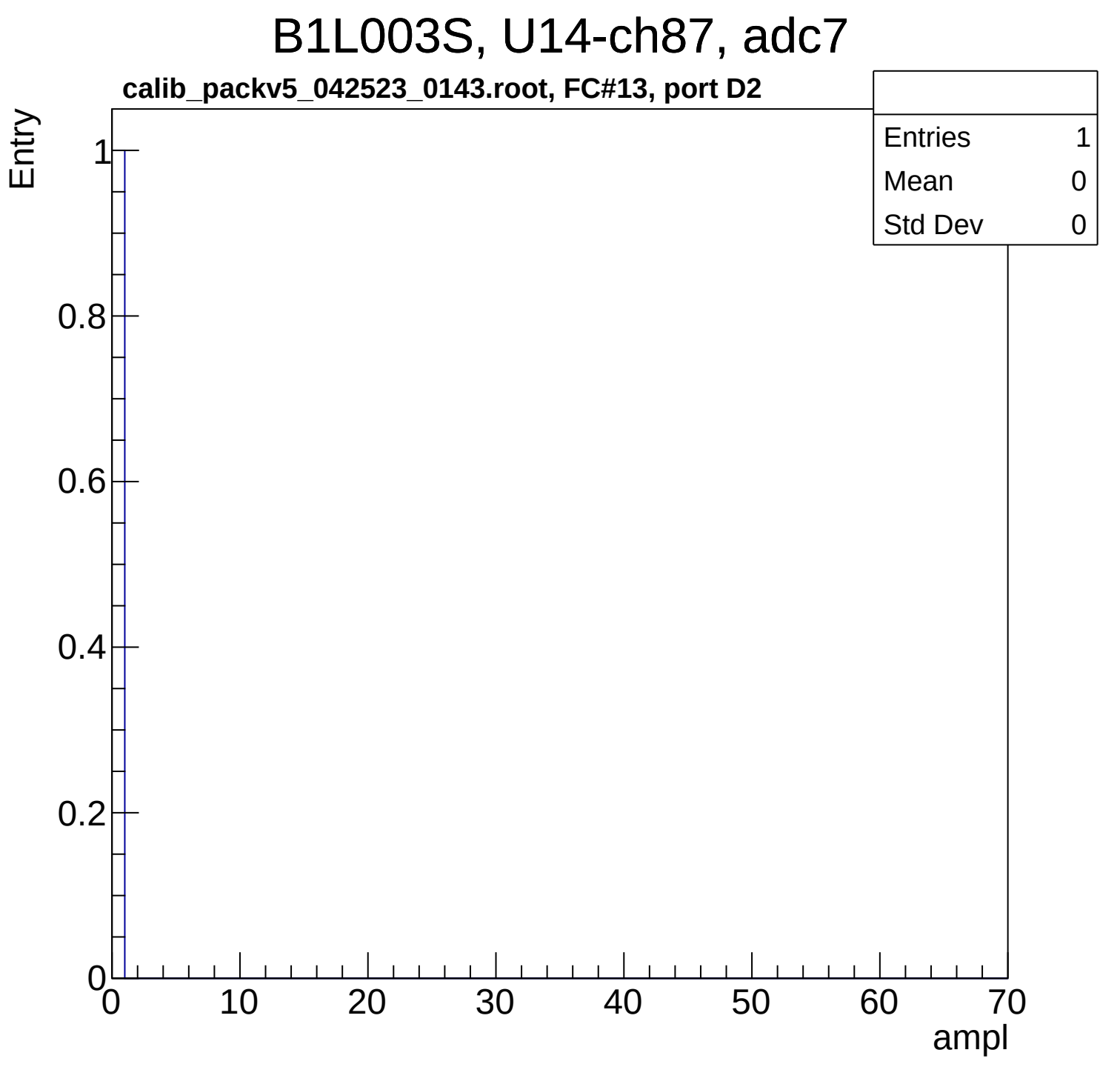

B1L003S, U14-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.22
Std Dev 3.645
Gaus mean : 43.6462
Gaus Width: 3.8833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

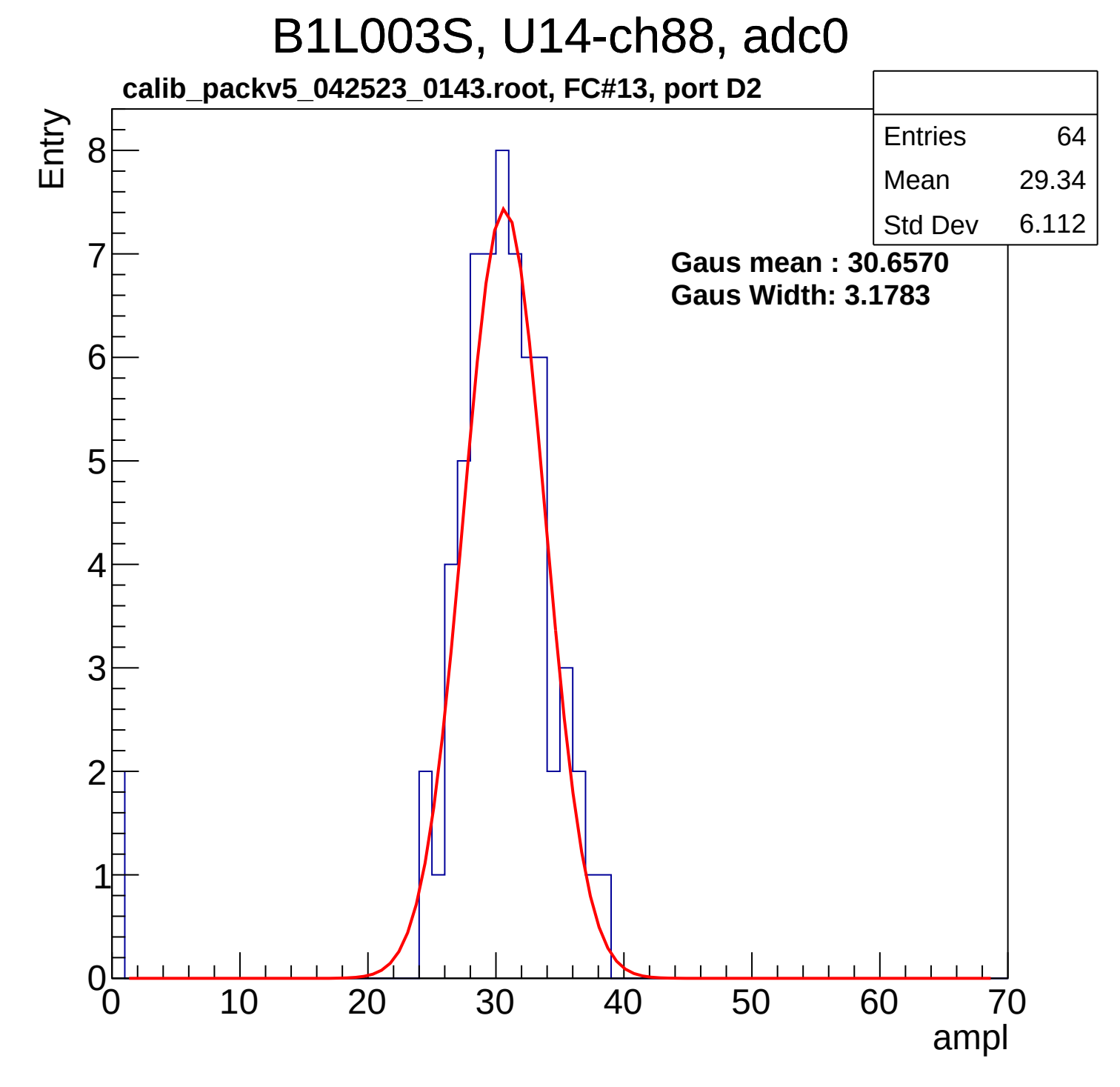

B1L003S, U14-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.11
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

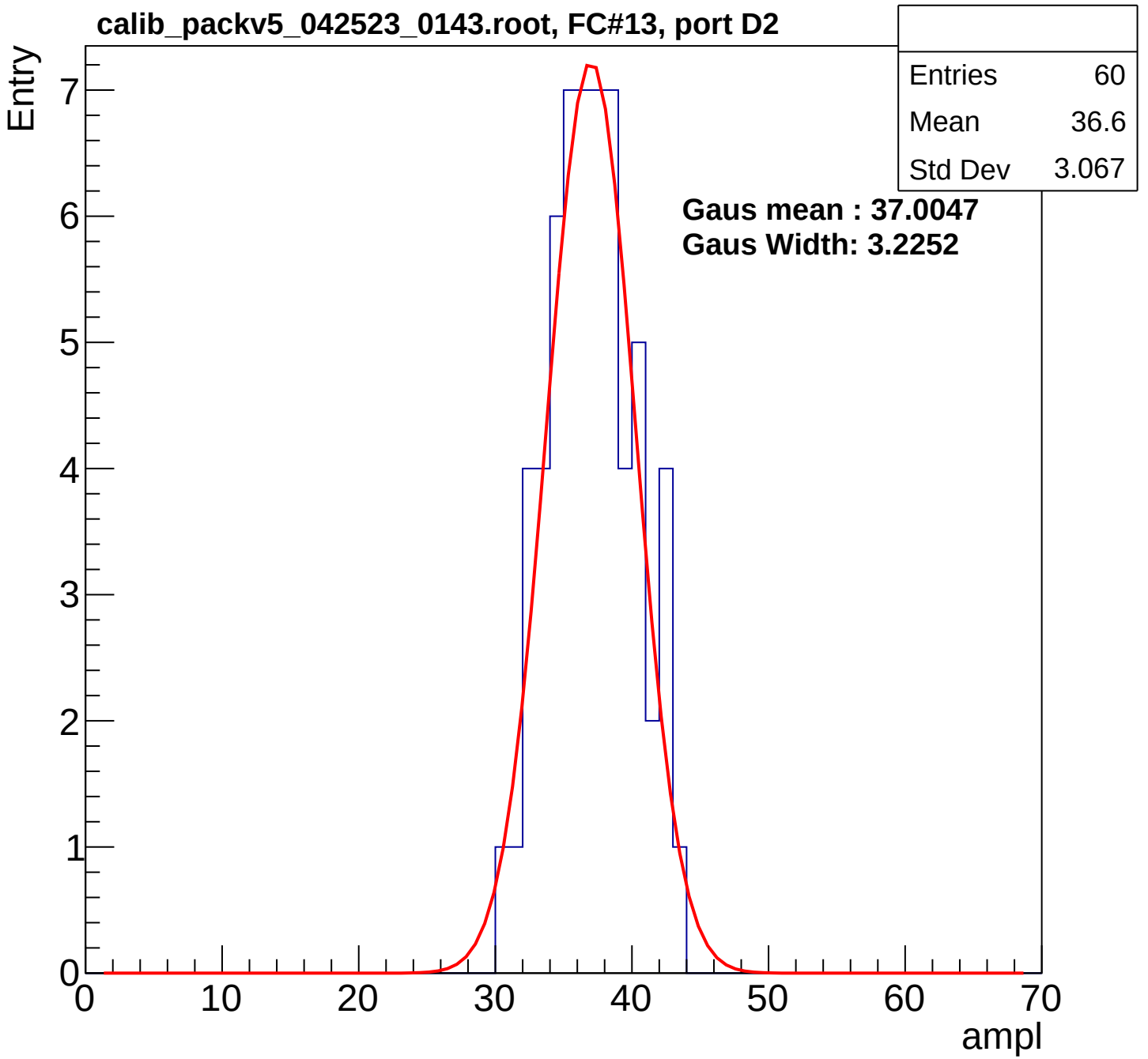

B1L003S, U14-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 55.52
Std Dev 3.446
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

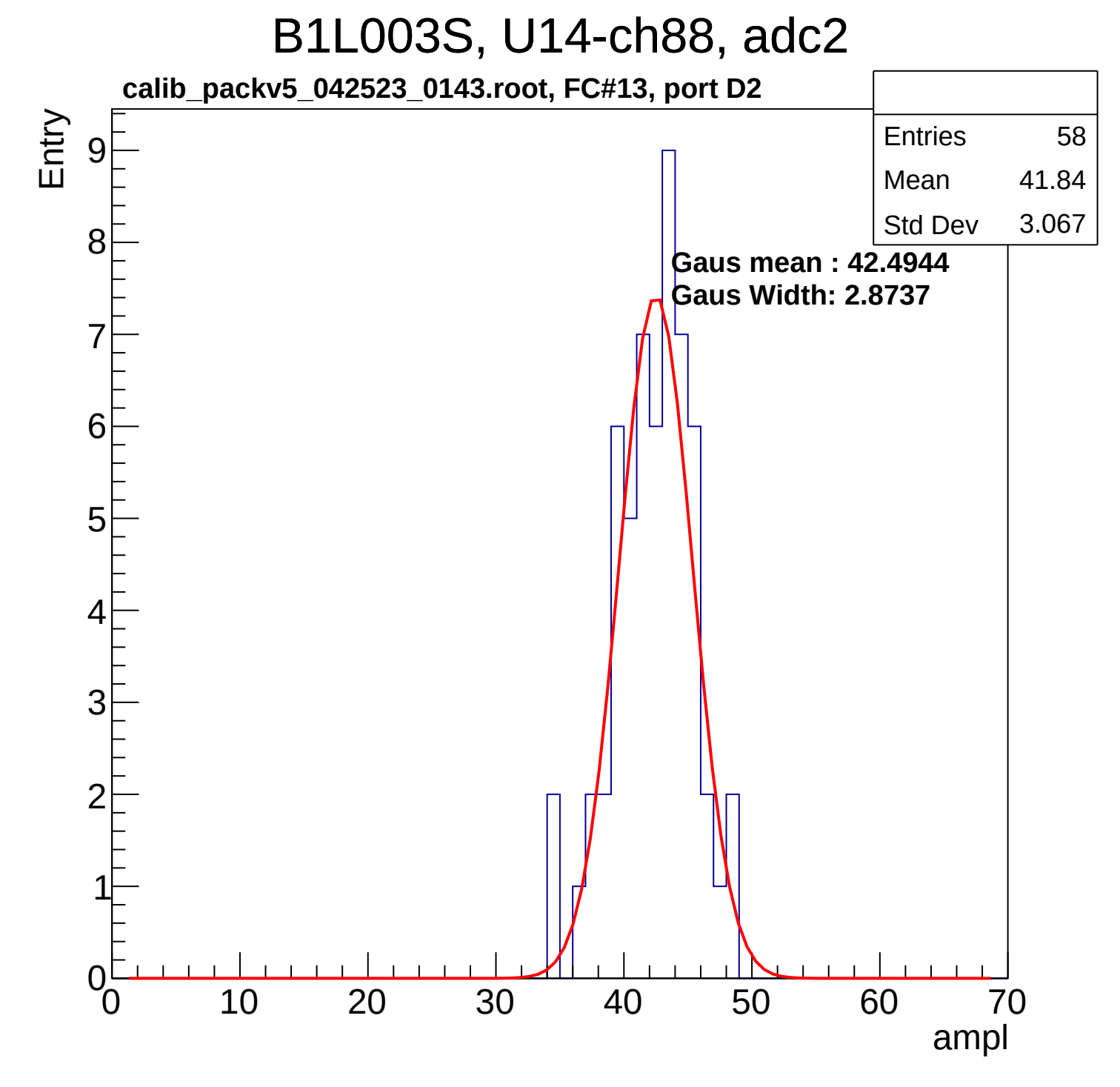

B1L003S, U14-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 57.33
Std Dev 10.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

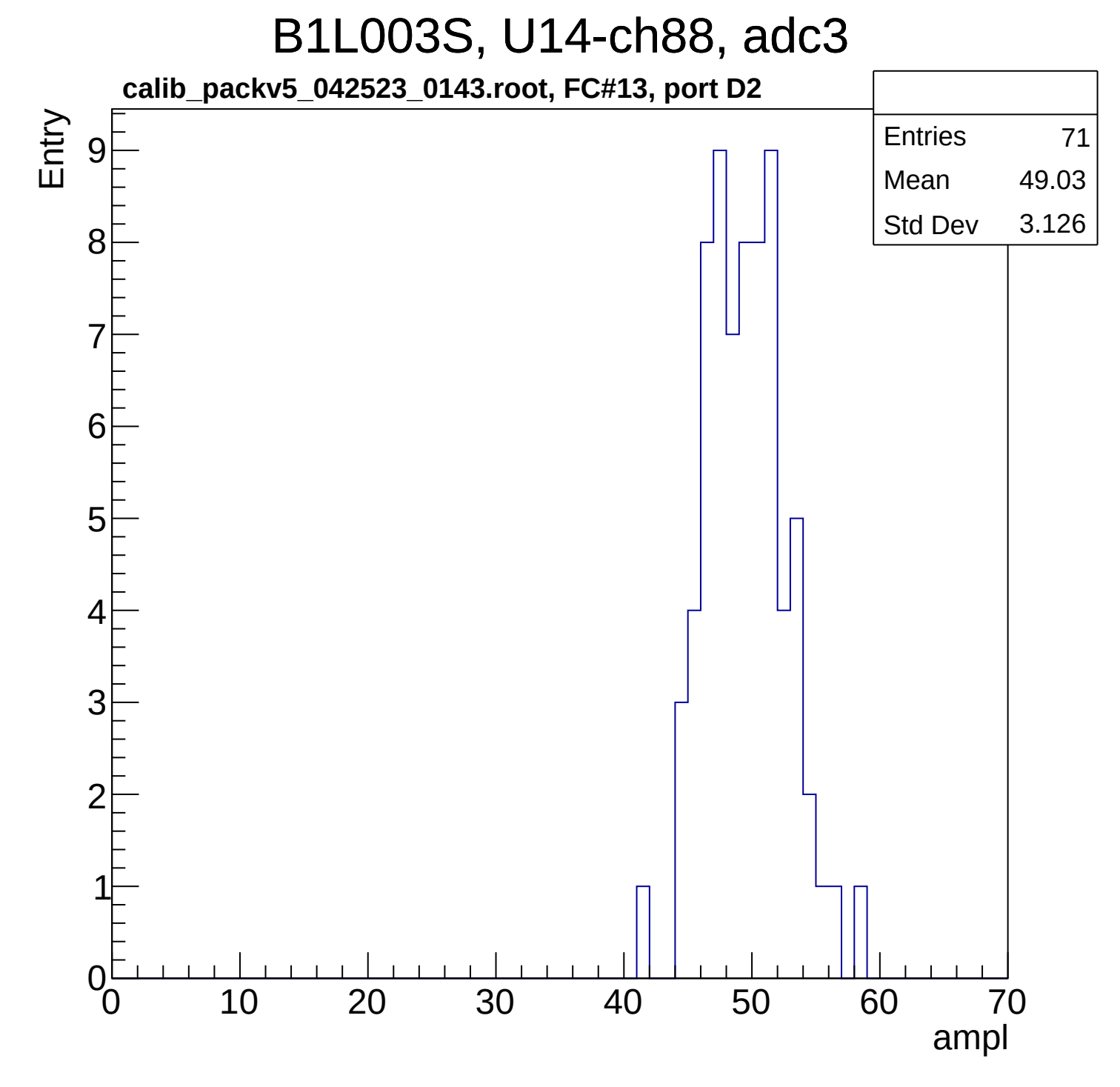

B1L003S, U14-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 61.59
Std Dev 1.497
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

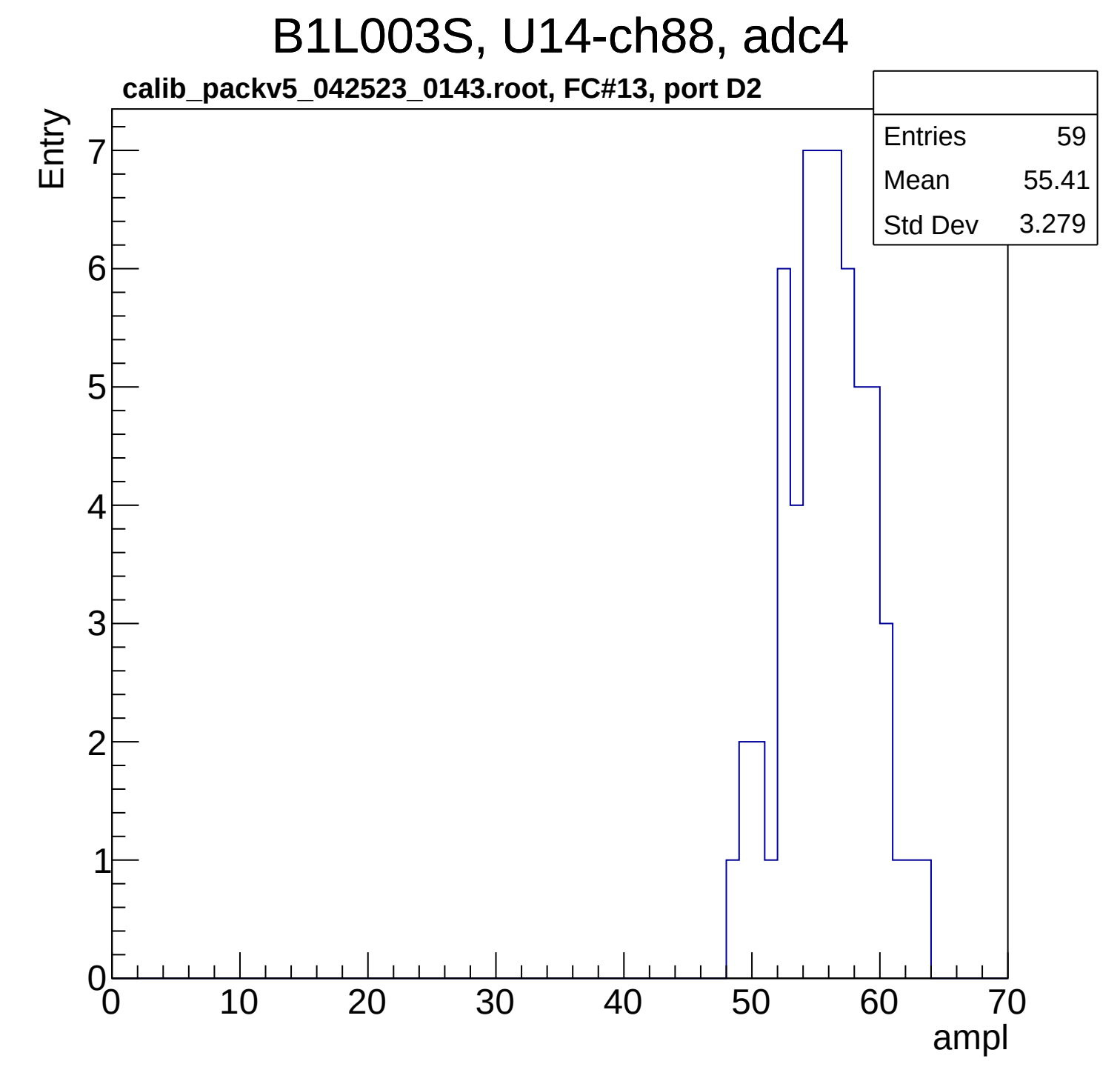

B1L003S, U14-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

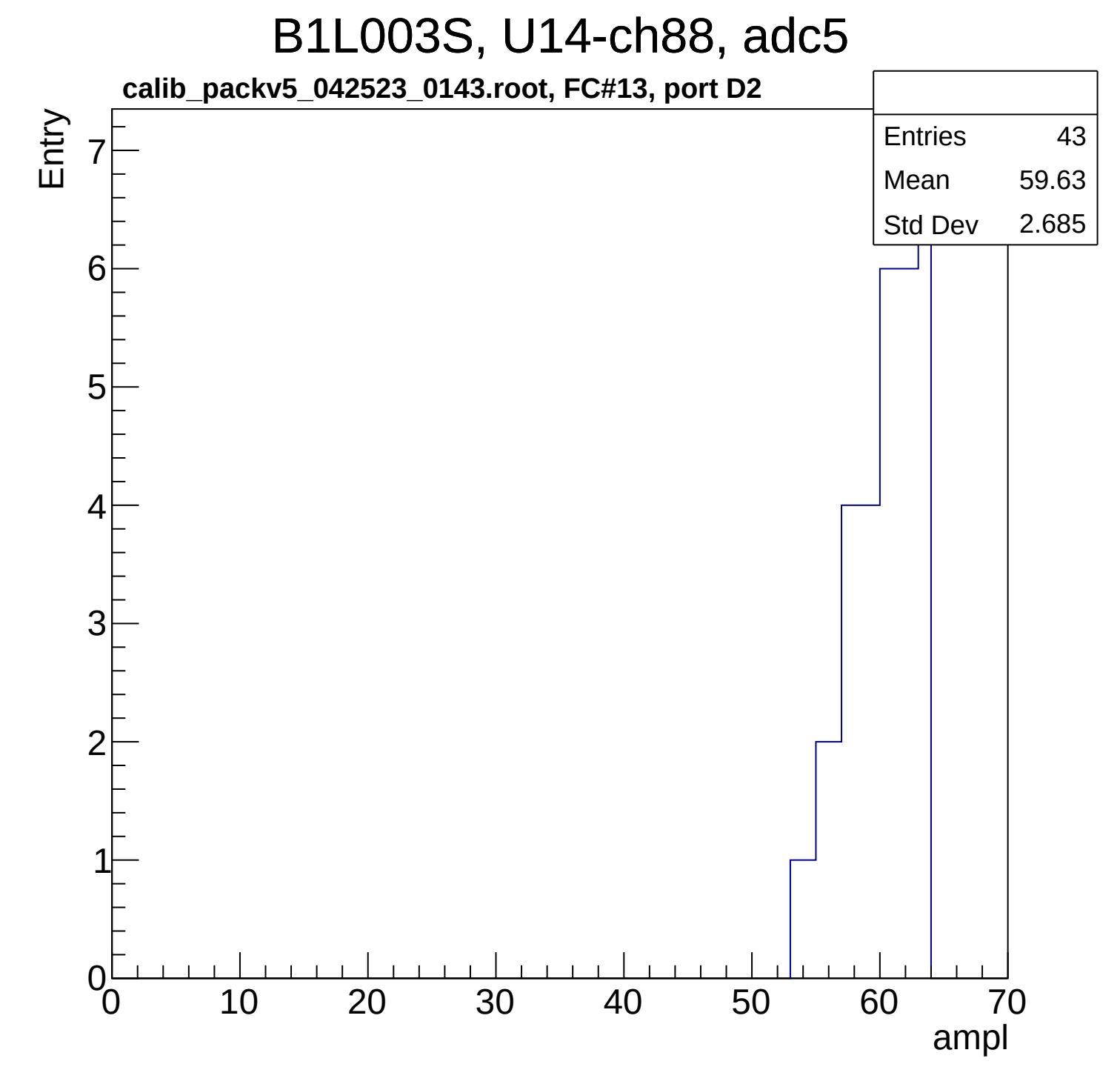

B1L003S, U14-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.34
Std Dev 6.112
Gaus mean : 30.6570
Gaus Width: 3.1783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

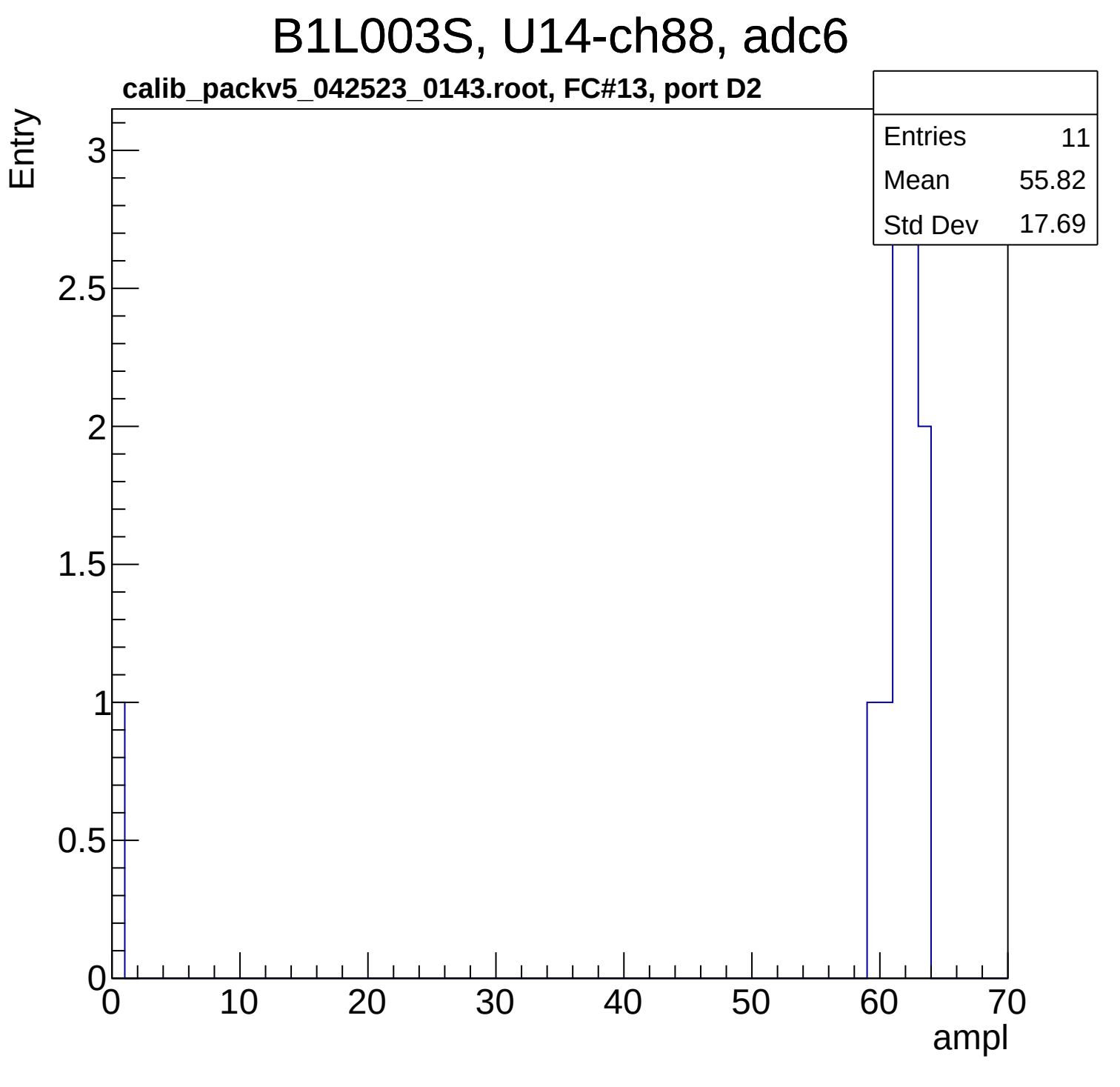

B1L003S, U14-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.6
Std Dev 3.067
Gaus mean : 37.0047
Gaus Width: 3.2252
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

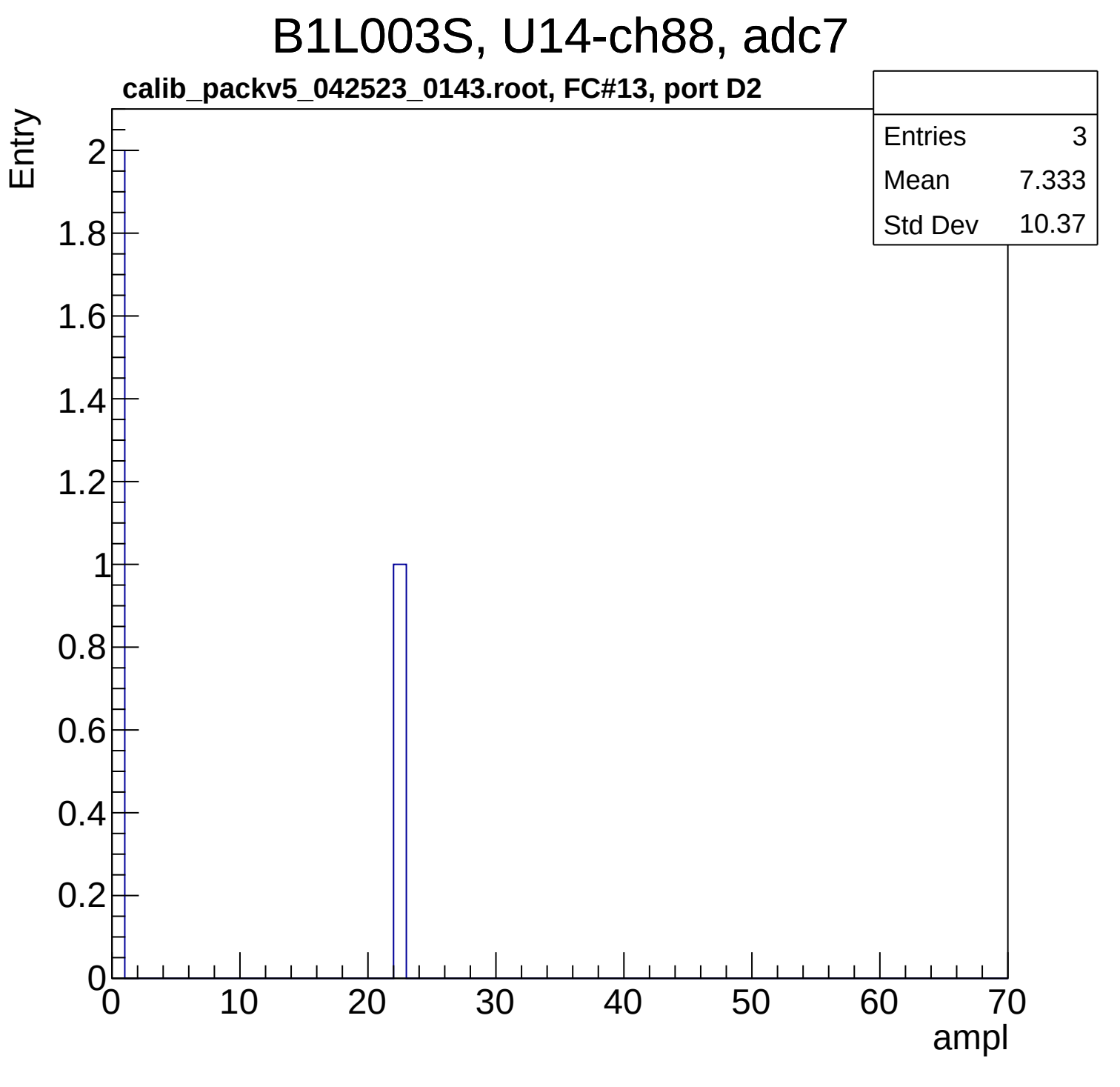

B1L003S, U14-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.84
Std Dev 3.067
Gaus mean : 42.4944
Gaus Width: 2.8737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

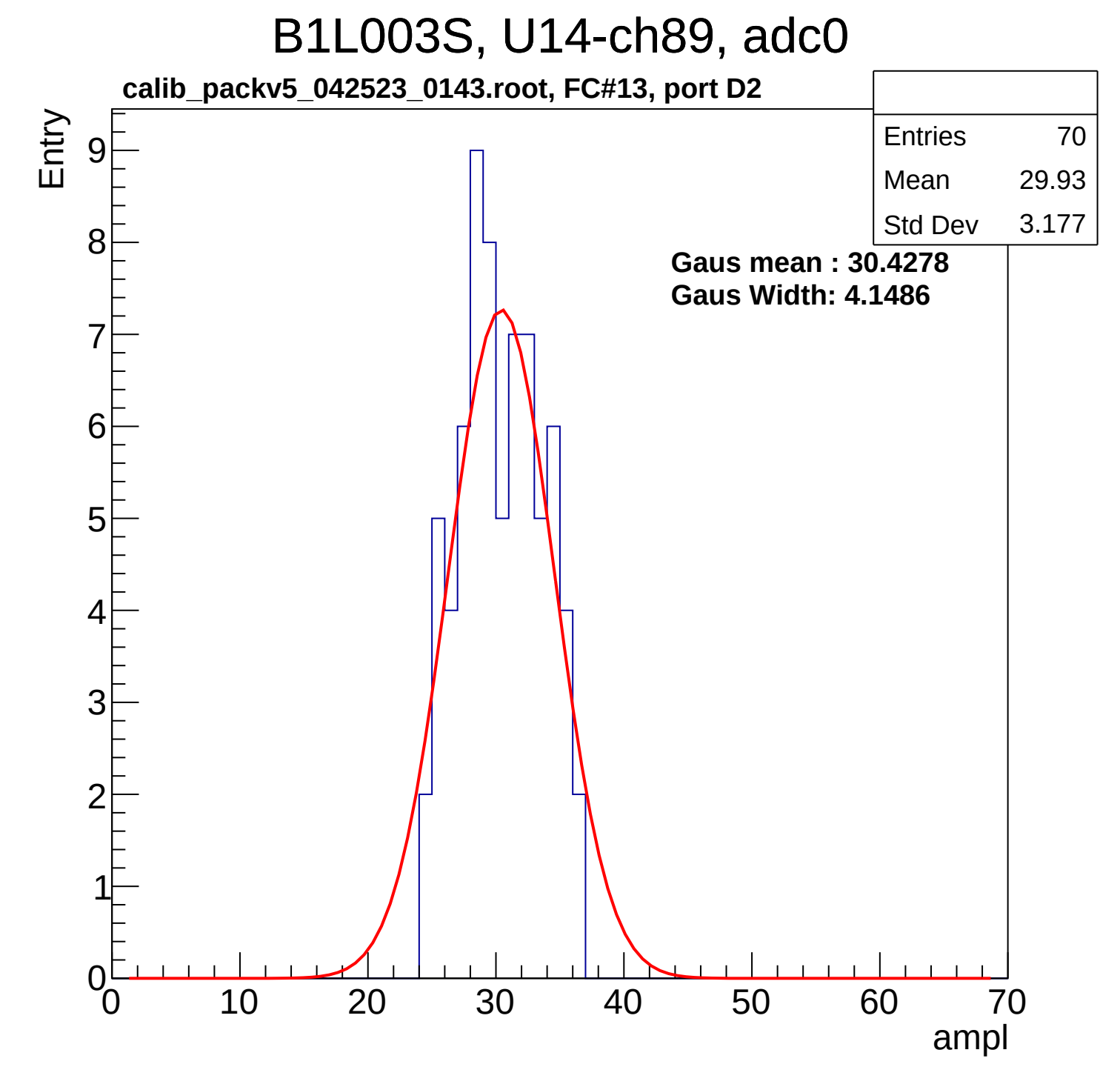

B1L003S, U14-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.03
Std Dev 3.126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

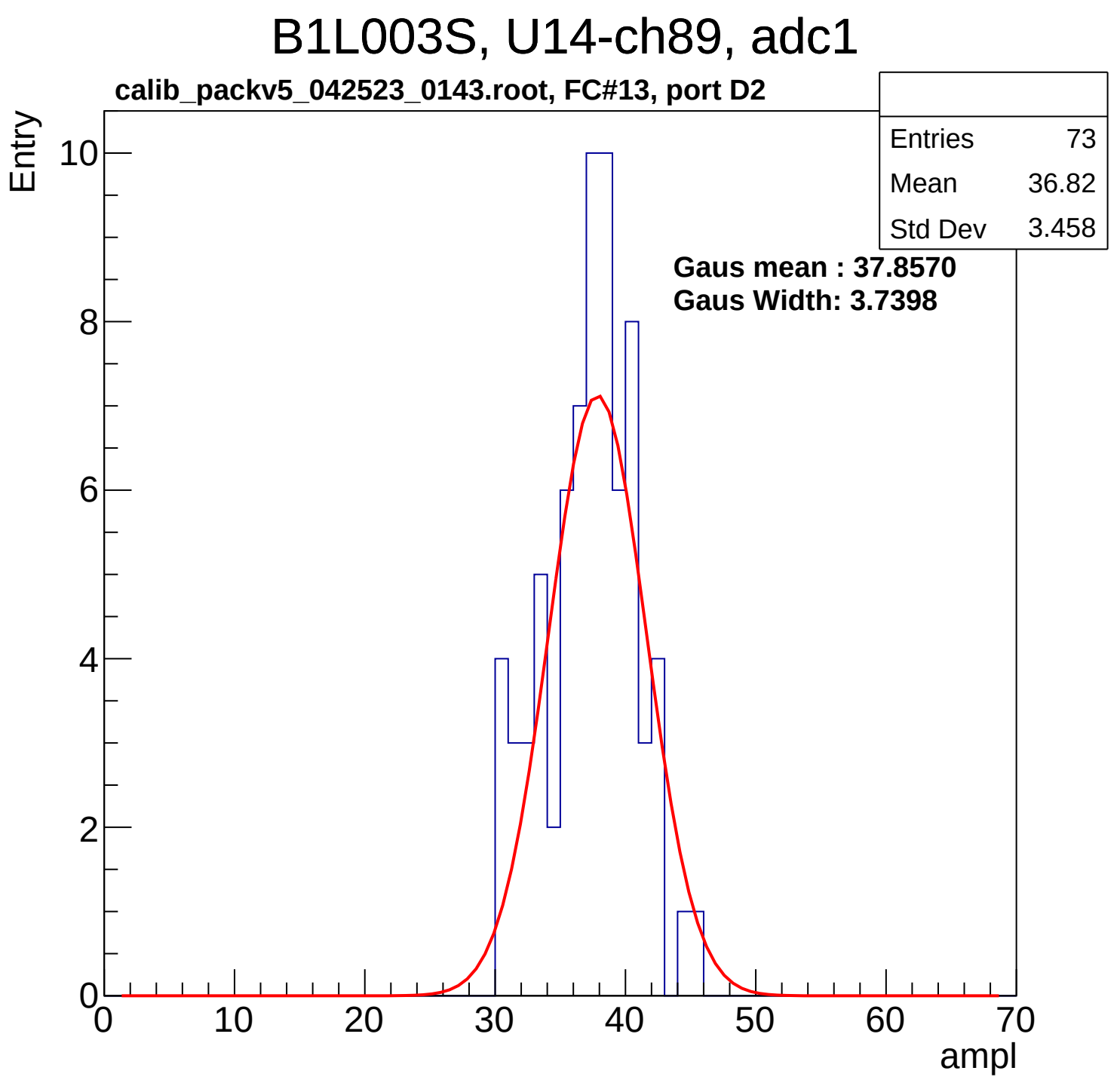

B1L003S, U14-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.41
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

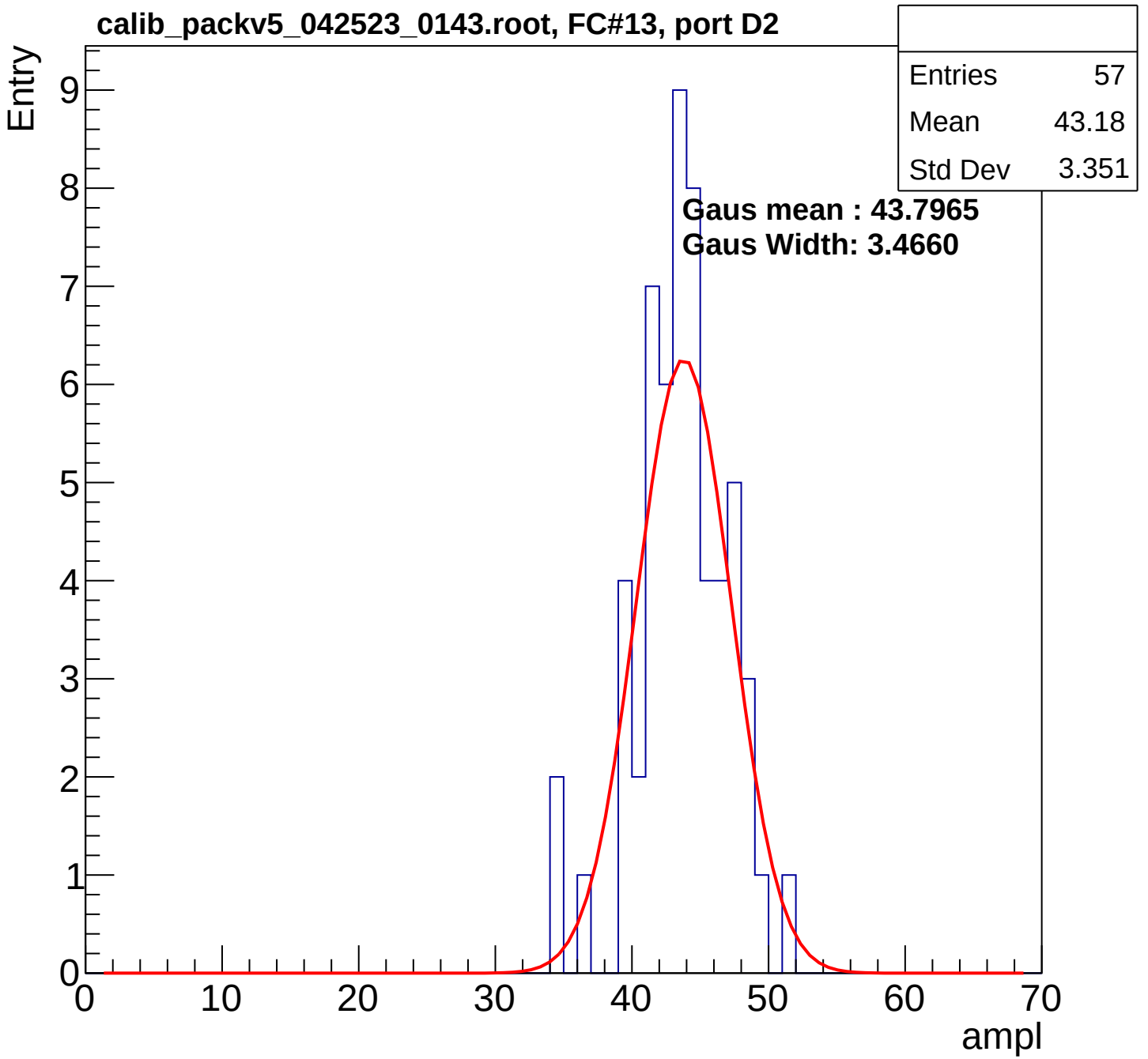

B1L003S, U14-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.63
Std Dev 2.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

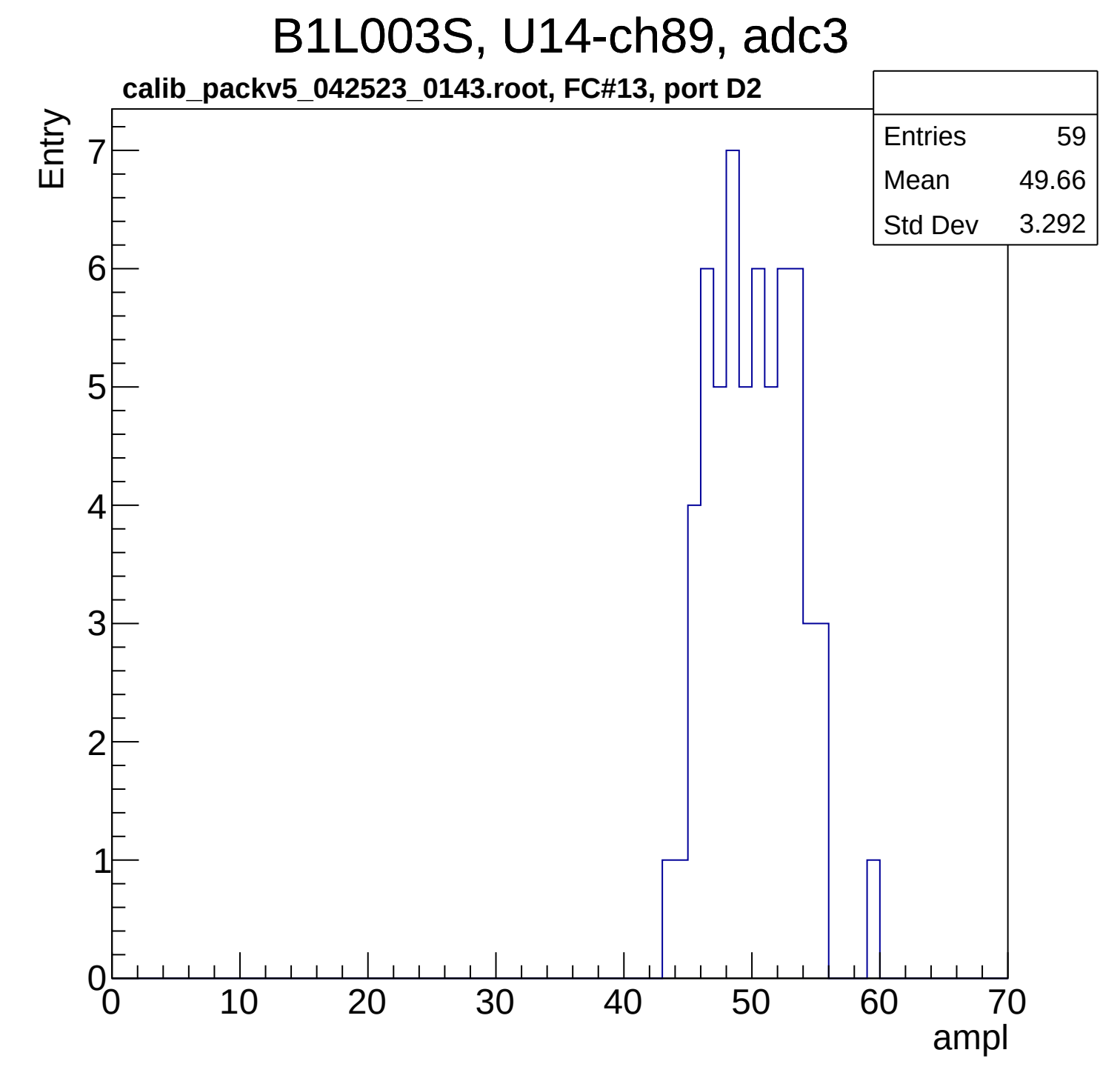

B1L003S, U14-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.82
Std Dev 17.69
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

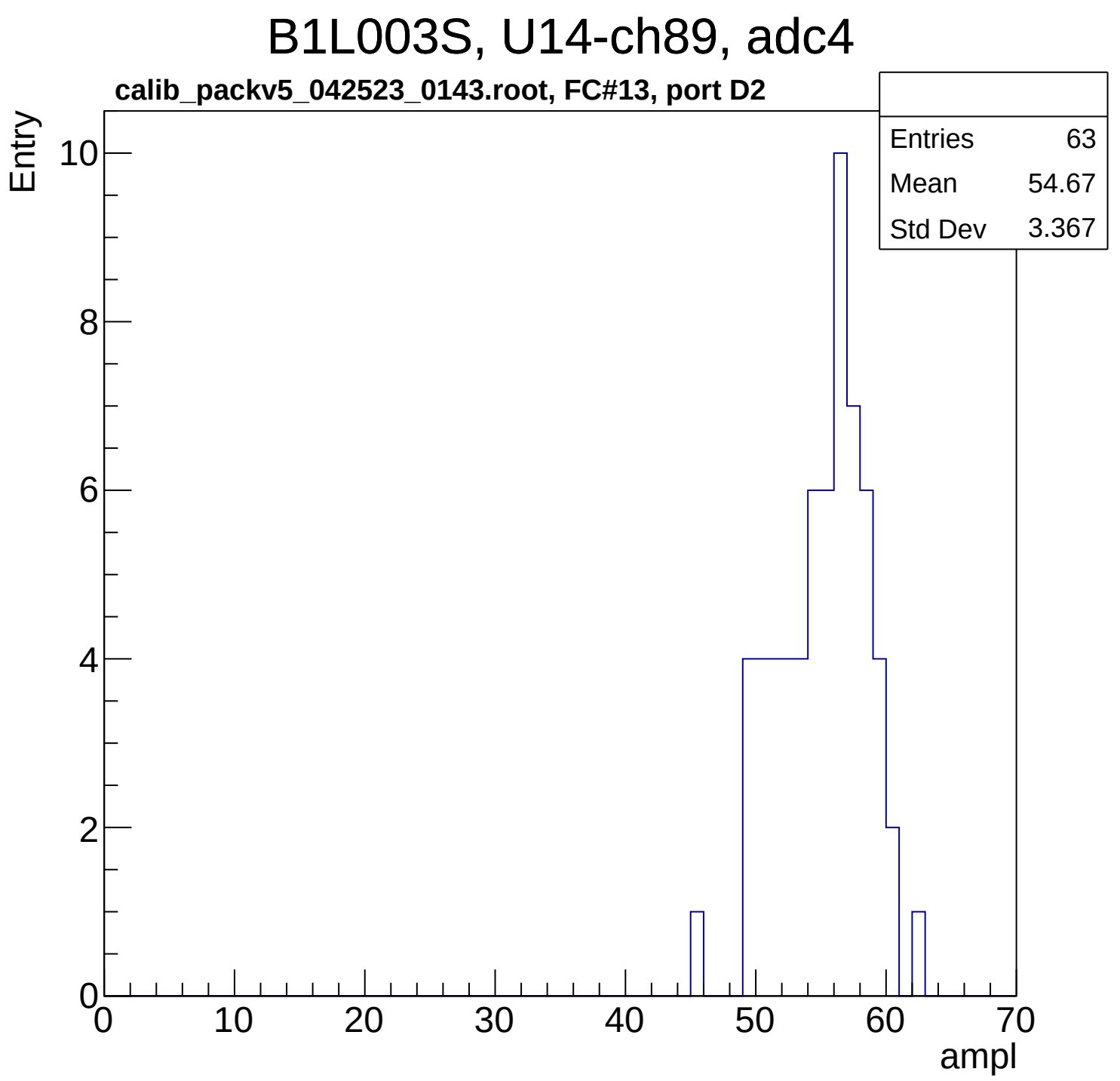

B1L003S, U14-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

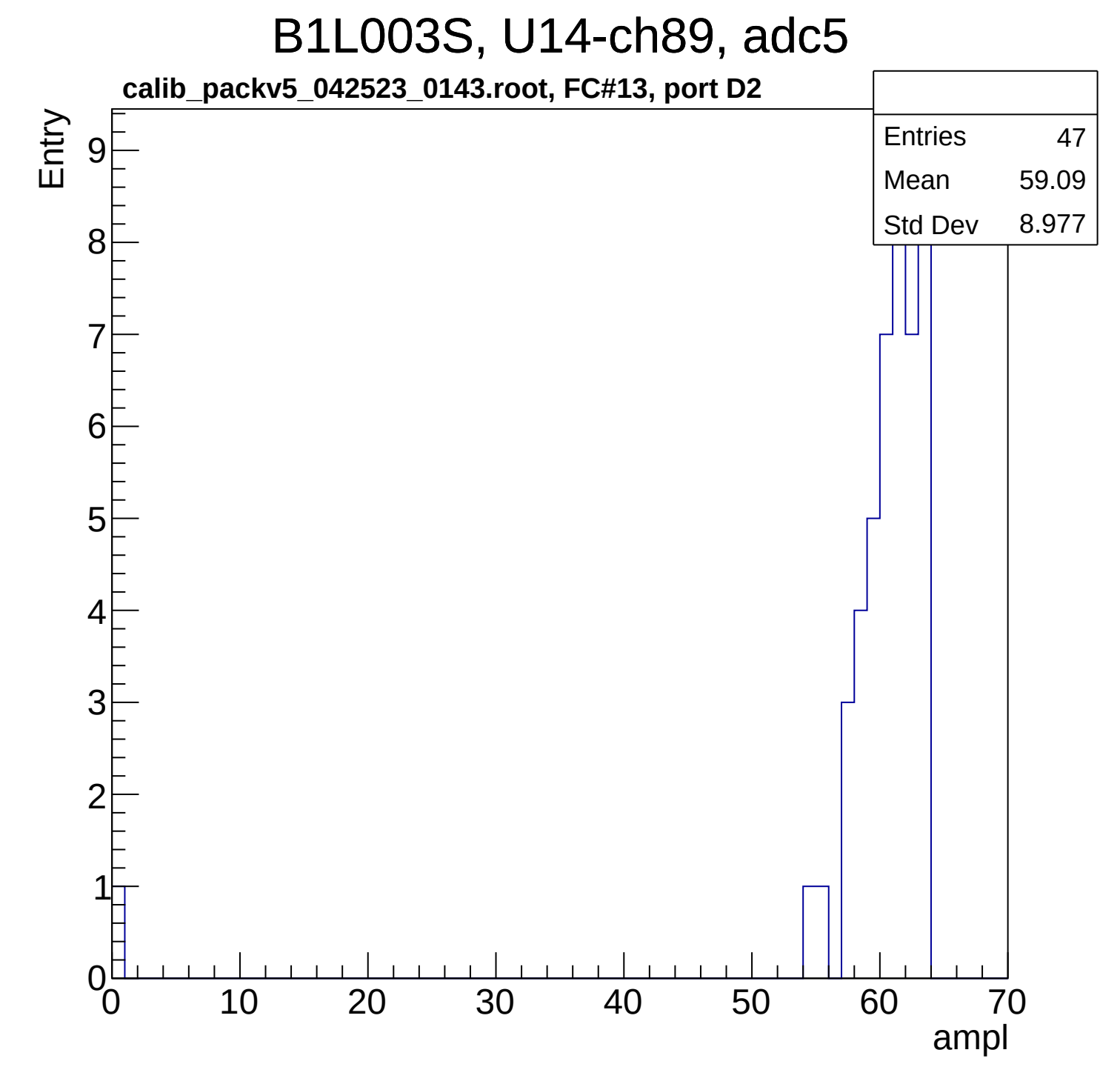

B1L003S, U14-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.93
Std Dev 3.177
Gaus mean : 30.4278
Gaus Width: 4.1486
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

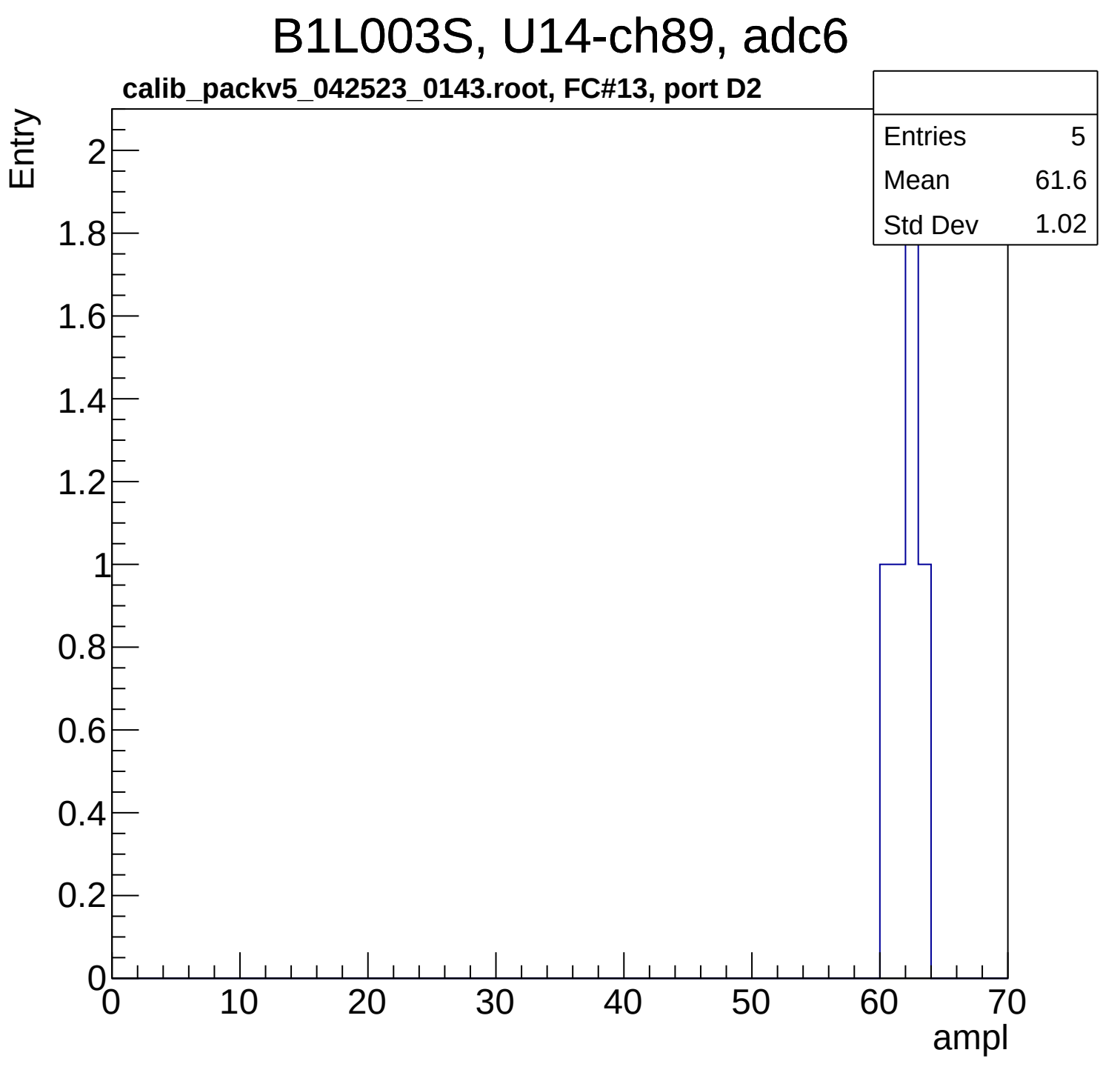

B1L003S, U14-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.82
Std Dev 3.458
Gaus mean : 37.8570
Gaus Width: 3.7398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

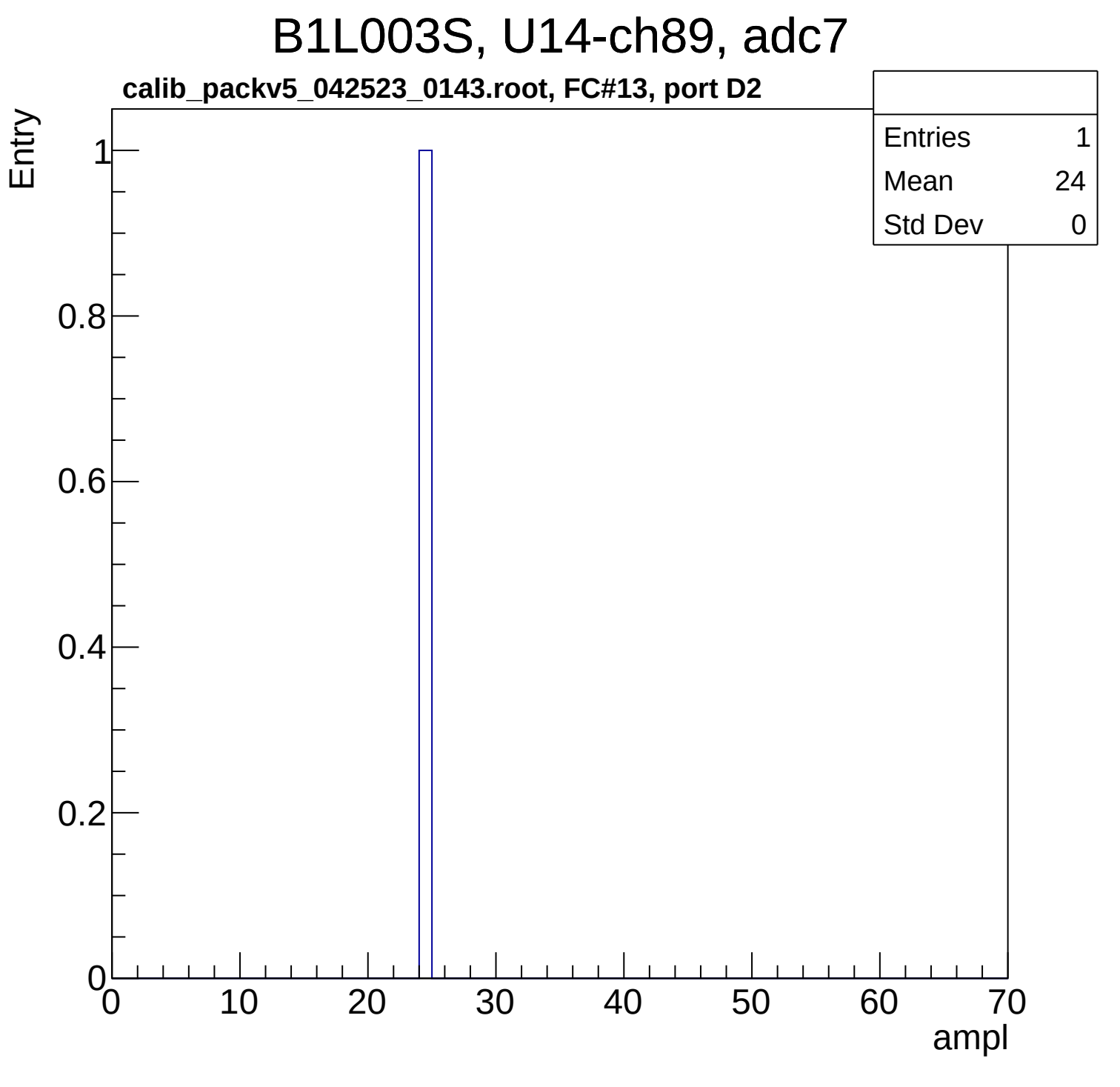

B1L003S, U14-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.18
Std Dev 3.351
Gaus mean : 43.7965
Gaus Width: 3.4660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

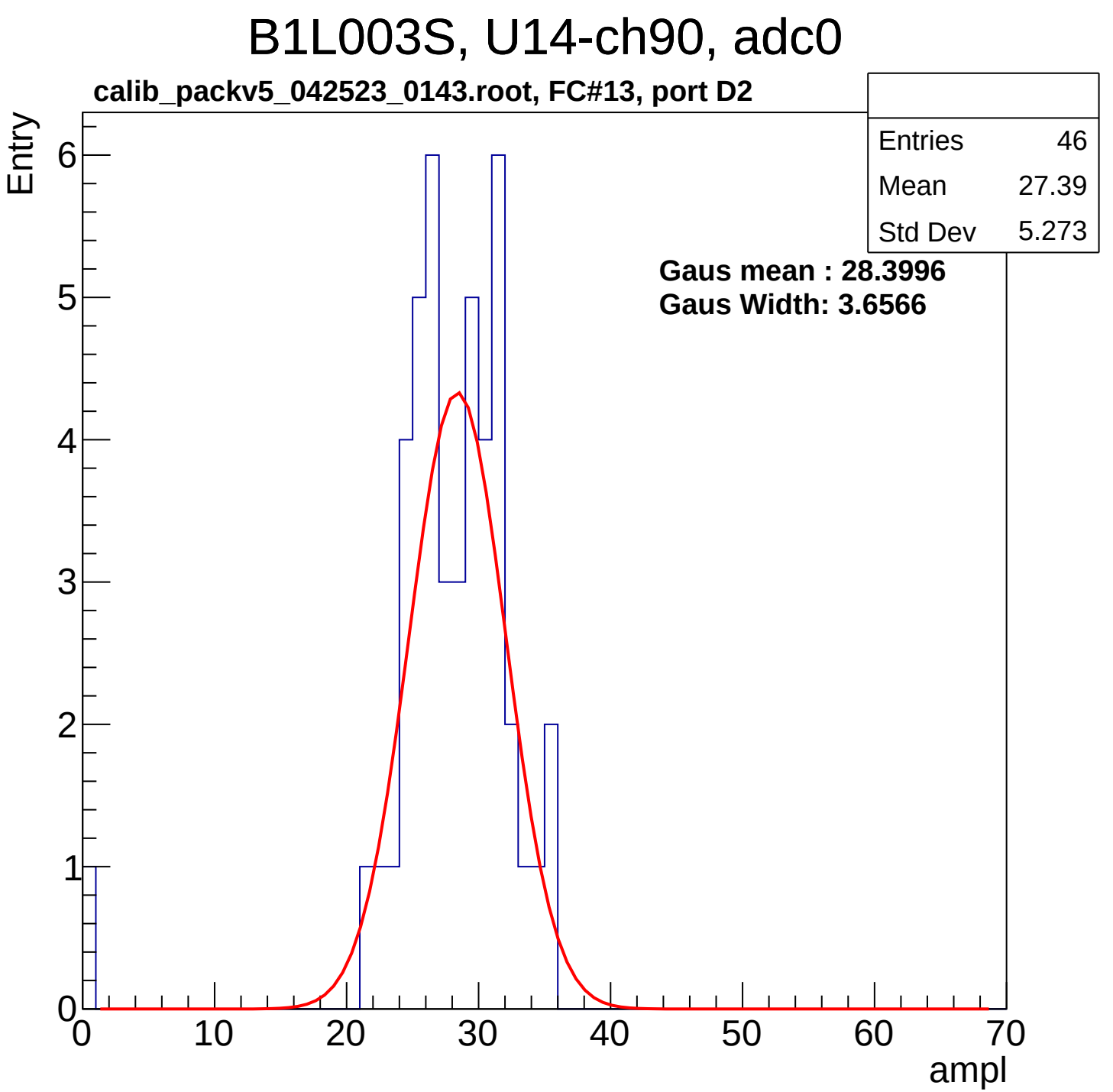

B1L003S, U14-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.66
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

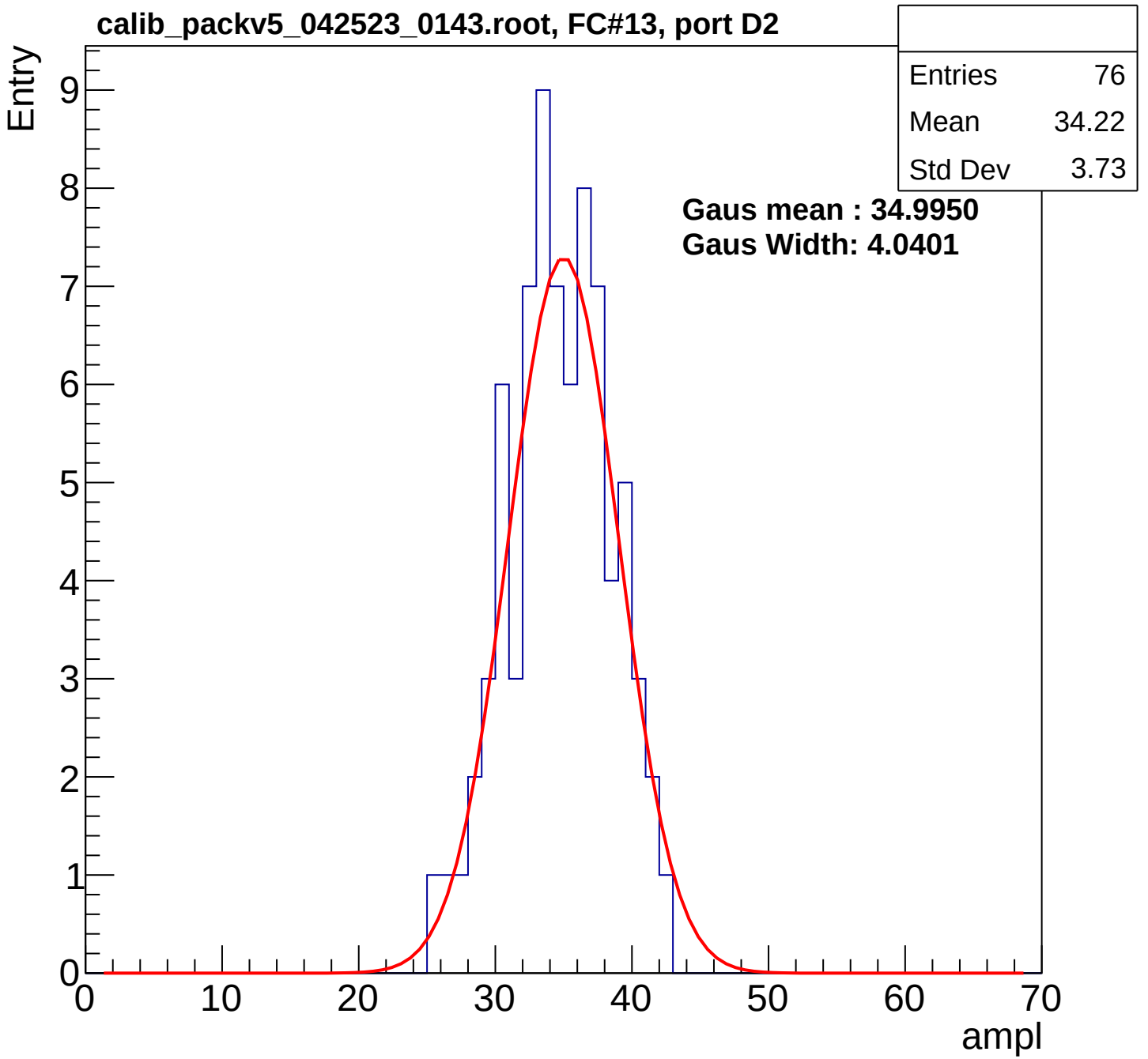

B1L003S, U14-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.67
Std Dev 3.367
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

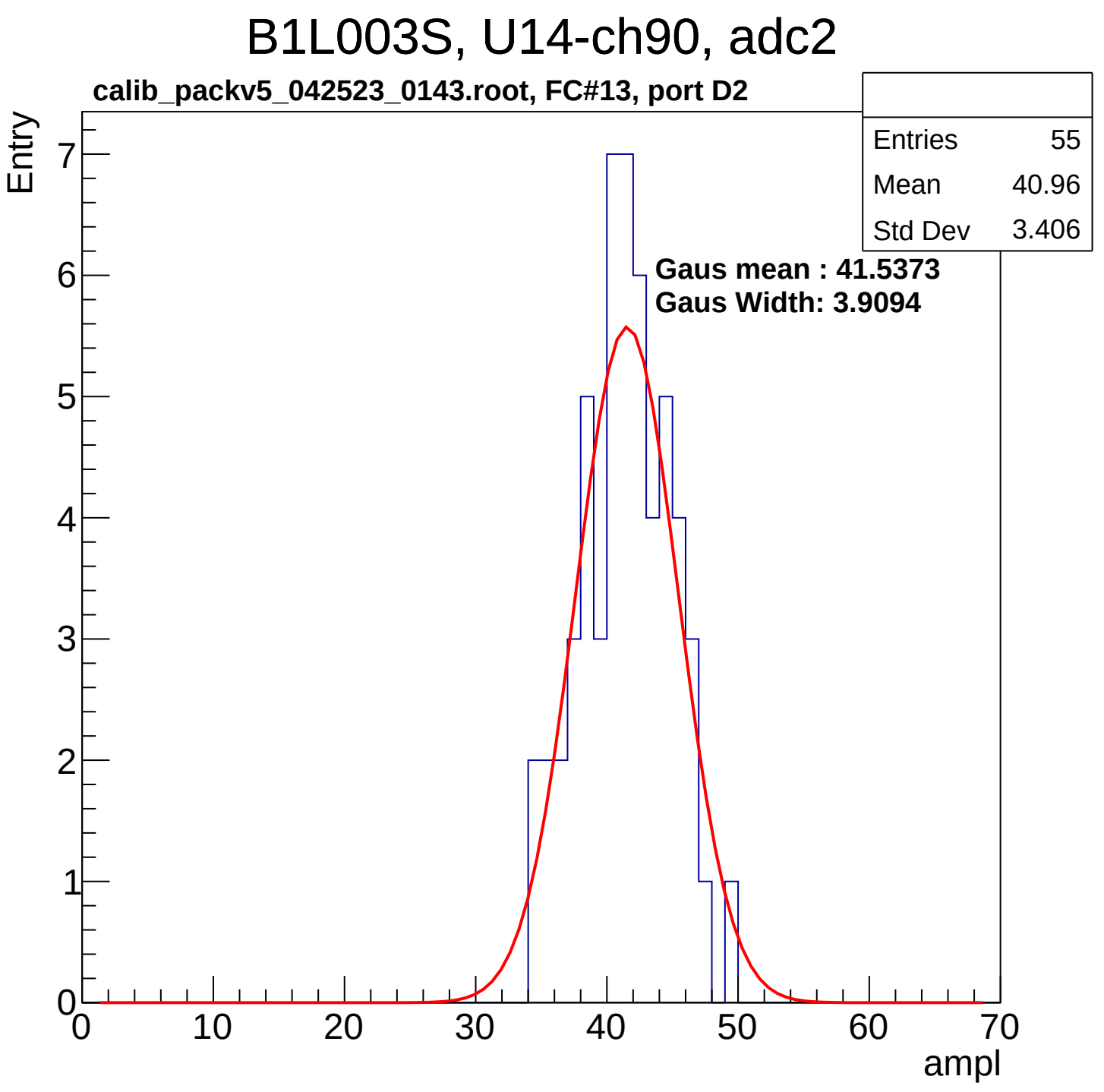

B1L003S, U14-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.09
Std Dev 8.977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

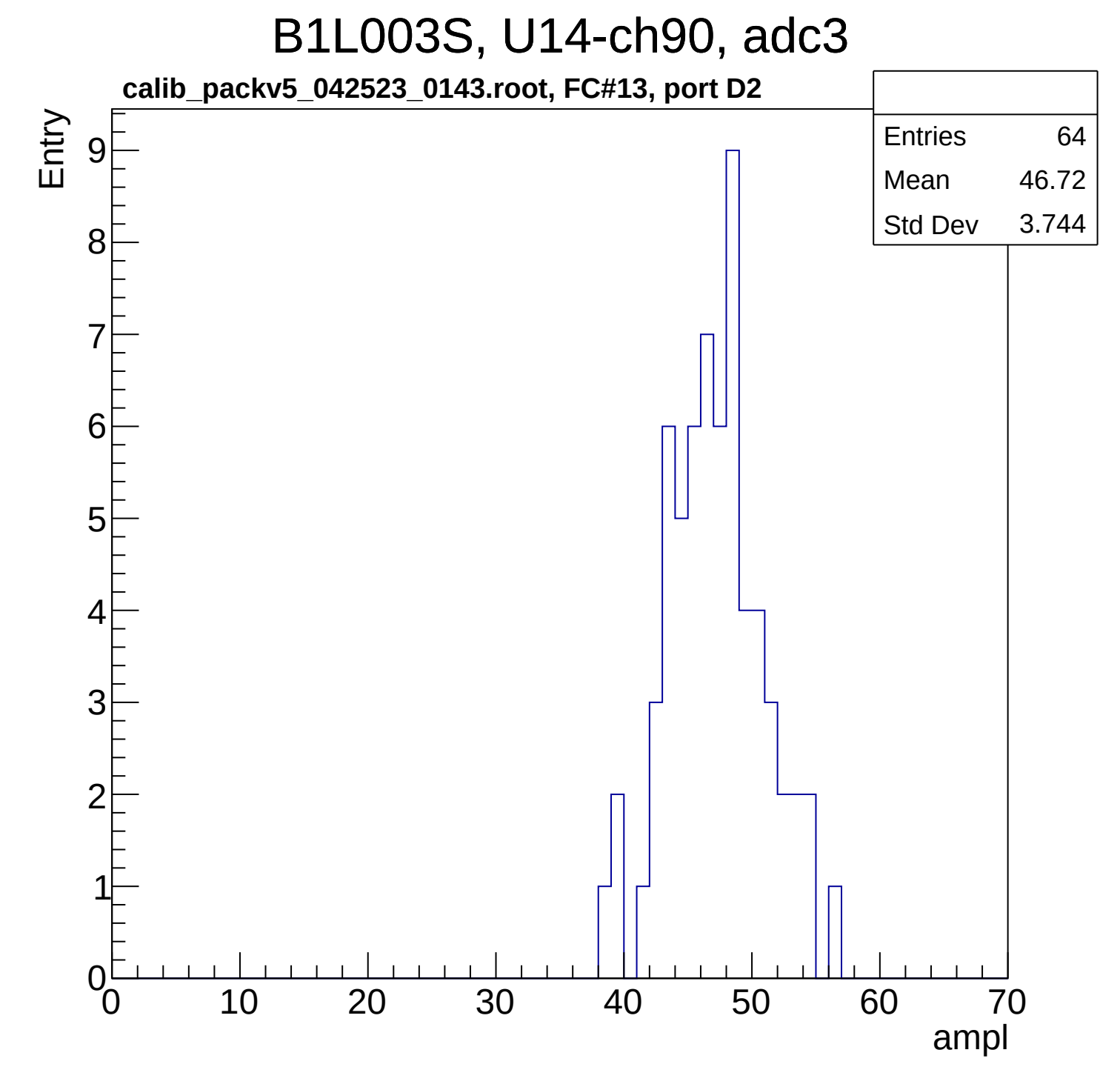

B1L003S, U14-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

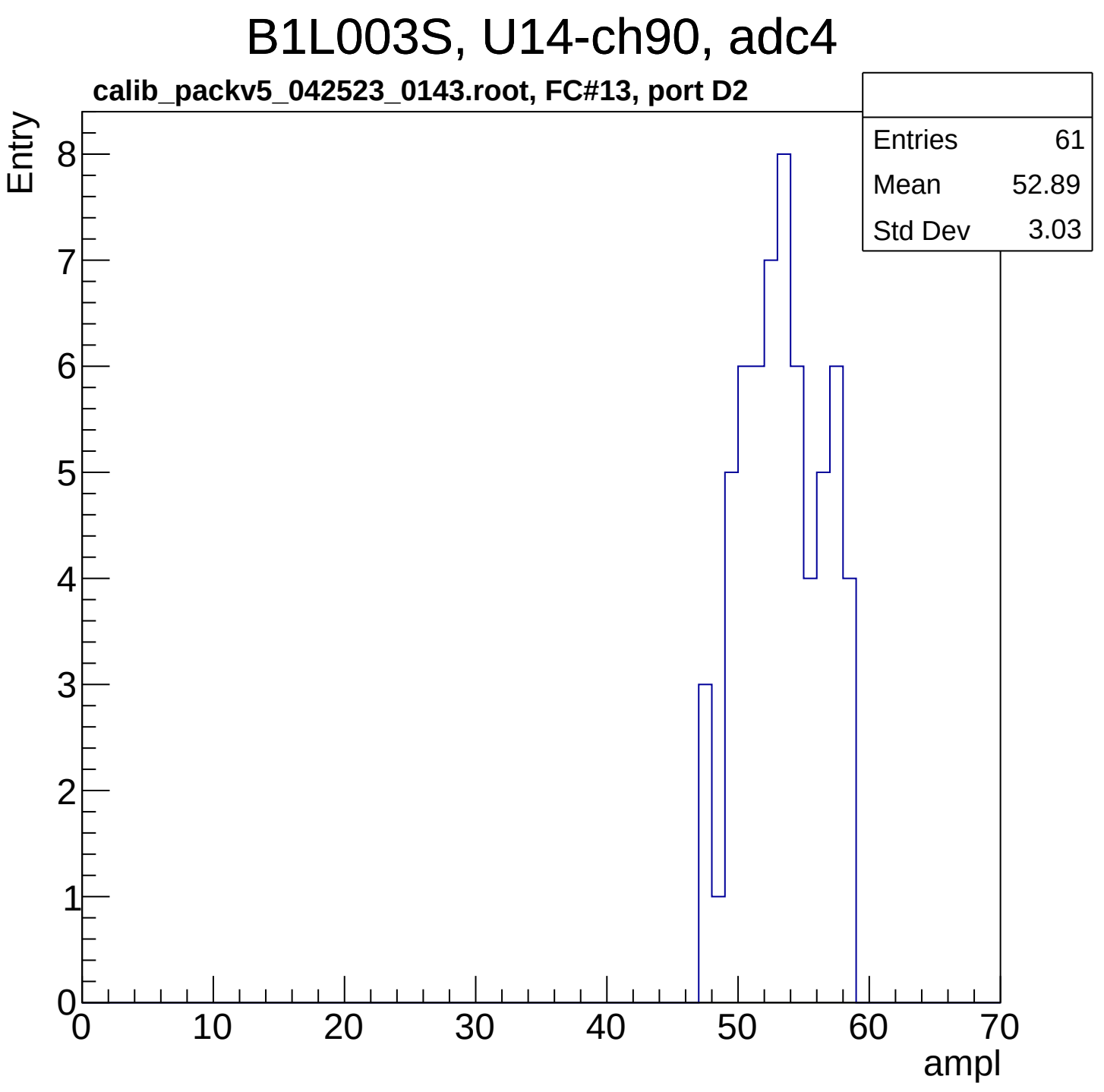

B1L003S, U14-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

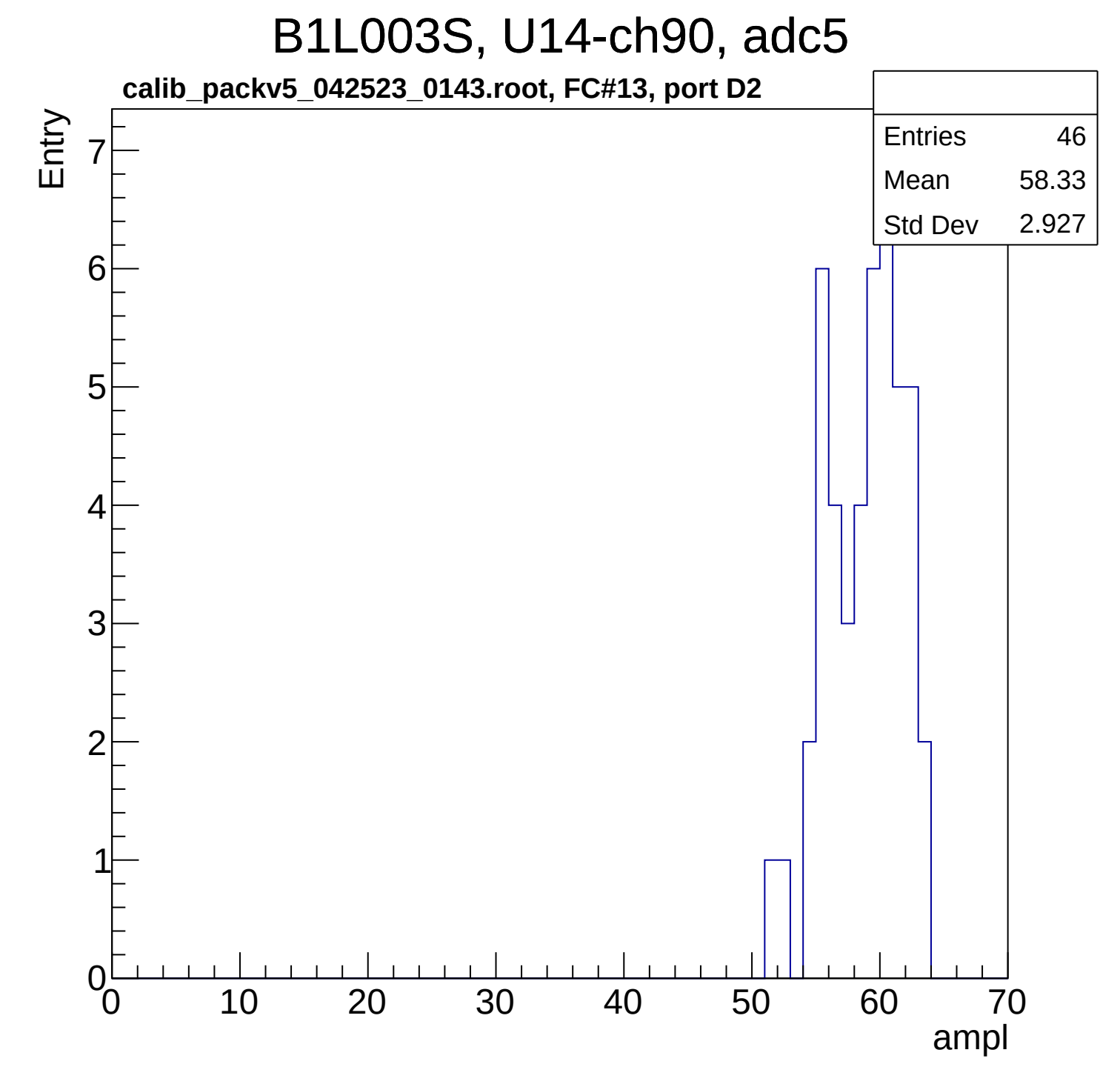

B1L003S, U14-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 27.39
Std Dev 5.273
Gaus mean : 28.3996
Gaus Width: 3.6566
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

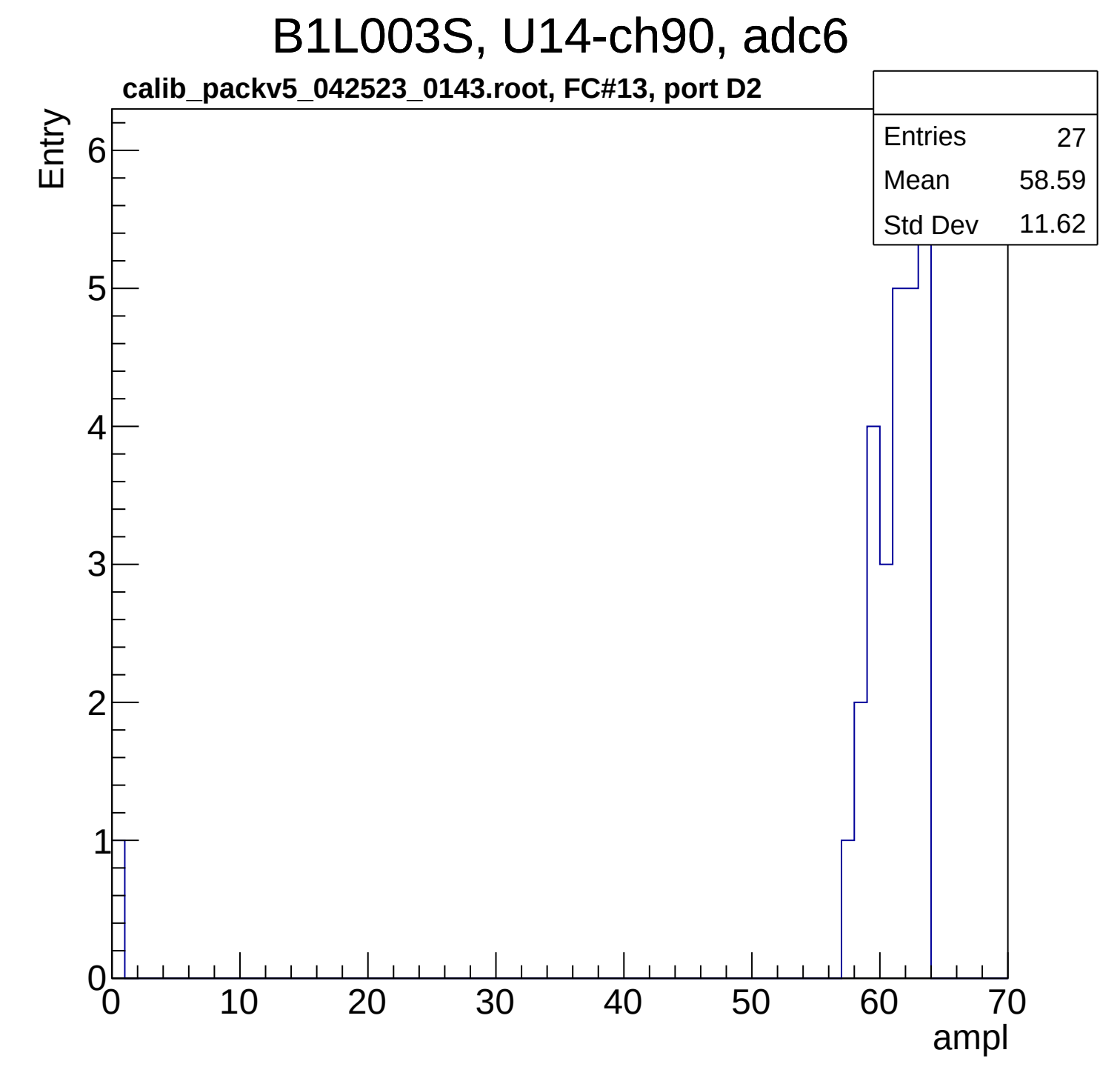

B1L003S, U14-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 34.22
Std Dev 3.73
Gaus mean : 34.9950
Gaus Width: 4.0401
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

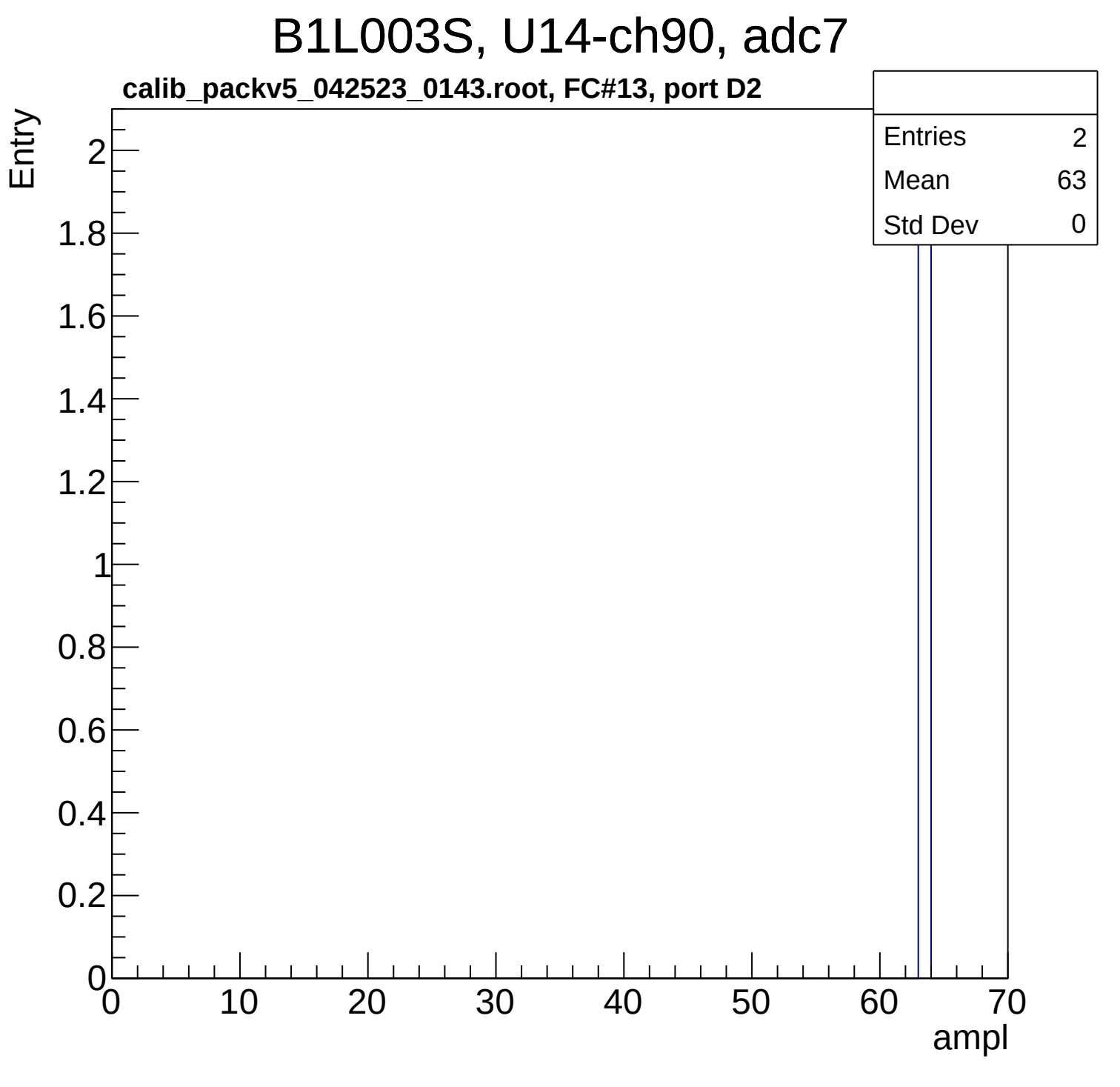

B1L003S, U14-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 40.96
Std Dev 3.406
Gaus mean : 41.5373
Gaus Width: 3.9094
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

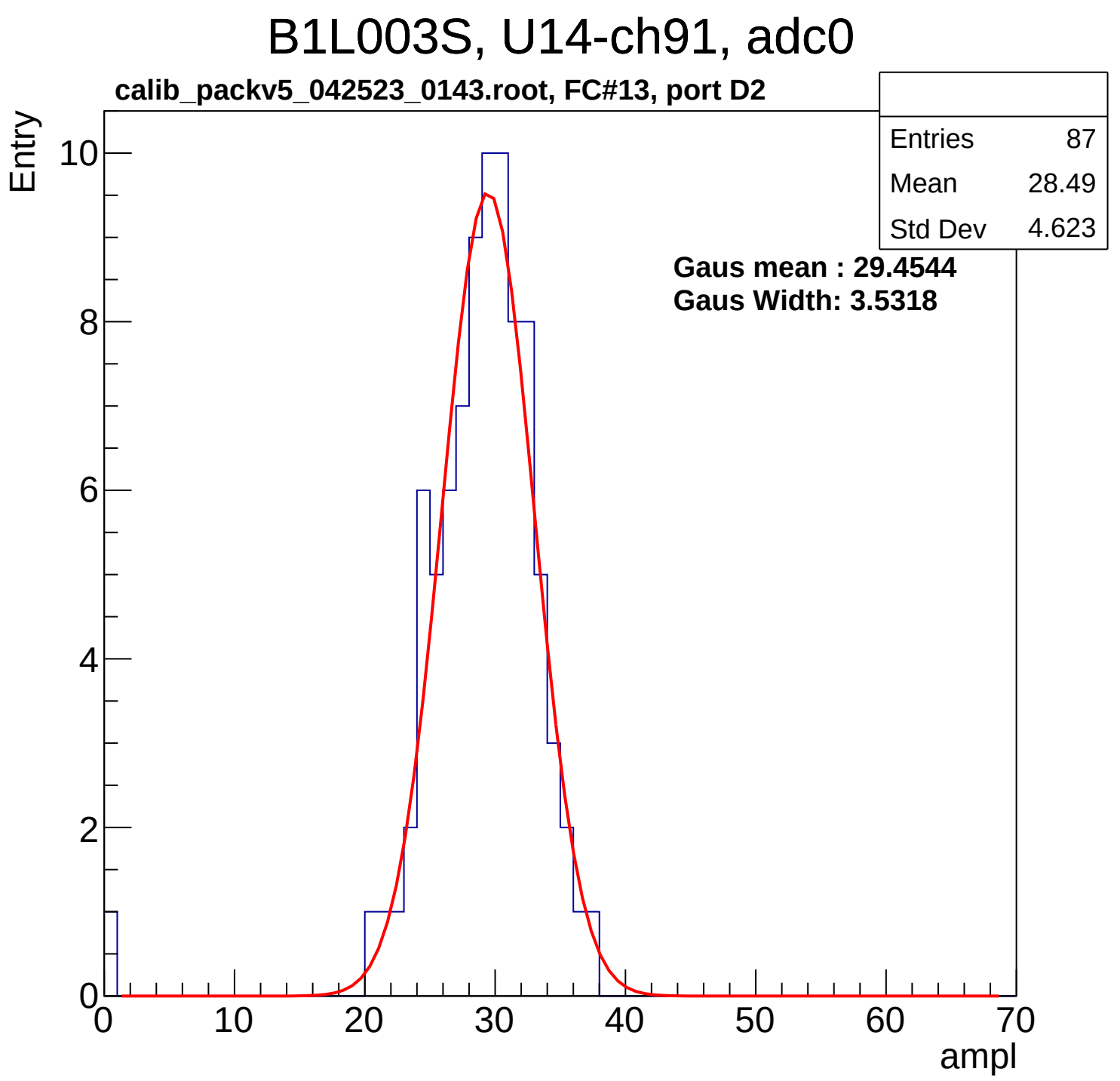

B1L003S, U14-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 46.72
Std Dev 3.744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

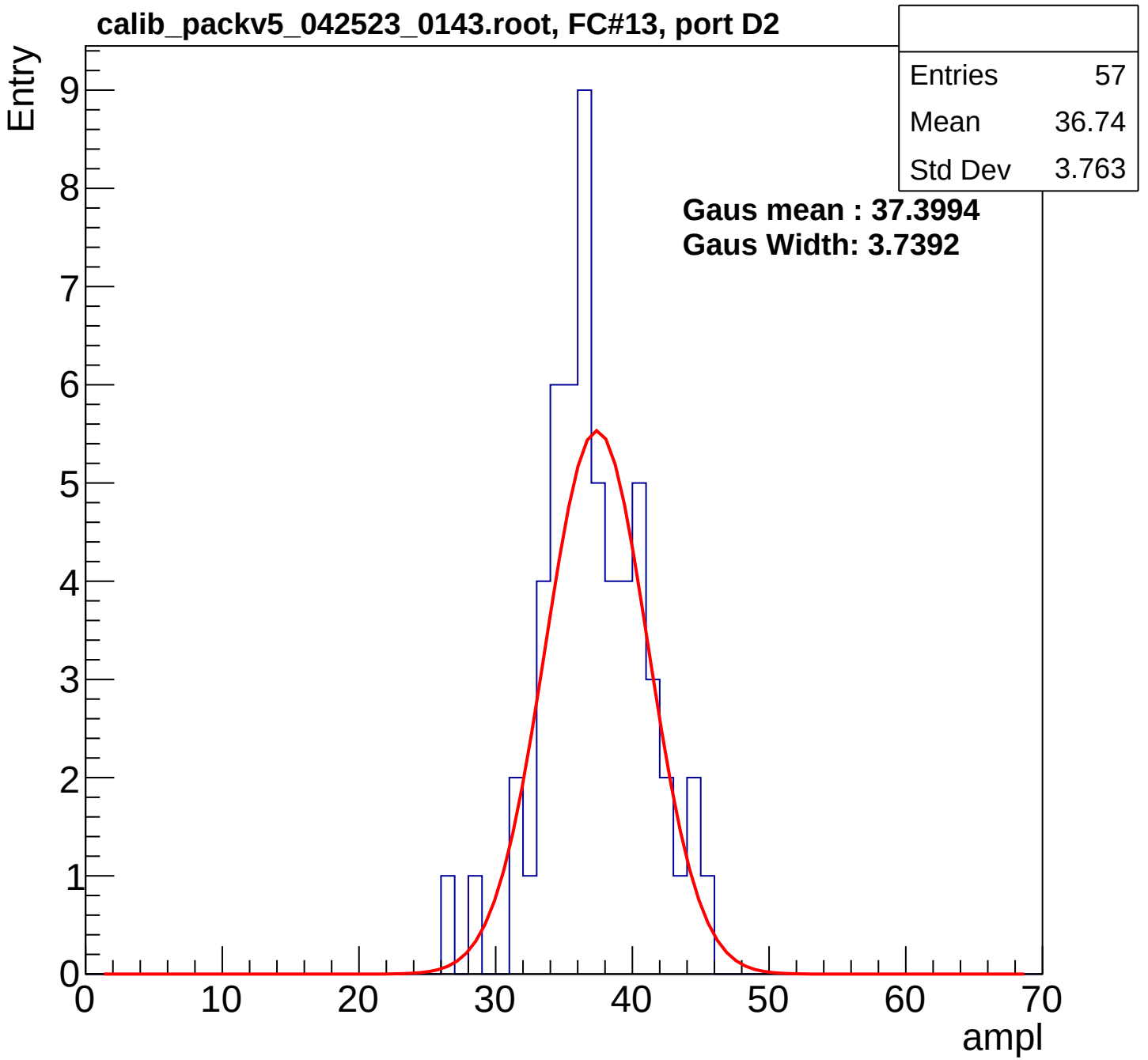

B1L003S, U14-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 52.89
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

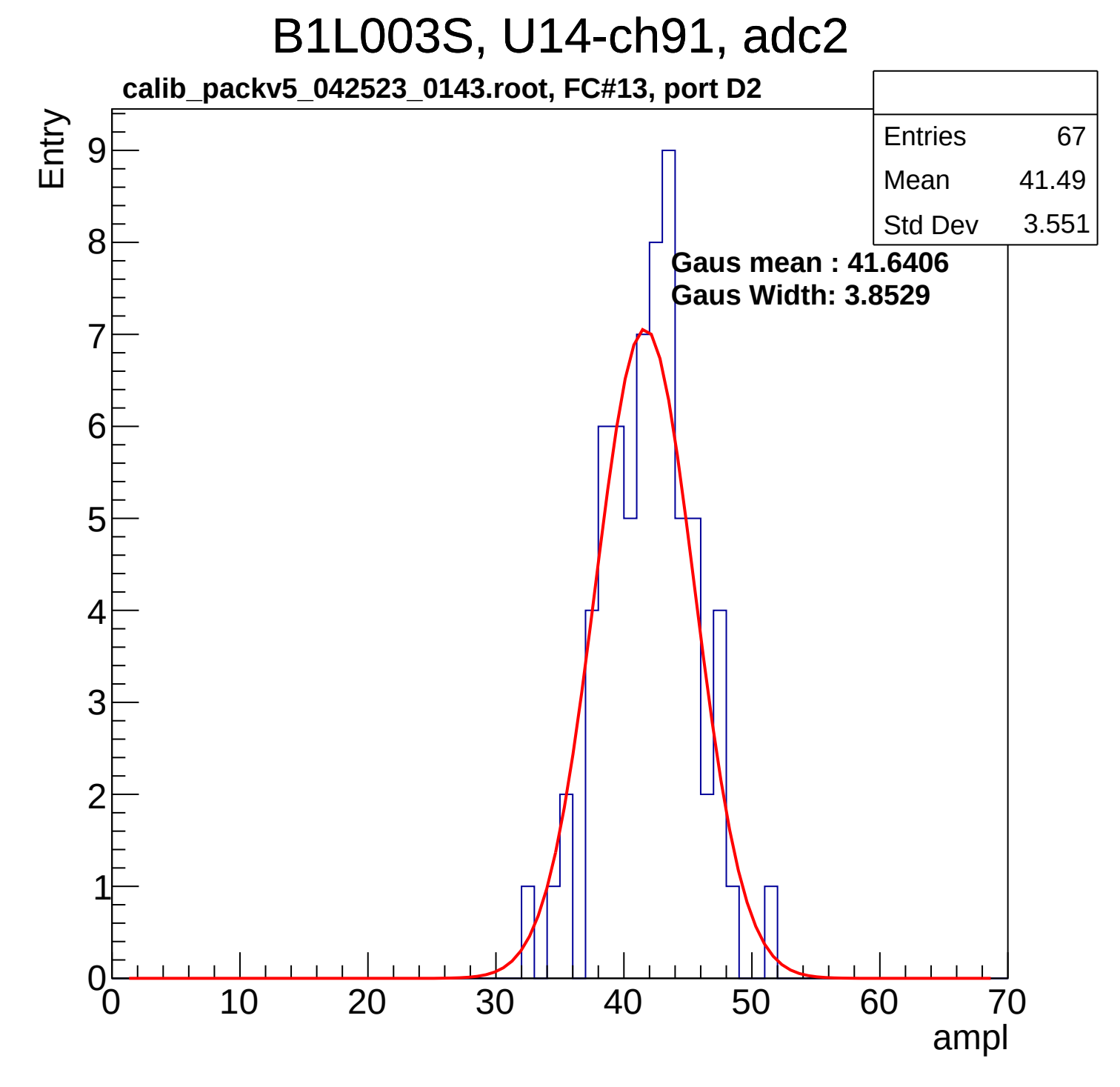

B1L003S, U14-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.33
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

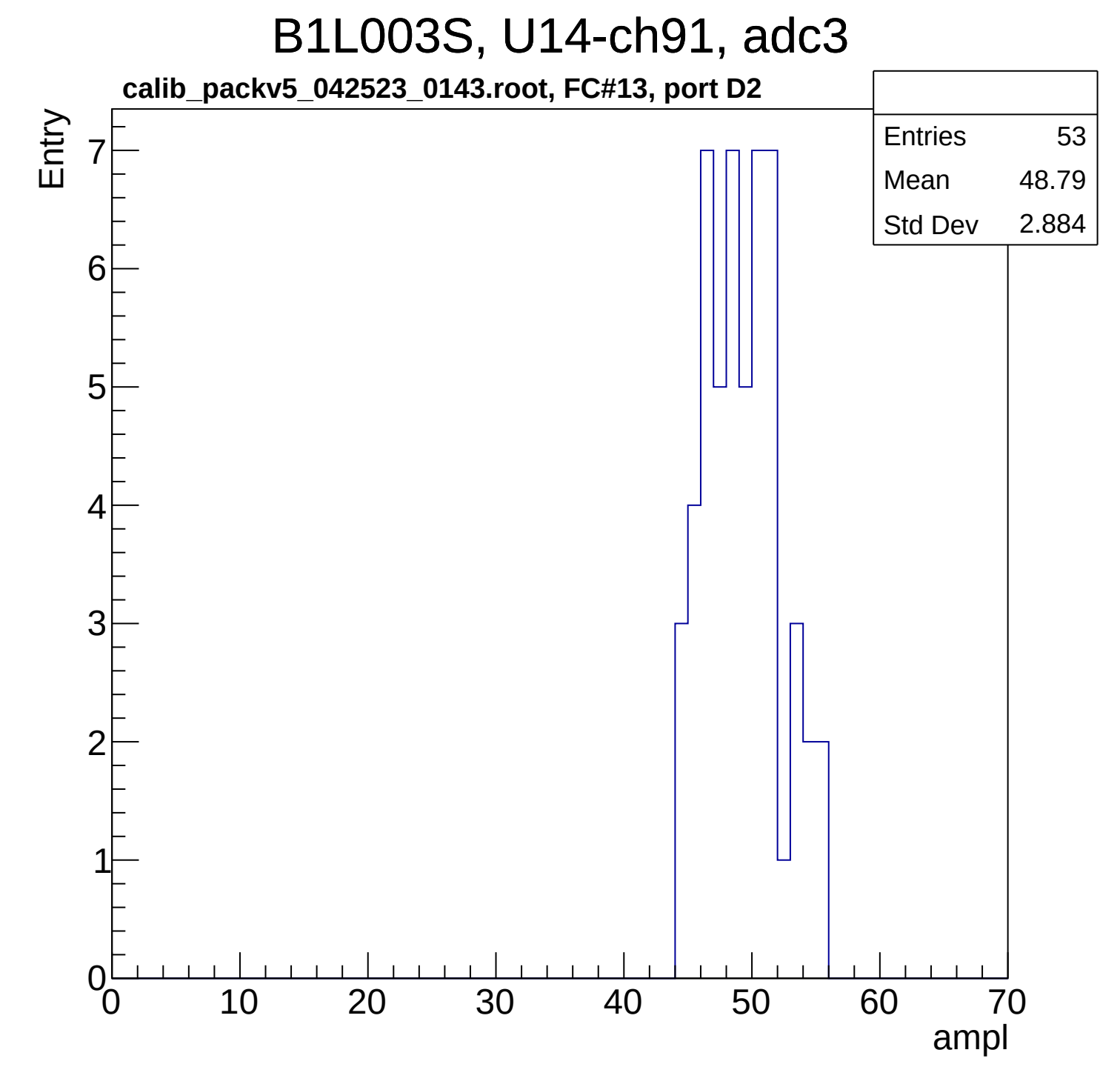

B1L003S, U14-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 27
Mean 58.59
Std Dev 11.62
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

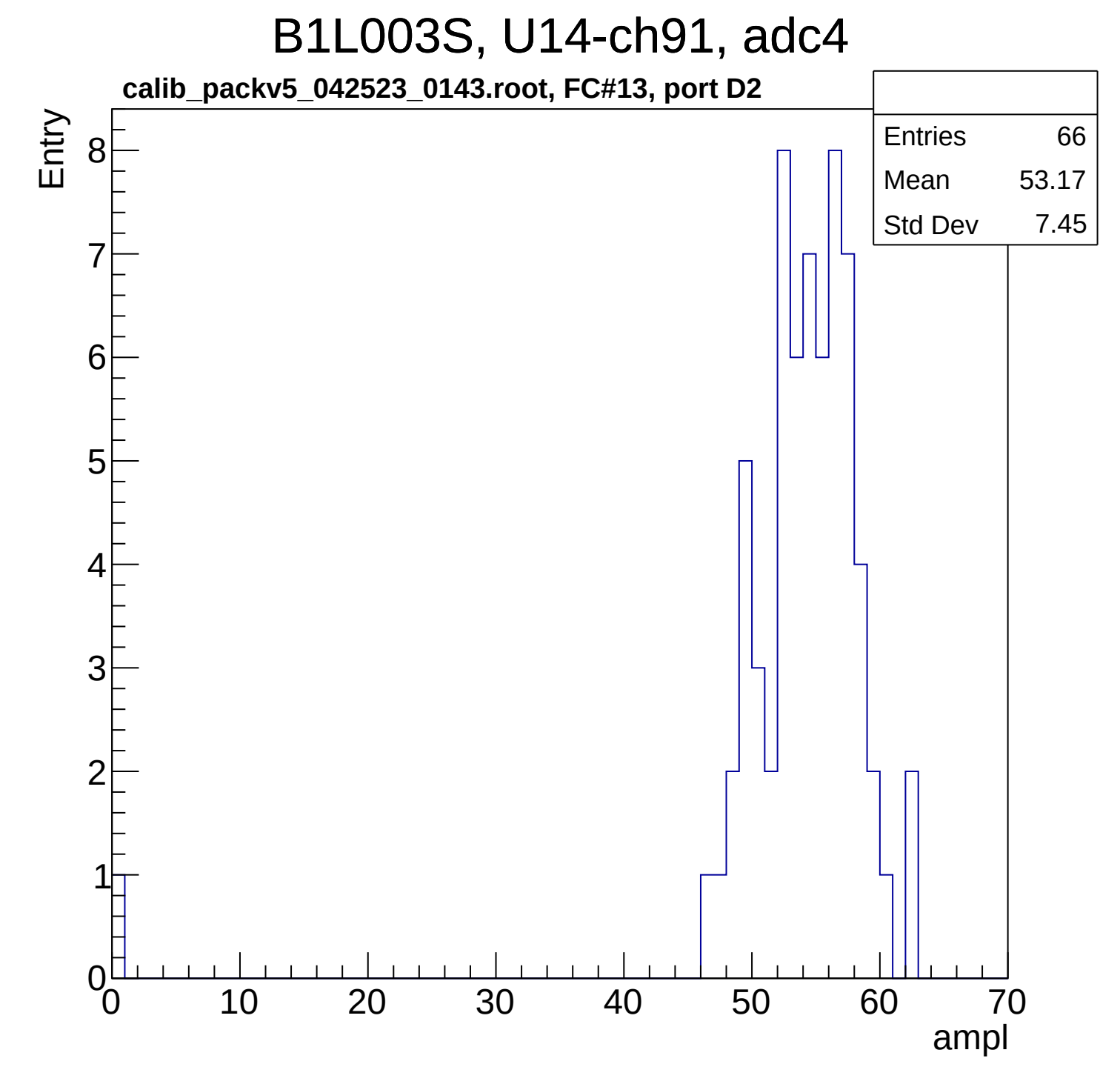

B1L003S, U14-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

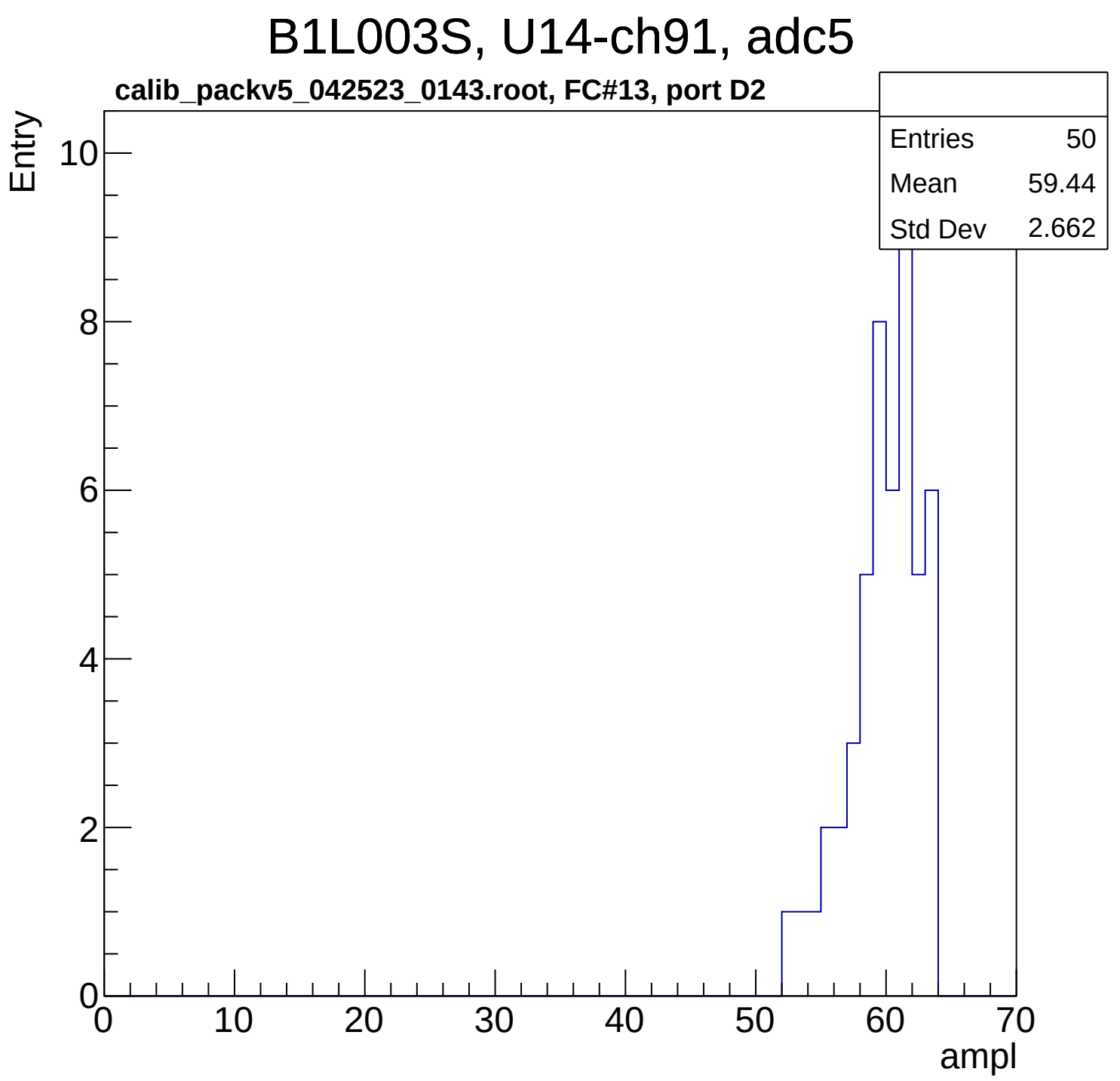

B1L003S, U14-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 28.49
Std Dev 4.623
Gaus mean : 29.4544
Gaus Width: 3.5318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

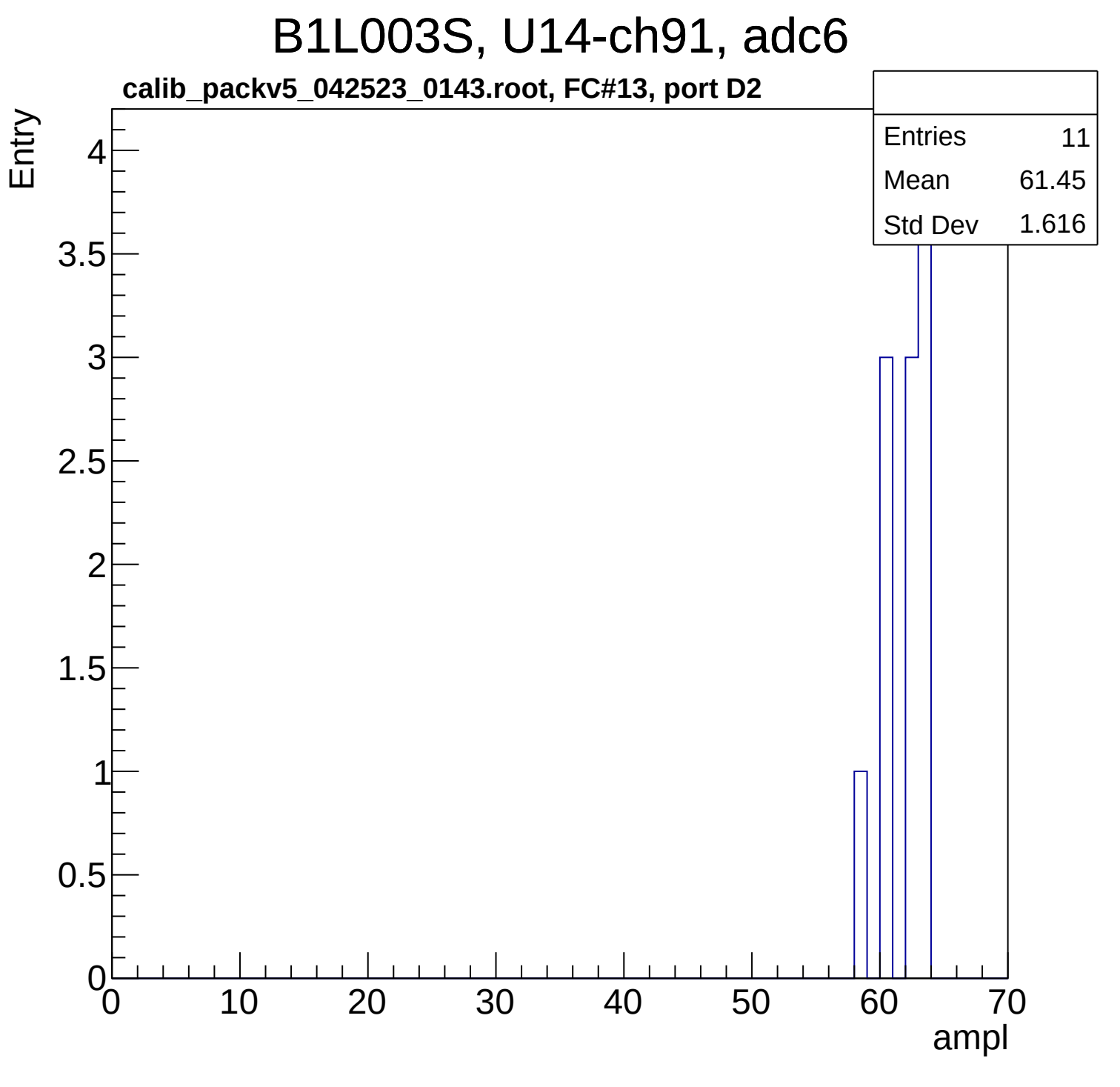

B1L003S, U14-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.74
Std Dev 3.763
Gaus mean : 37.3994
Gaus Width: 3.7392
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

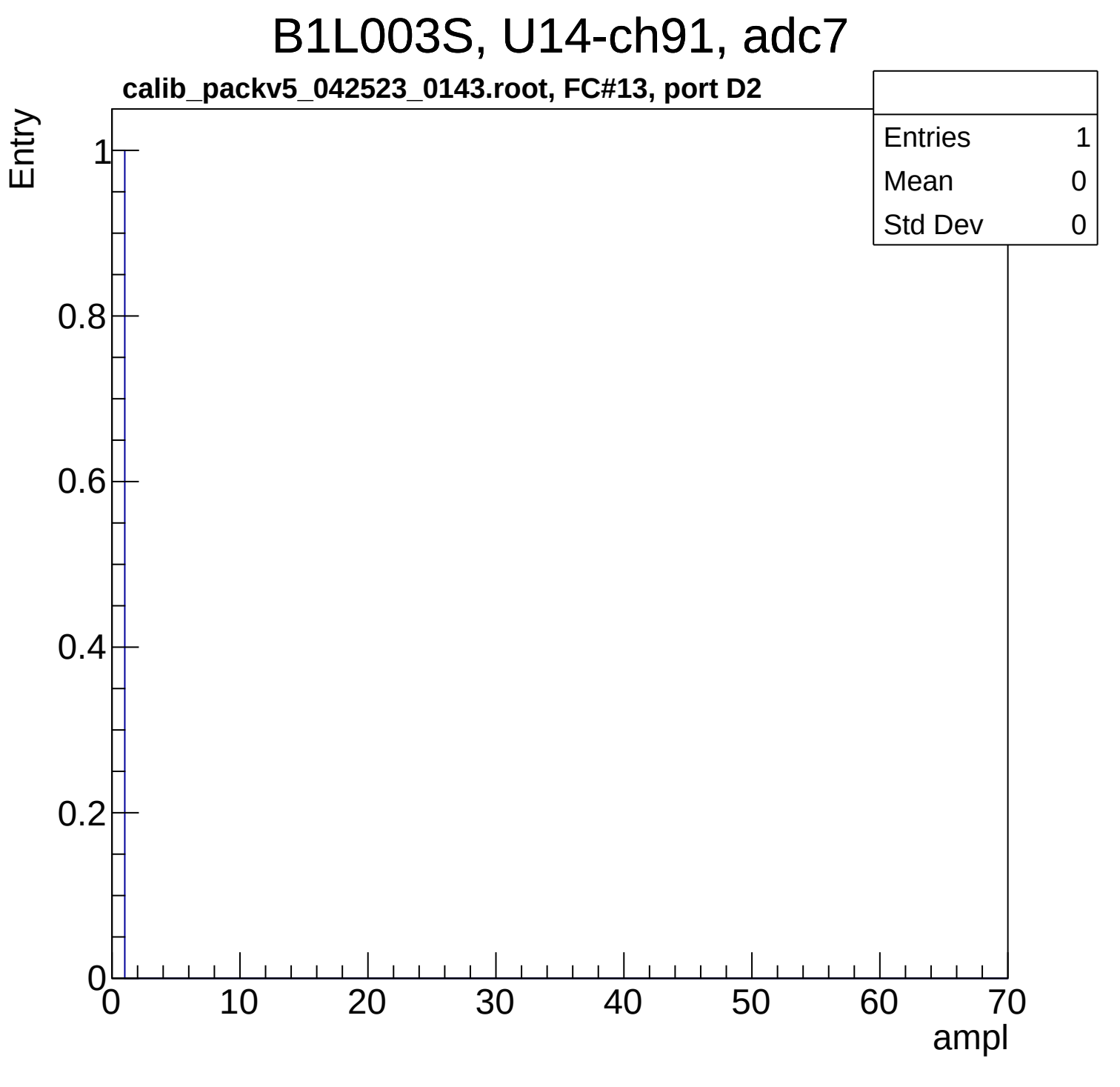

B1L003S, U14-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.49
Std Dev 3.551
Gaus mean : 41.6406
Gaus Width: 3.8529
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

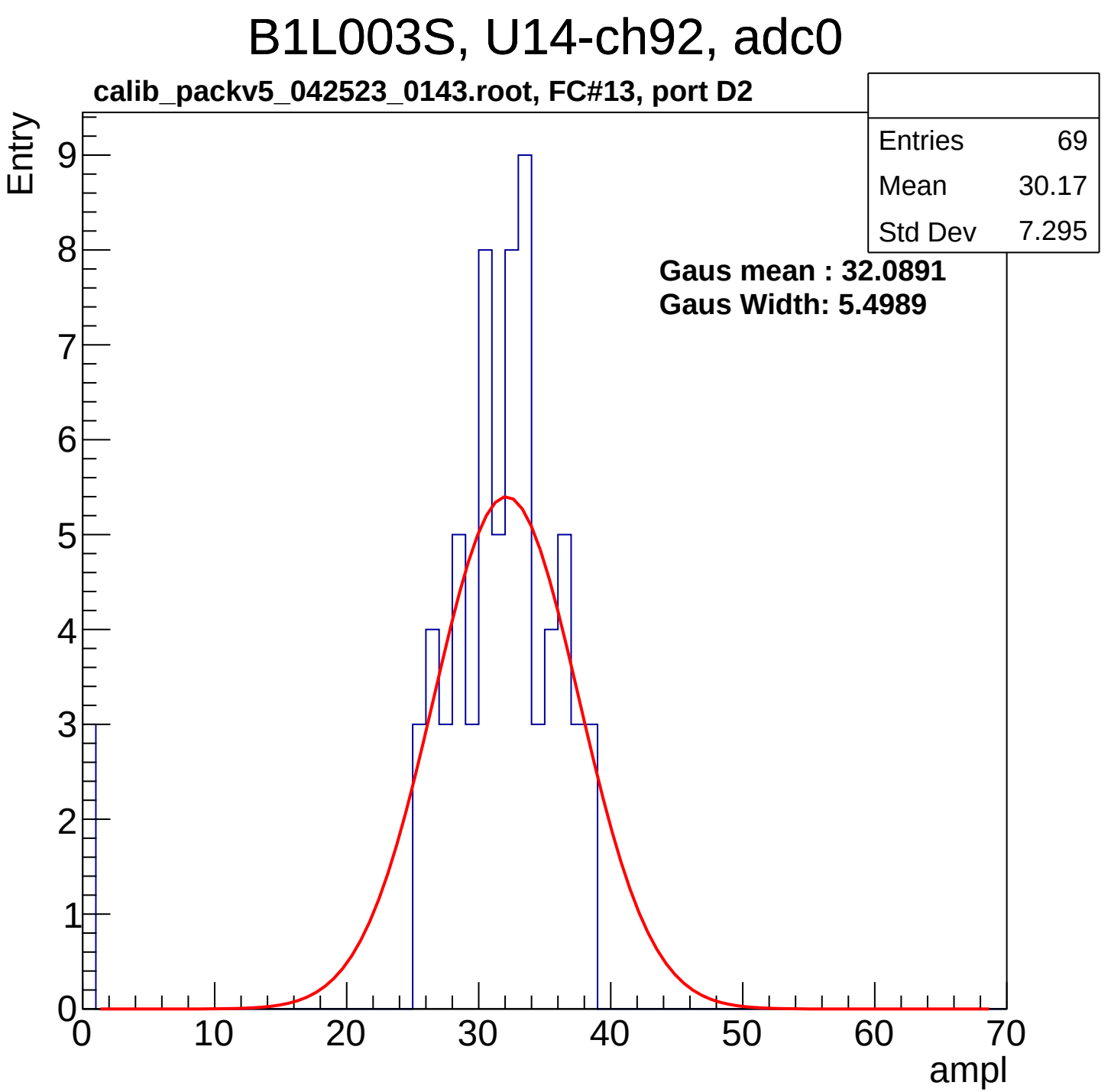

B1L003S, U14-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.79
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

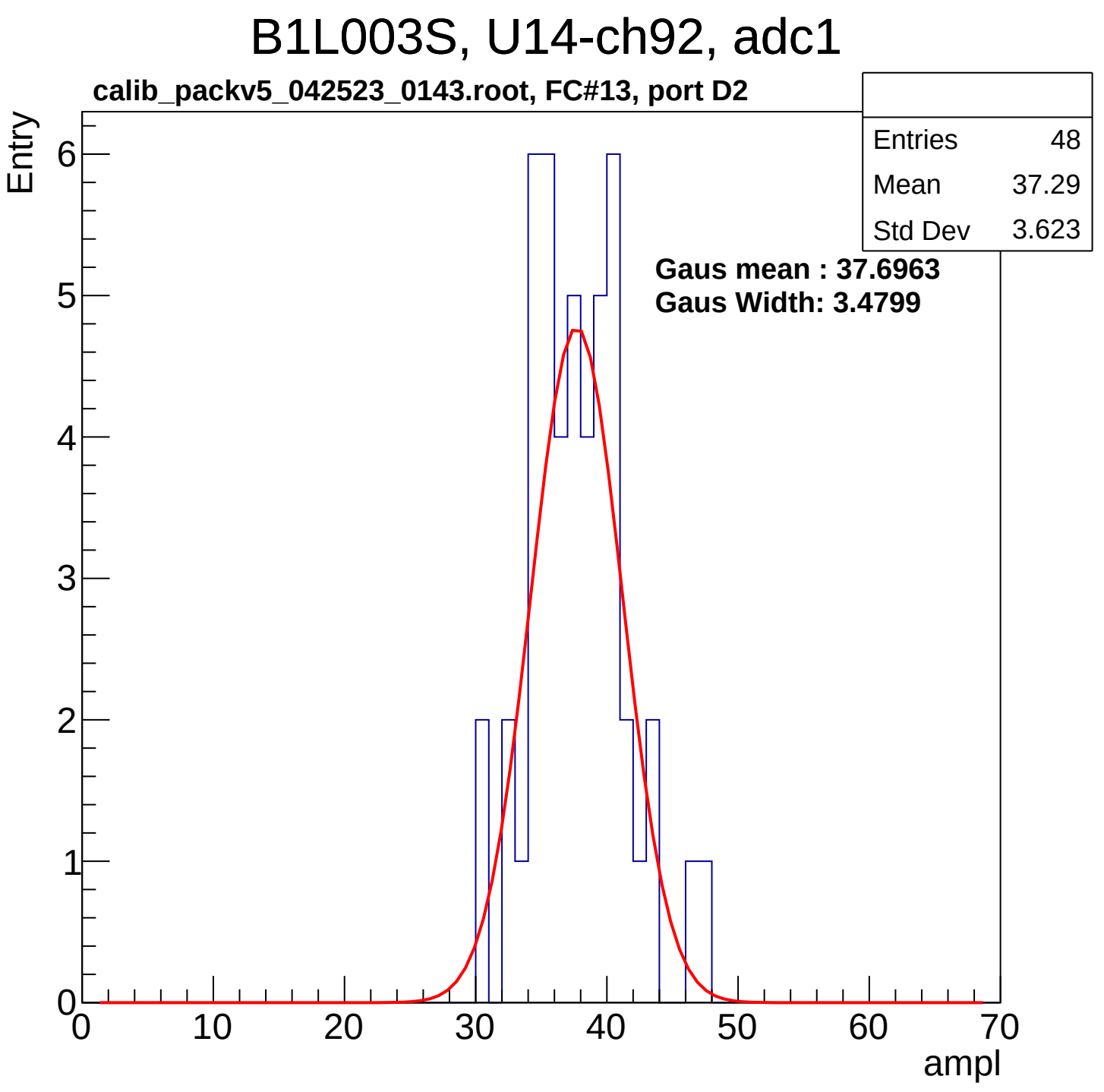

B1L003S, U14-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.17
Std Dev 7.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

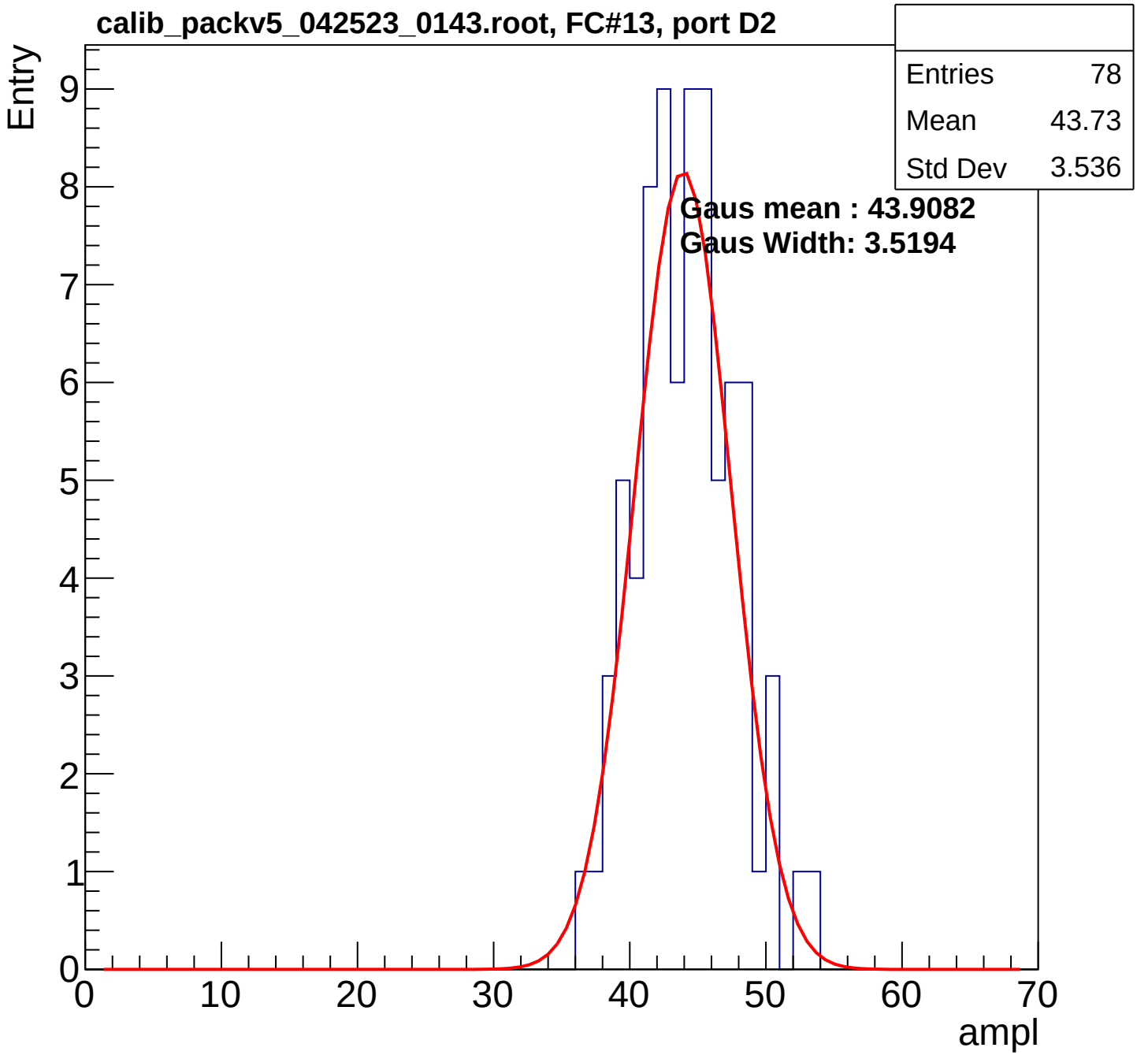

B1L003S, U14-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.44
Std Dev 2.662
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

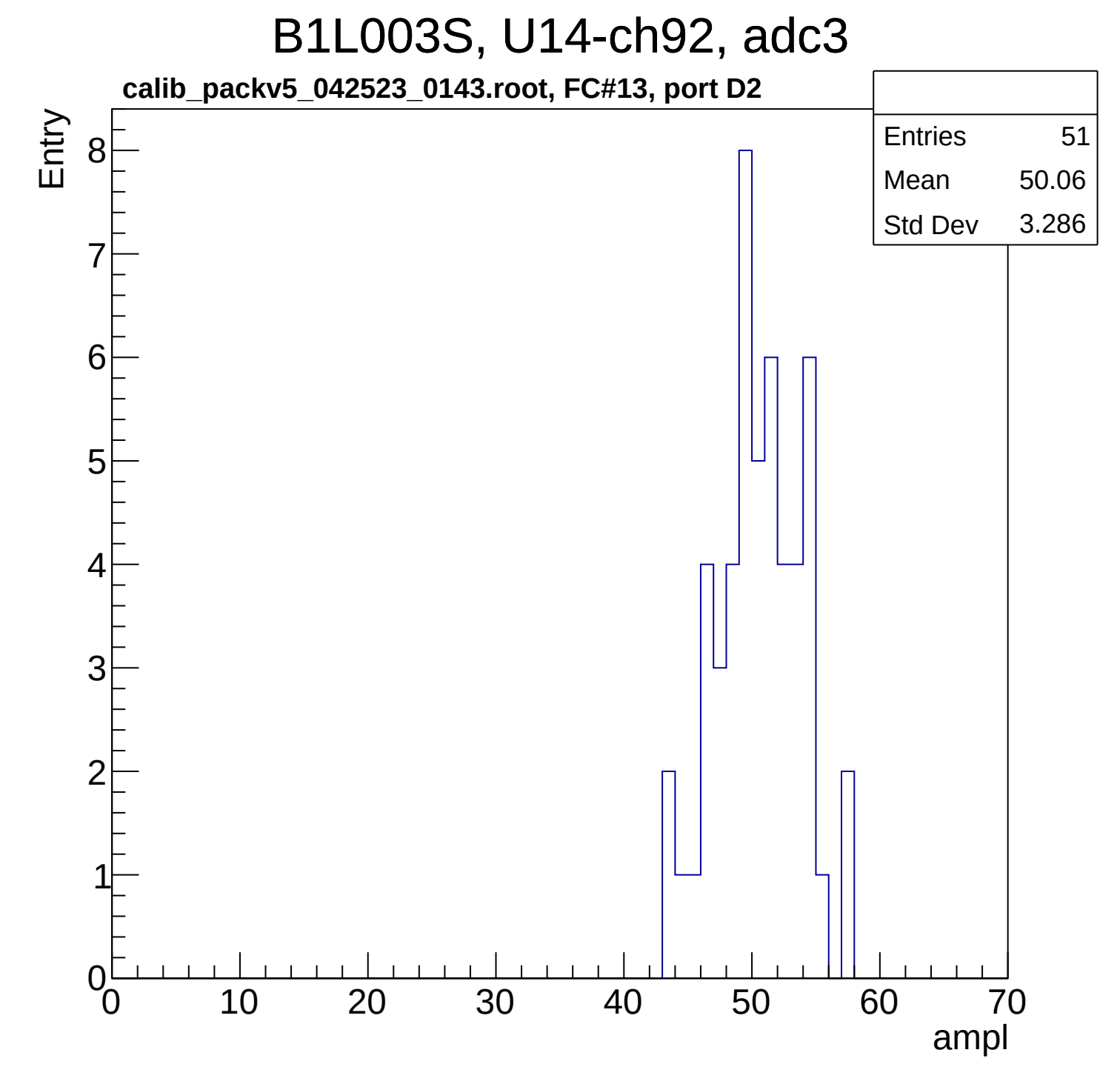

B1L003S, U14-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.45
Std Dev 1.616
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

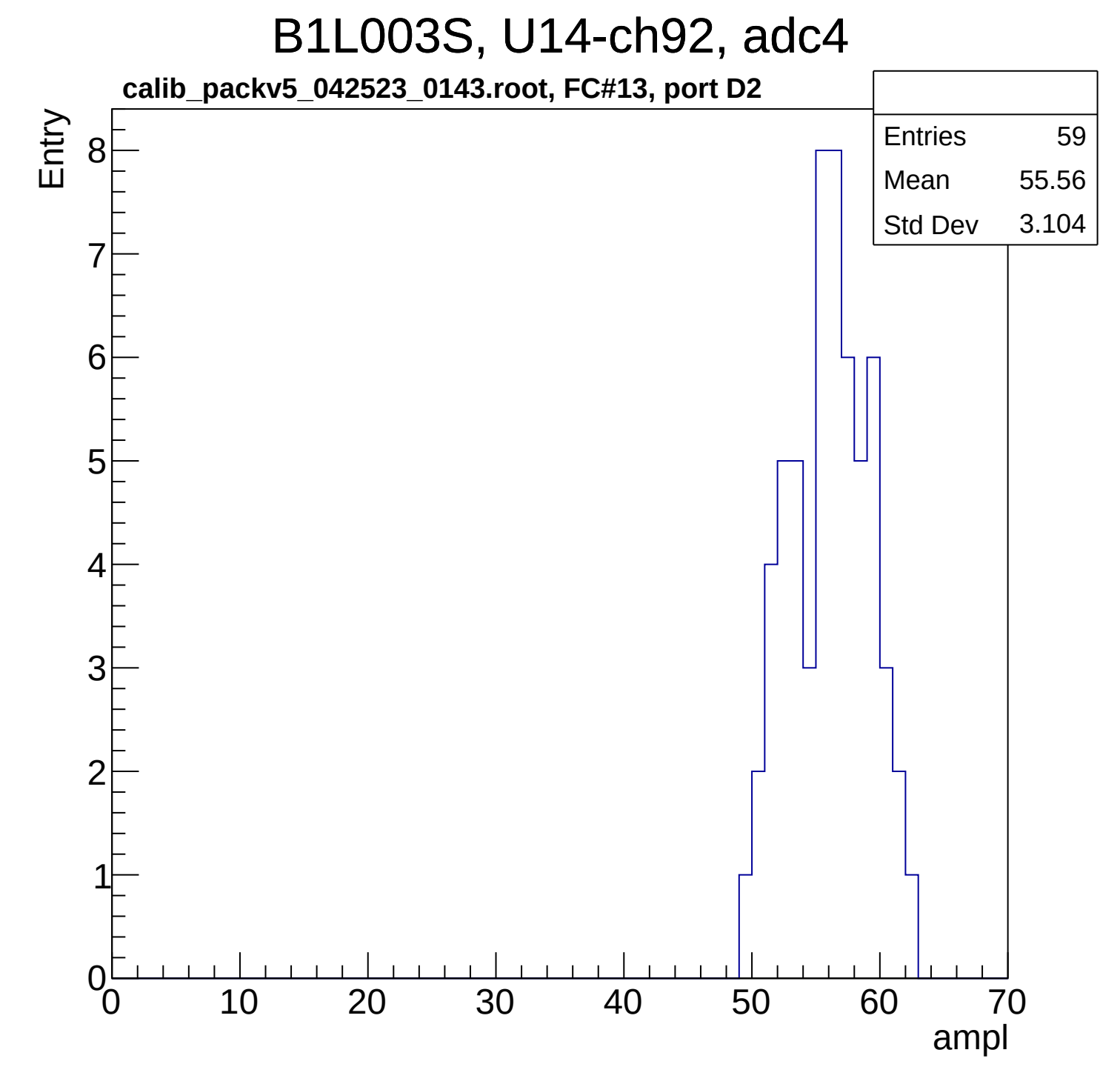

B1L003S, U14-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

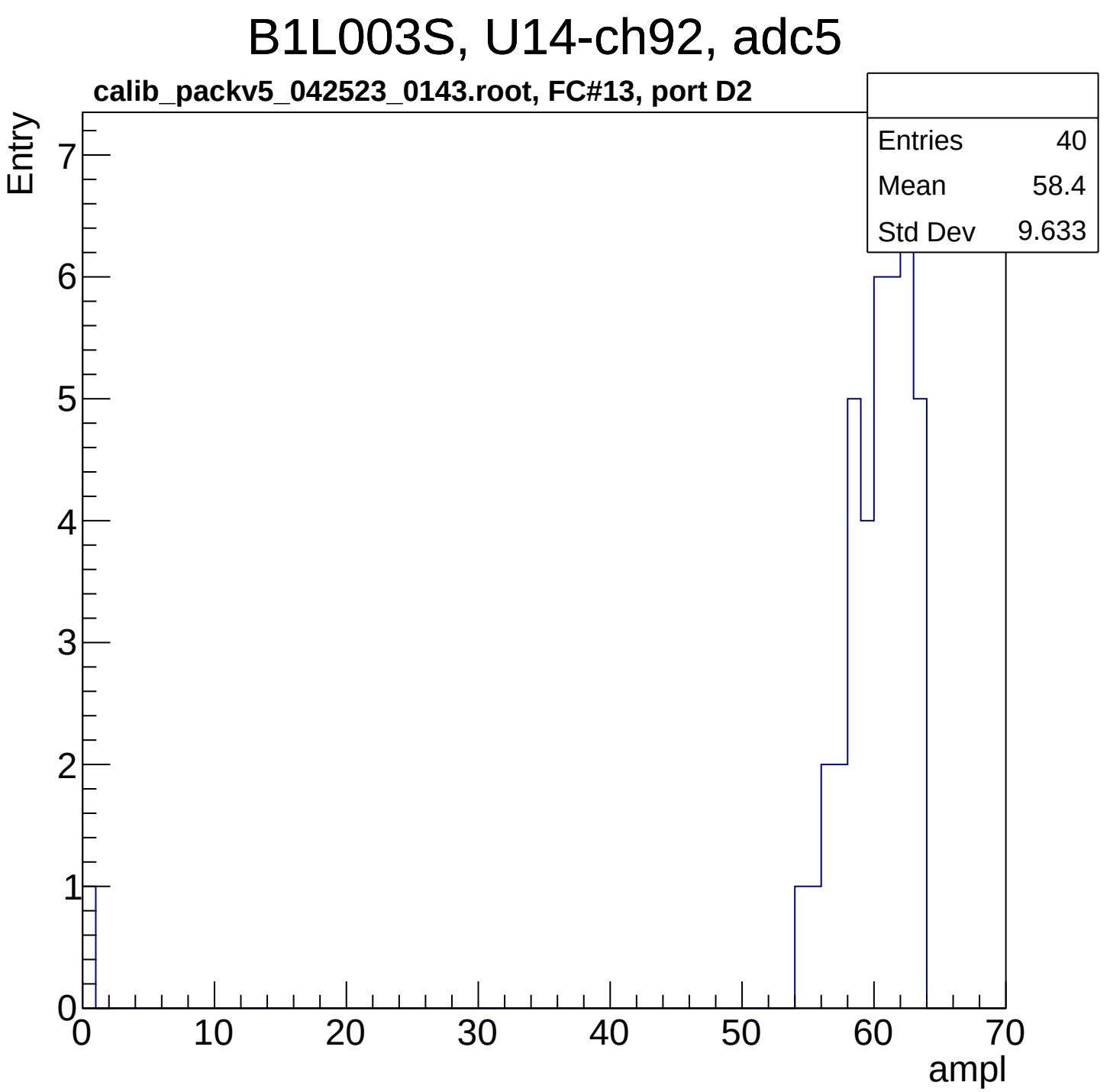

B1L003S, U14-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 30.17
Std Dev 7.295
Gaus mean : 32.0891
Gaus Width: 5.4989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

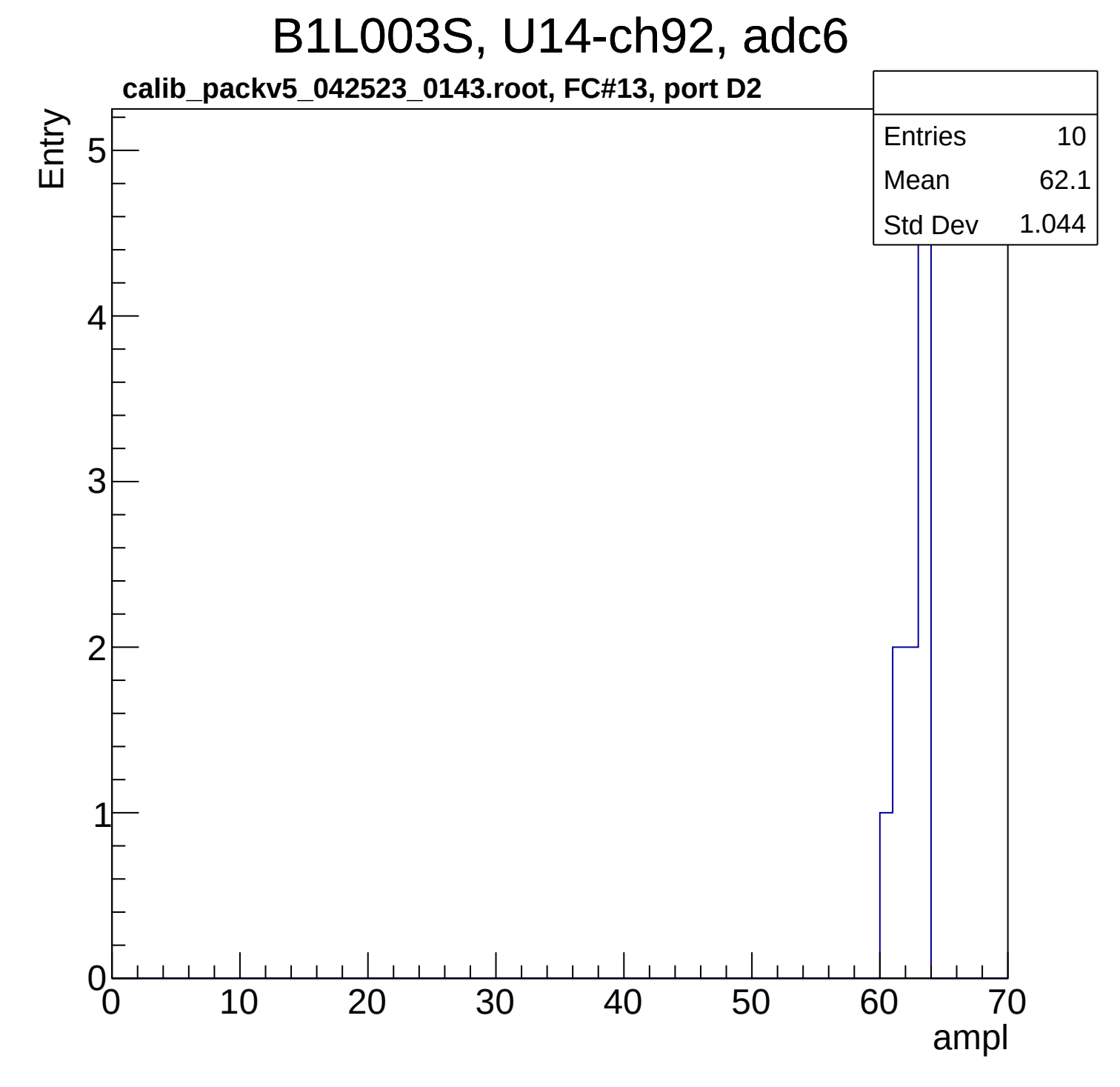

B1L003S, U14-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 37.29
Std Dev 3.623
Gaus mean : 37.6963
Gaus Width: 3.4799
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

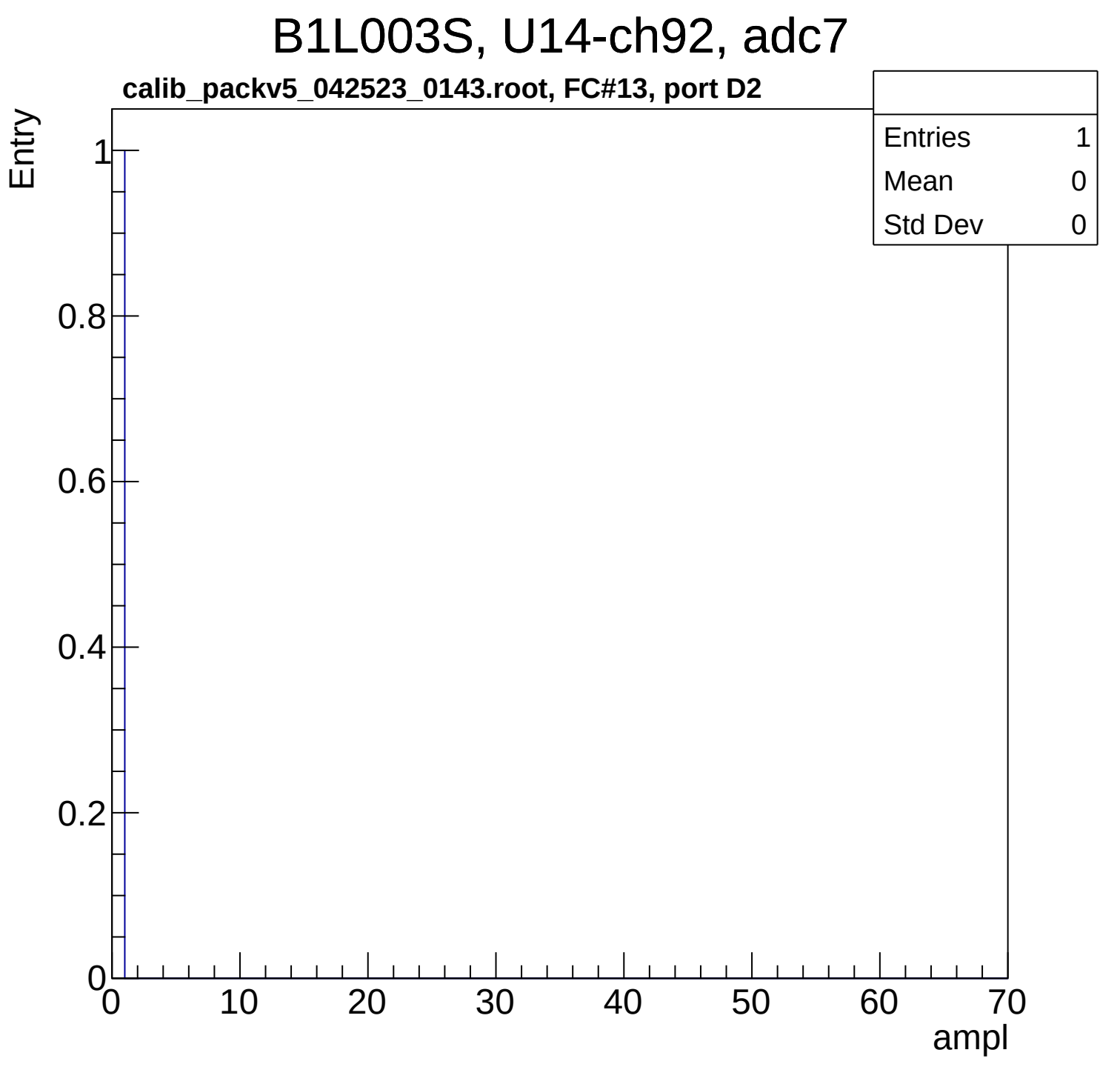

B1L003S, U14-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 43.73
Std Dev 3.536
Gaus mean : 43.9082
Gaus Width: 3.5194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

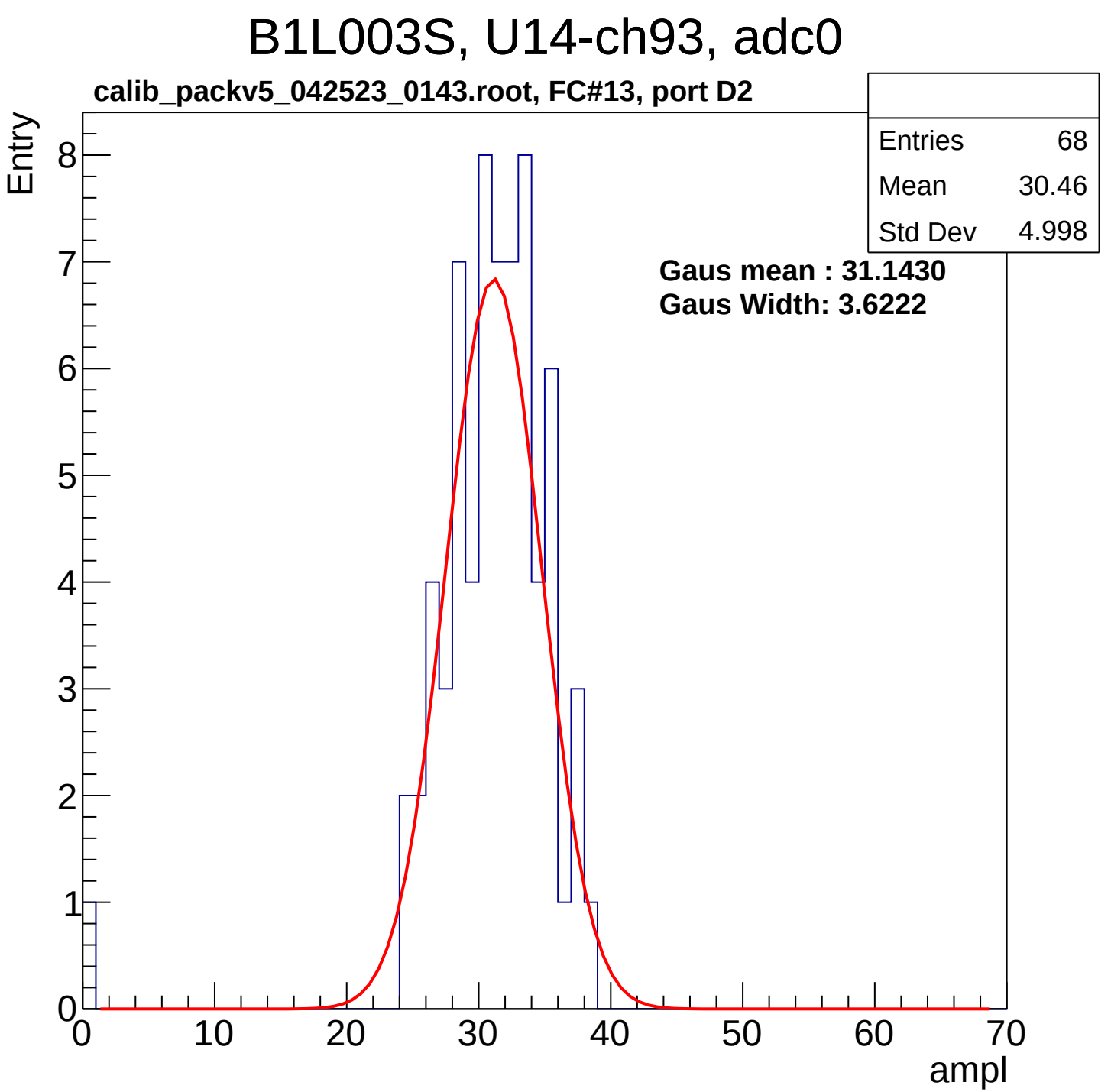

B1L003S, U14-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 50.06
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

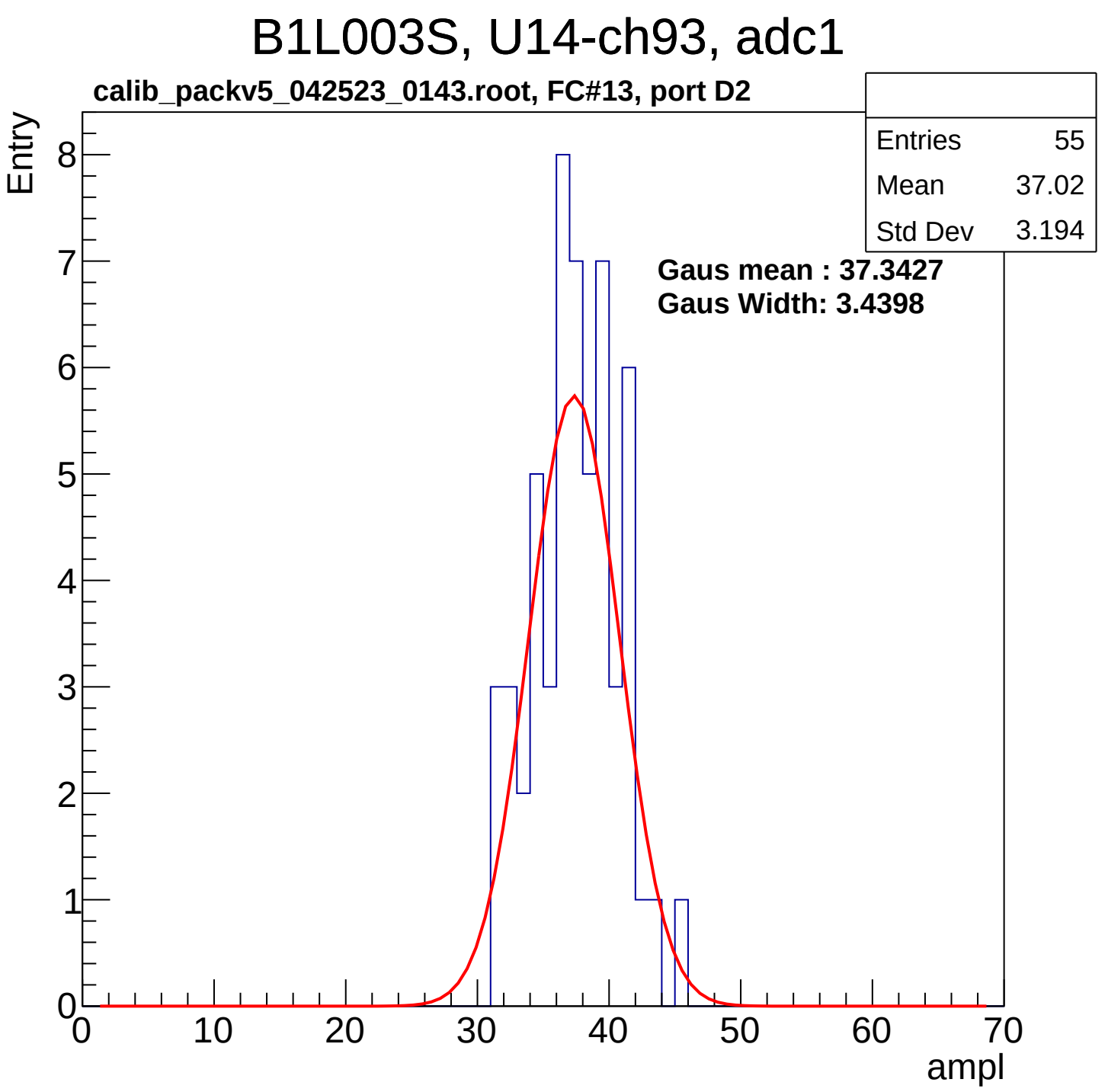

B1L003S, U14-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.56
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

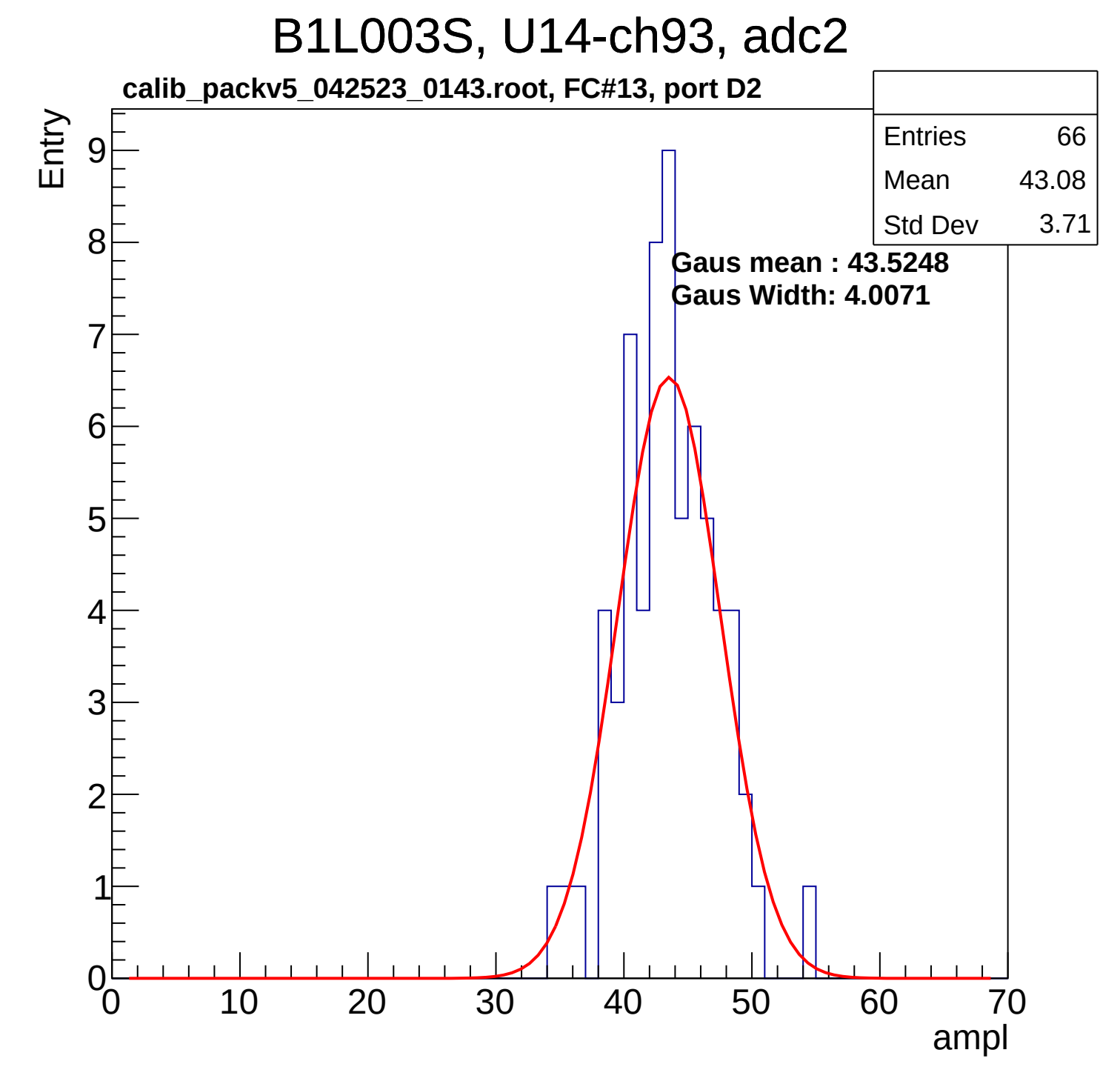

B1L003S, U14-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.4
Std Dev 9.633
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

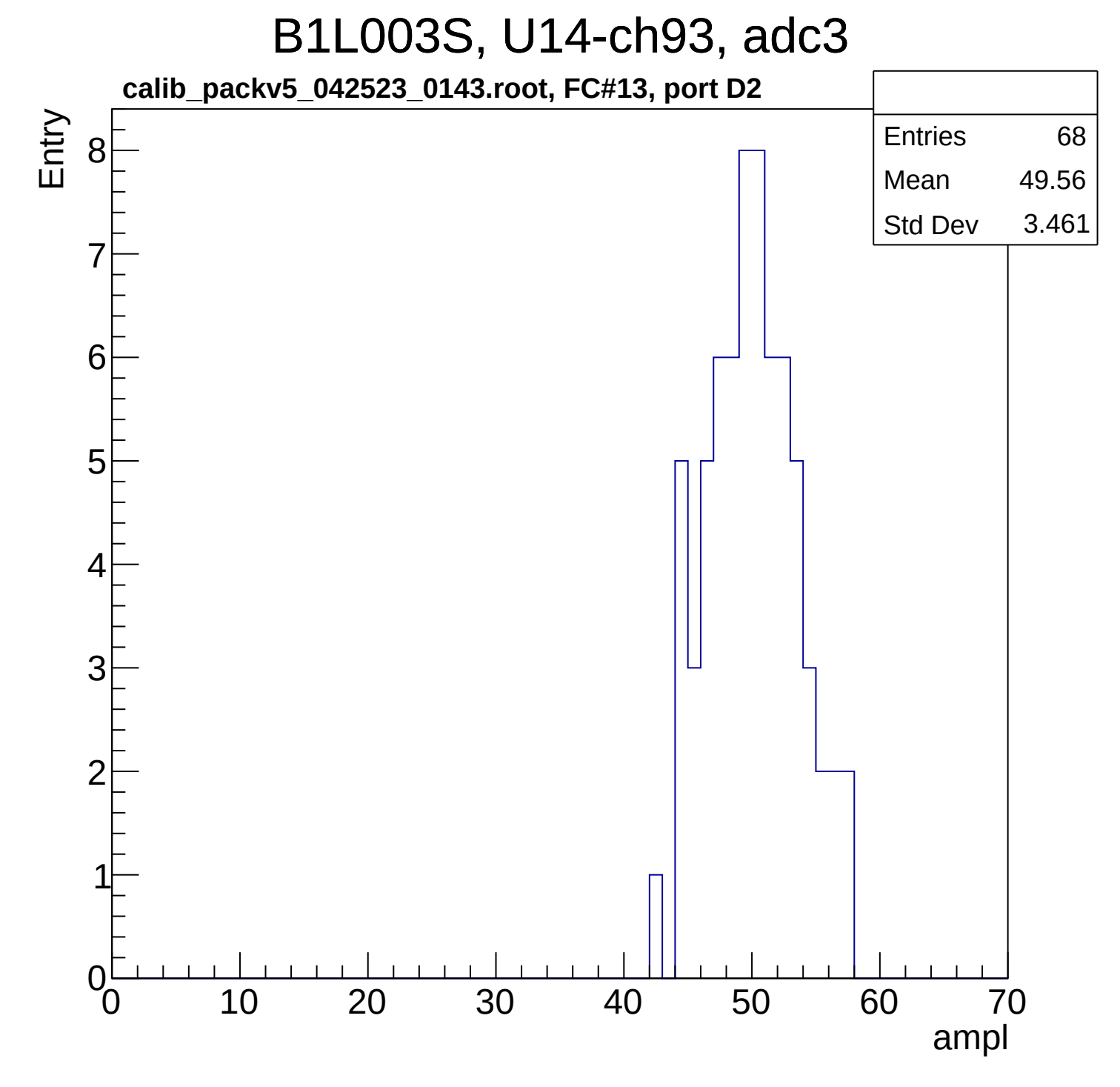

B1L003S, U14-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.1
Std Dev 1.044
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

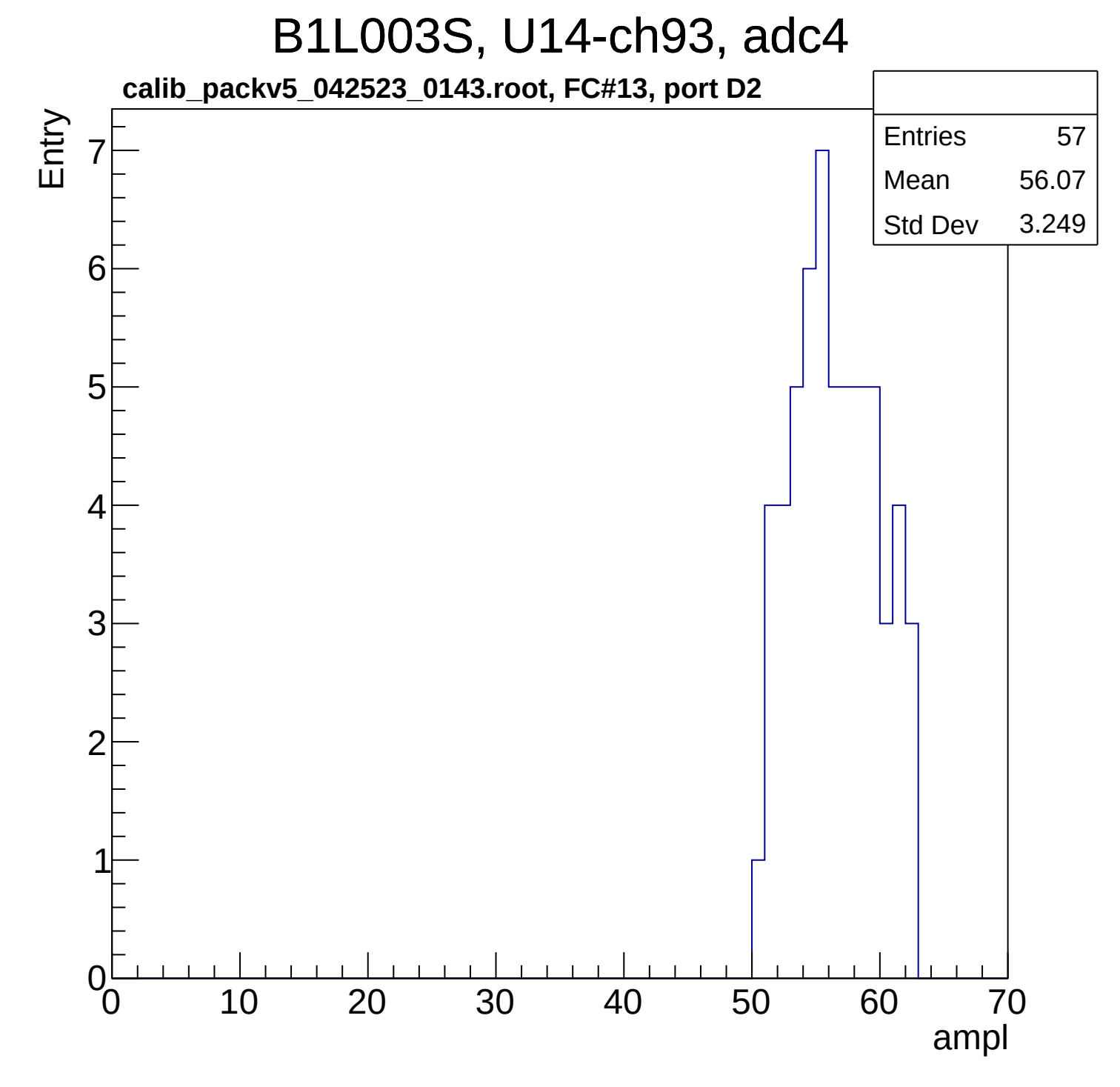

B1L003S, U14-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

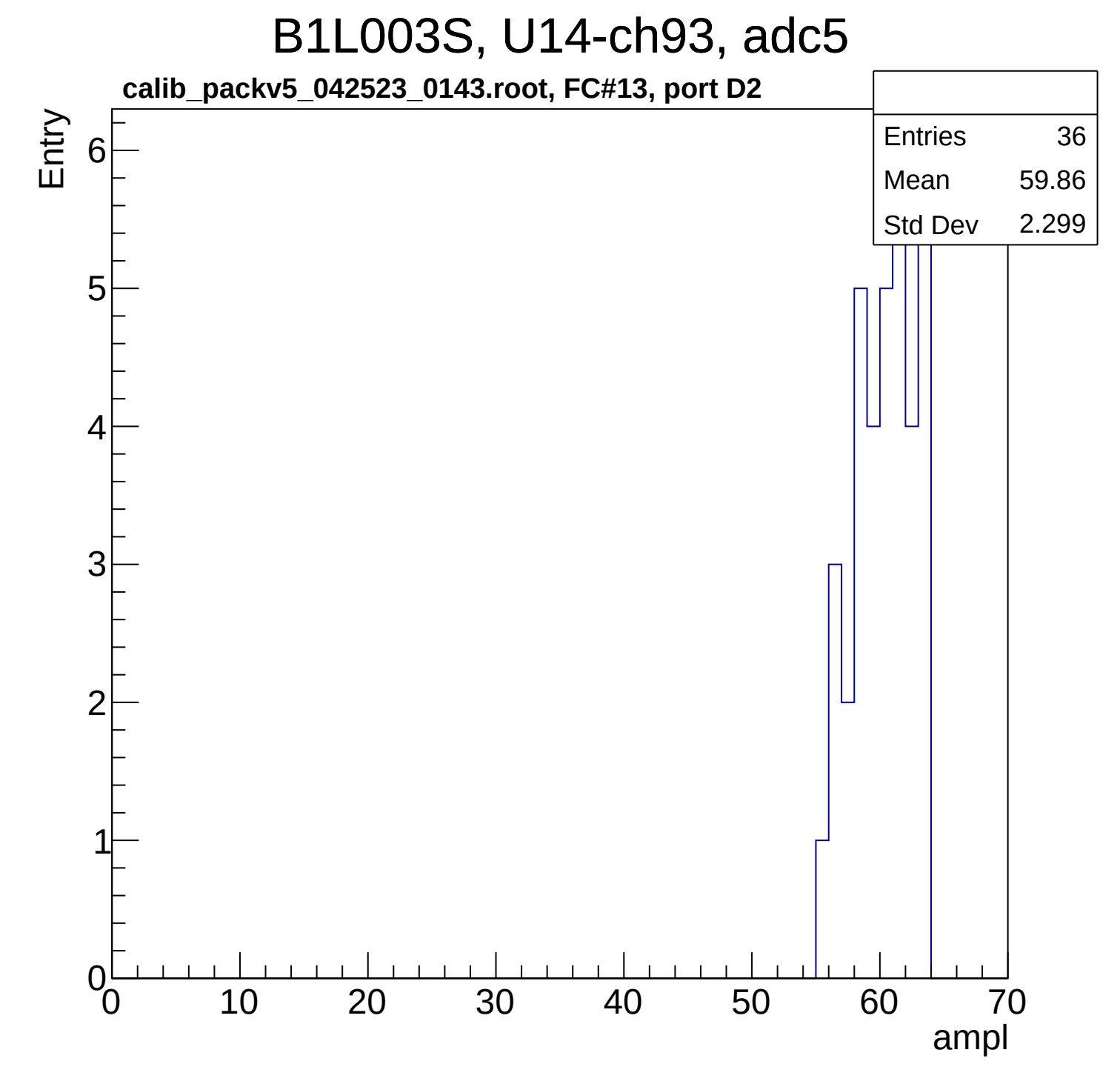

B1L003S, U14-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.46
Std Dev 4.998
Gaus mean : 31.1430
Gaus Width: 3.6222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

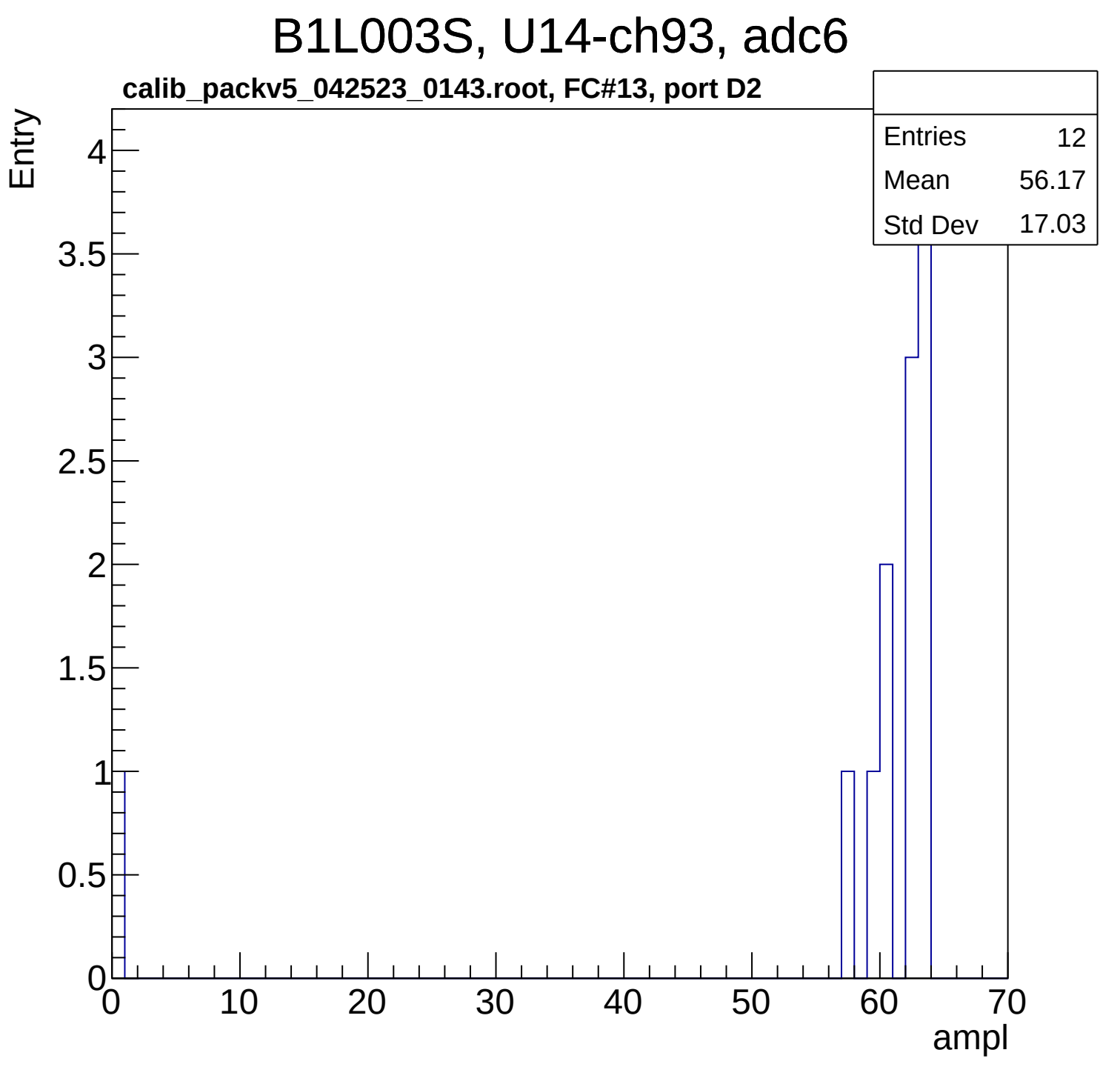

B1L003S, U14-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.02
Std Dev 3.194
Gaus mean : 37.3427
Gaus Width: 3.4398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

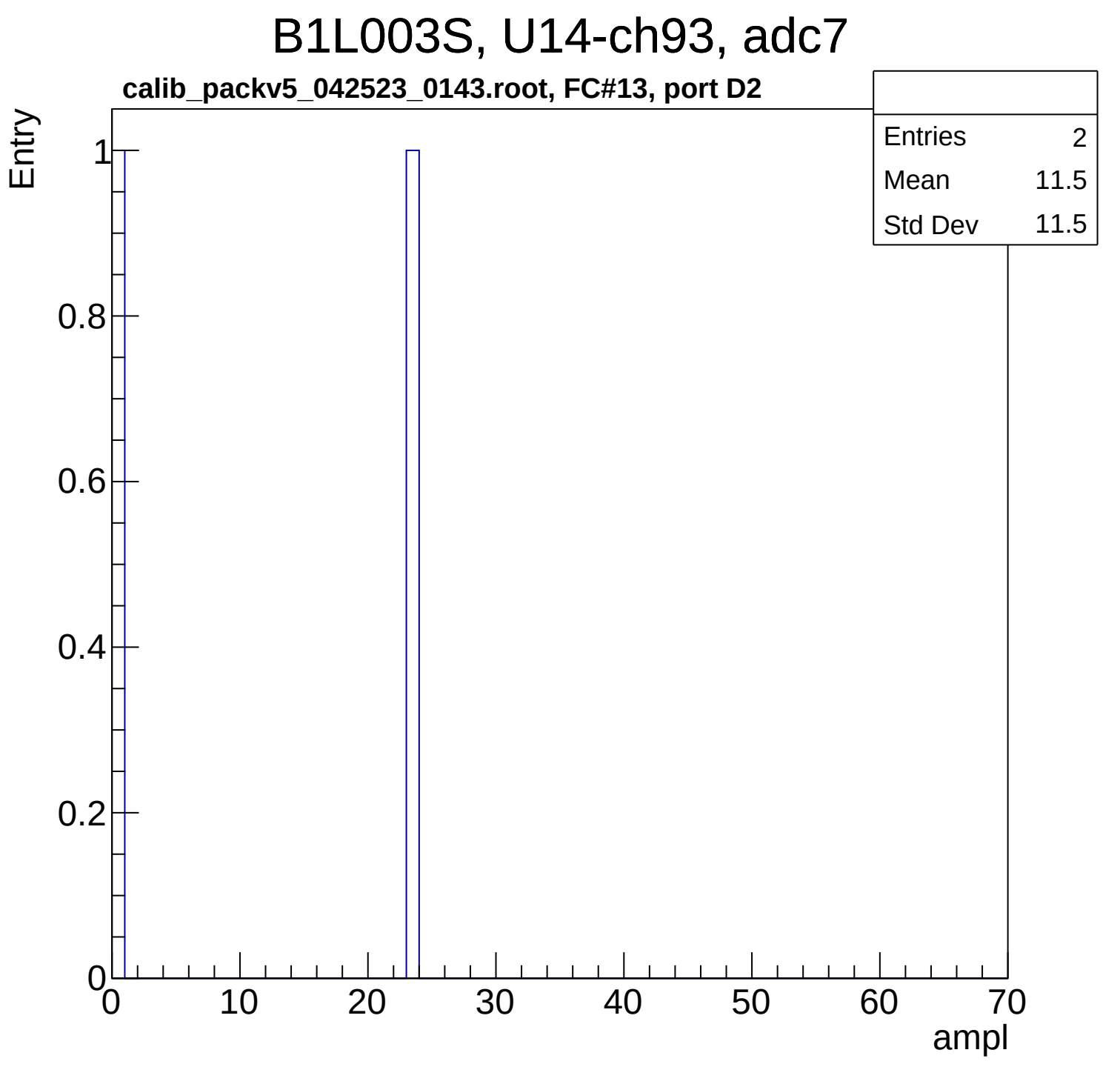

B1L003S, U14-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.08
Std Dev 3.71
Gaus mean : 43.5248
Gaus Width: 4.0071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

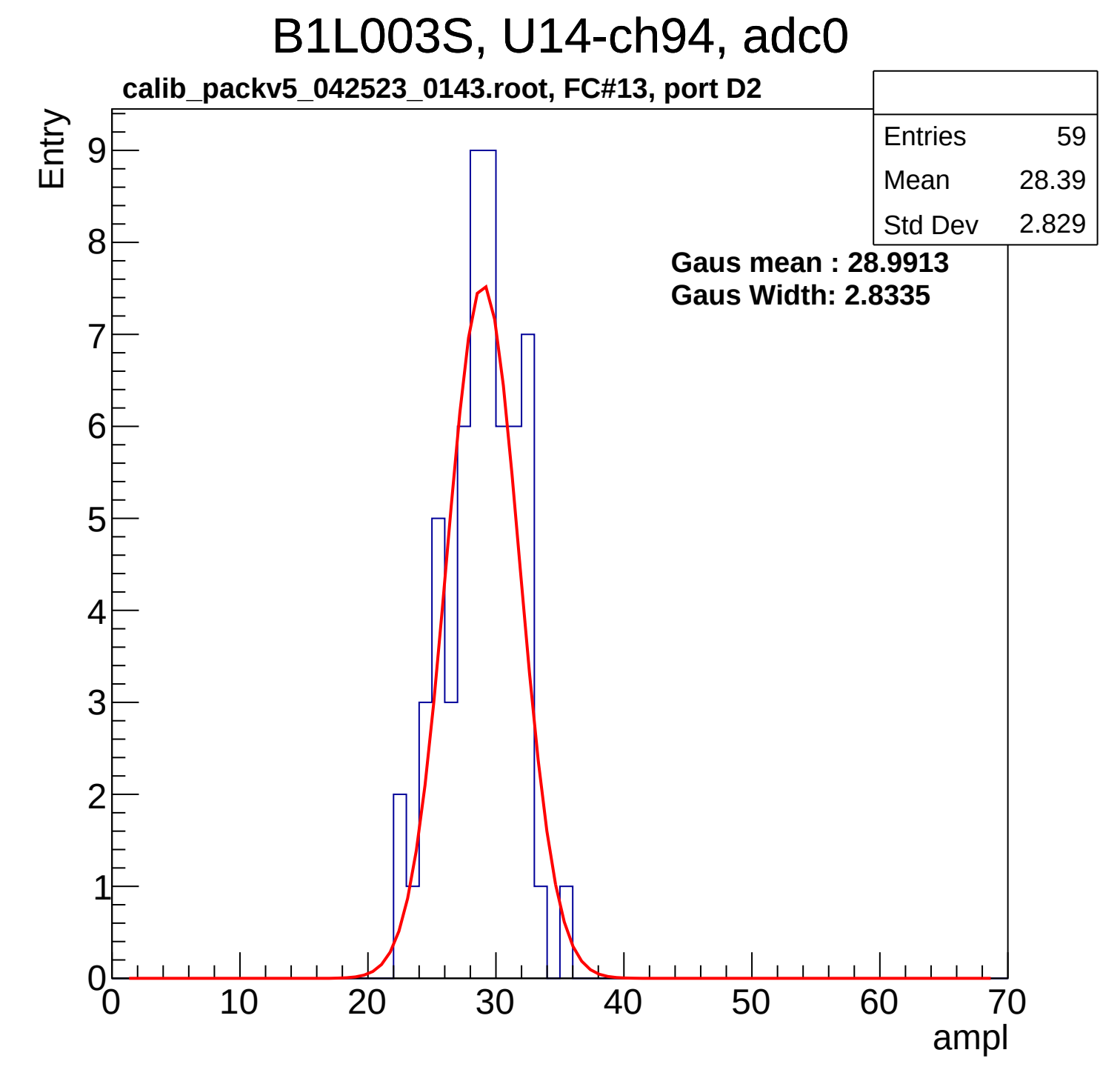

B1L003S, U14-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.56
Std Dev 3.461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

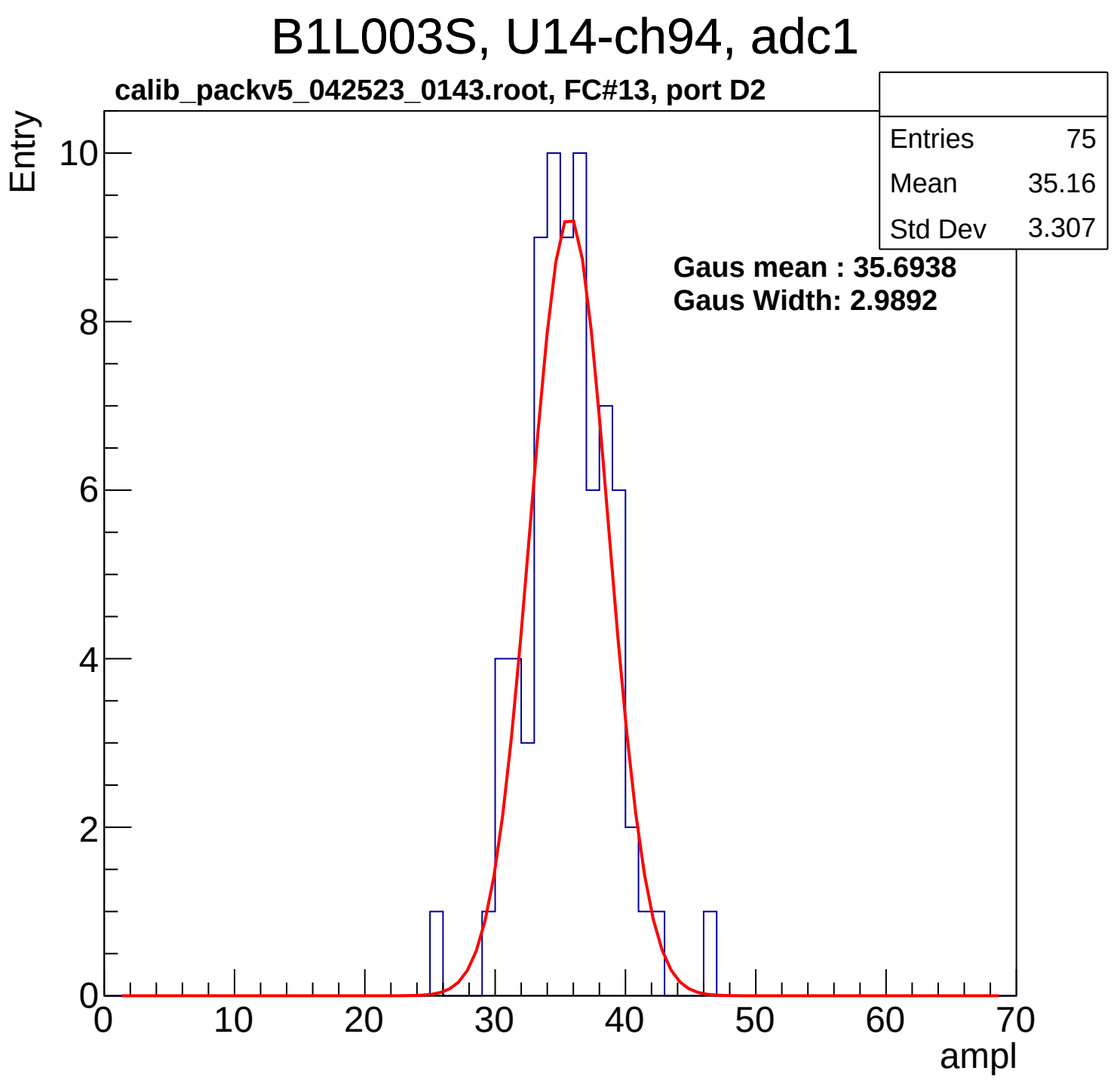

B1L003S, U14-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.07
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

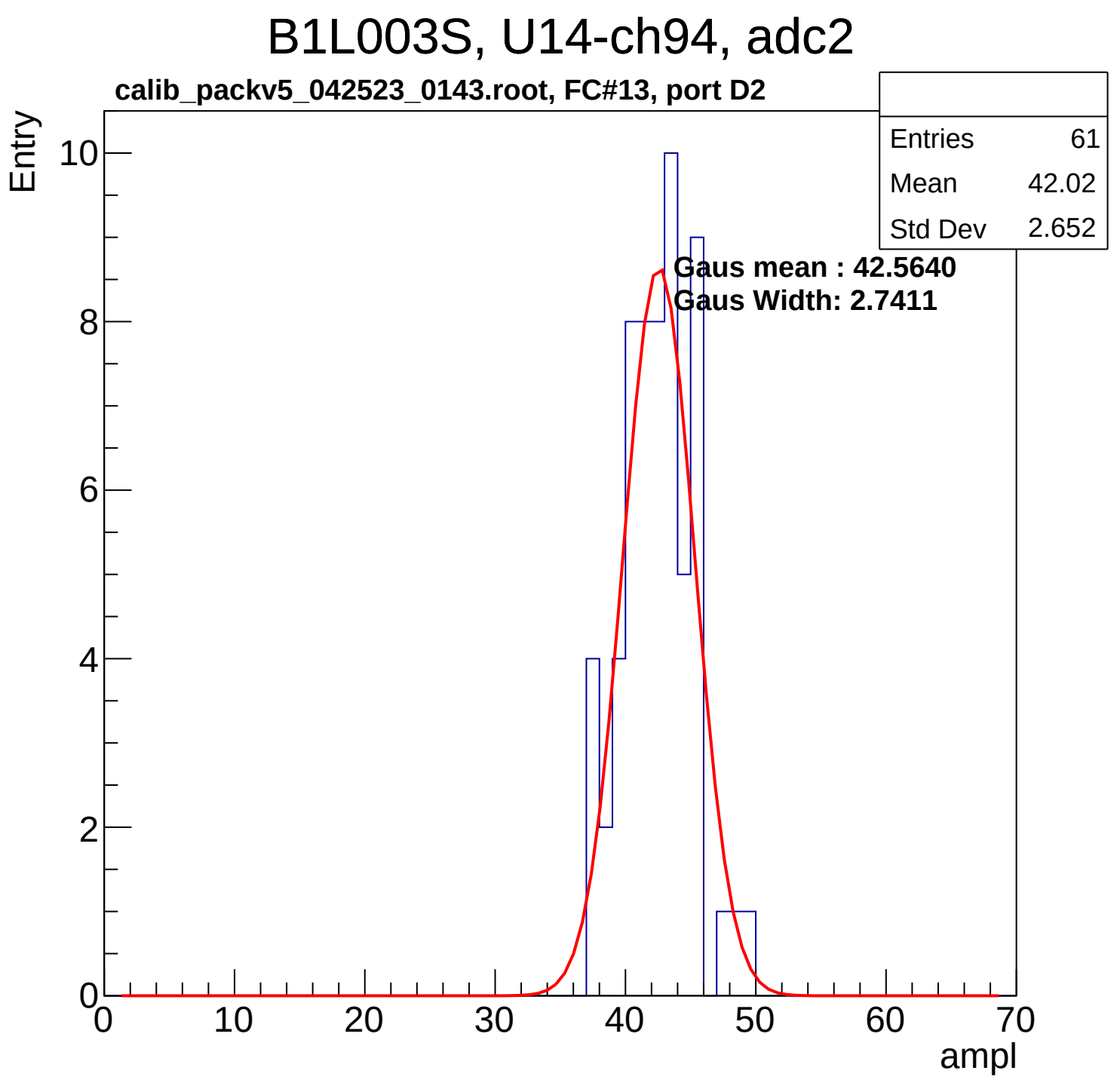

B1L003S, U14-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 59.86
Std Dev 2.299
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

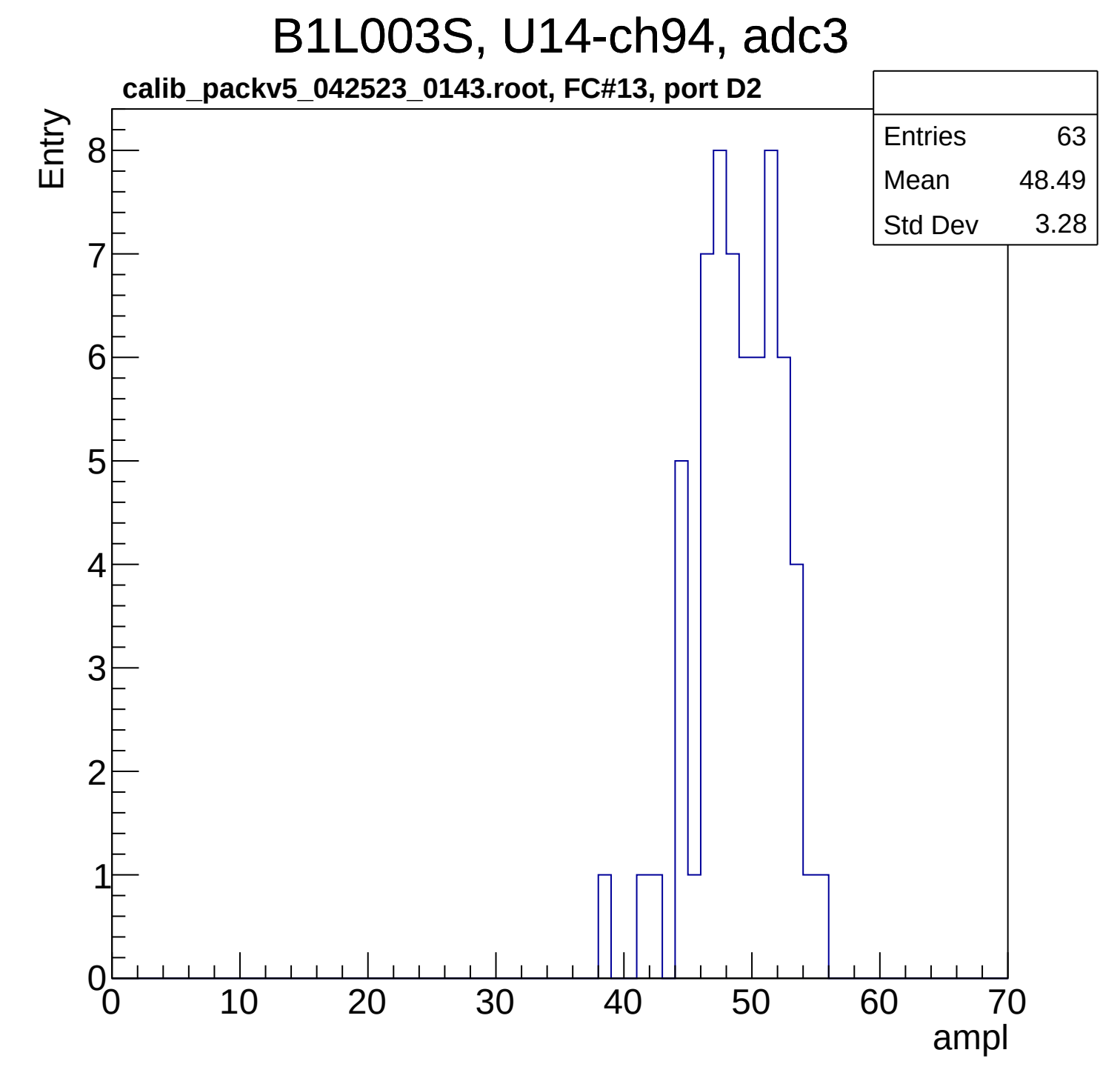

B1L003S, U14-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.17
Std Dev 17.03
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

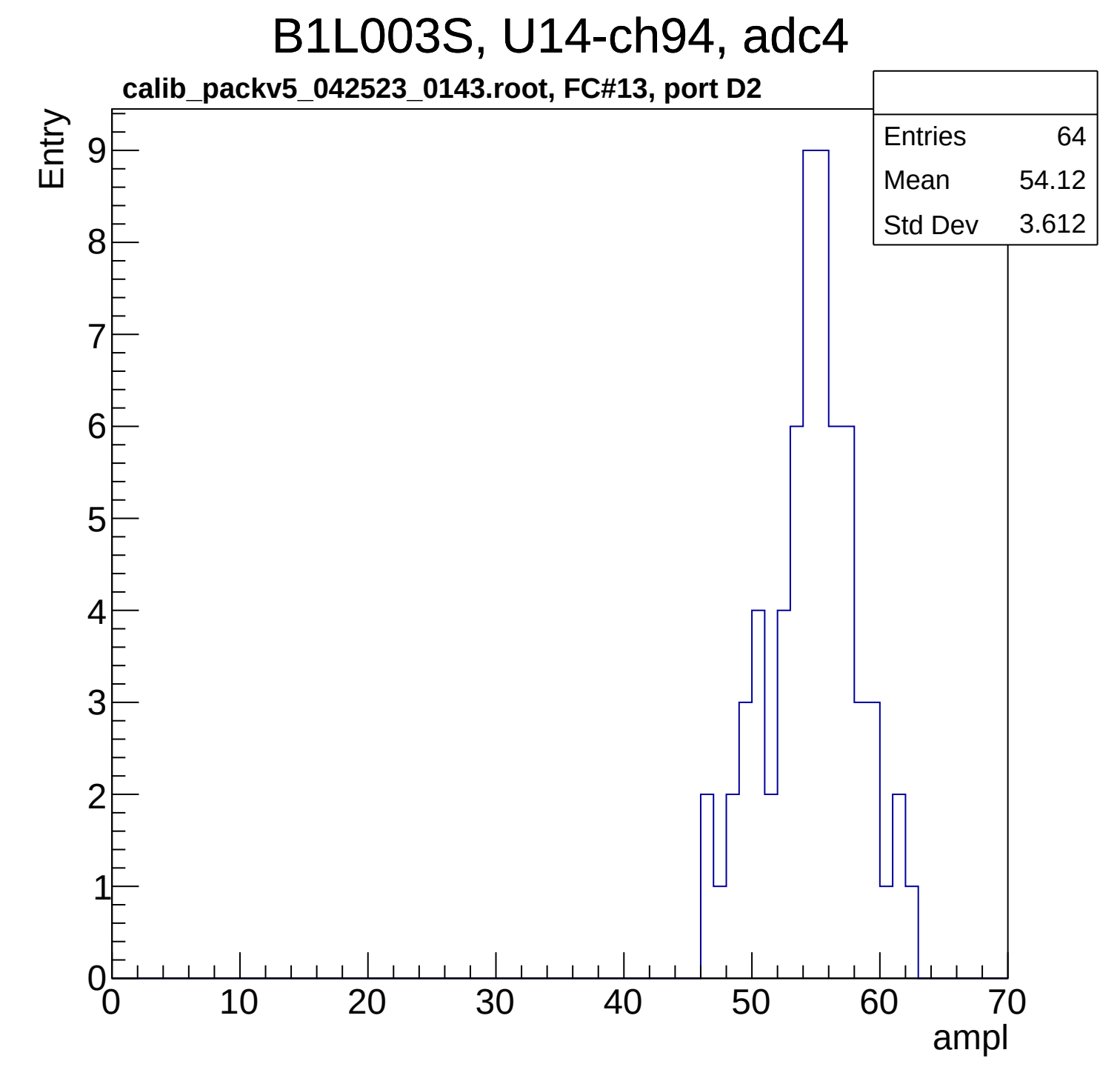

B1L003S, U14-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

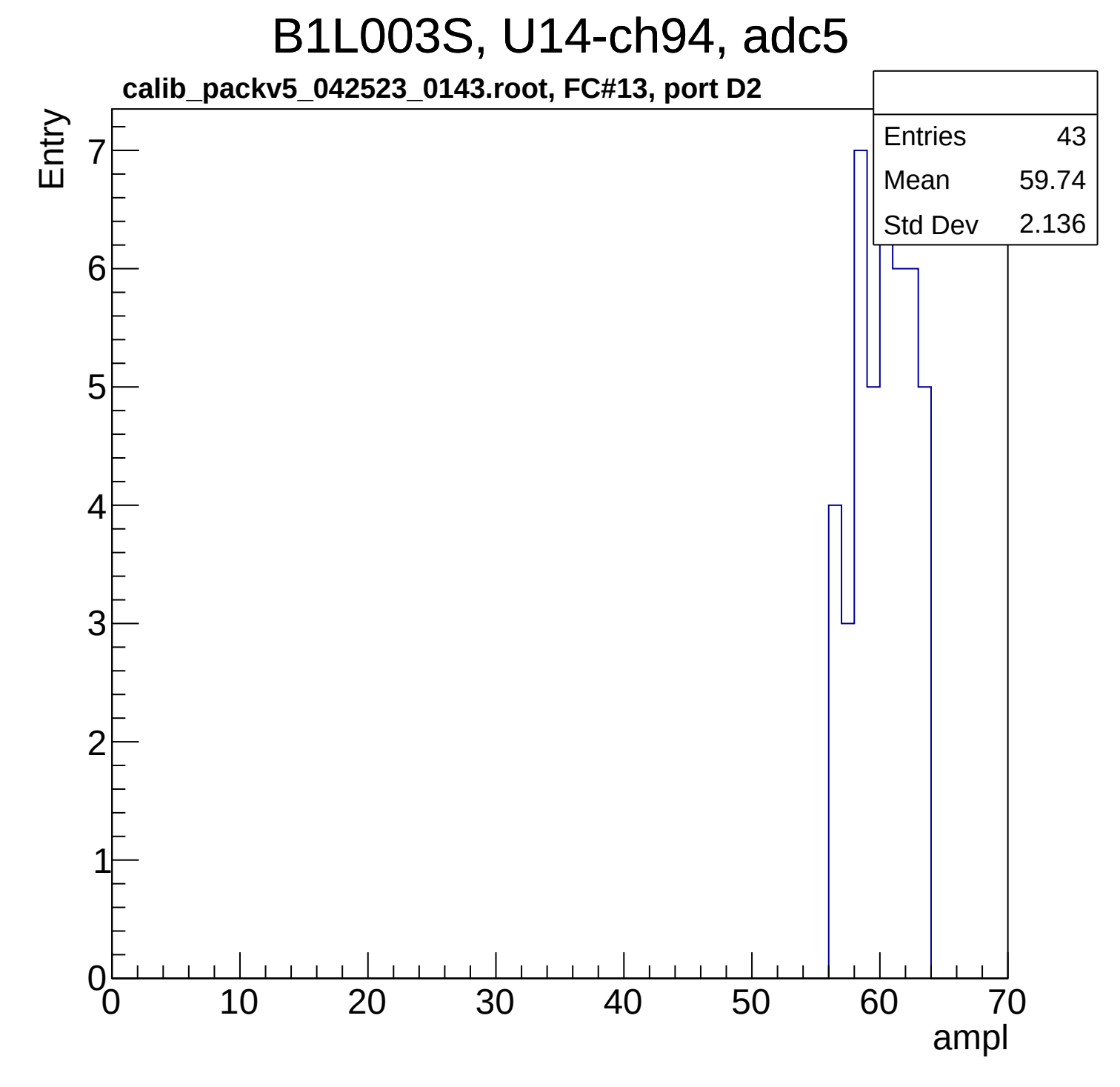

B1L003S, U14-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.39
Std Dev 2.829
Gaus mean : 28.9913
Gaus Width: 2.8335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

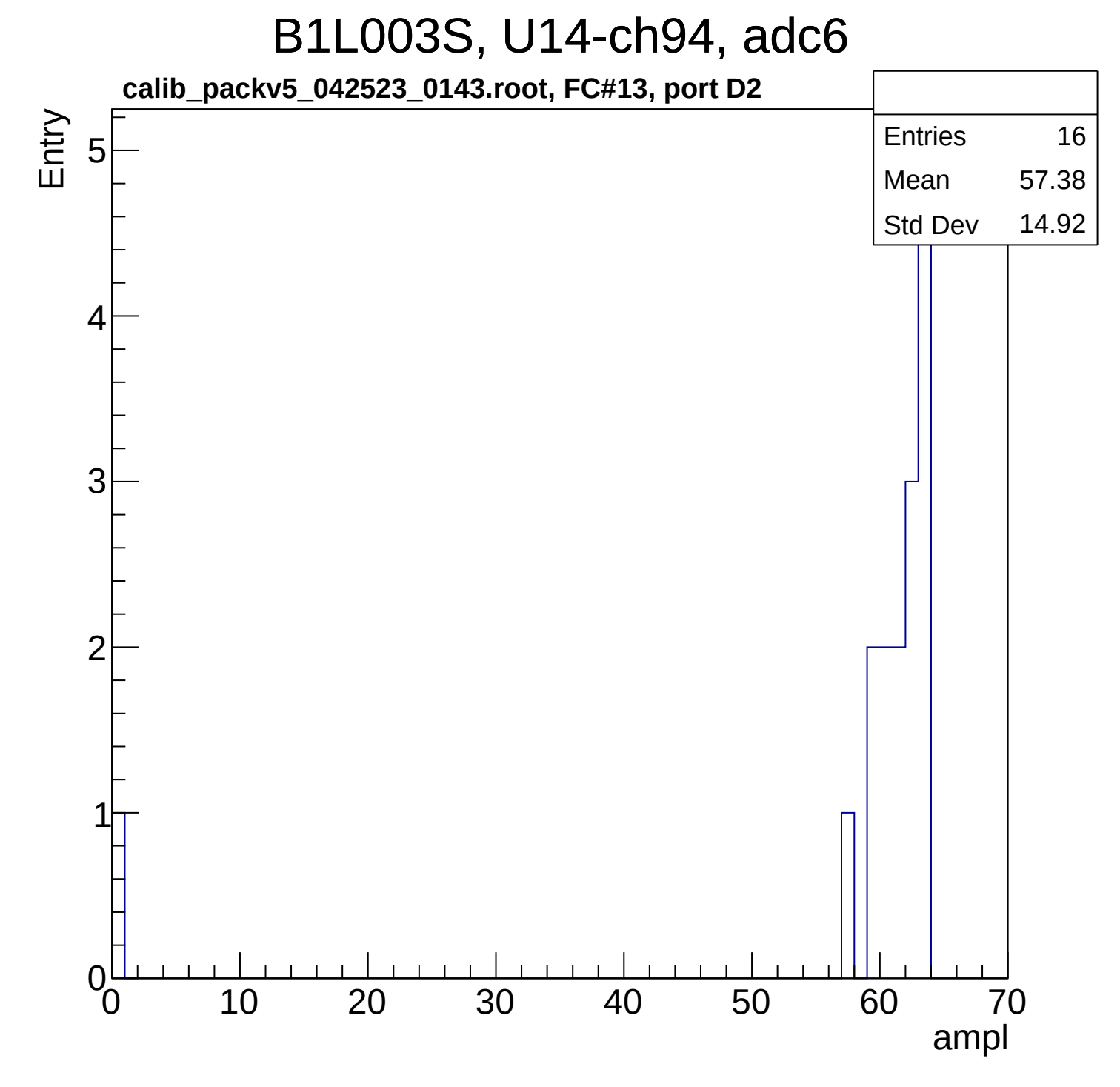

B1L003S, U14-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.16
Std Dev 3.307
Gaus mean : 35.6938
Gaus Width: 2.9892
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

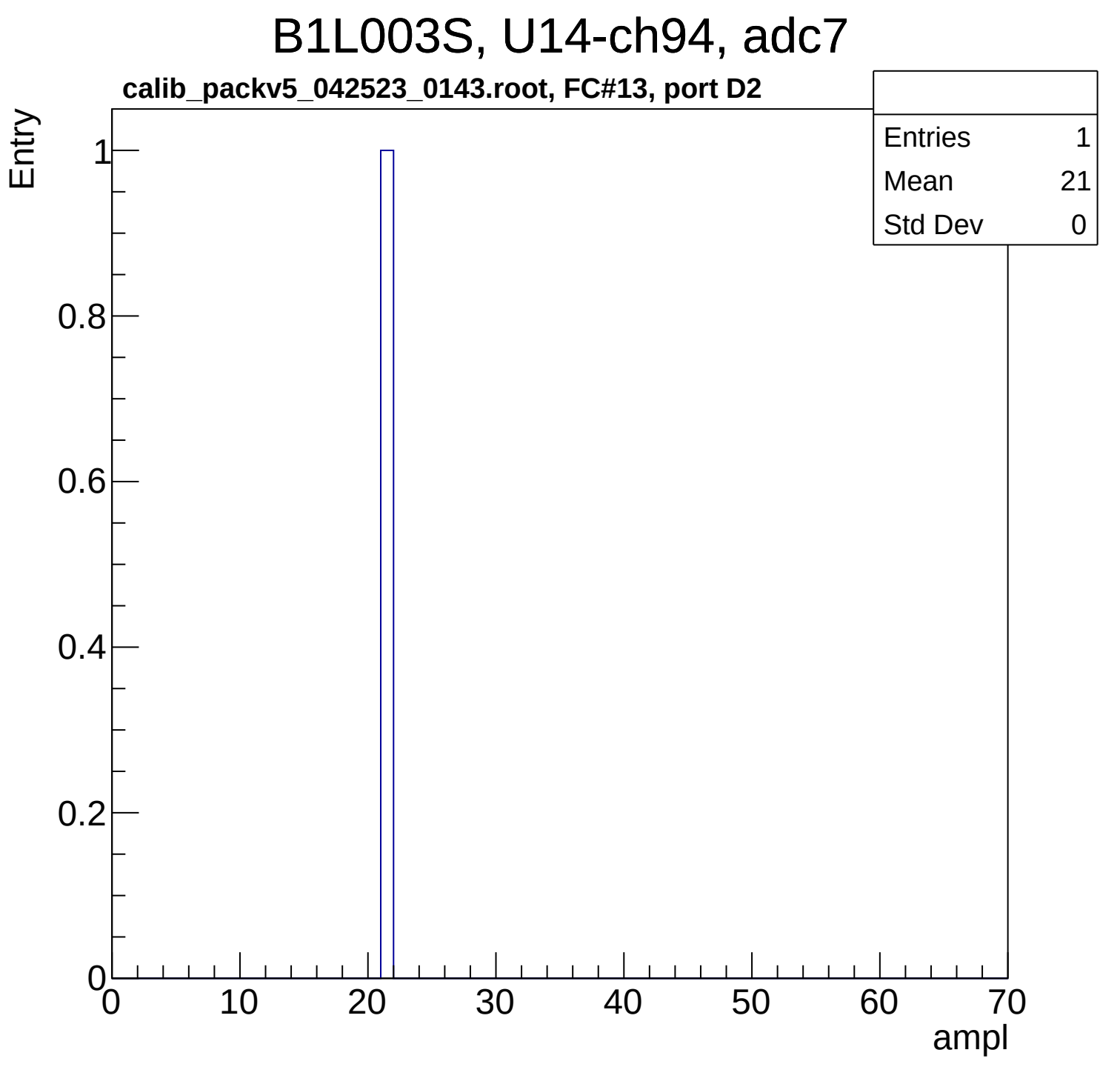

B1L003S, U14-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.02
Std Dev 2.652
Gaus mean : 42.5640
Gaus Width: 2.7411
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

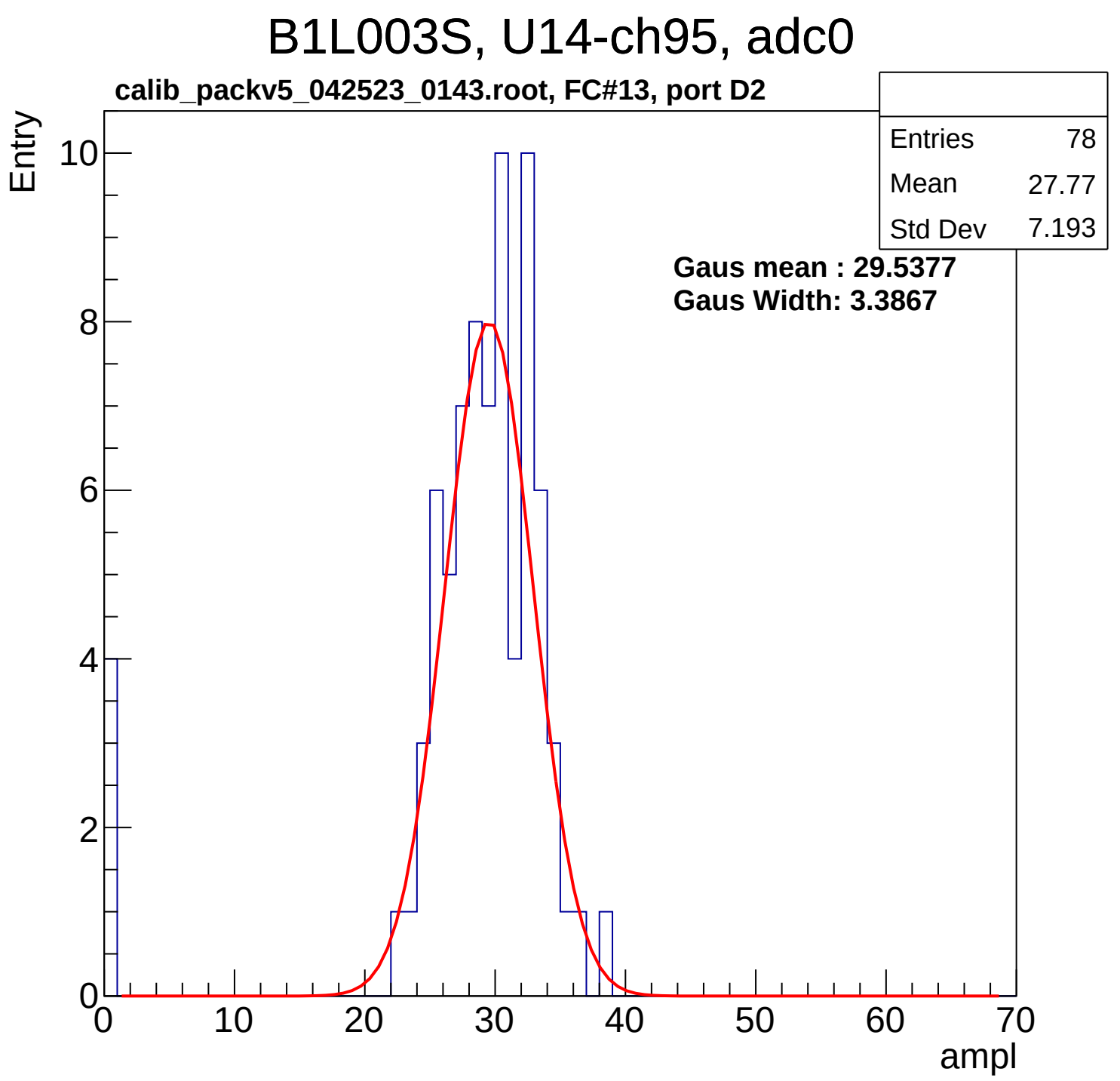

B1L003S, U14-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.49
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

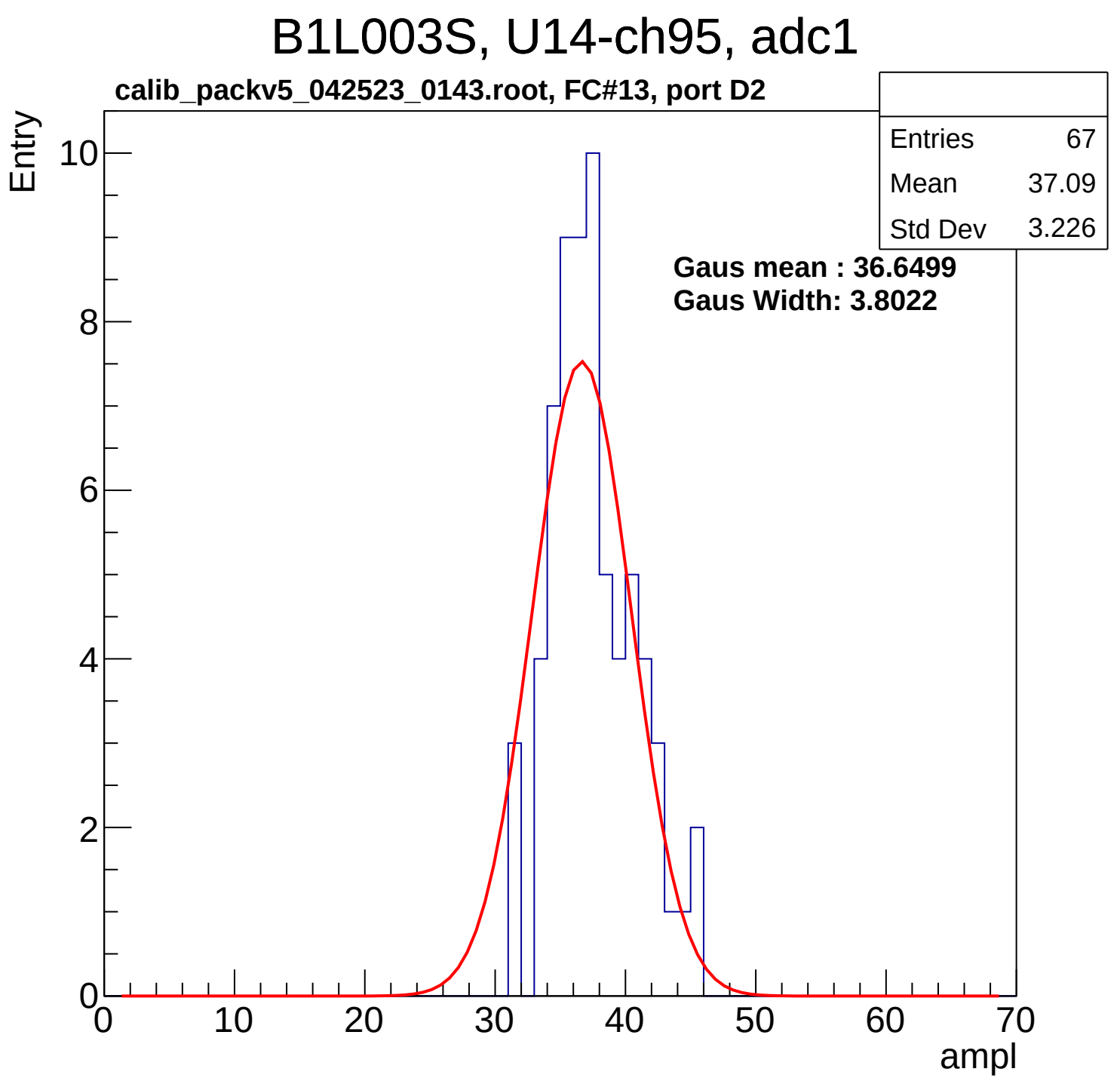

B1L003S, U14-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.12
Std Dev 3.612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

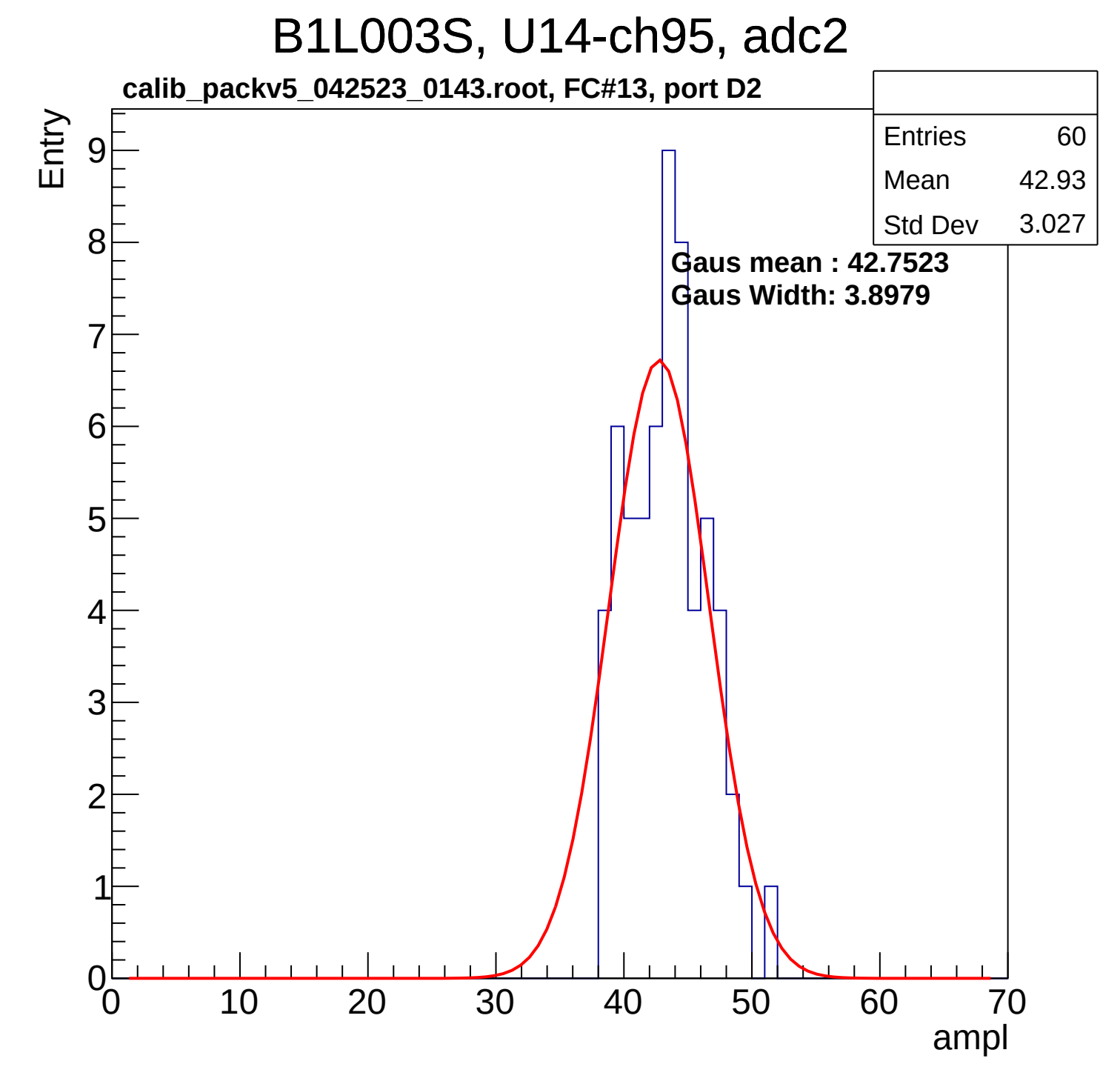

B1L003S, U14-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.74
Std Dev 2.136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

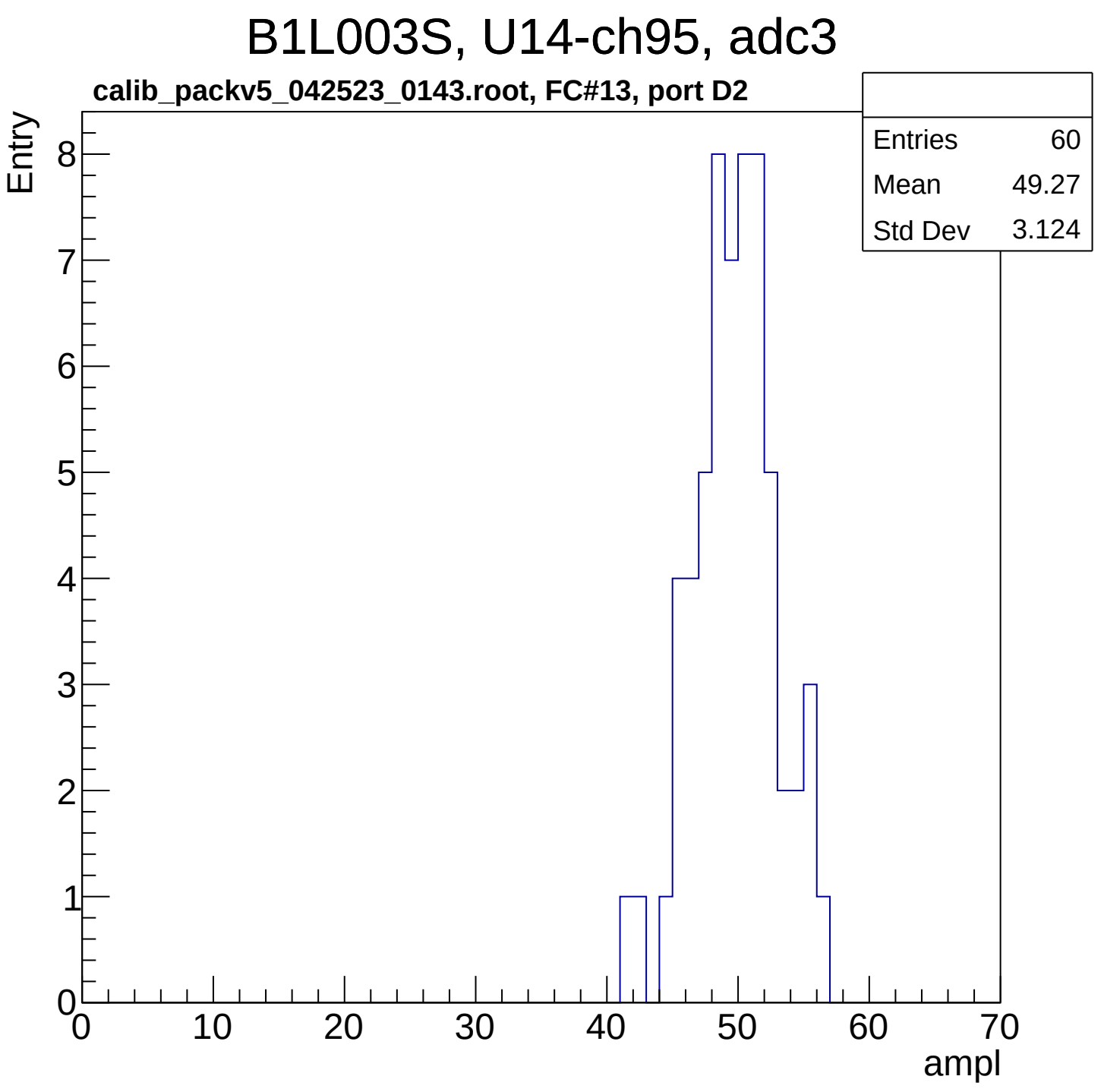

B1L003S, U14-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.38
Std Dev 14.92
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

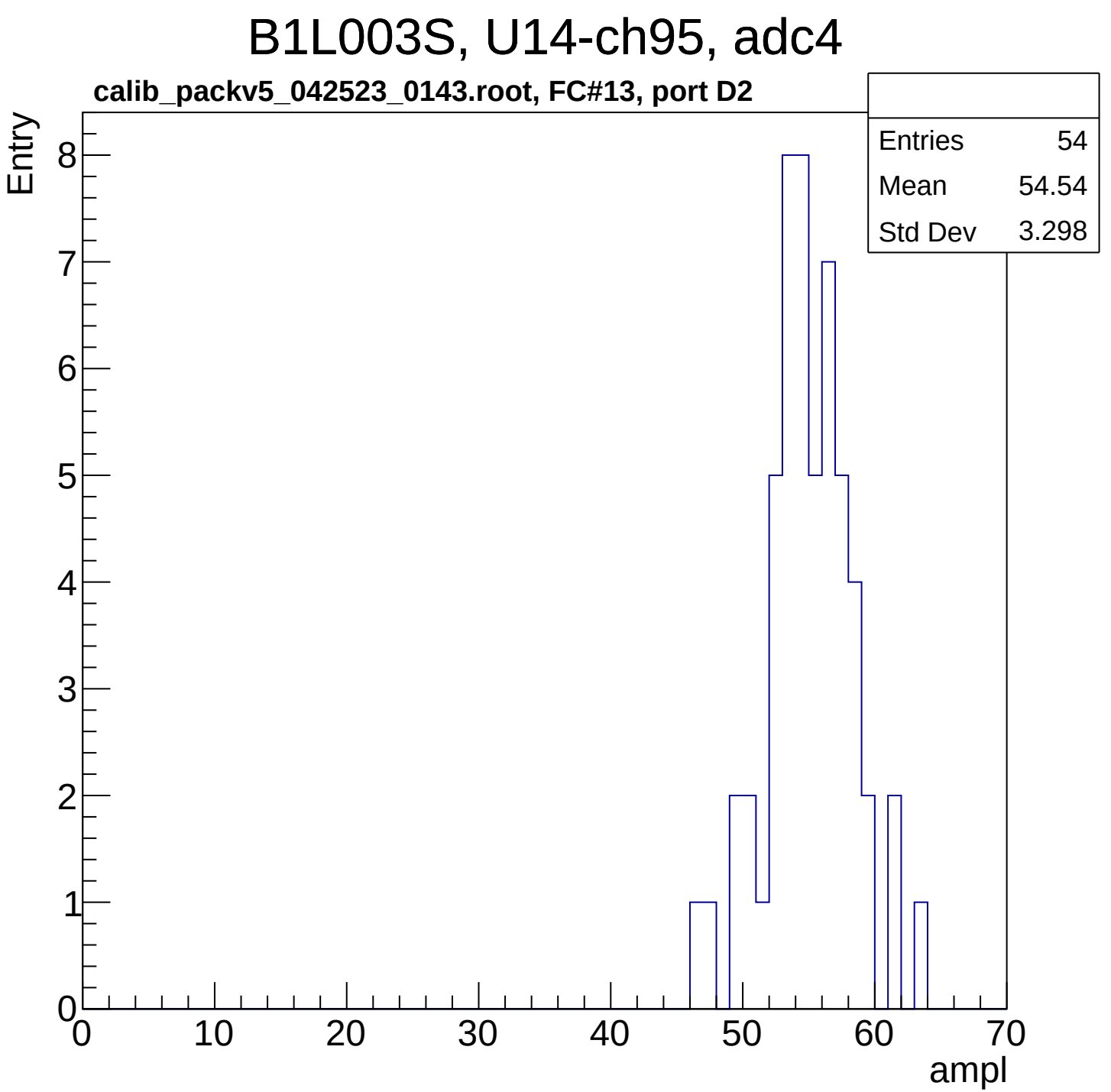

B1L003S, U14-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

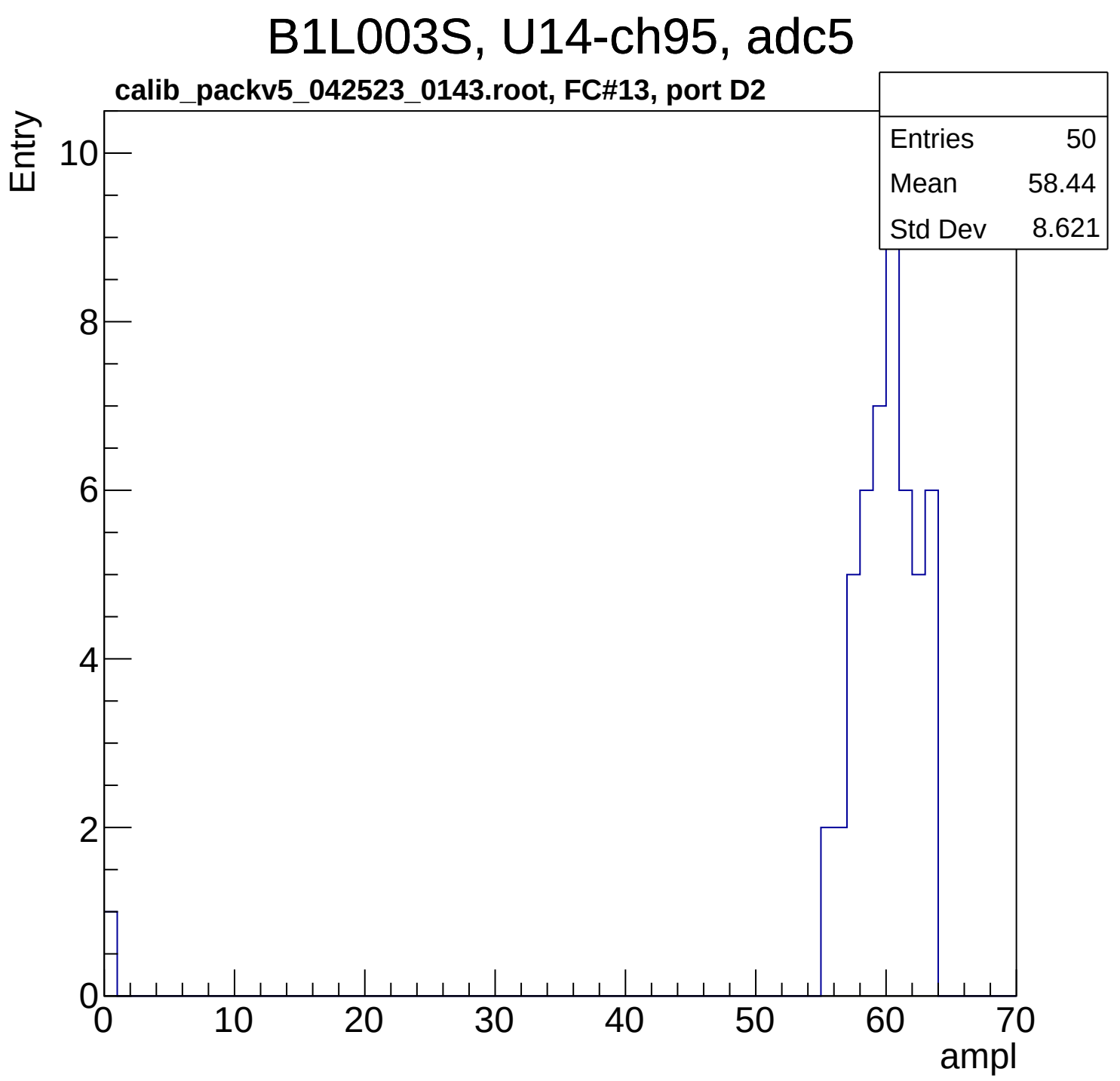

B1L003S, U14-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 27.77
Std Dev 7.193
Gaus mean : 29.5377
Gaus Width: 3.3867
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

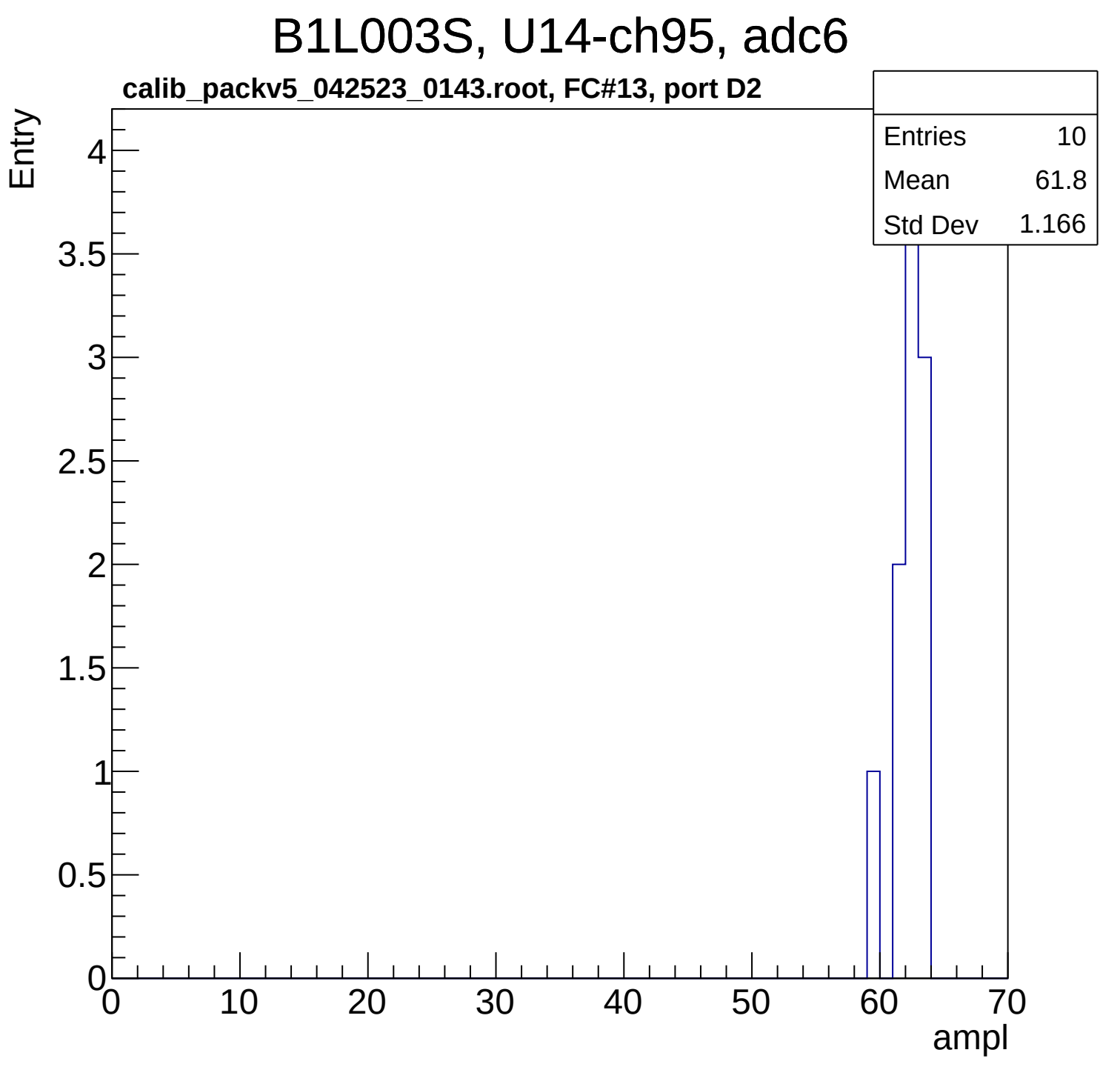

B1L003S, U14-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.09
Std Dev 3.226
Gaus mean : 36.6499
Gaus Width: 3.8022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

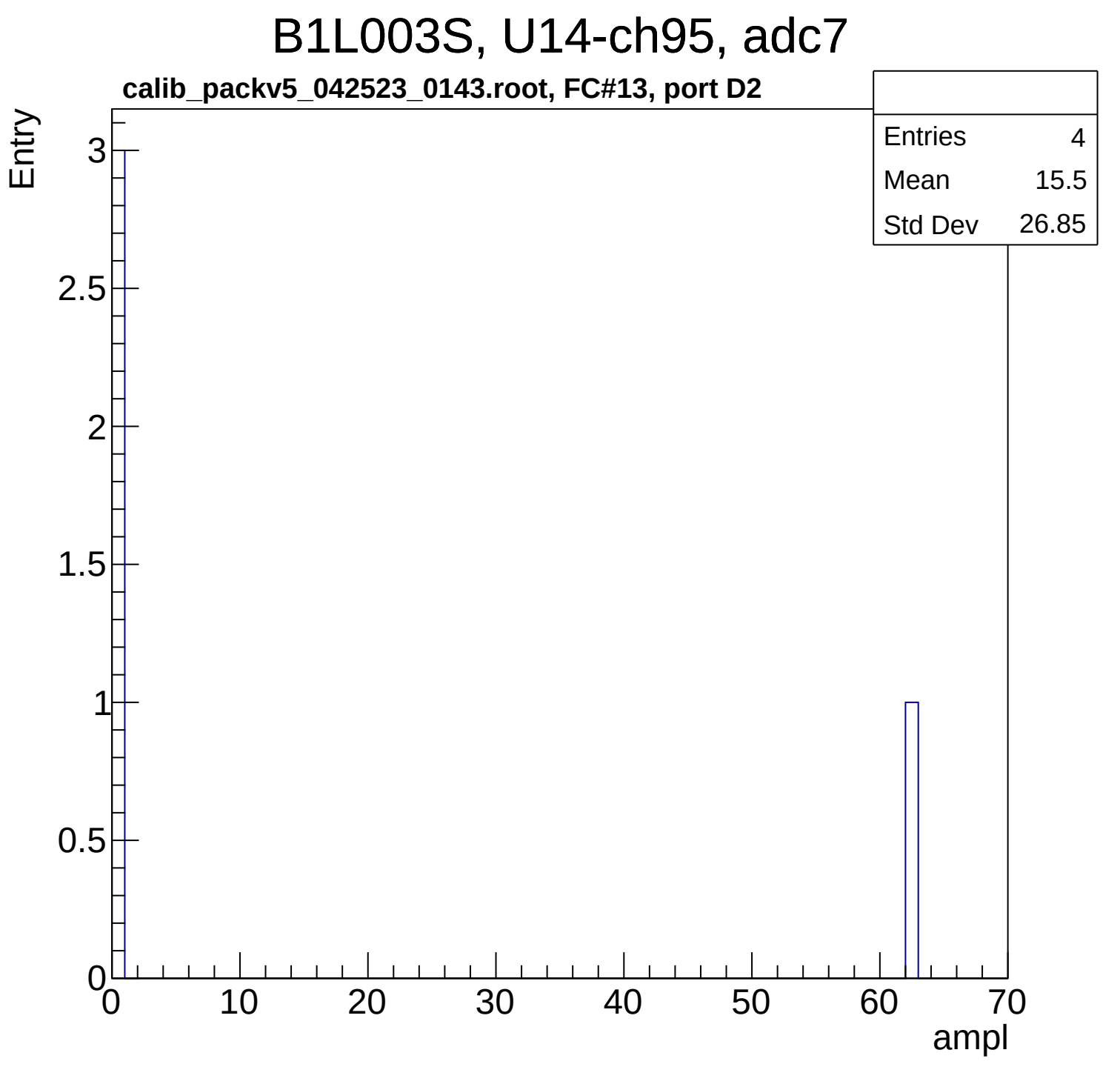

B1L003S, U14-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.93
Std Dev 3.027
Gaus mean : 42.7523
Gaus Width: 3.8979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

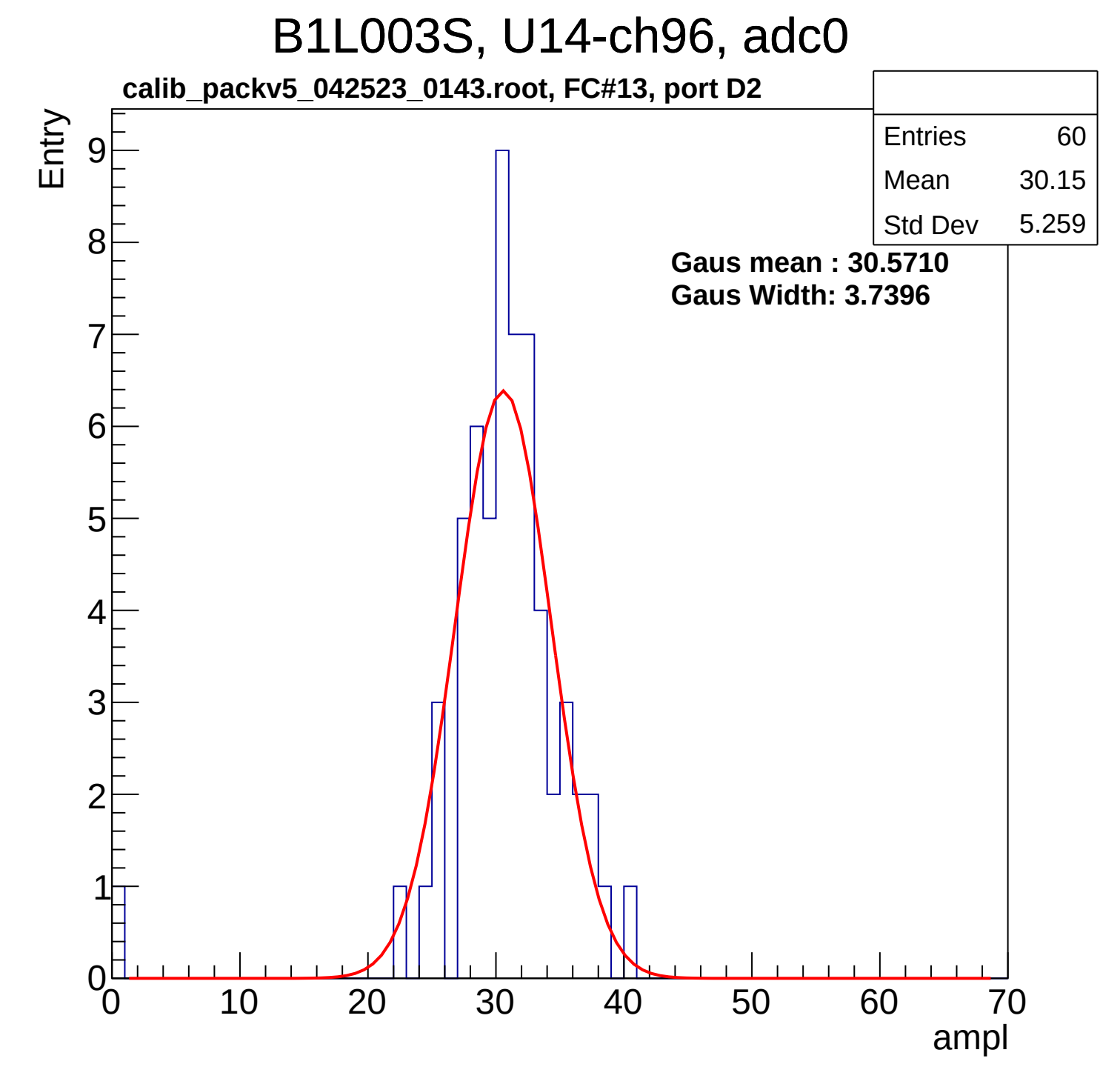

B1L003S, U14-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.27
Std Dev 3.124
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

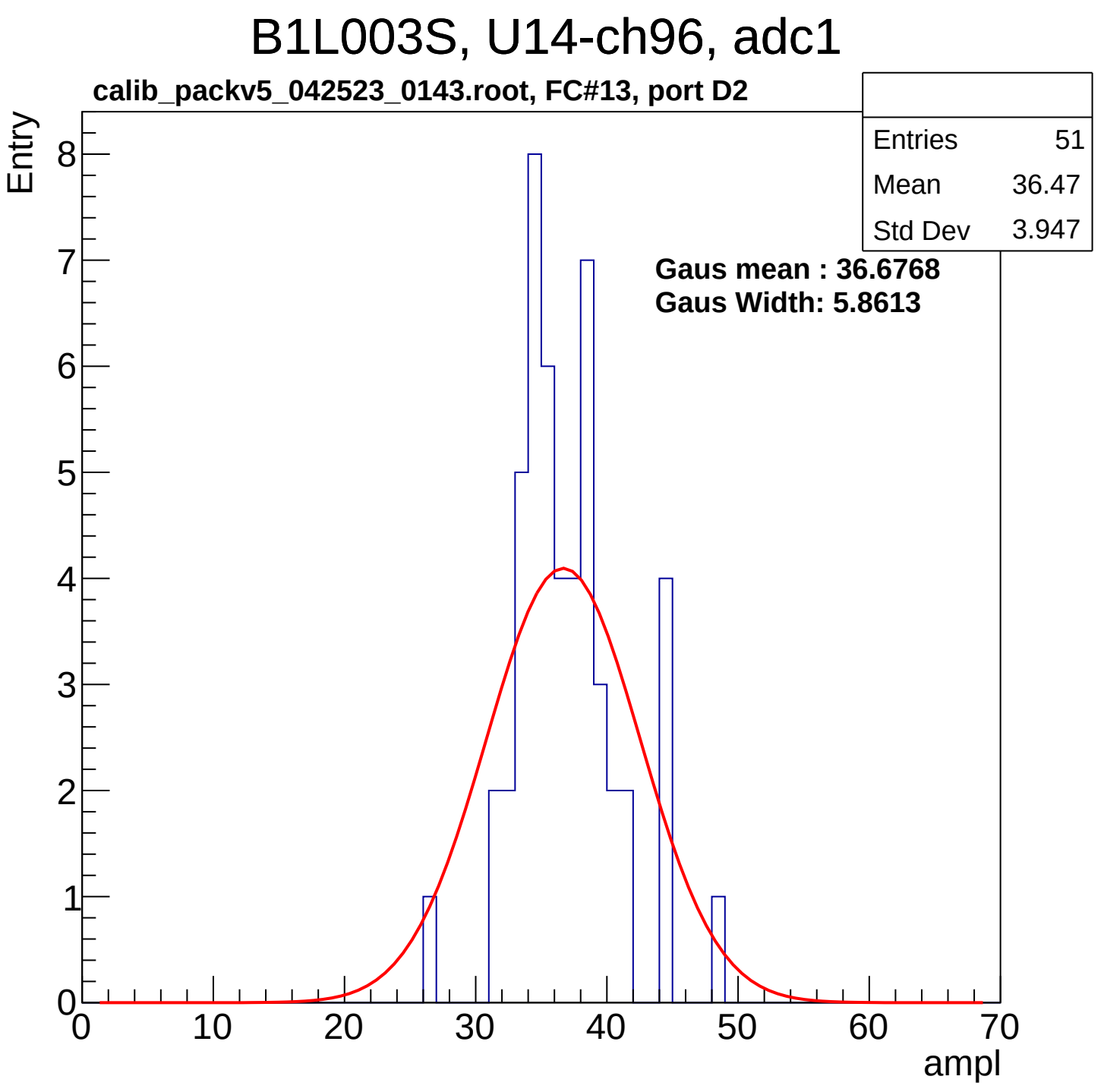

B1L003S, U14-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.54
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

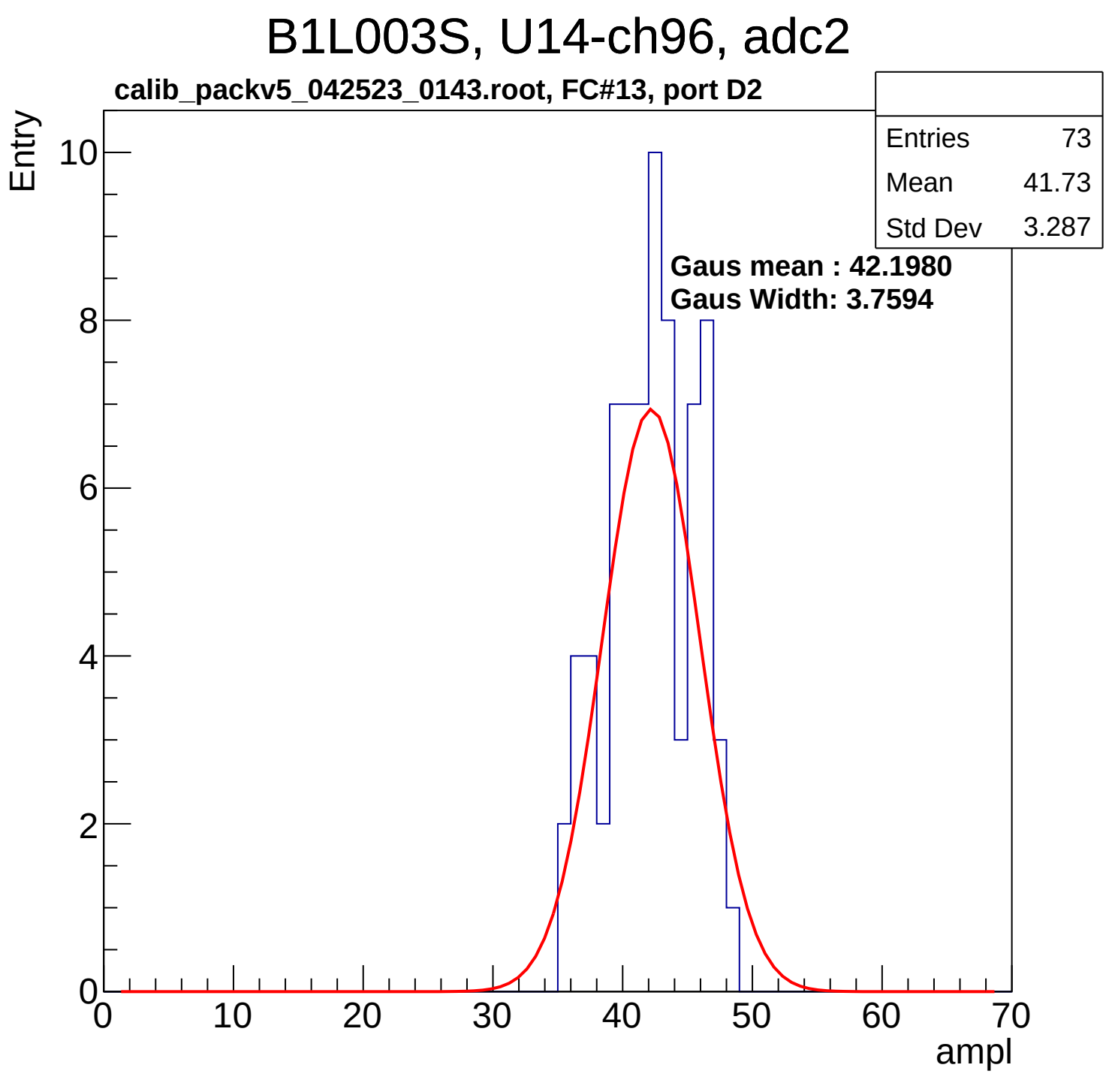

B1L003S, U14-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.44
Std Dev 8.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

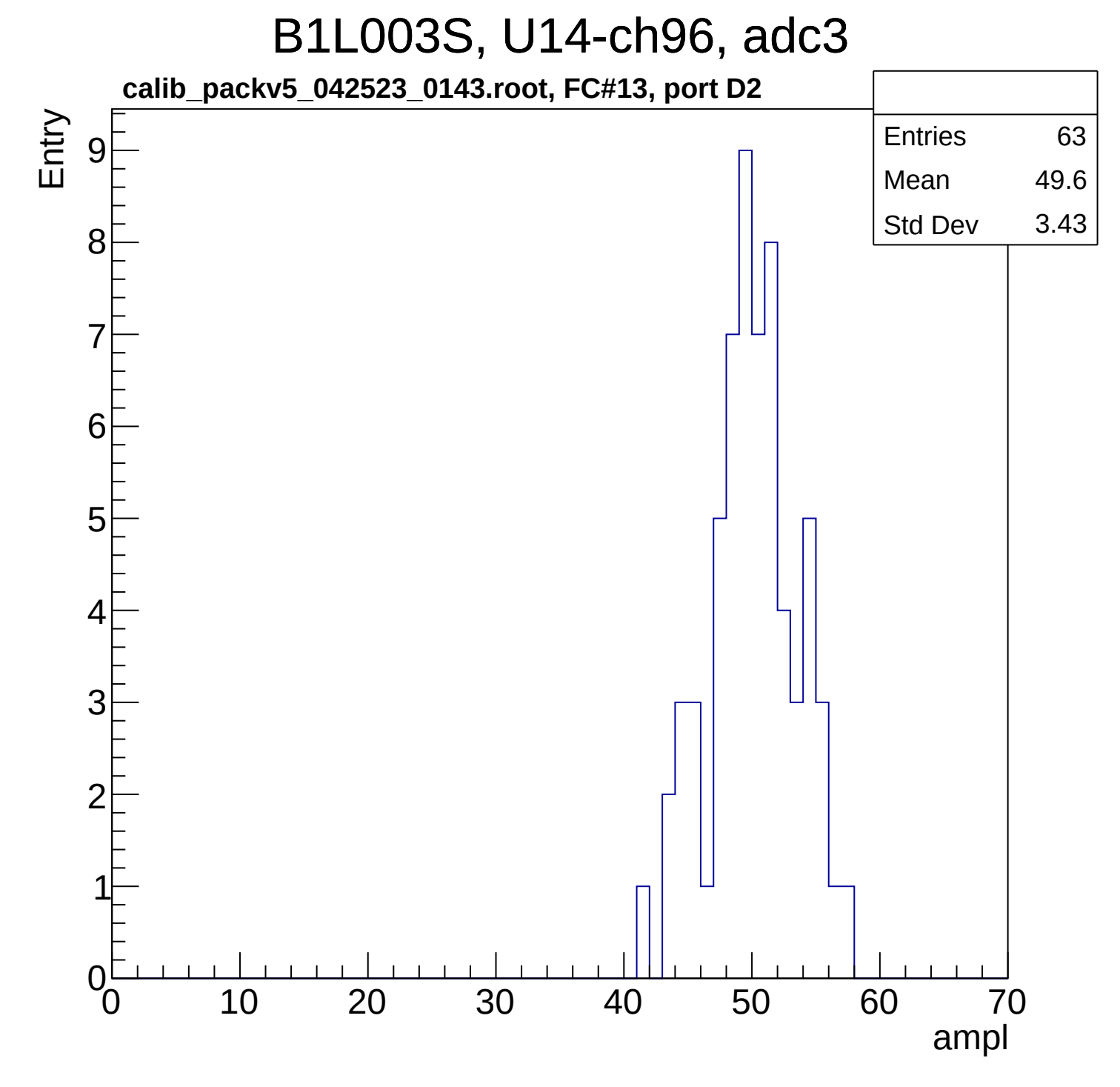

B1L003S, U14-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

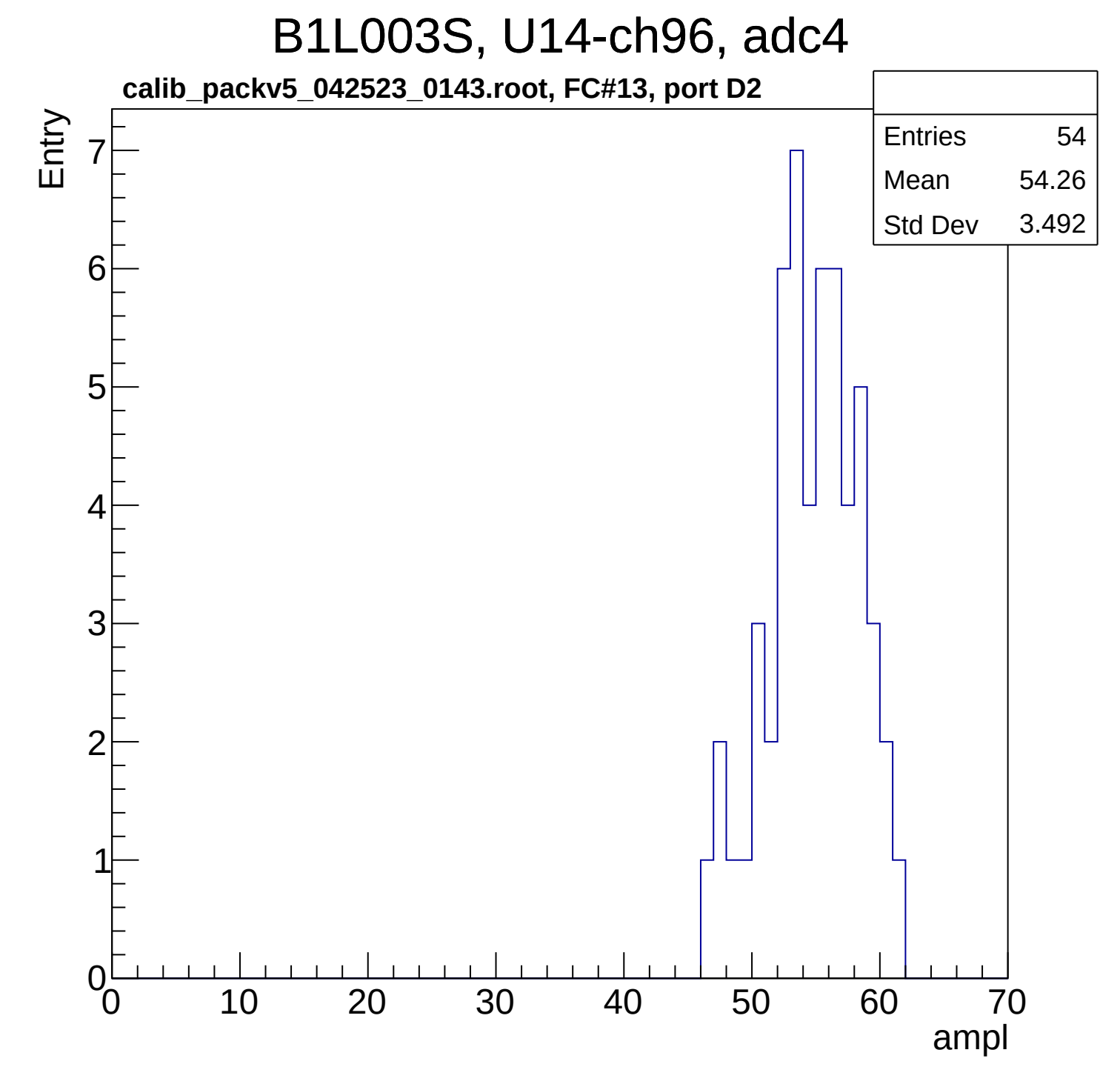

B1L003S, U14-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

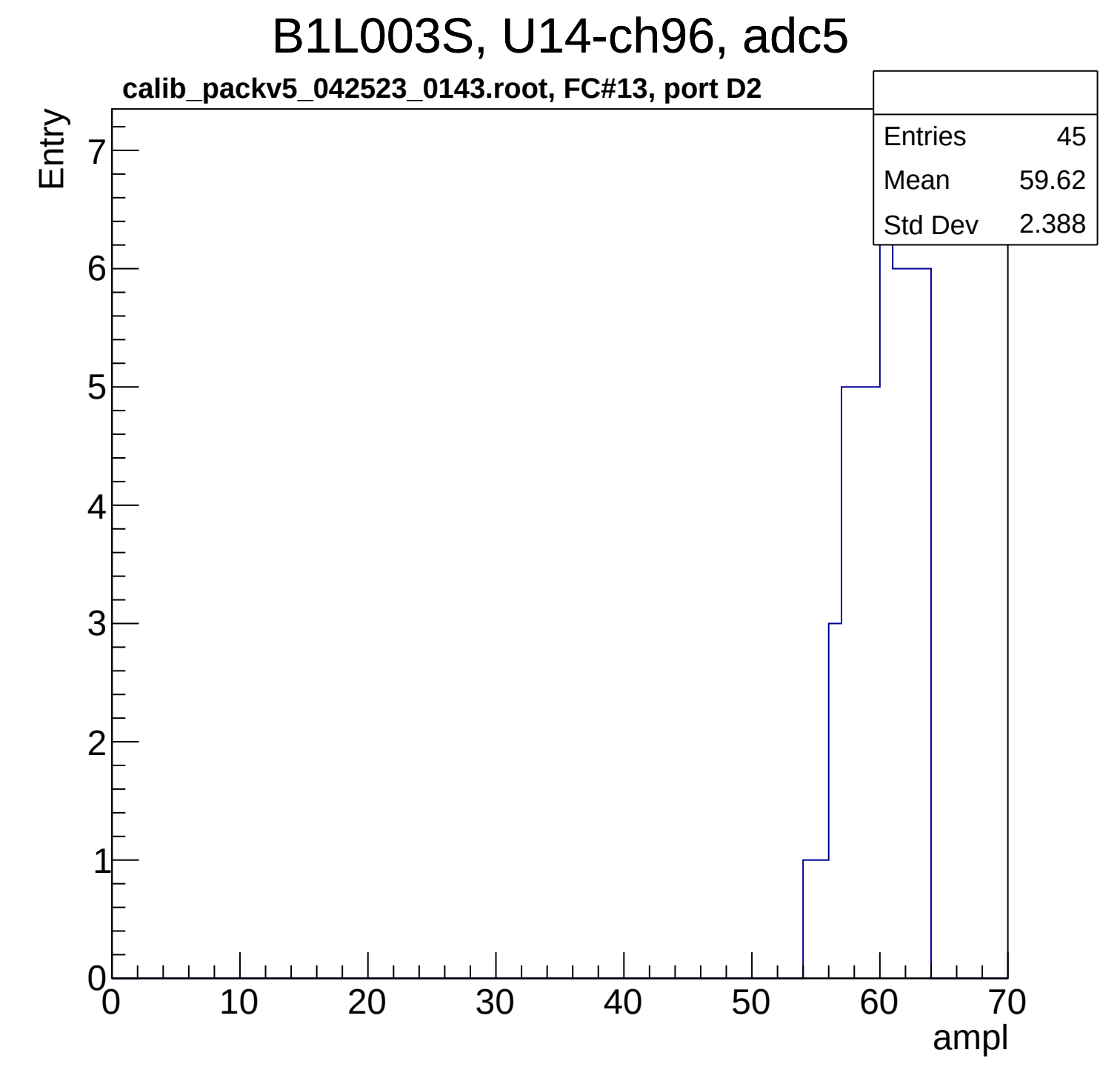

B1L003S, U14-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.15
Std Dev 5.259
Gaus mean : 30.5710
Gaus Width: 3.7396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

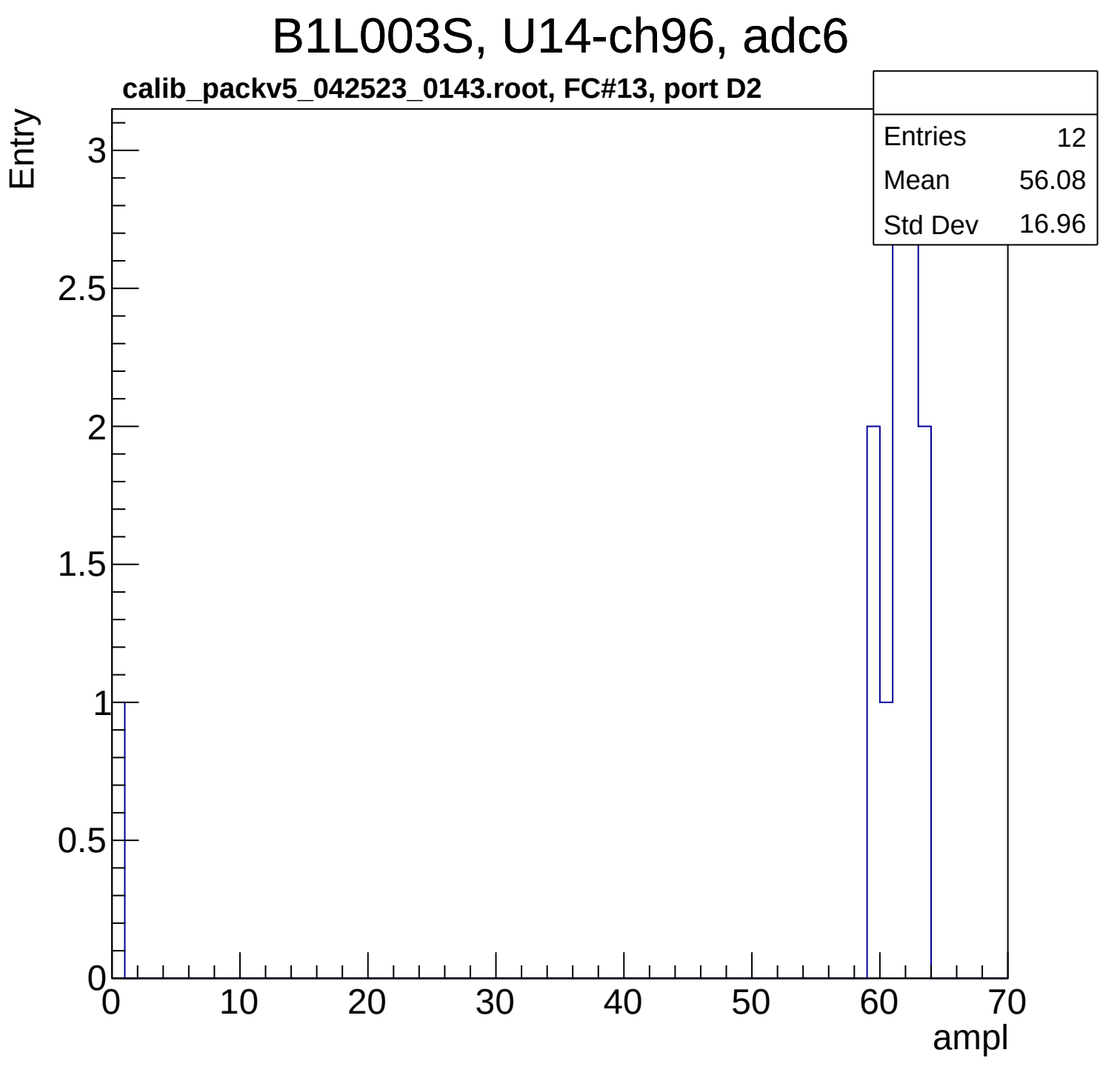

B1L003S, U14-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 36.47
Std Dev 3.947
Gaus mean : 36.6768
Gaus Width: 5.8613
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

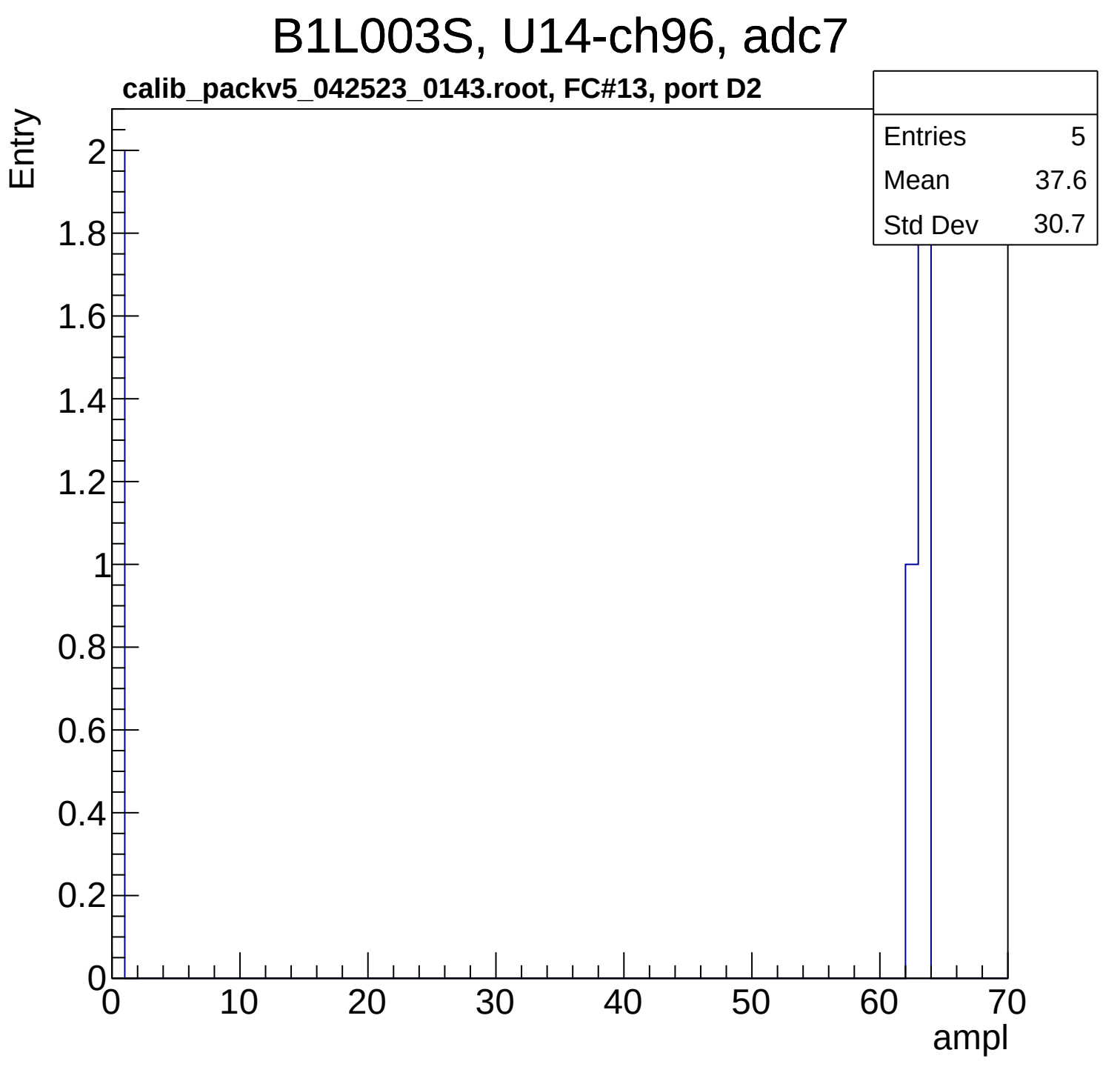

B1L003S, U14-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.73
Std Dev 3.287
Gaus mean : 42.1980
Gaus Width: 3.7594
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

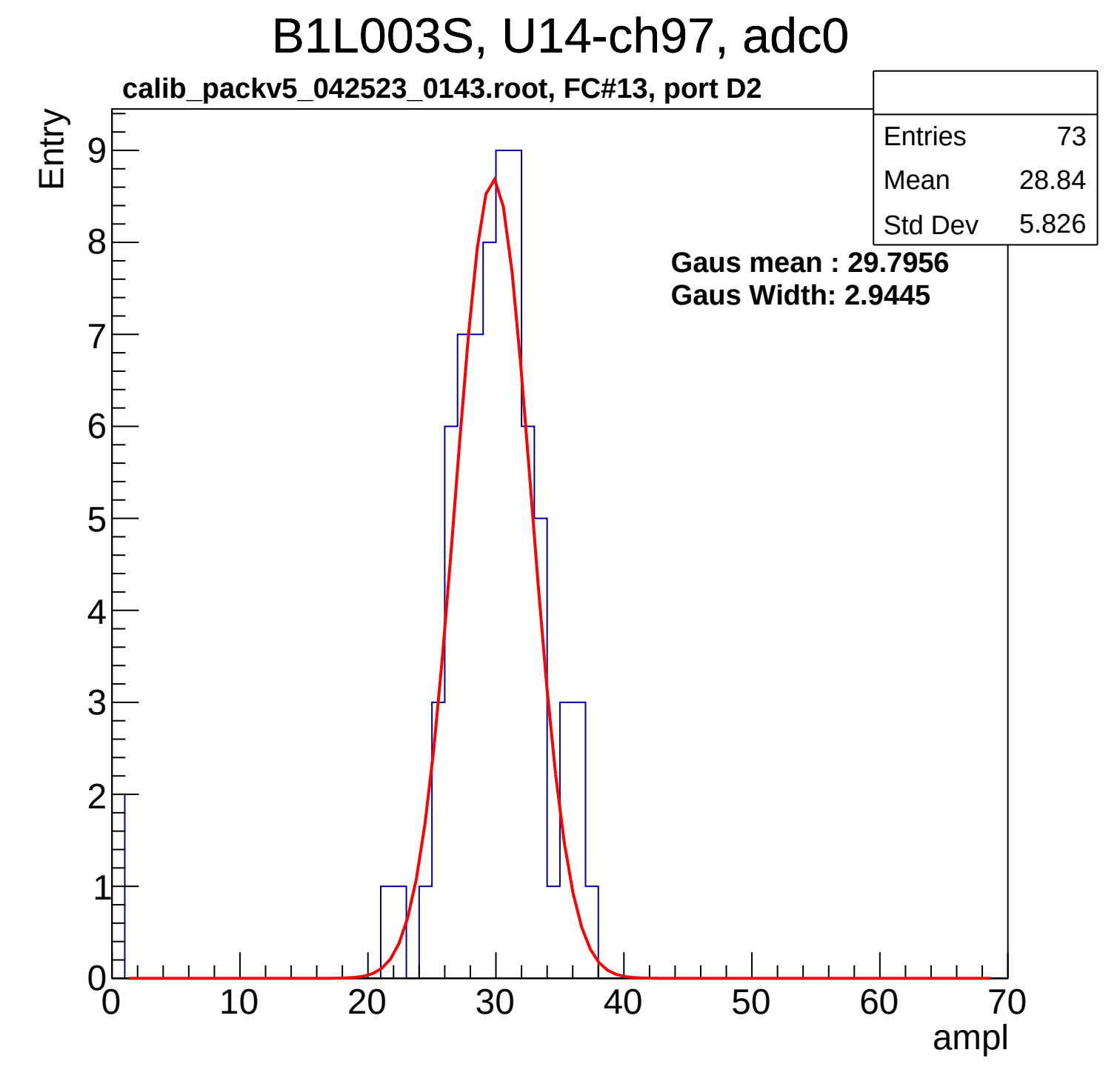

B1L003S, U14-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.6
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

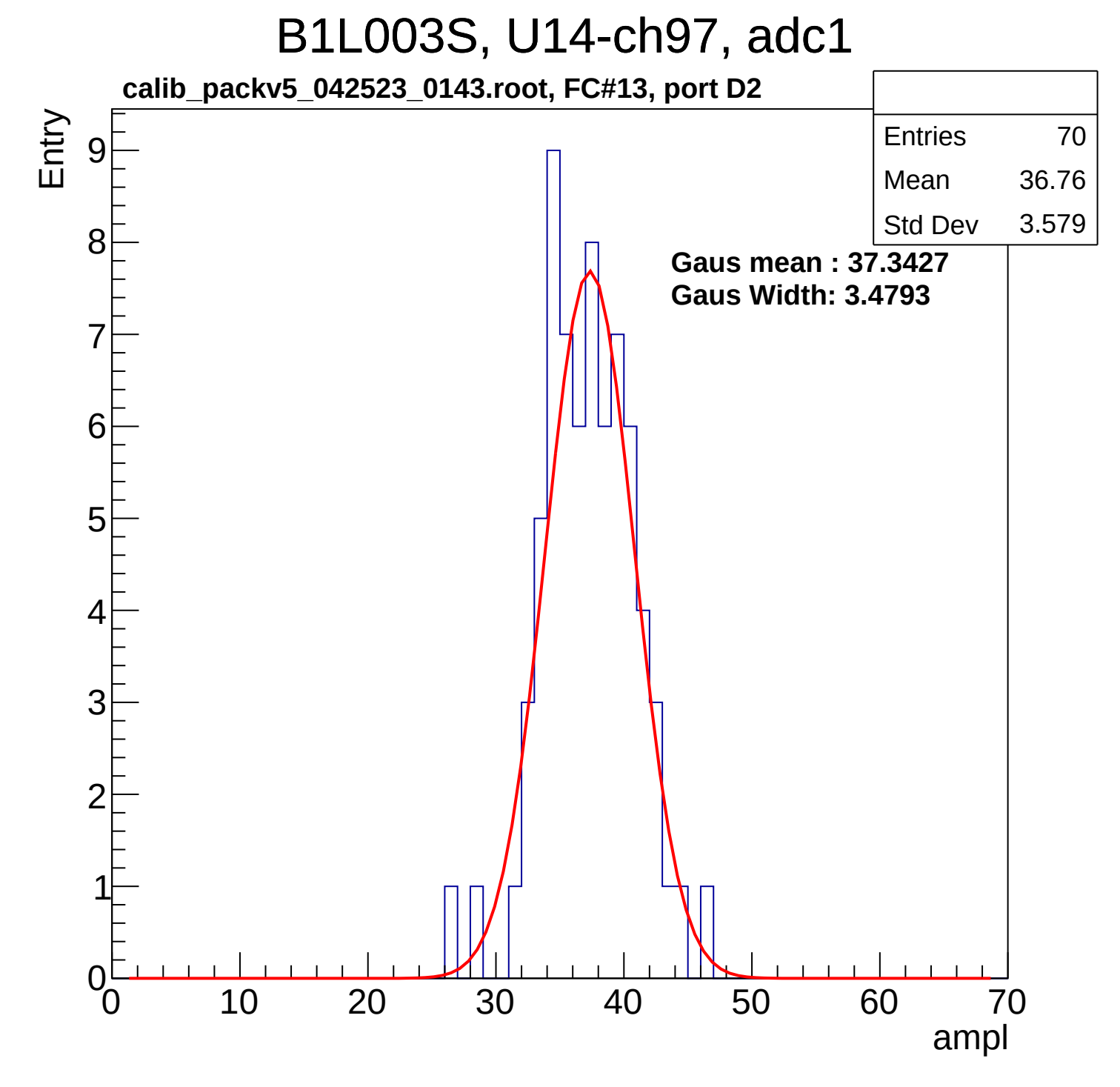

B1L003S, U14-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.26
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

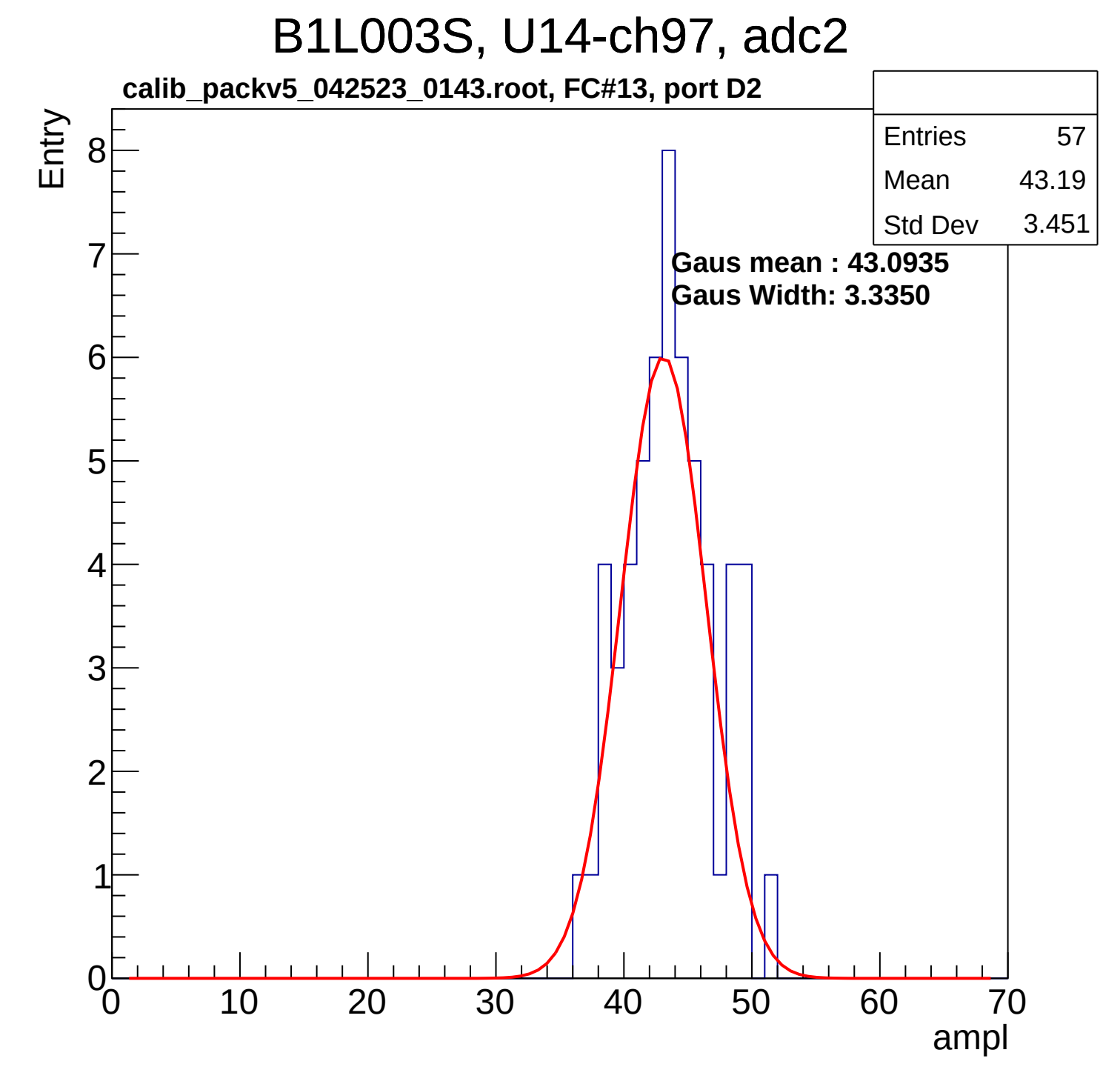

B1L003S, U14-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.62
Std Dev 2.388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

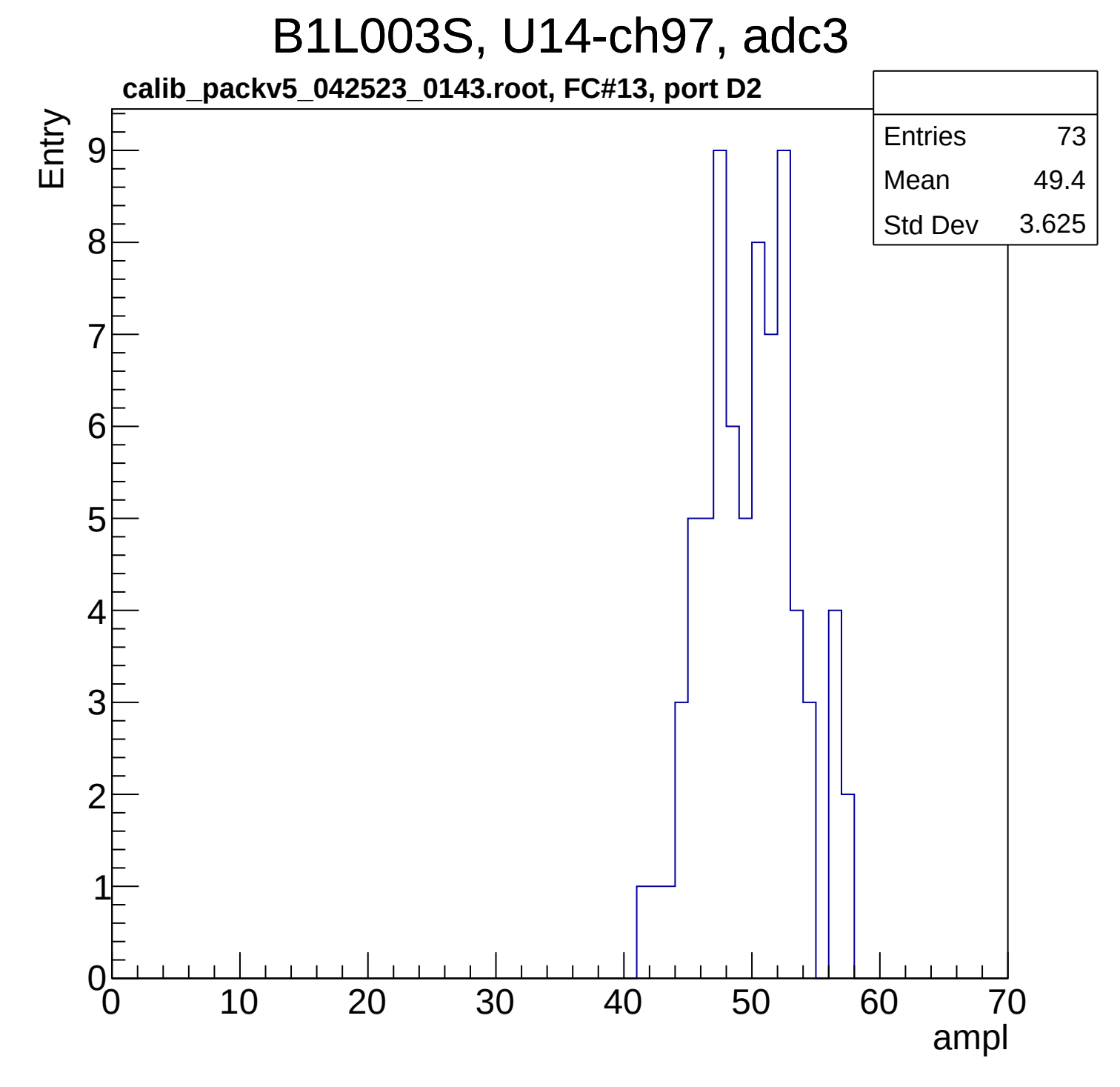

B1L003S, U14-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.08
Std Dev 16.96
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

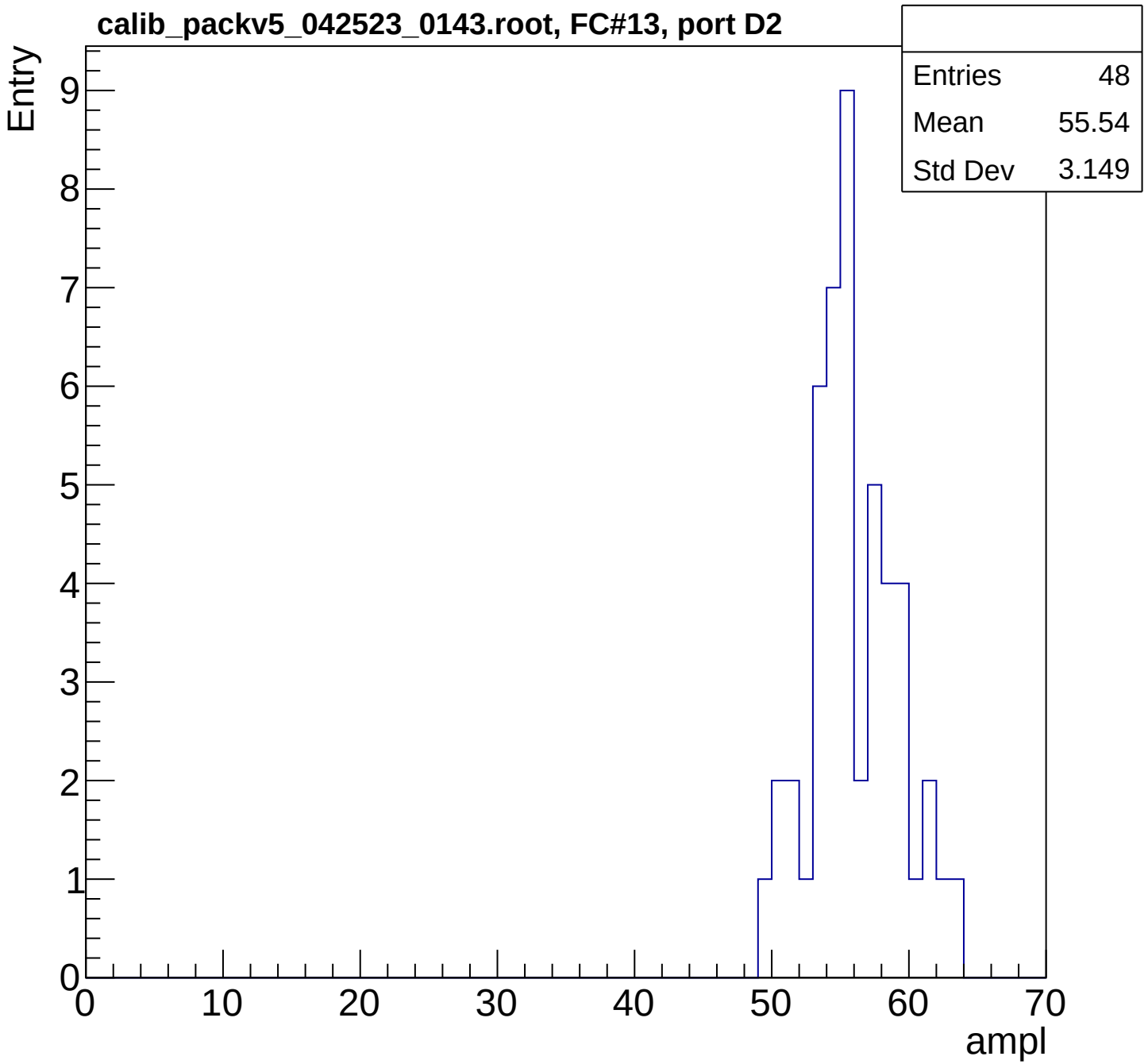

B1L003S, U14-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

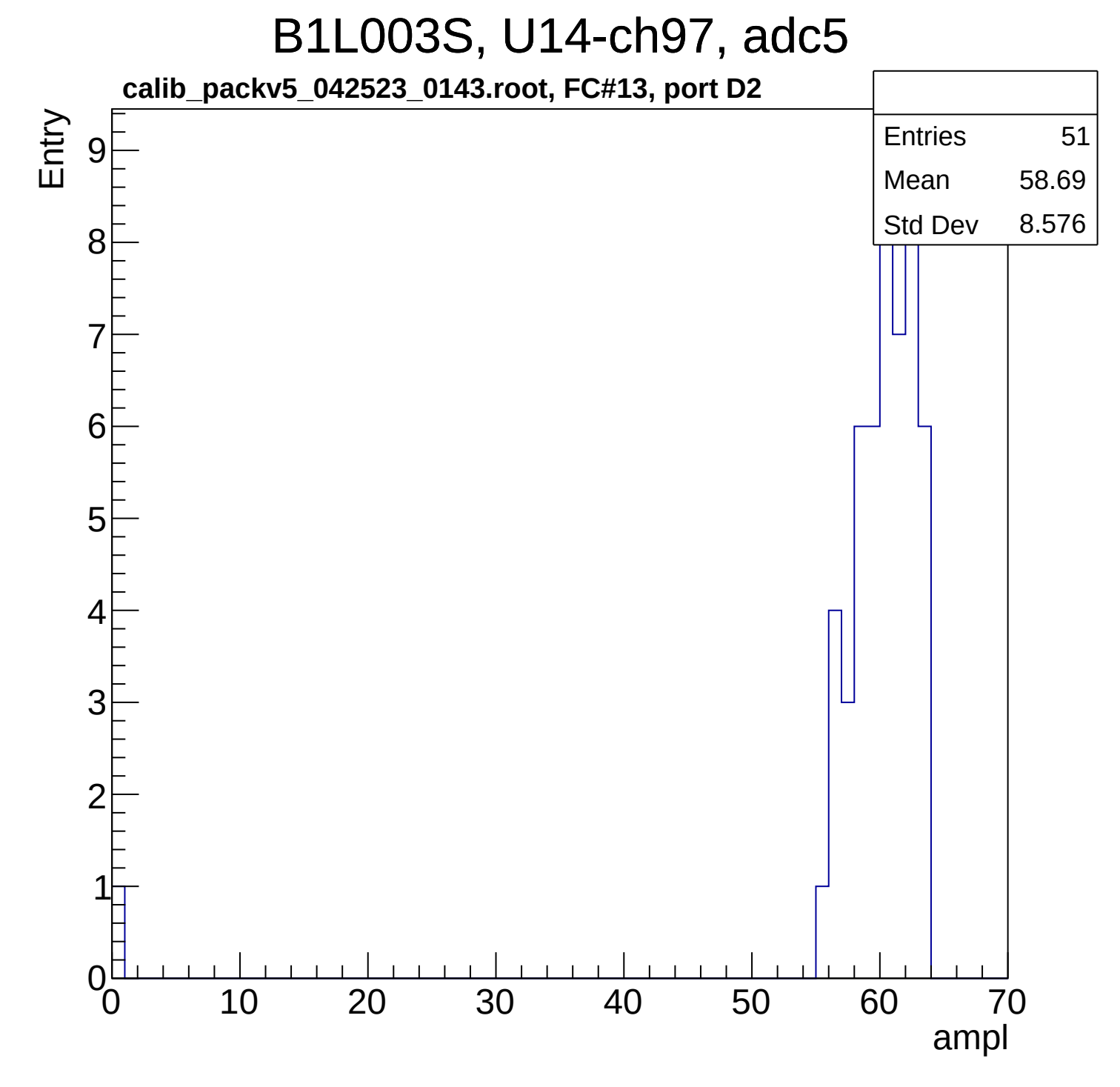

B1L003S, U14-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.84
Std Dev 5.826
Gaus mean : 29.7956
Gaus Width: 2.9445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

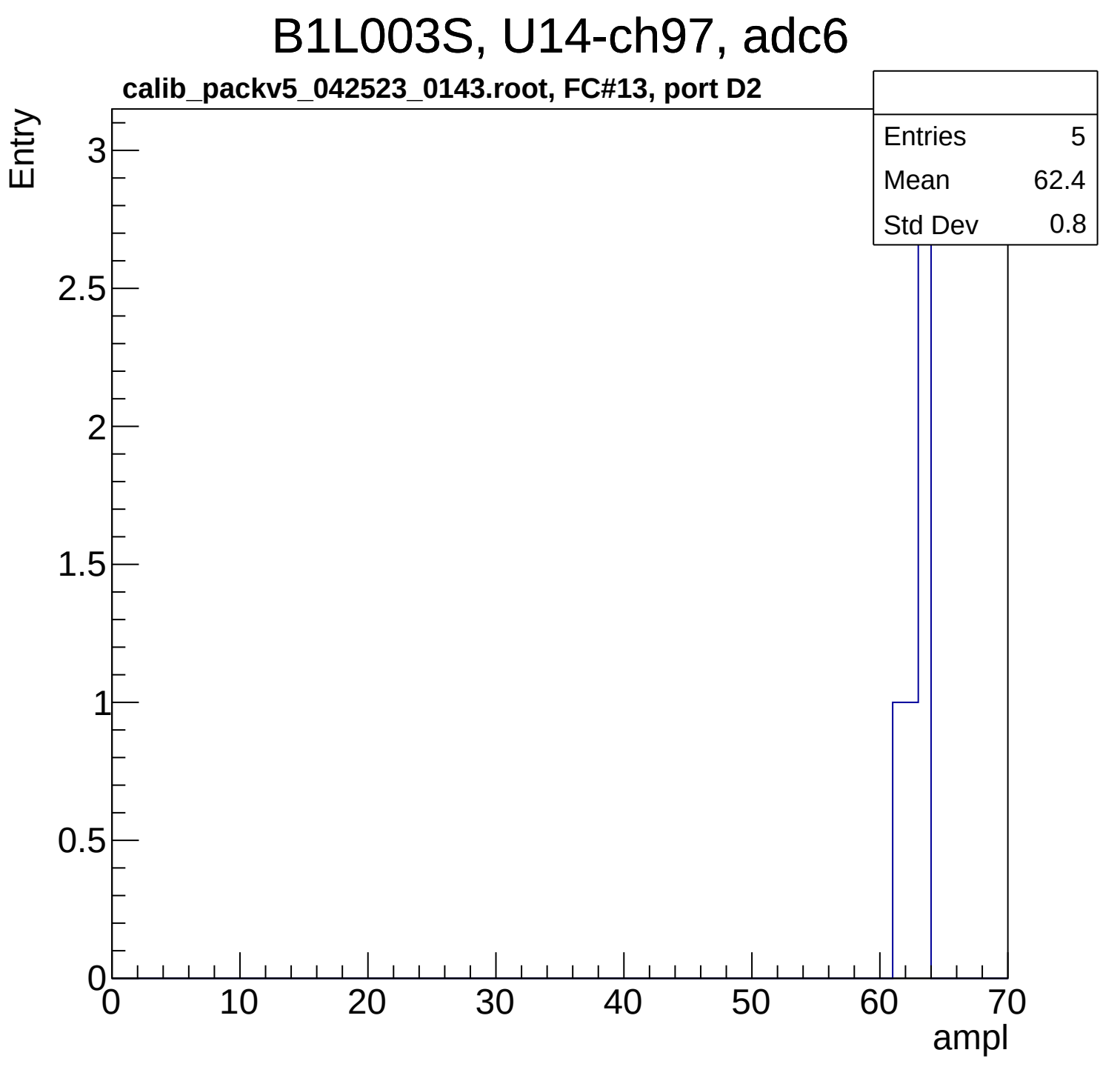

B1L003S, U14-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.76
Std Dev 3.579
Gaus mean : 37.3427
Gaus Width: 3.4793
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

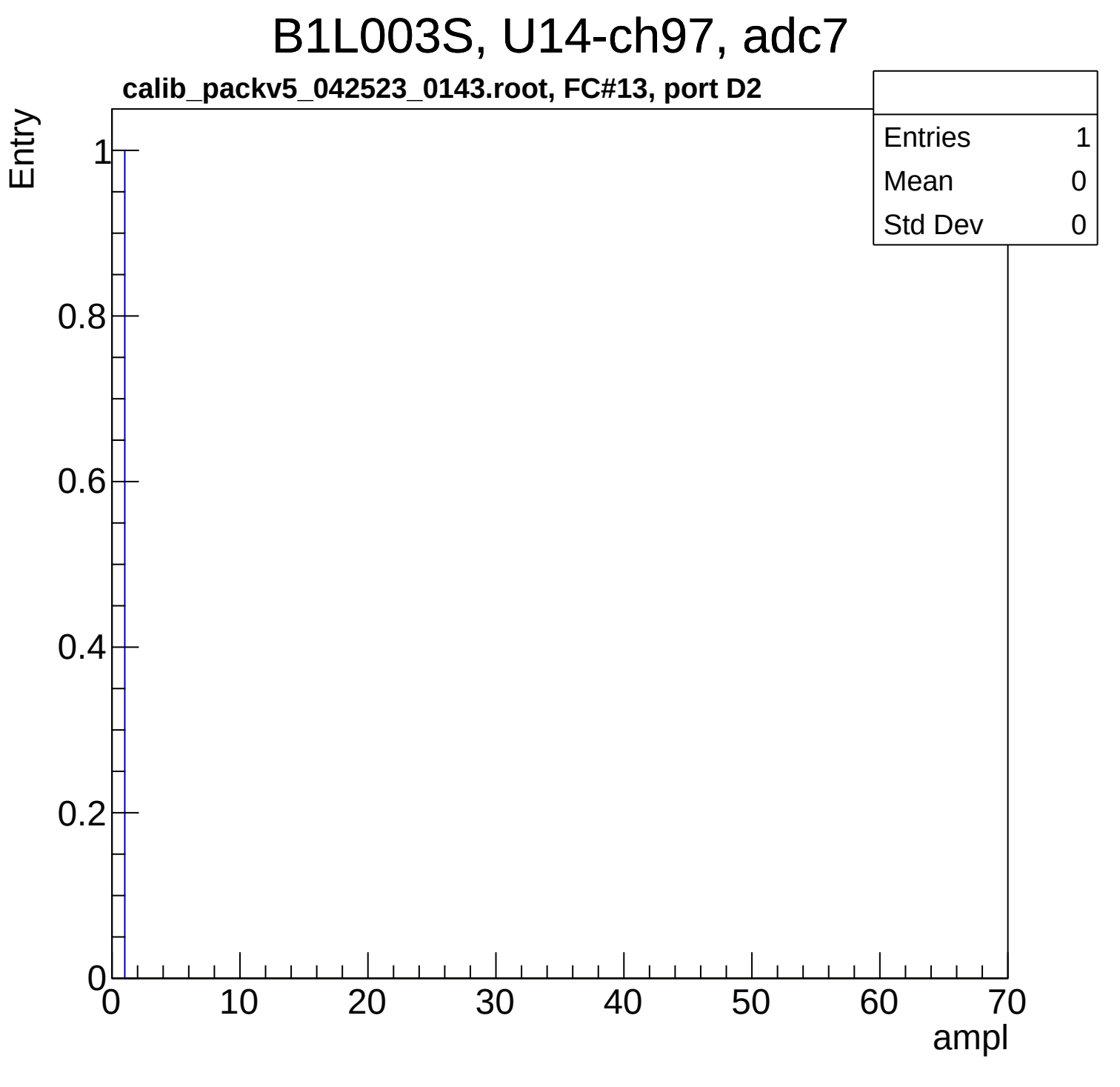

B1L003S, U14-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.19
Std Dev 3.451
Gaus mean : 43.0935
Gaus Width: 3.3350
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

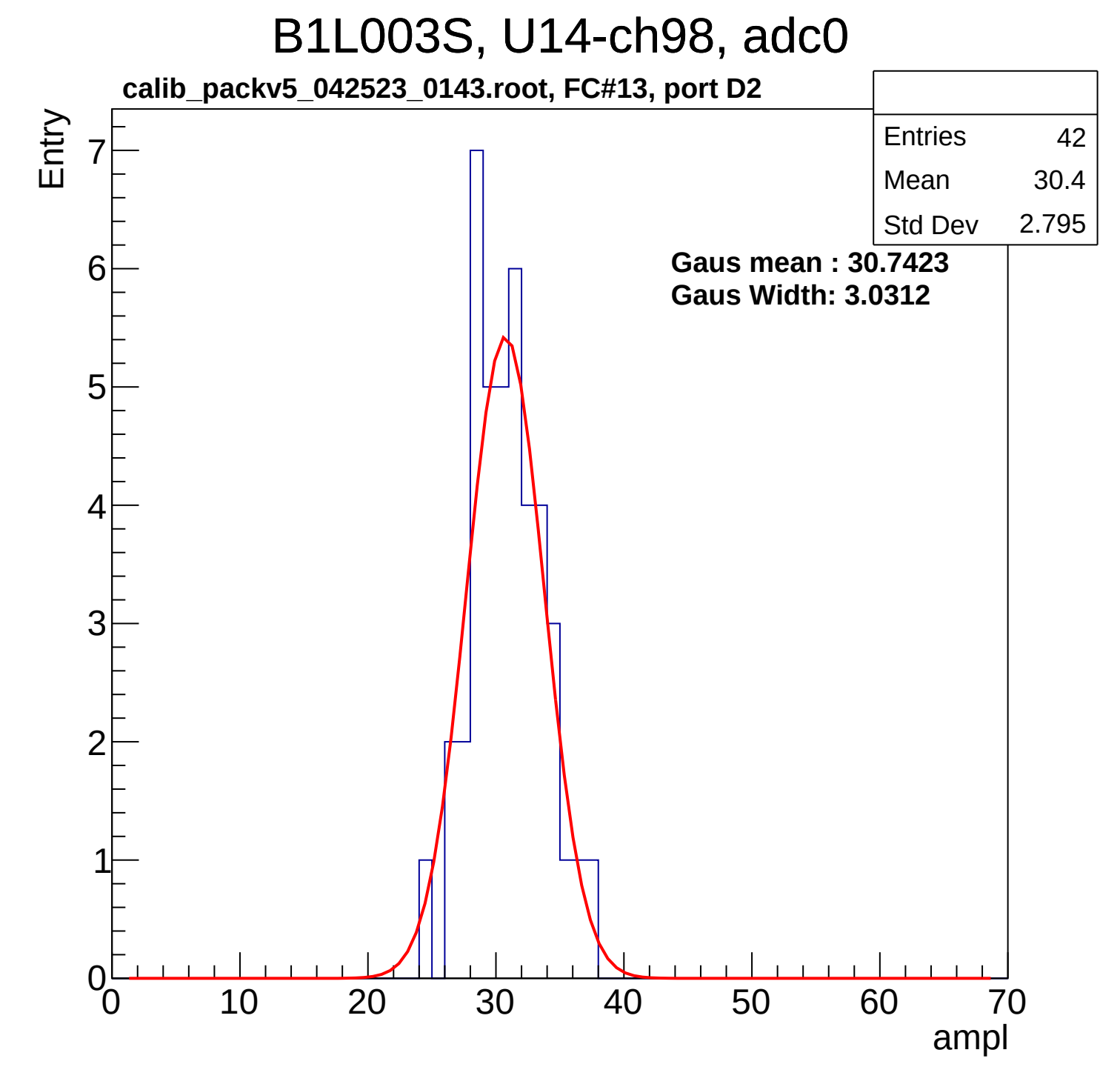

B1L003S, U14-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.4
Std Dev 3.625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

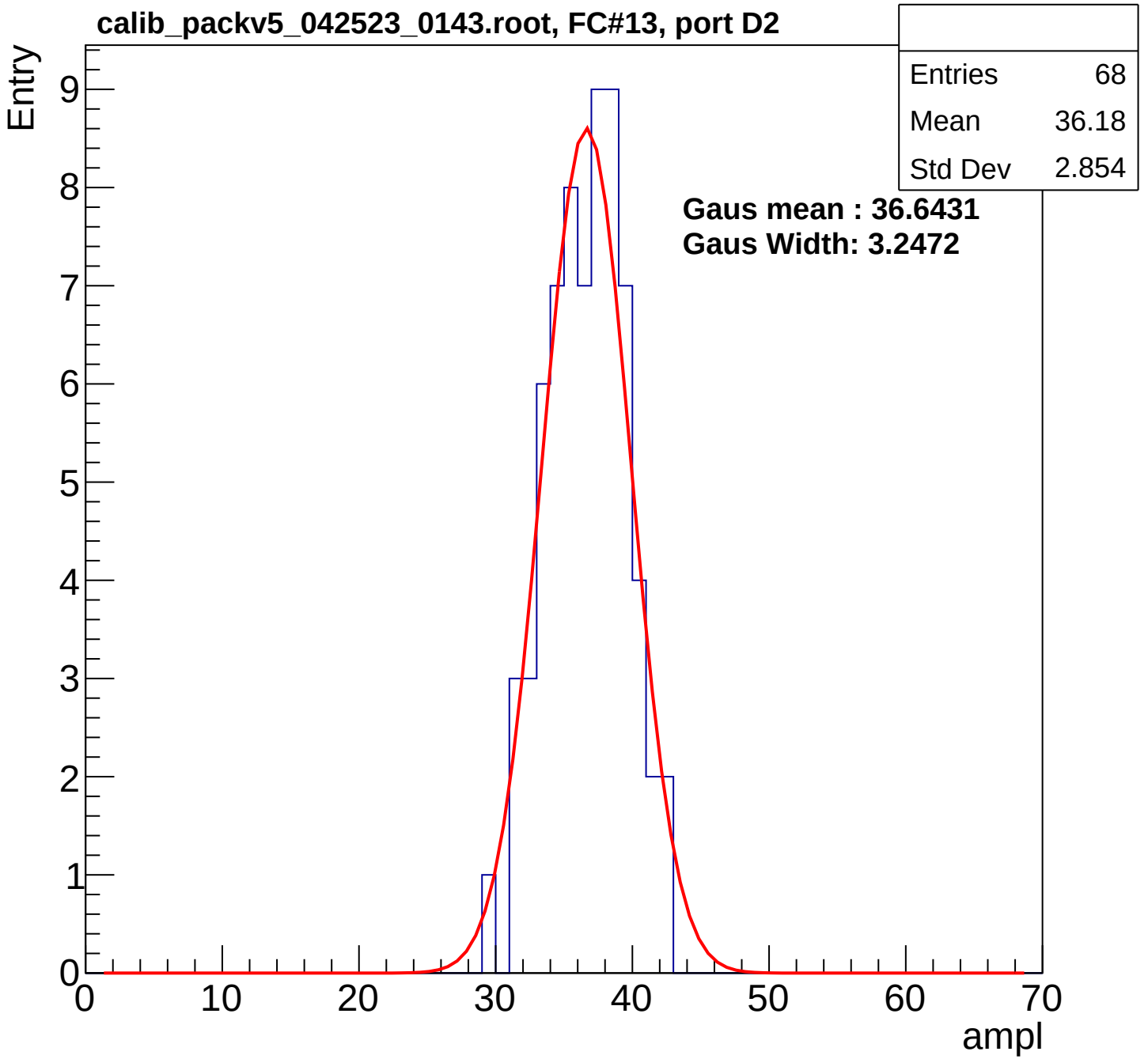

B1L003S, U14-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.54
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

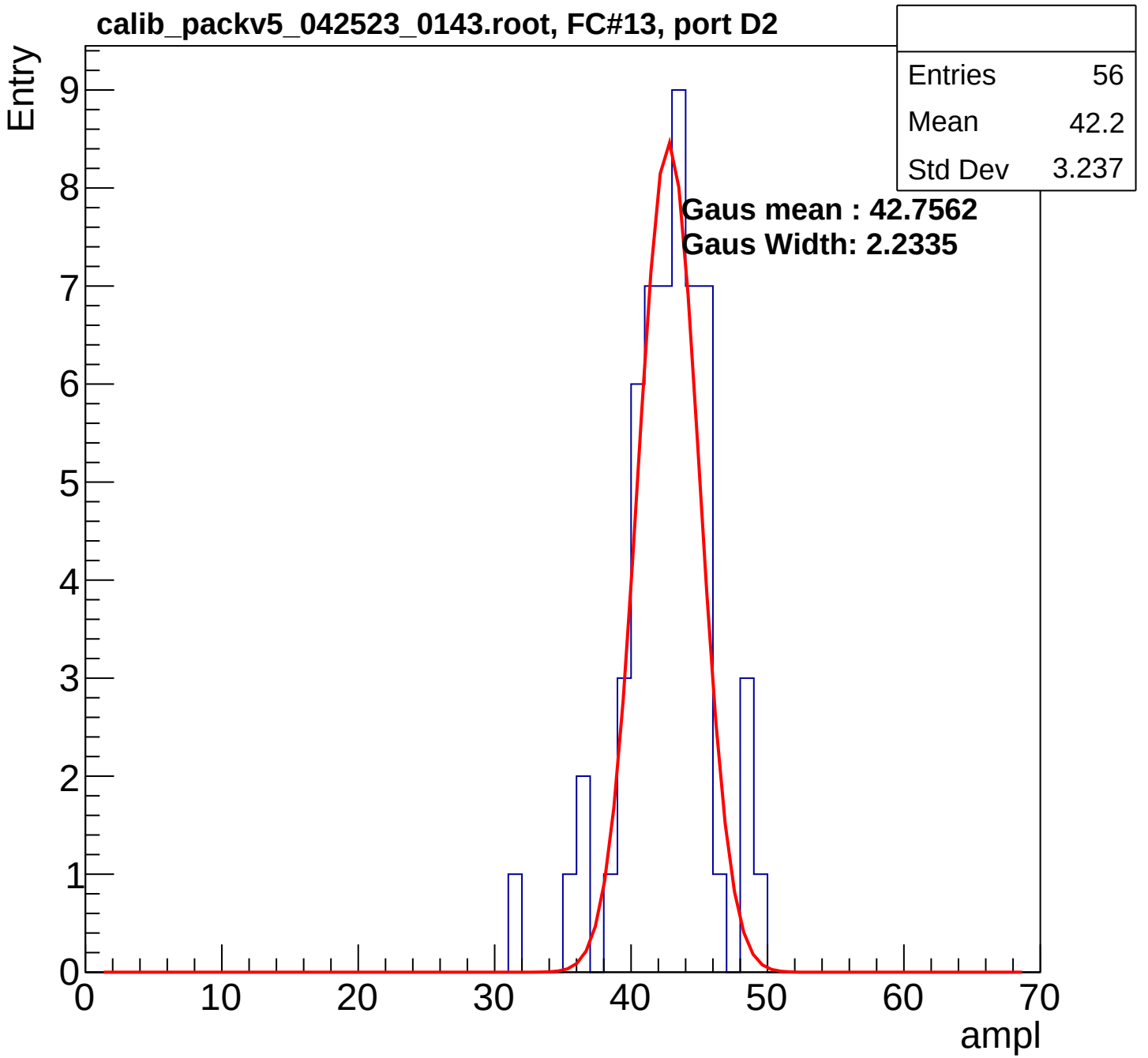

B1L003S, U14-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.69
Std Dev 8.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

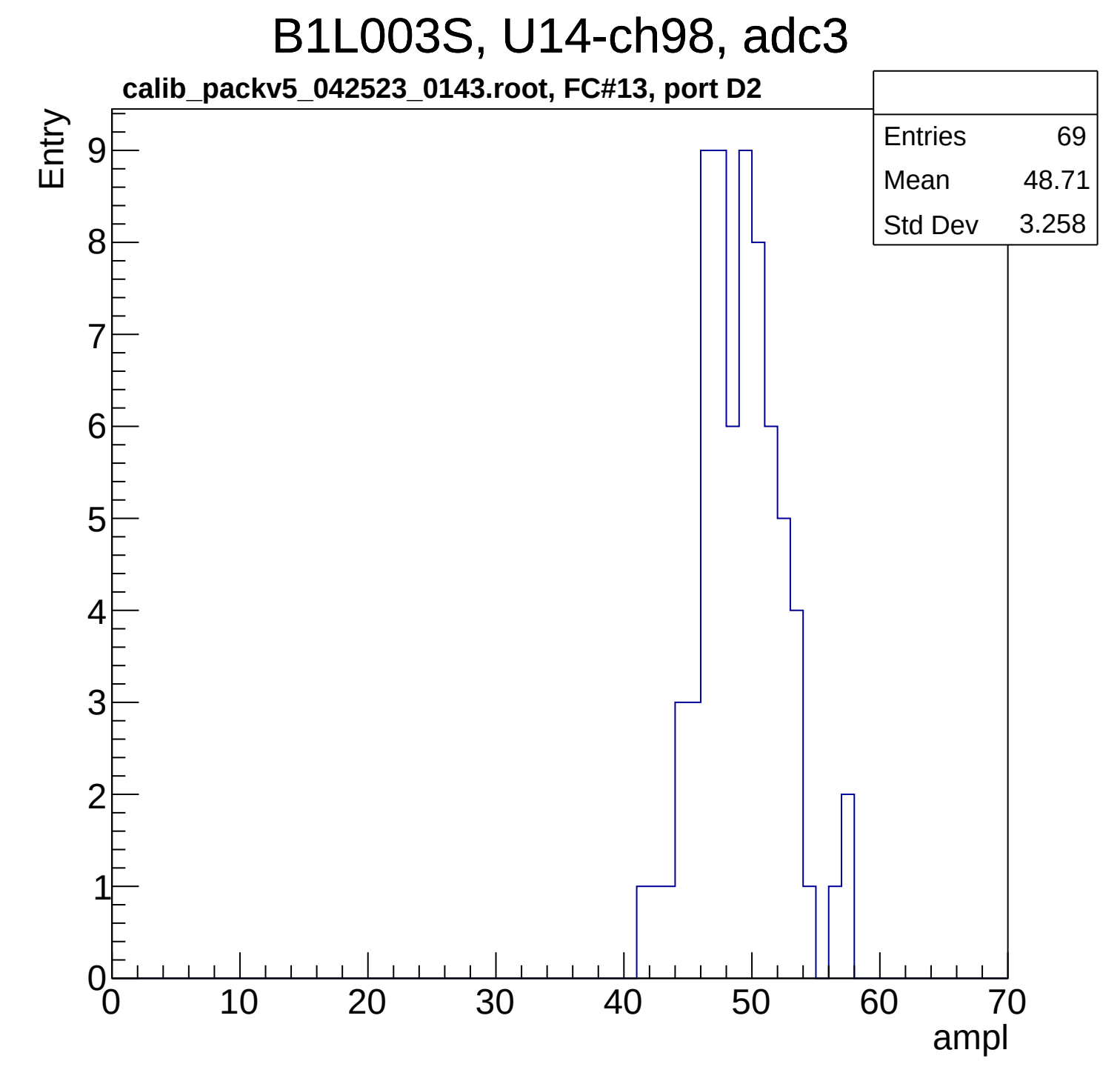

B1L003S, U14-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

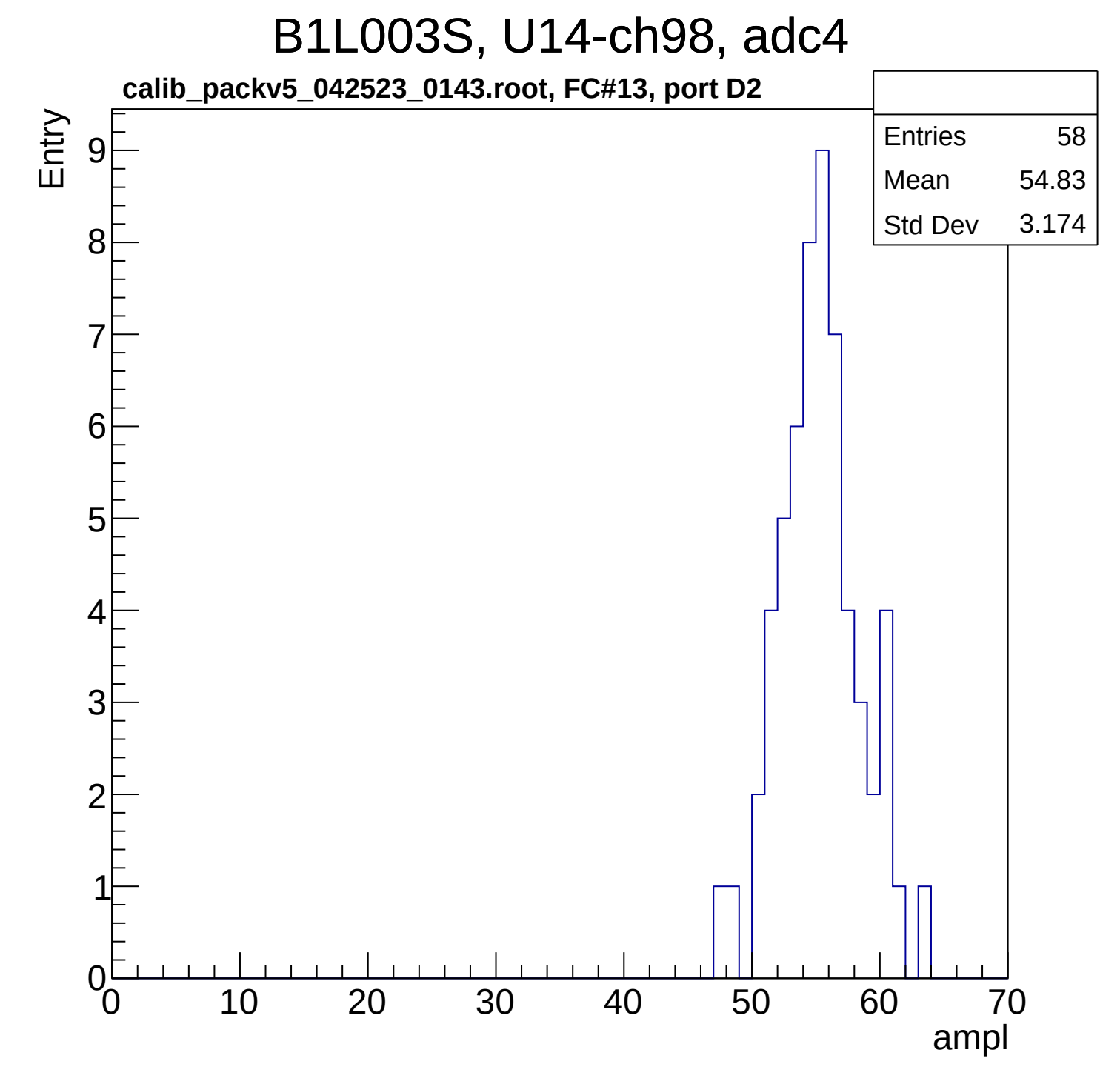

B1L003S, U14-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

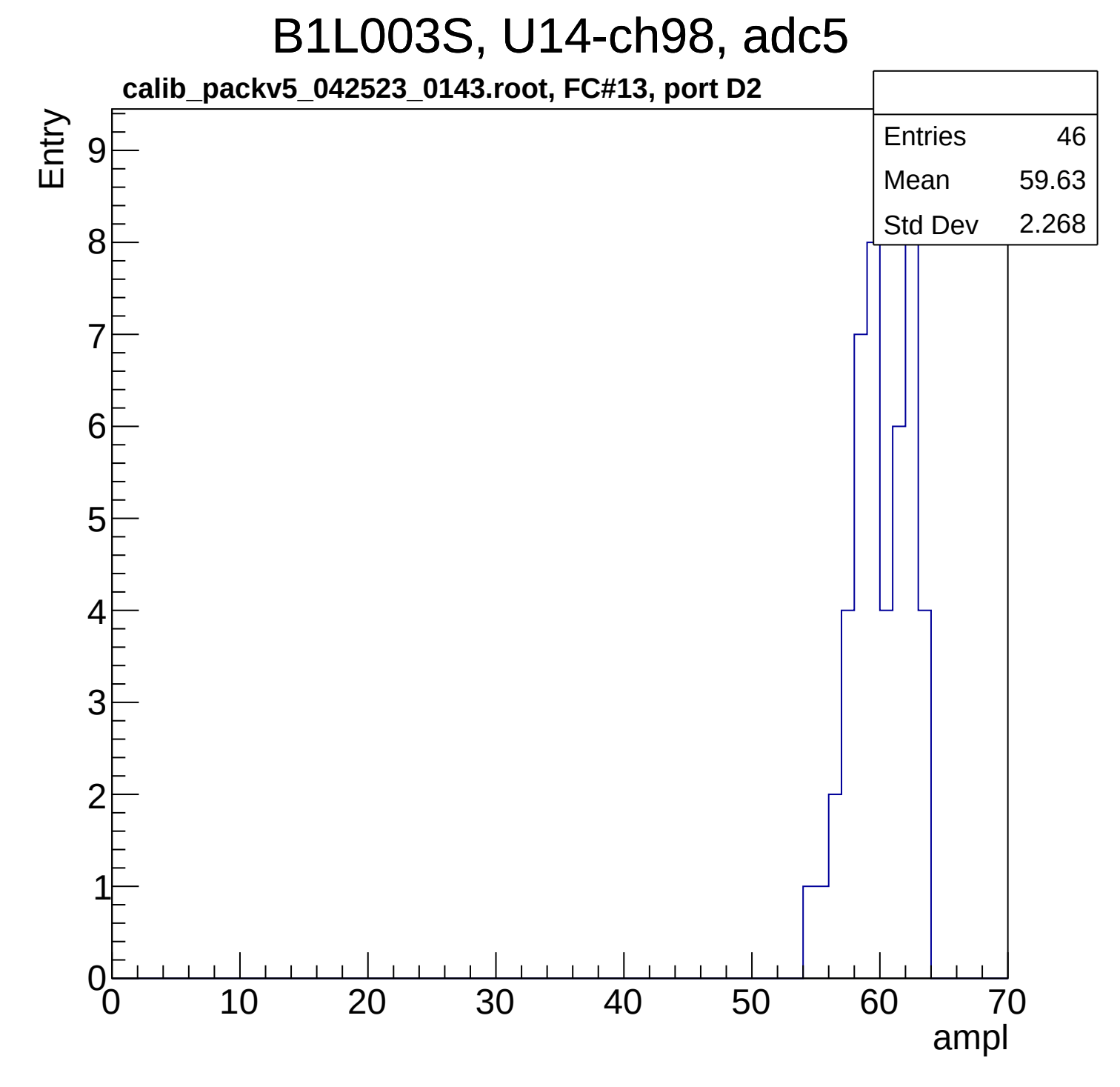

B1L003S, U14-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 30.4
Std Dev 2.795
Gaus mean : 30.7423
Gaus Width: 3.0312
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

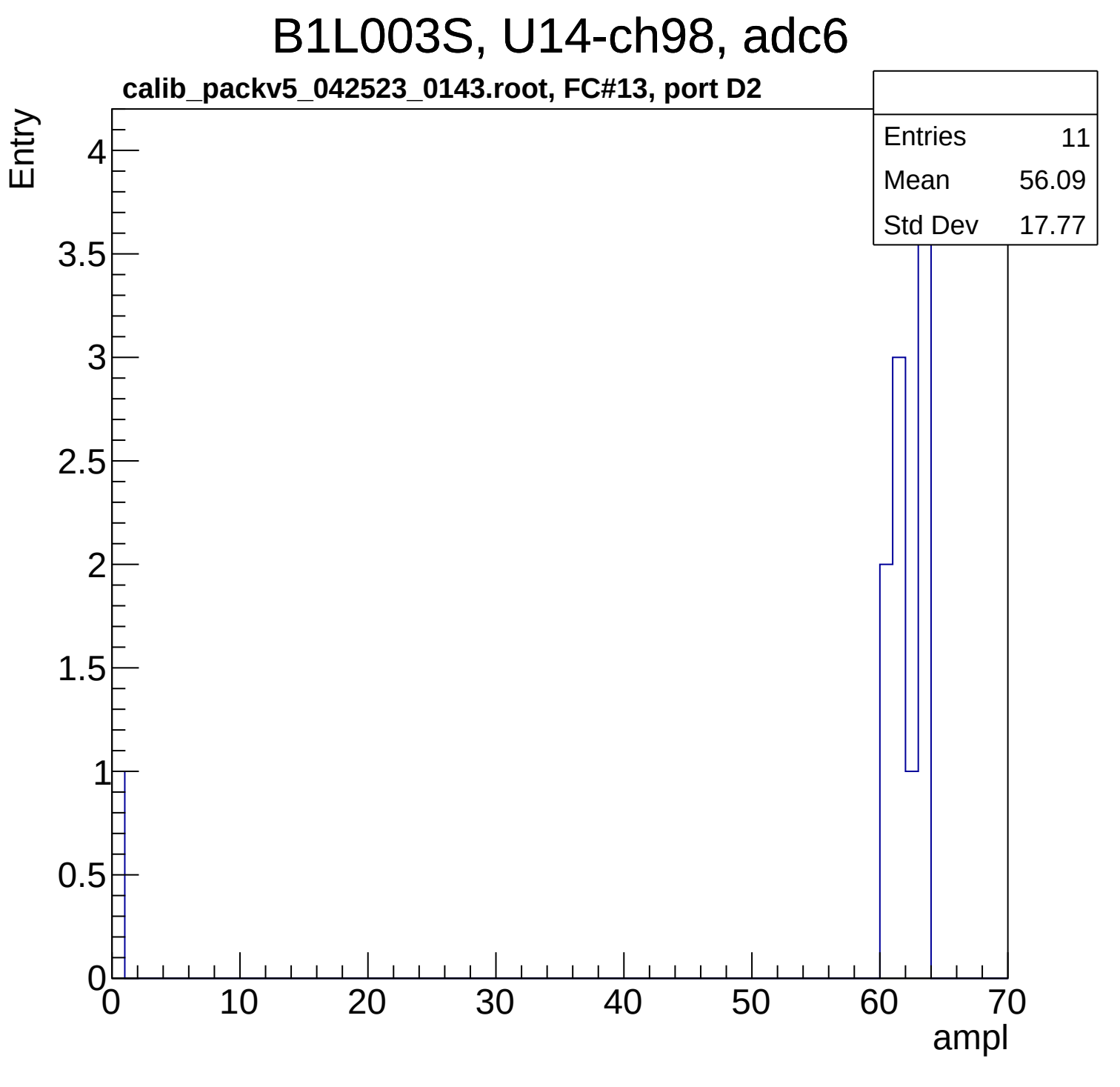

B1L003S, U14-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.18
Std Dev 2.854
Gaus mean : 36.6431
Gaus Width: 3.2472
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

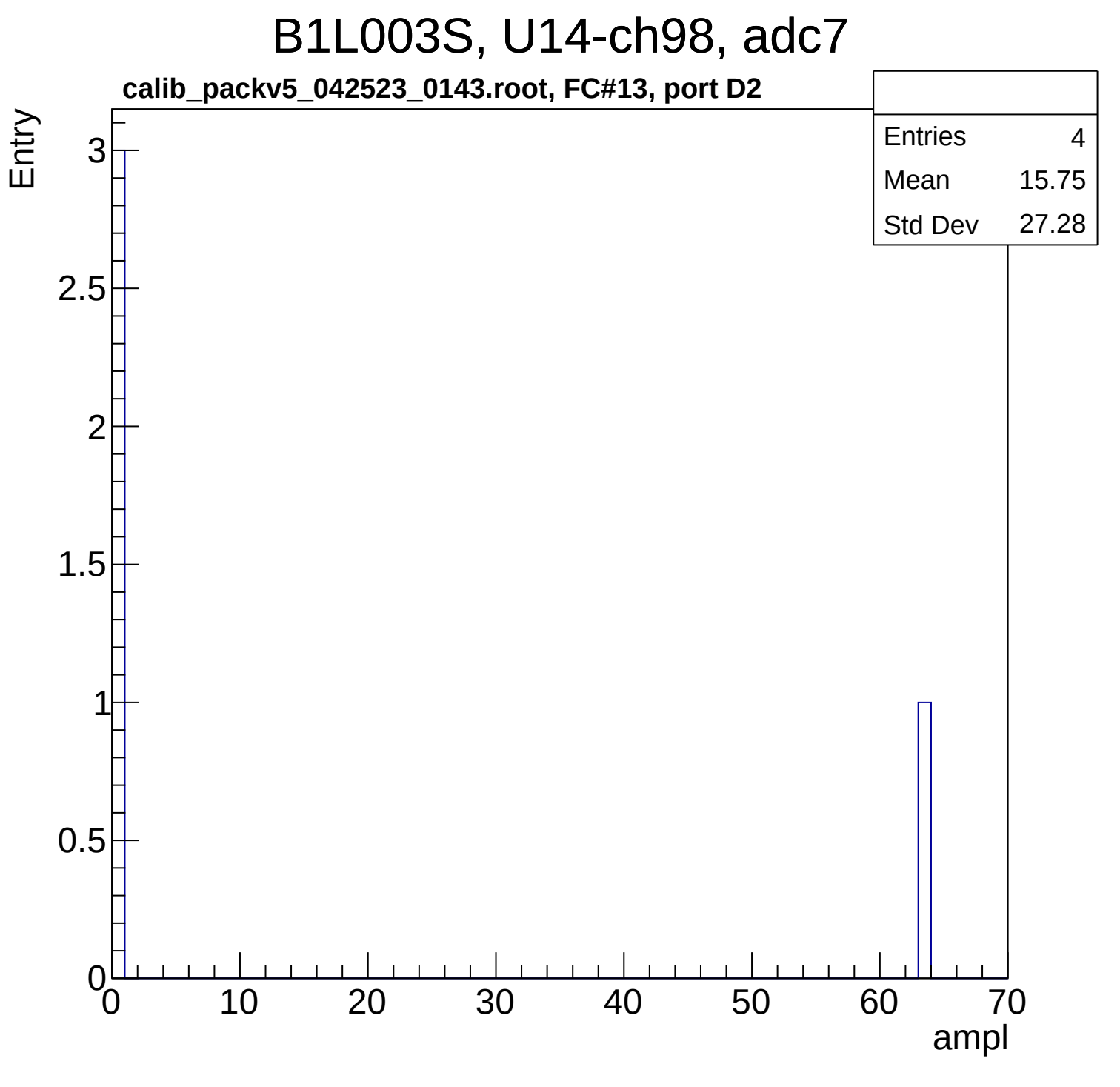

B1L003S, U14-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.2
Std Dev 3.237
Gaus mean : 42.7562
Gaus Width: 2.2335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

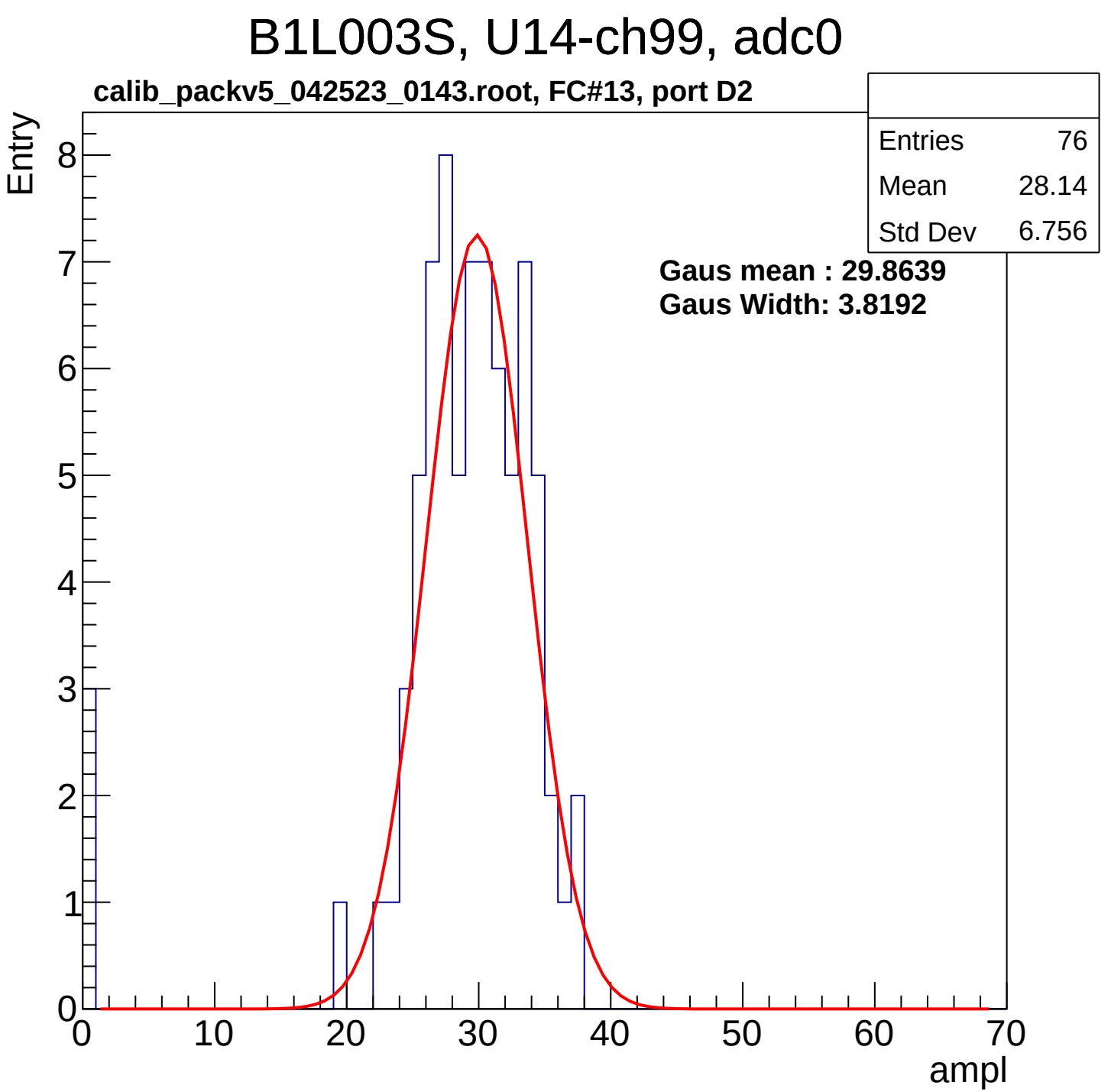

B1L003S, U14-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.71
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

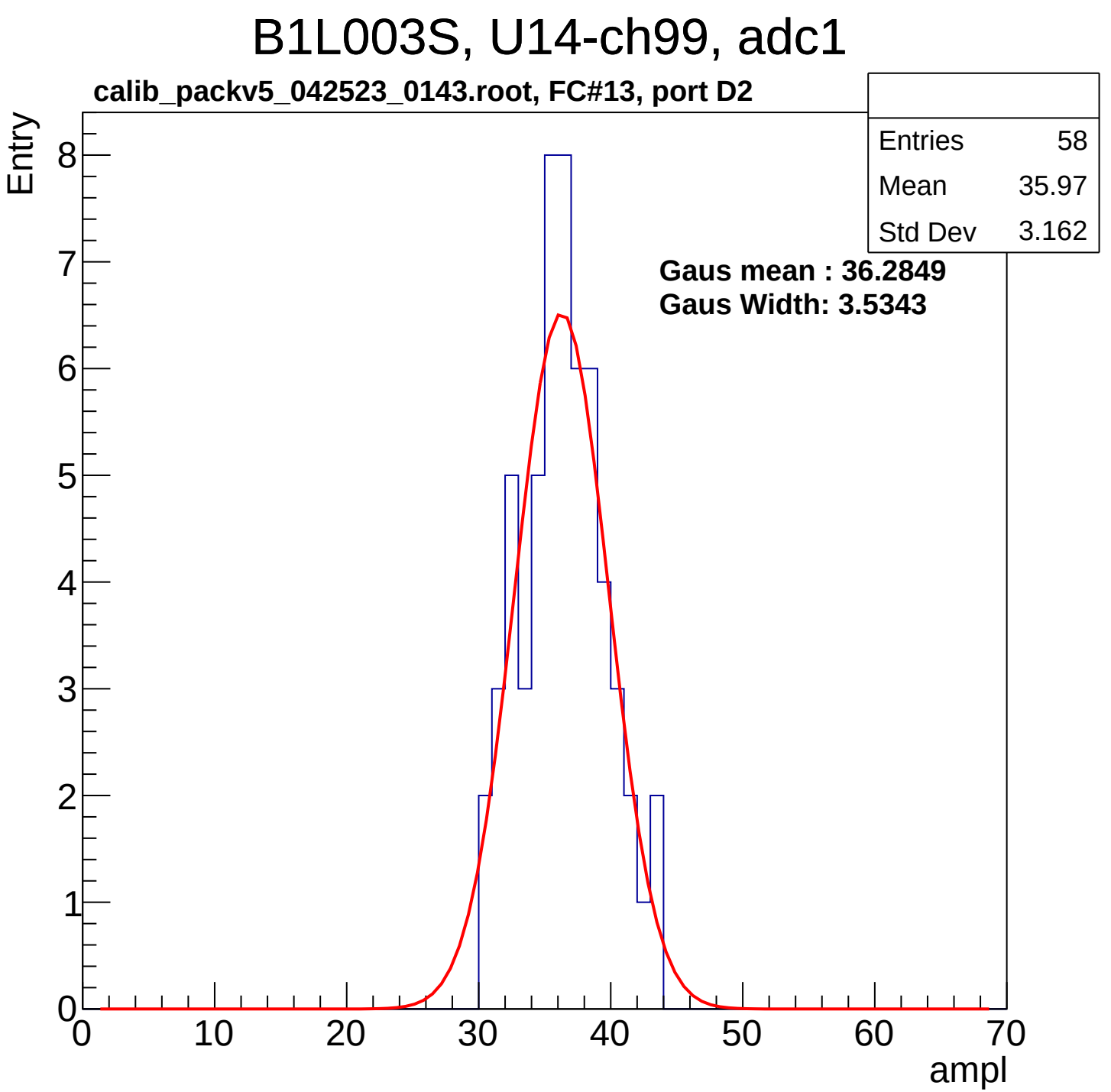

B1L003S, U14-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.83
Std Dev 3.174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

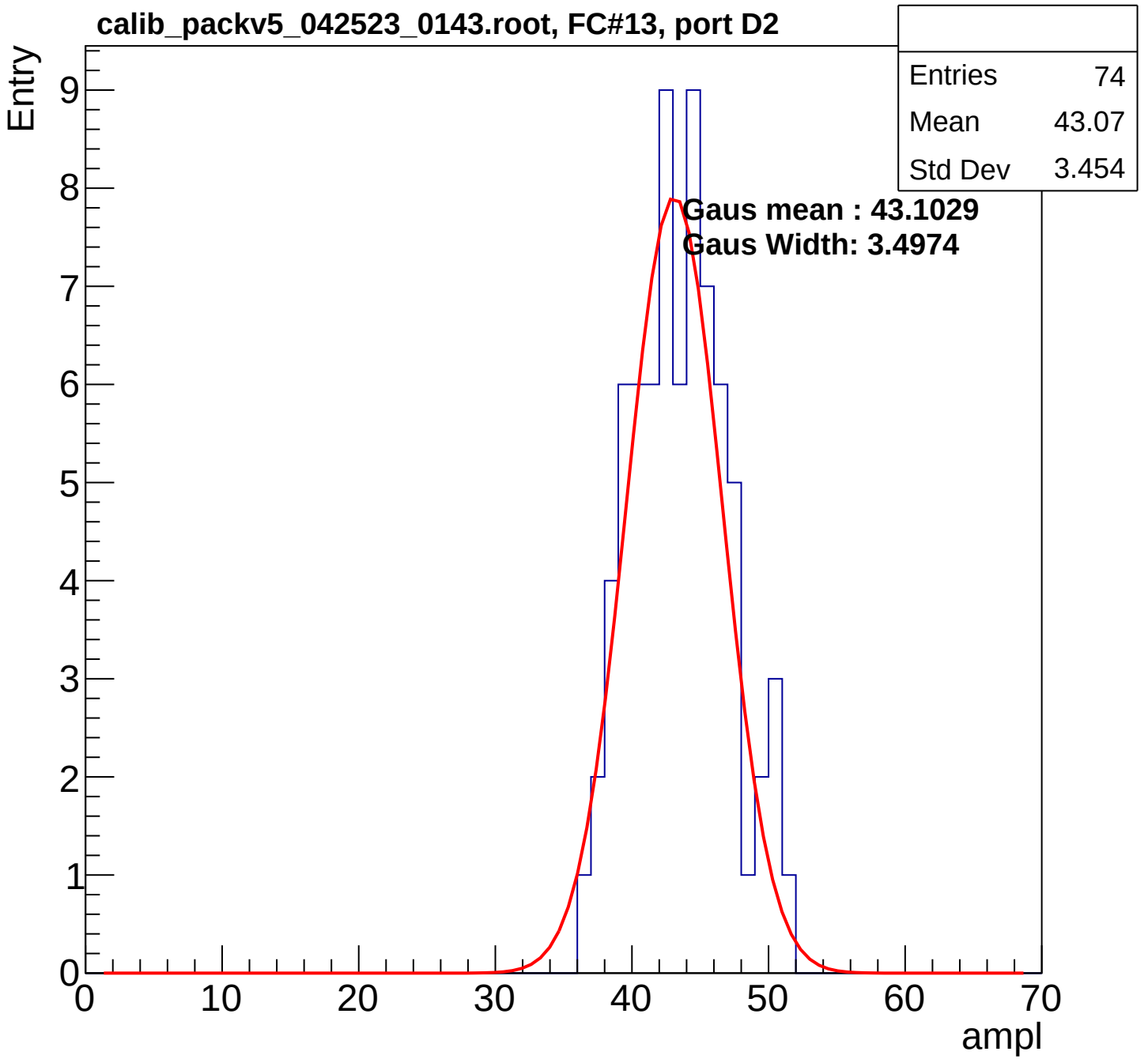

B1L003S, U14-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.63
Std Dev 2.268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

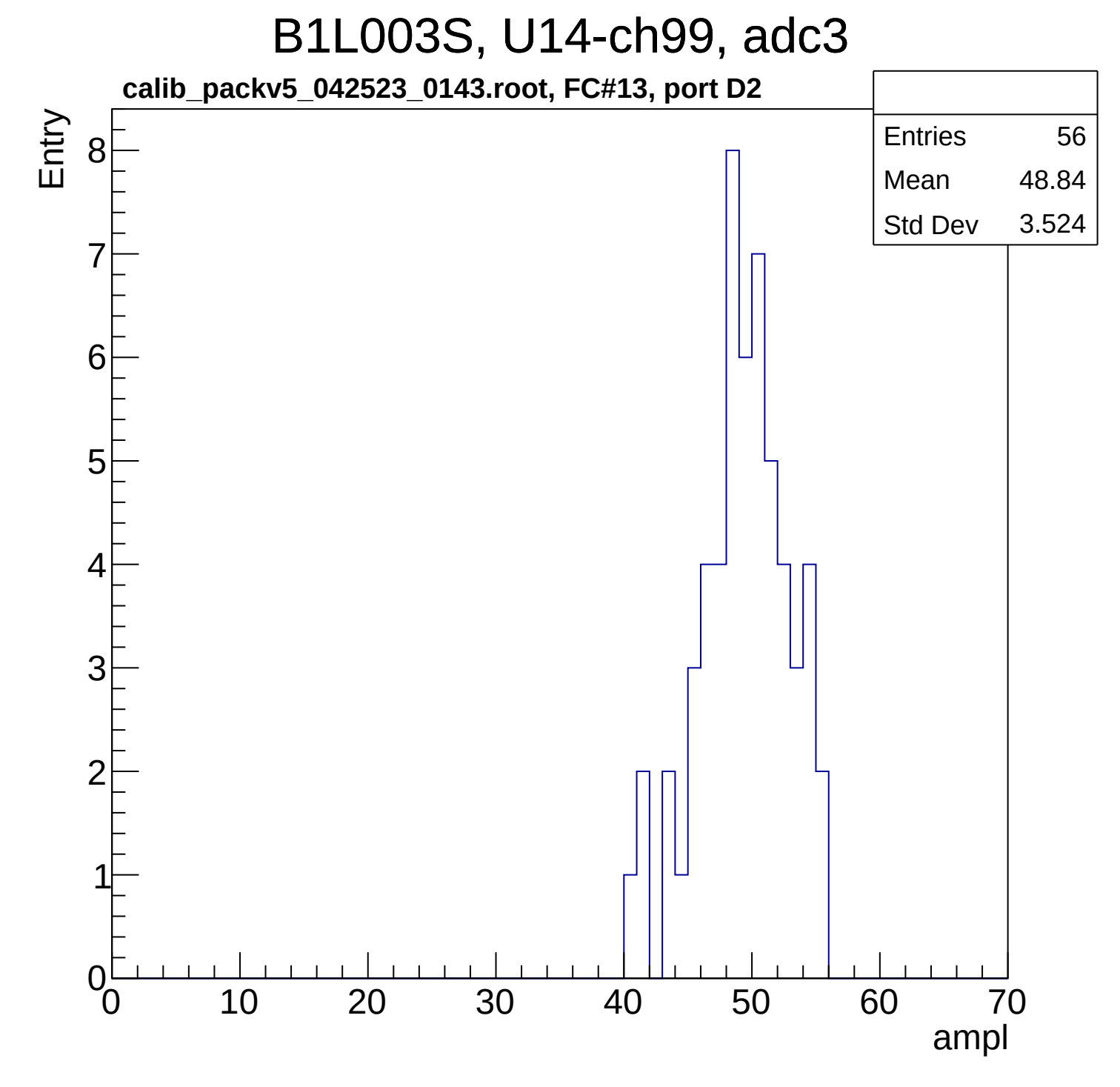

B1L003S, U14-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.09
Std Dev 17.77
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

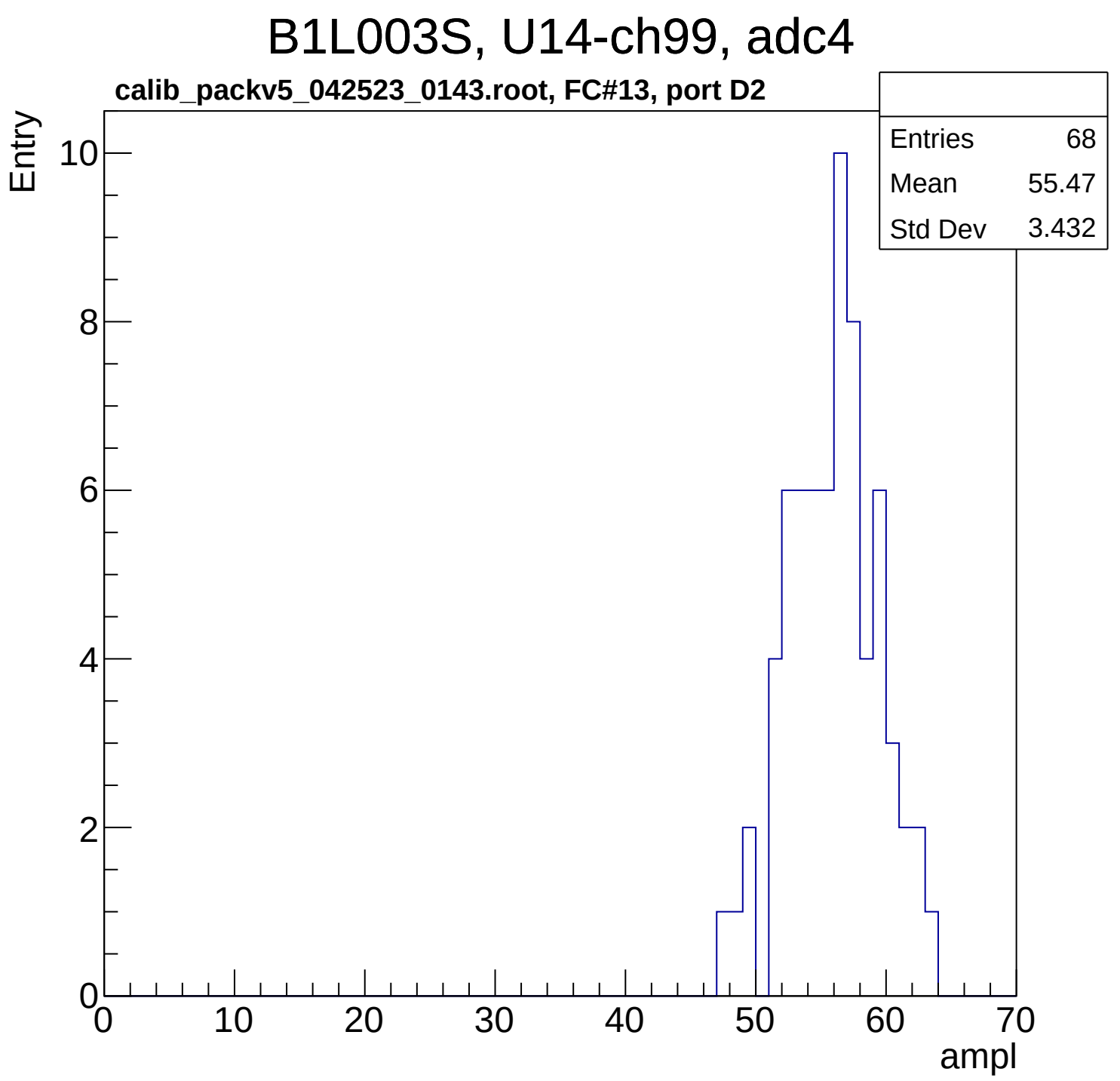

B1L003S, U14-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

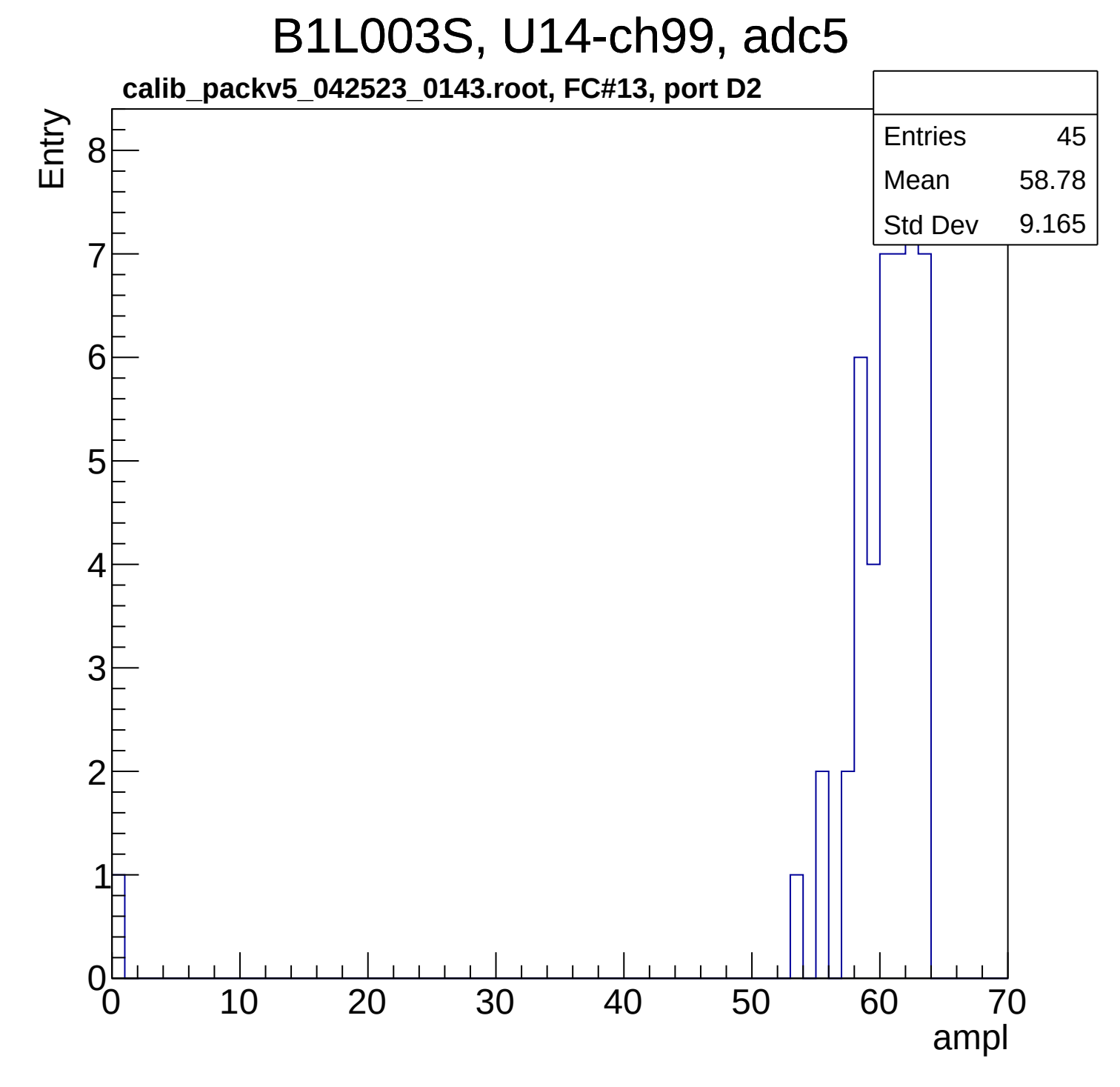

B1L003S, U14-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.14
Std Dev 6.756
Gaus mean : 29.8639
Gaus Width: 3.8192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

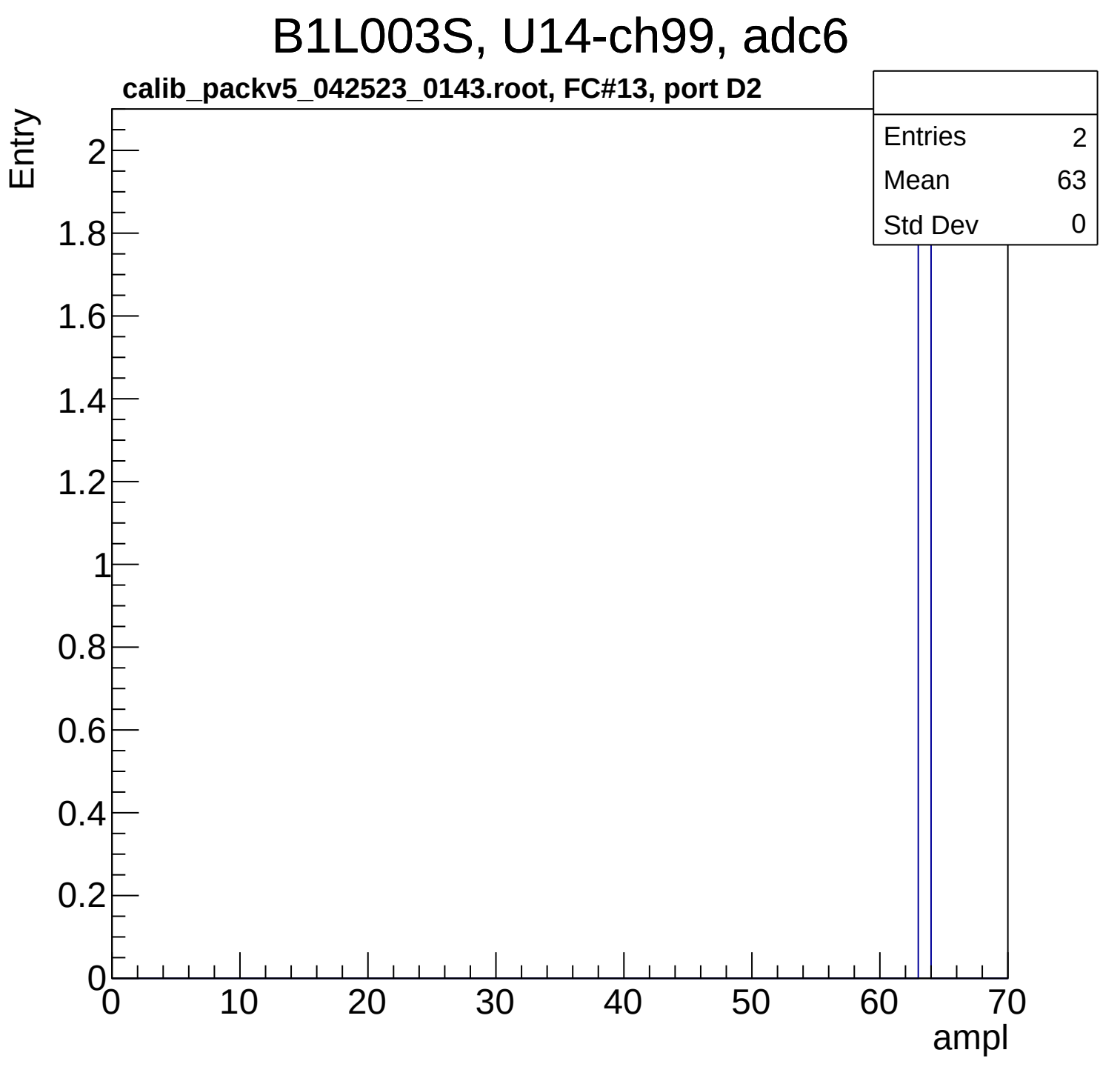

B1L003S, U14-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.97
Std Dev 3.162
Gaus mean : 36.2849
Gaus Width: 3.5343
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

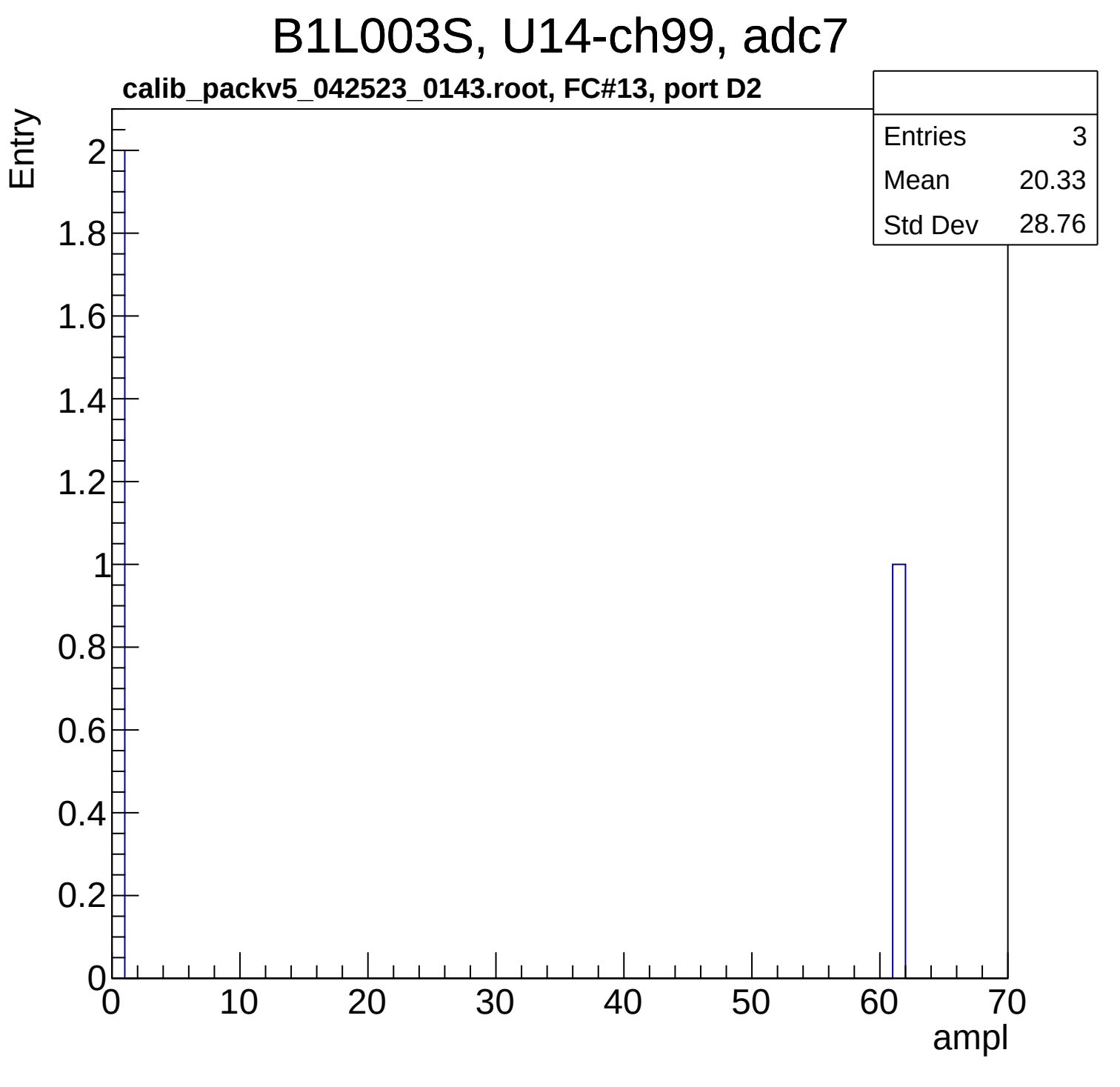

B1L003S, U14-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.07
Std Dev 3.454
Gaus mean : 43.1029
Gaus Width: 3.4974
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

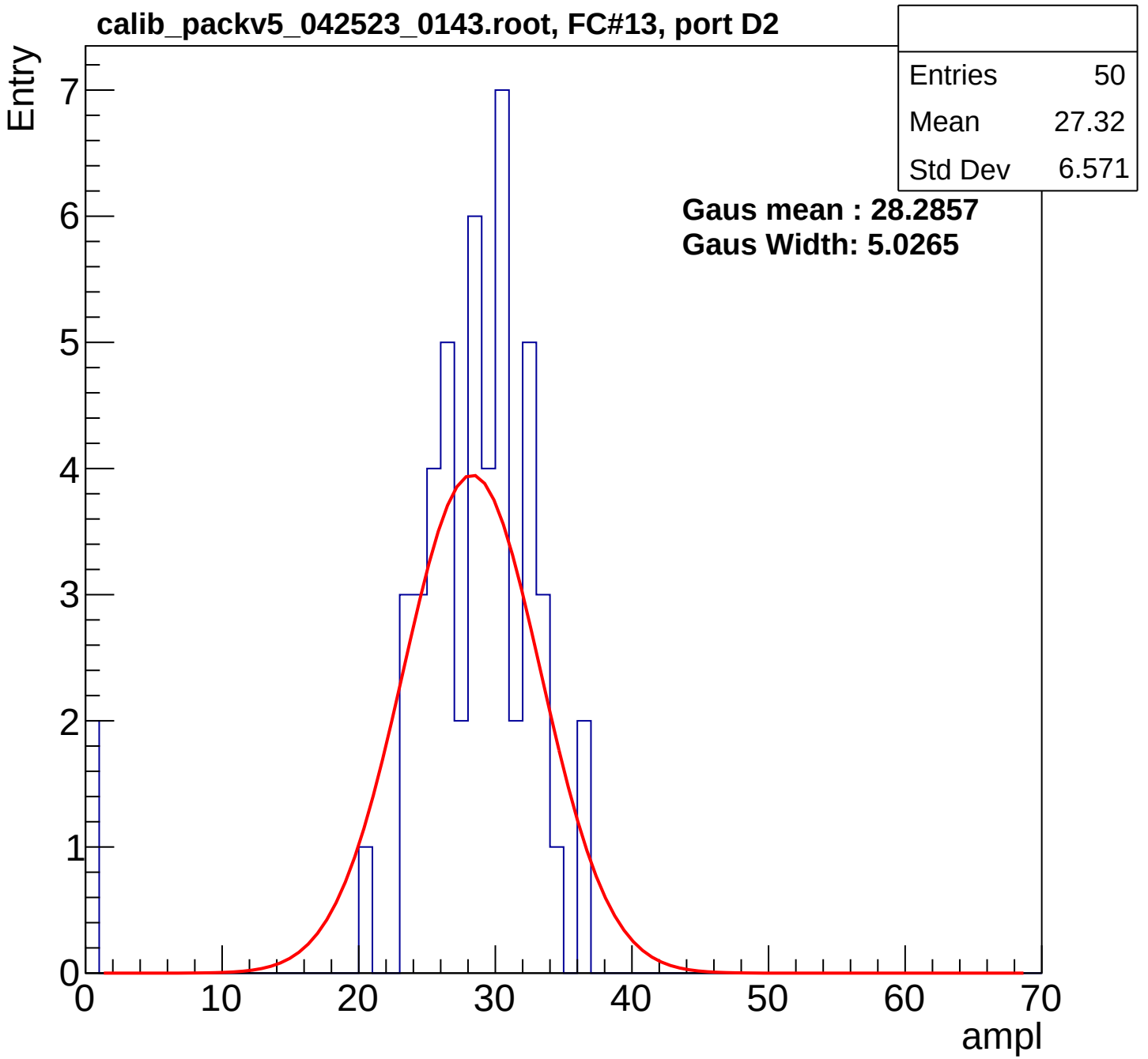

B1L003S, U14-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.84
Std Dev 3.524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

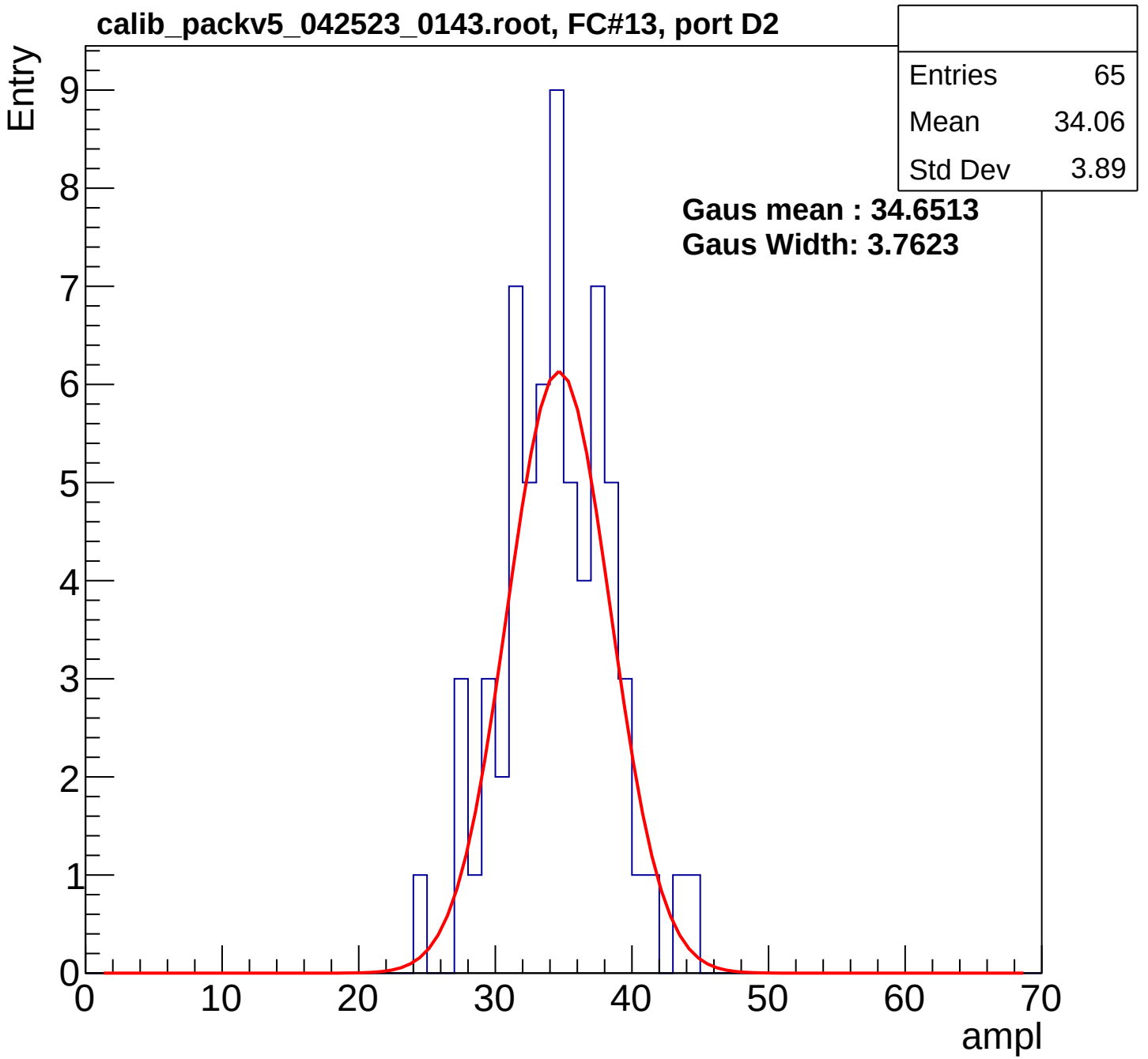

B1L003S, U14-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 55.47
Std Dev 3.432
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

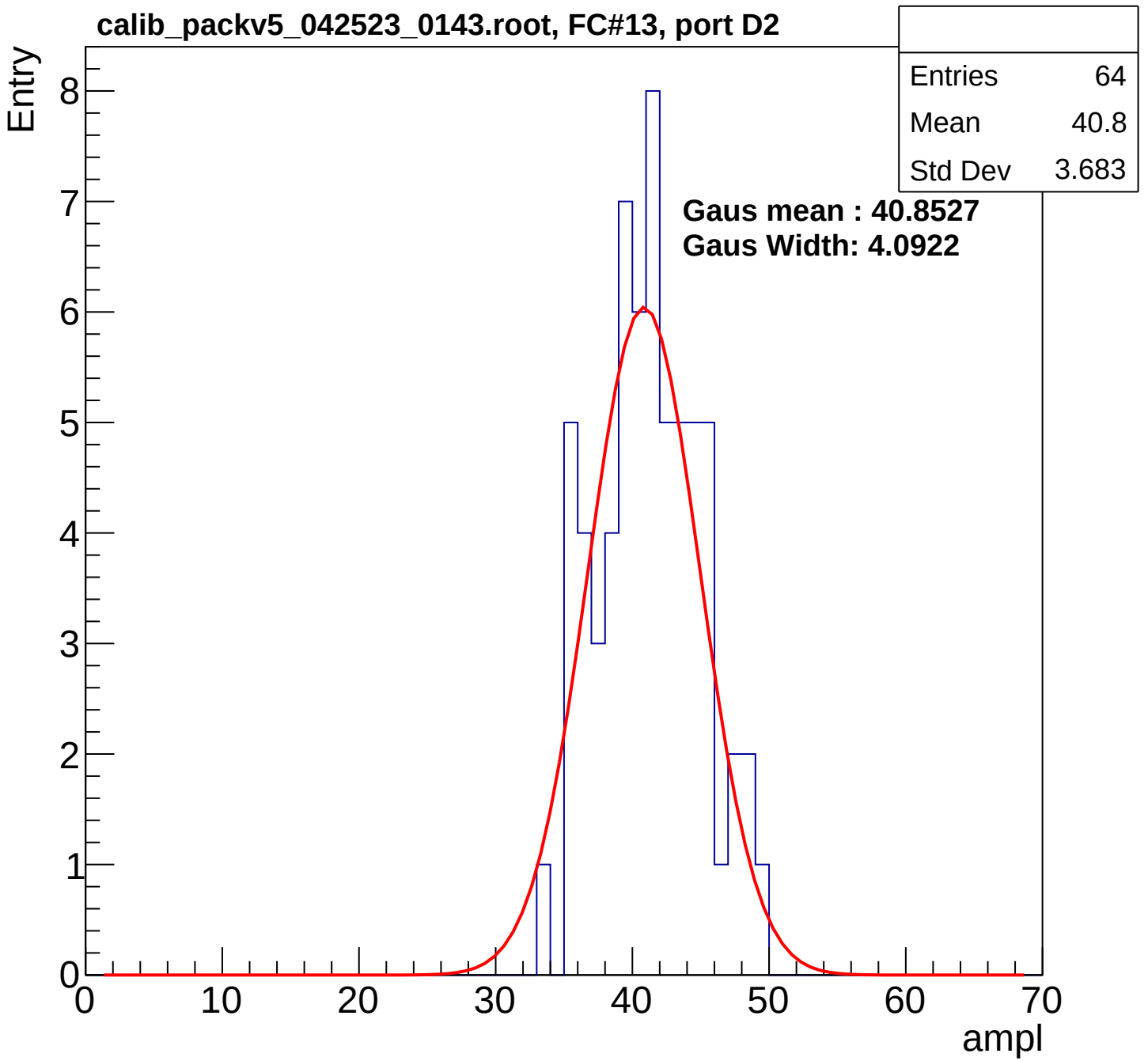

B1L003S, U14-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.78
Std Dev 9.165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

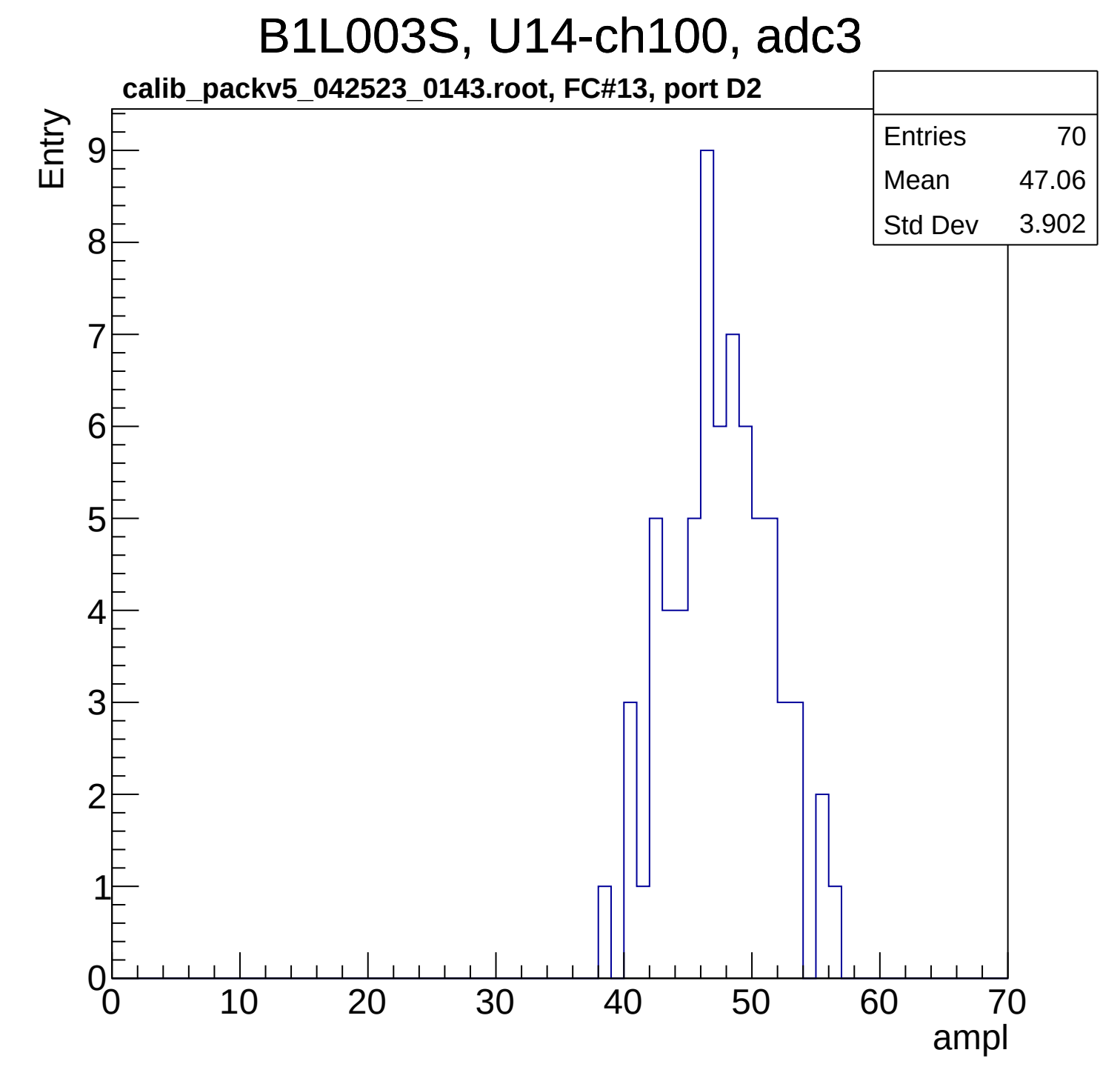

B1L003S, U14-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

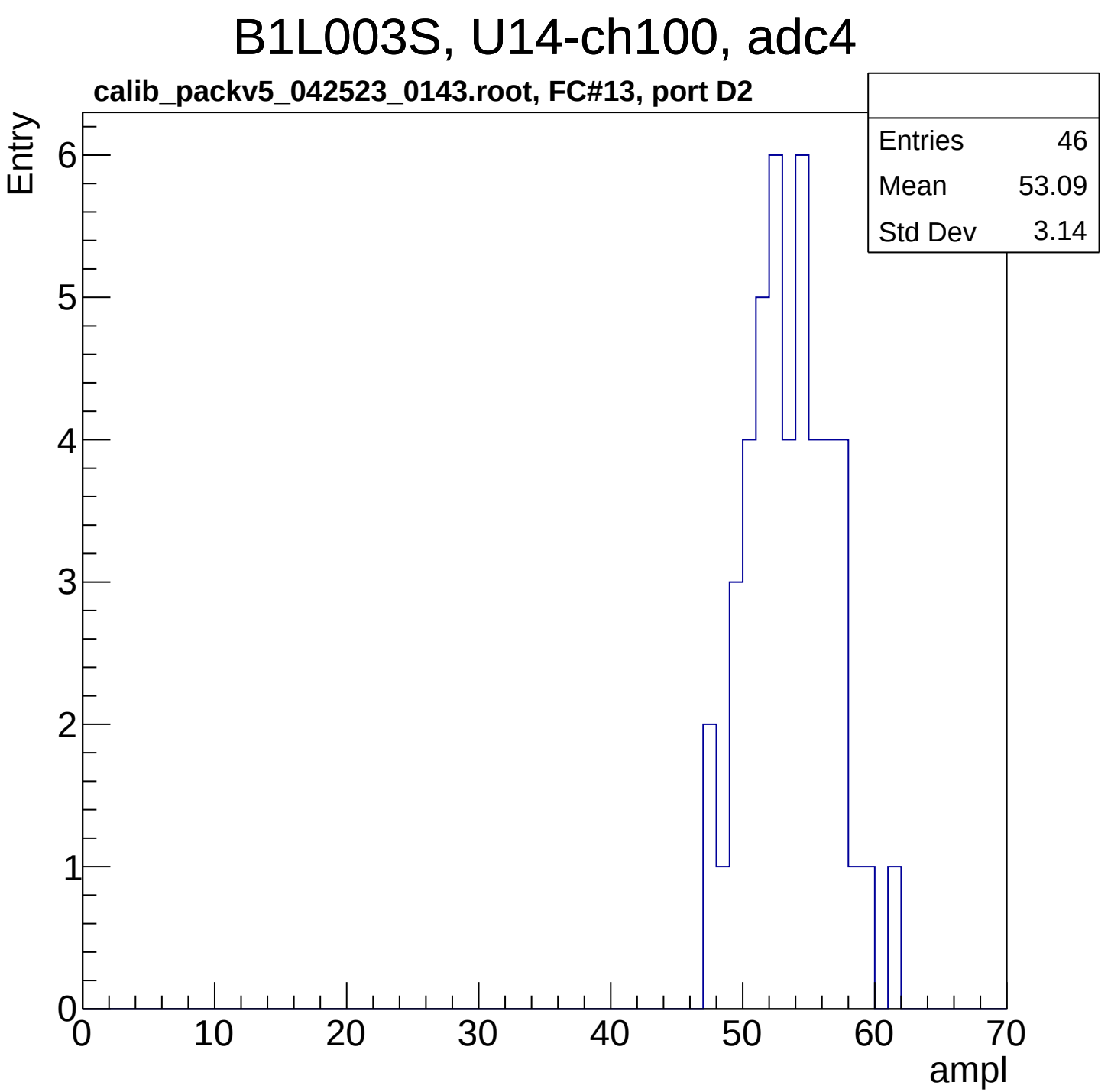

B1L003S, U14-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

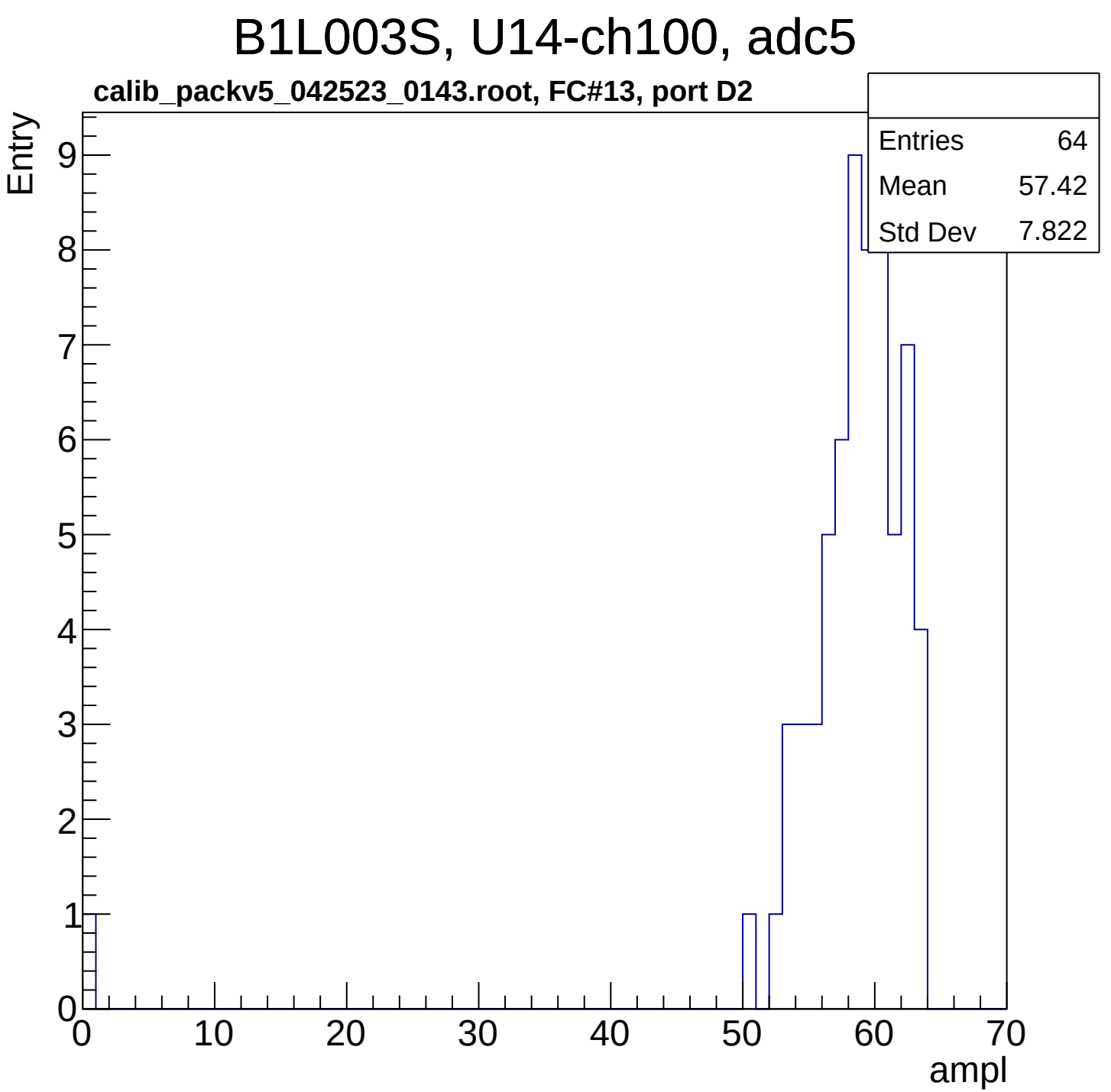

B1L003S, U14-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 27.32
Std Dev 6.571
Gaus mean : 28.2857
Gaus Width: 5.0265
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

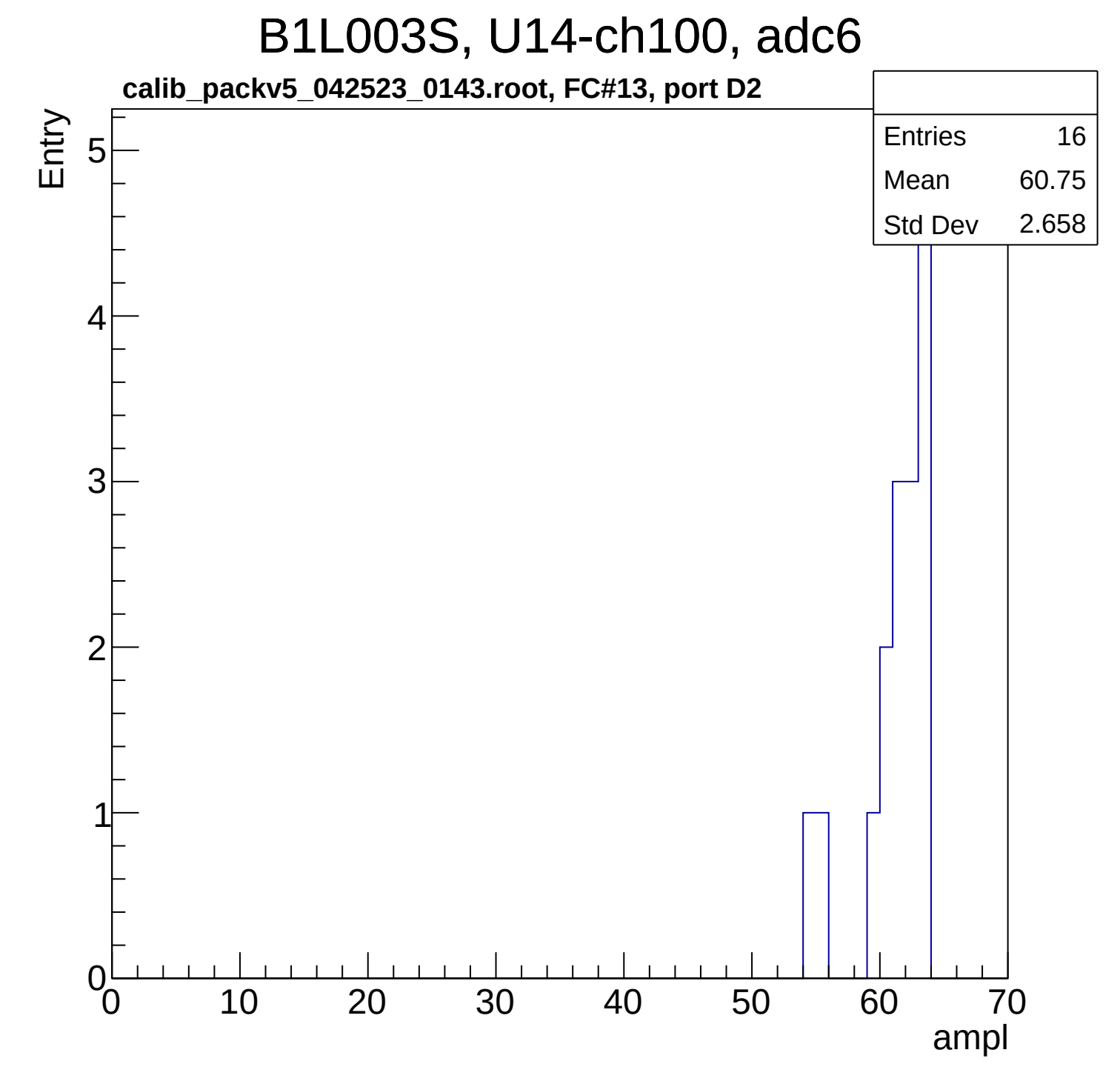

B1L003S, U14-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 34.06
Std Dev 3.89
Gaus mean : 34.6513
Gaus Width: 3.7623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

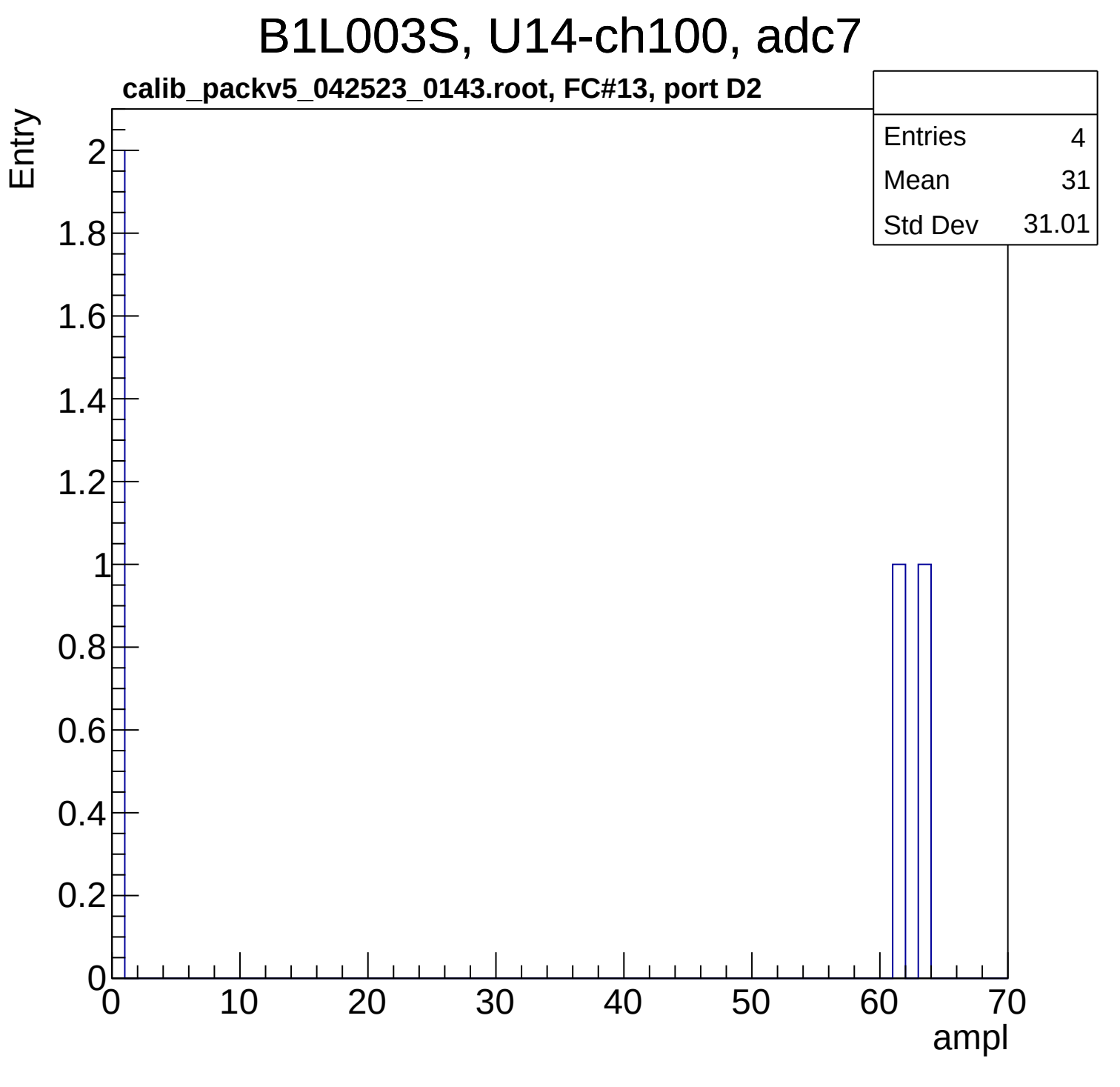

B1L003S, U14-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 40.8
Std Dev 3.683
Gaus mean : 40.8527
Gaus Width: 4.0922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

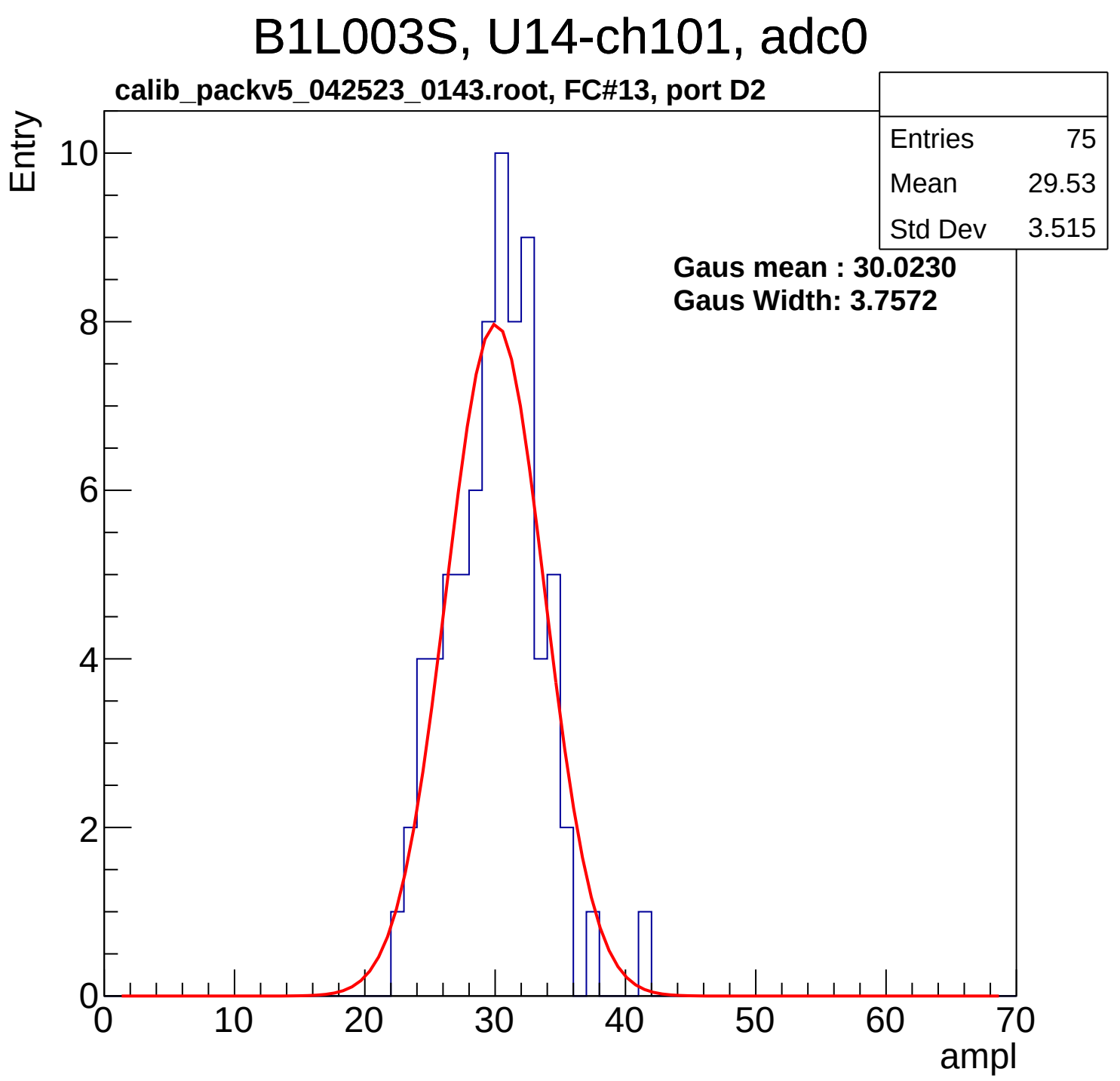

B1L003S, U14-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.06
Std Dev 3.902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

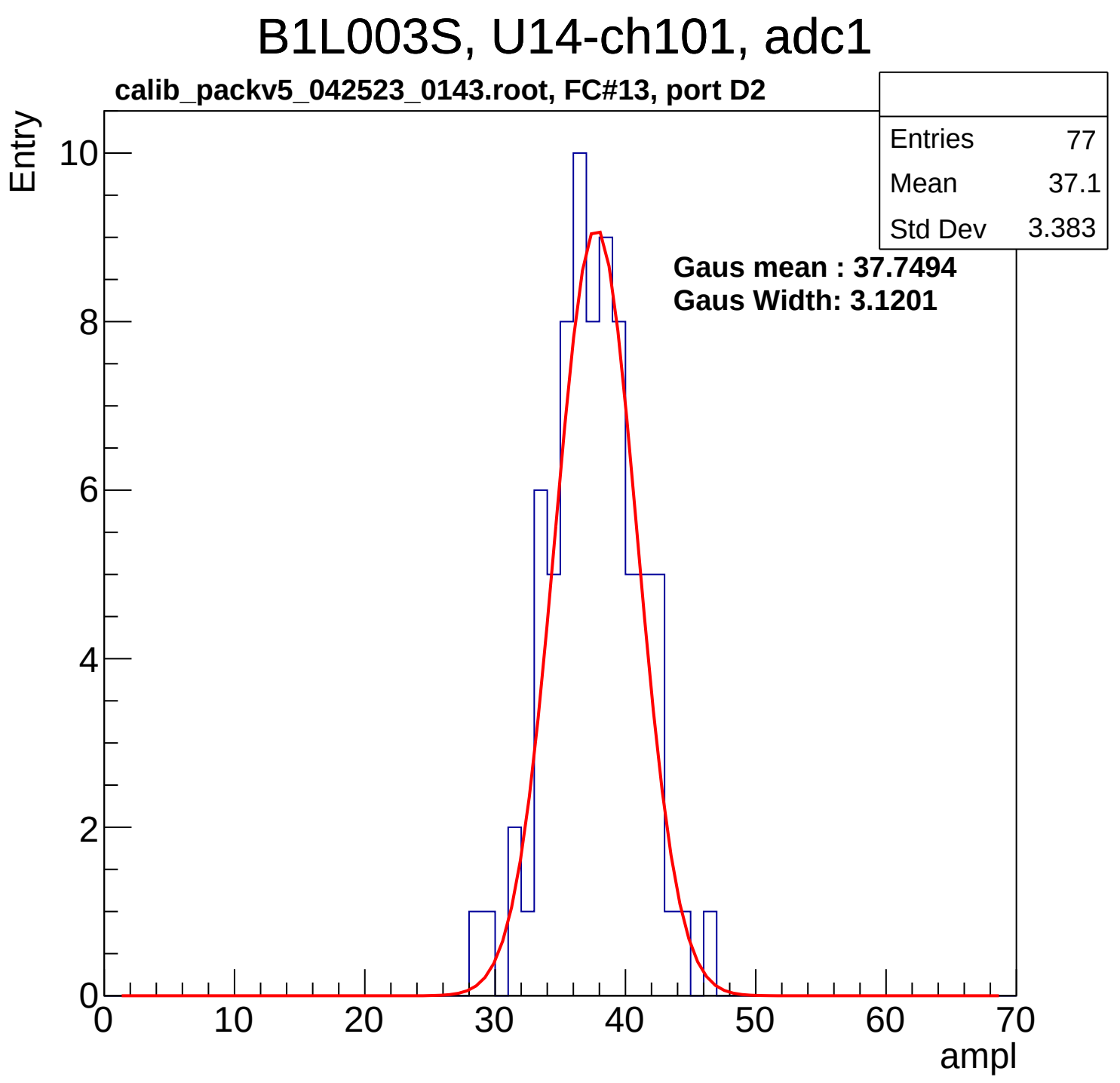

B1L003S, U14-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 53.09
Std Dev 3.14
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

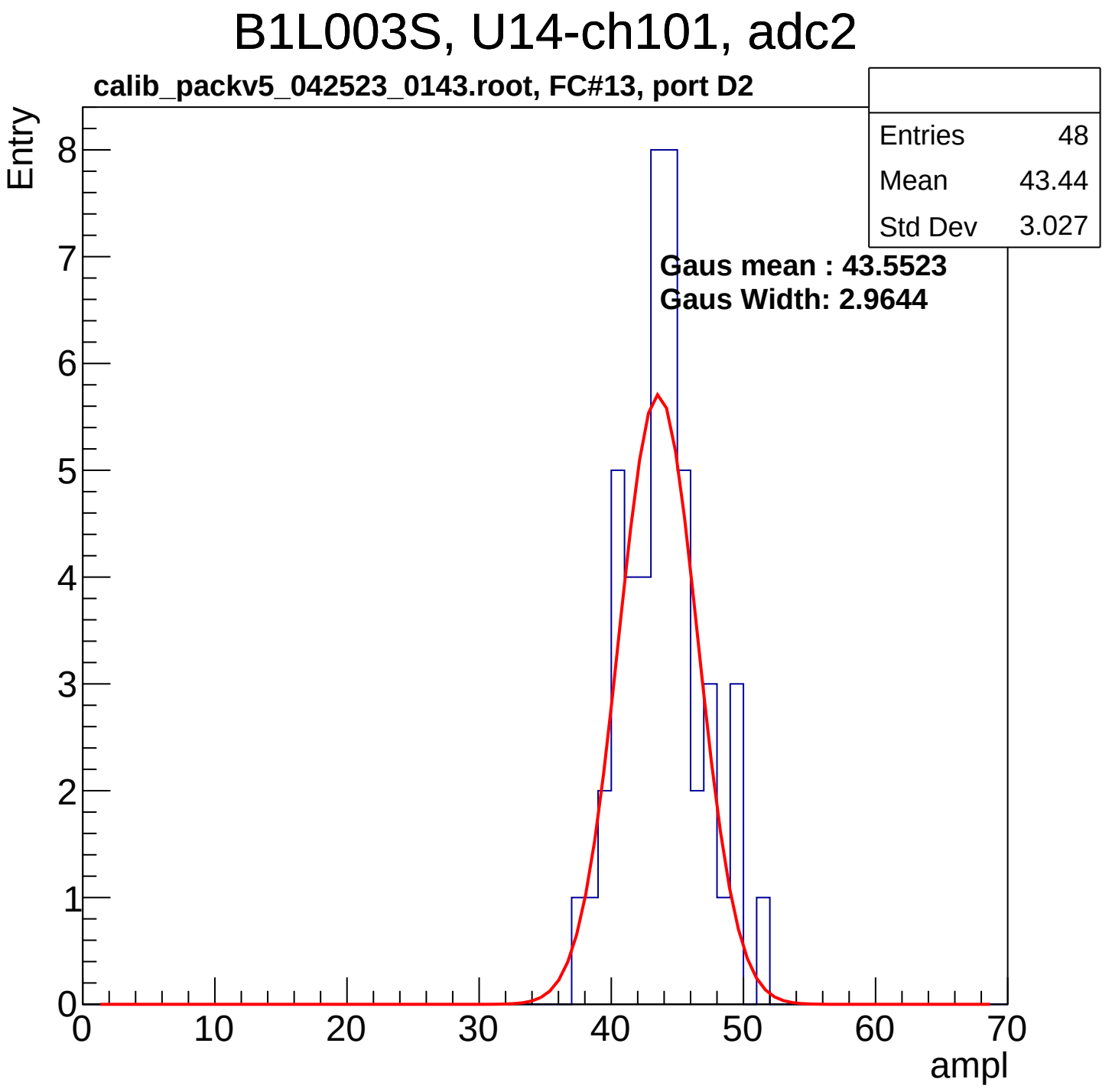

B1L003S, U14-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 57.42
Std Dev 7.822
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

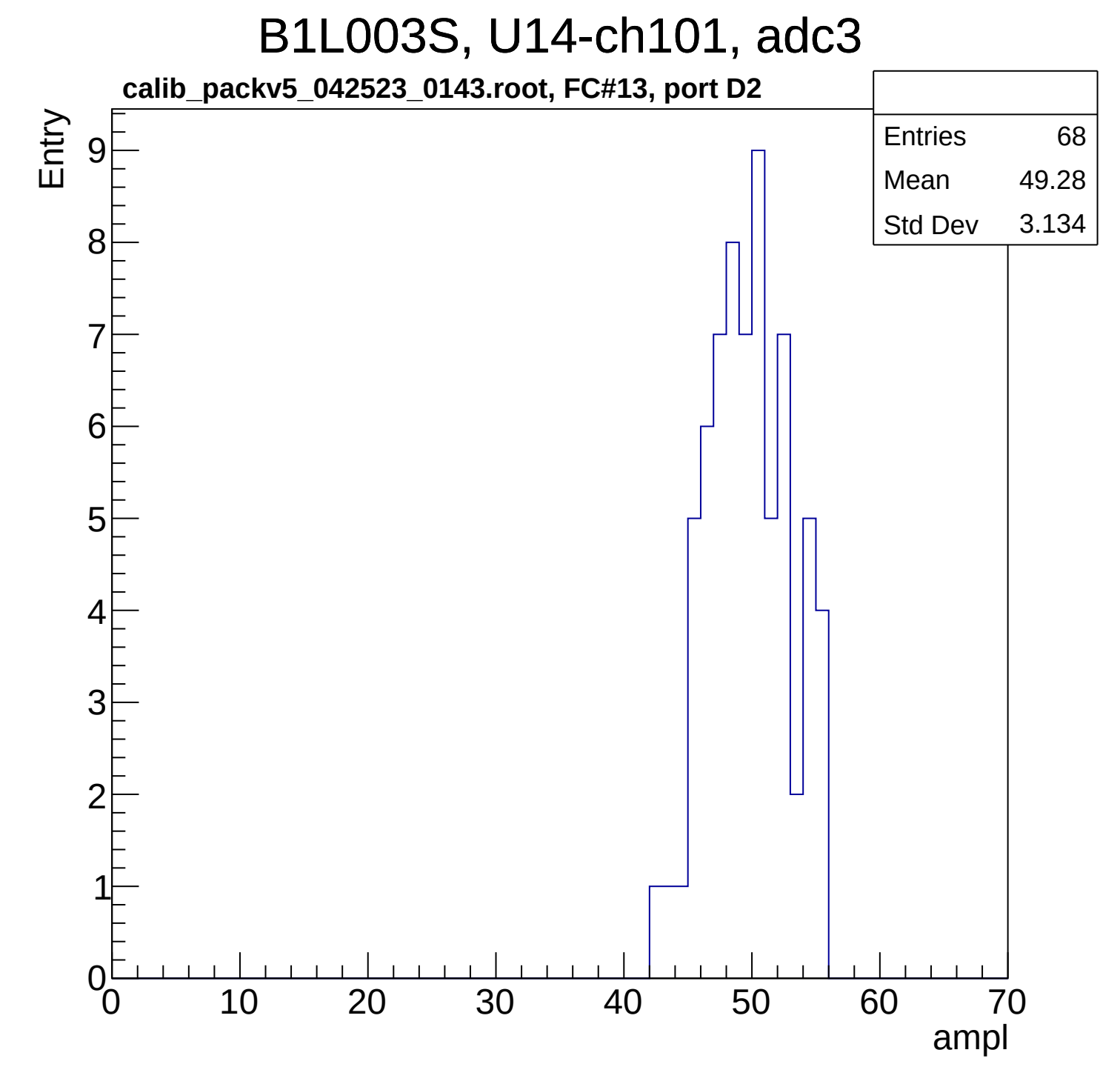

B1L003S, U14-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 60.75
Std Dev 2.658
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

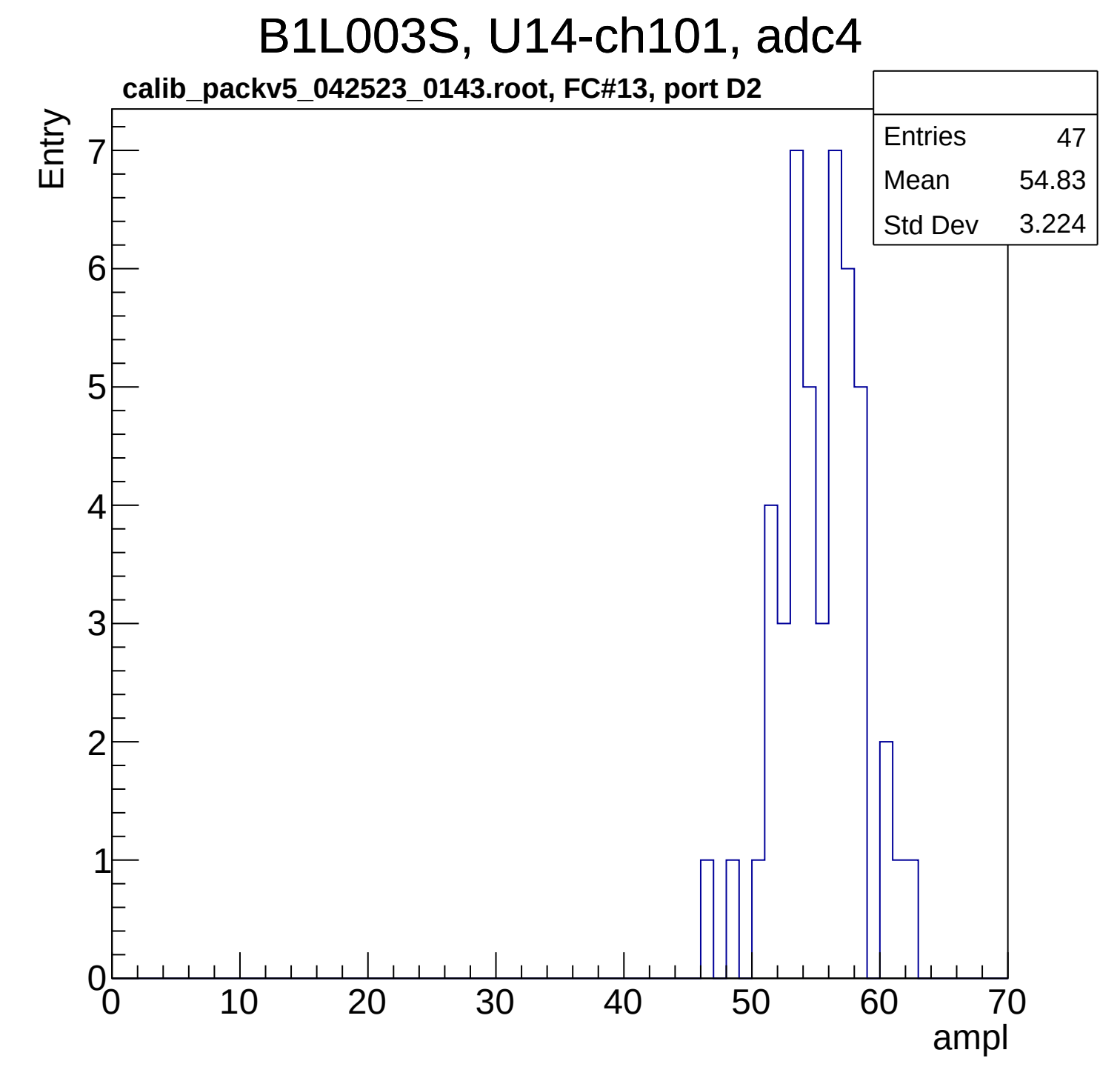

B1L003S, U14-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

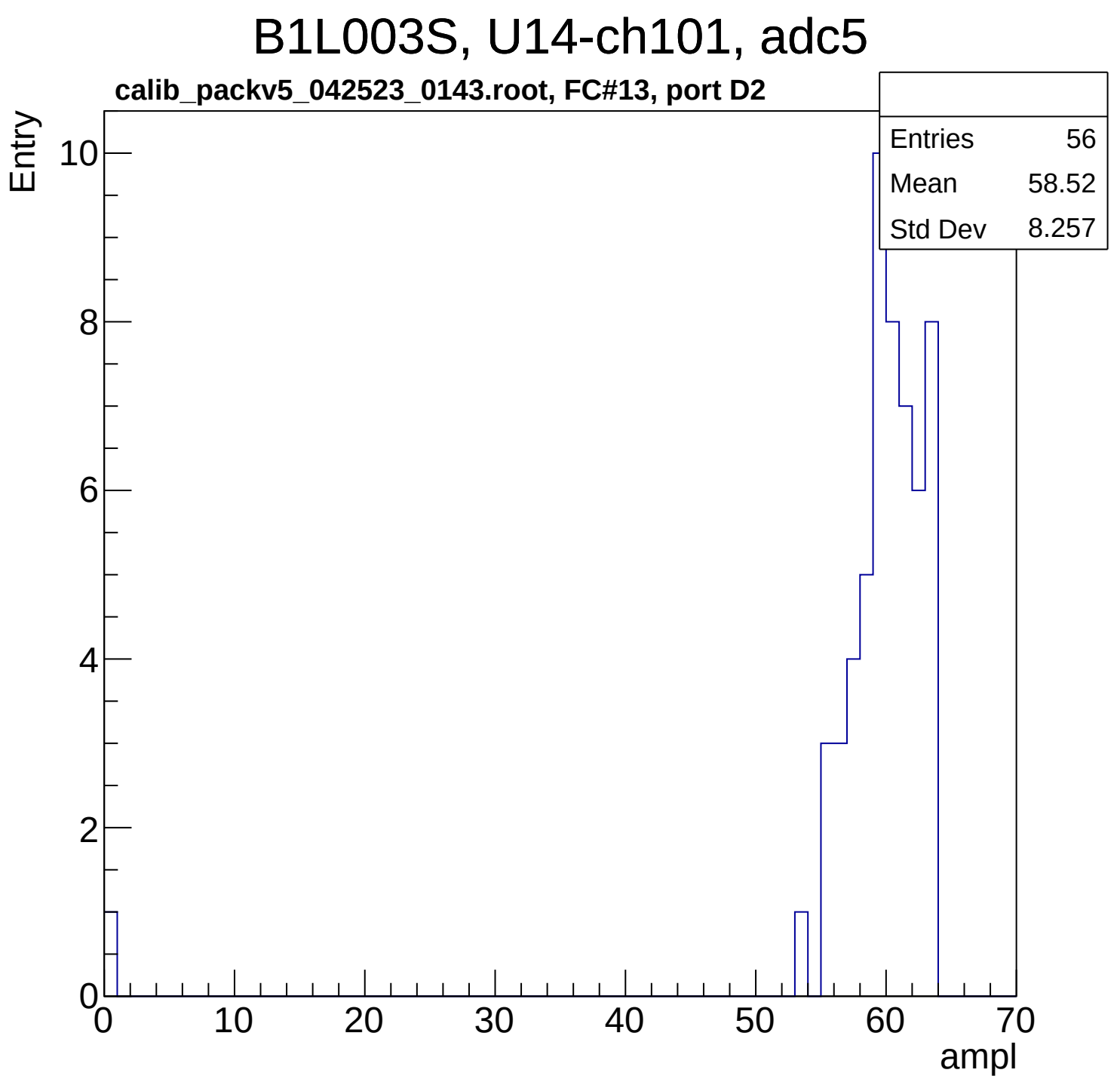

B1L003S, U14-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.53
Std Dev 3.515
Gaus mean : 30.0230
Gaus Width: 3.7572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

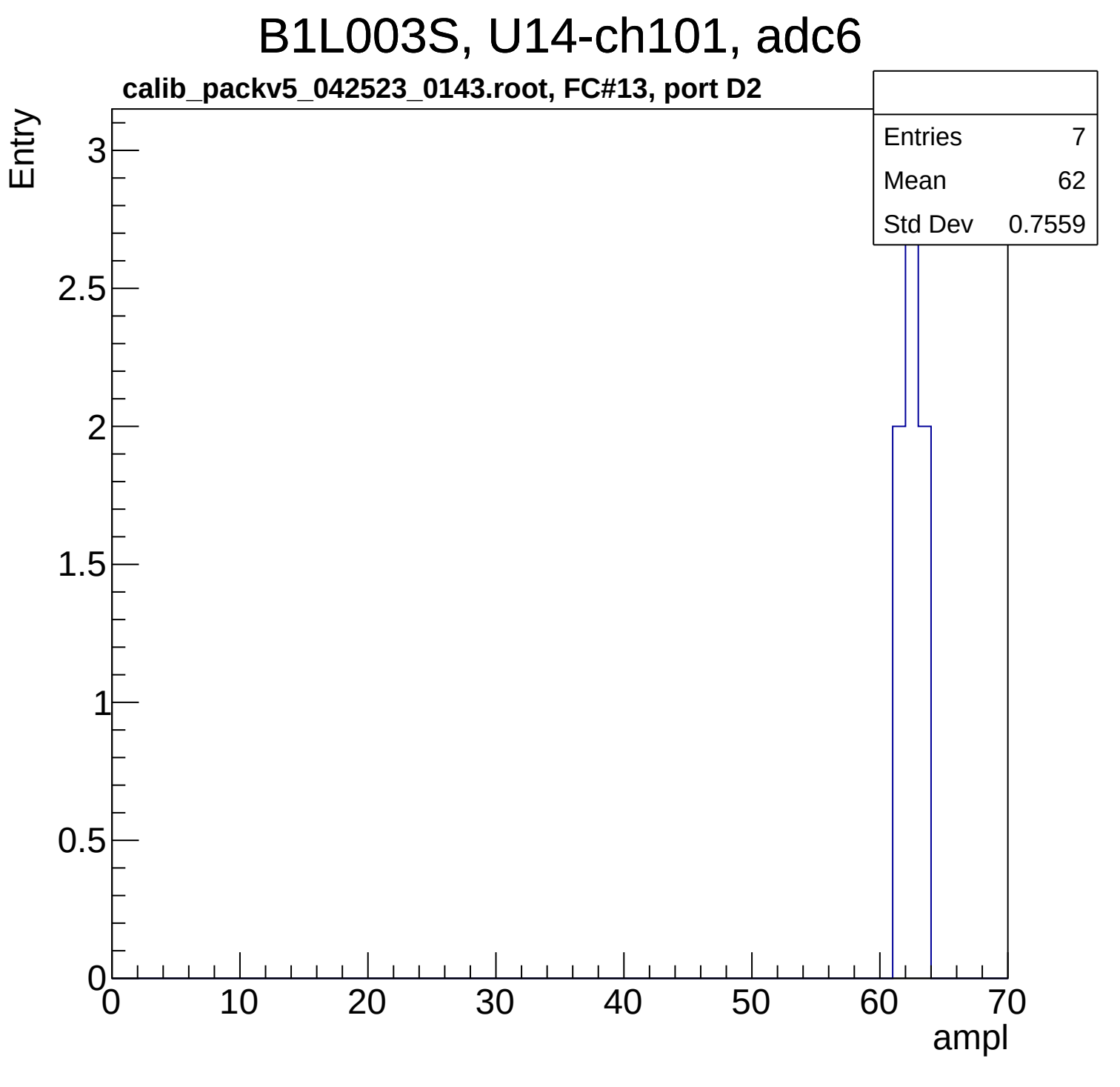

B1L003S, U14-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 37.1
Std Dev 3.383
Gaus mean : 37.7494
Gaus Width: 3.1201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

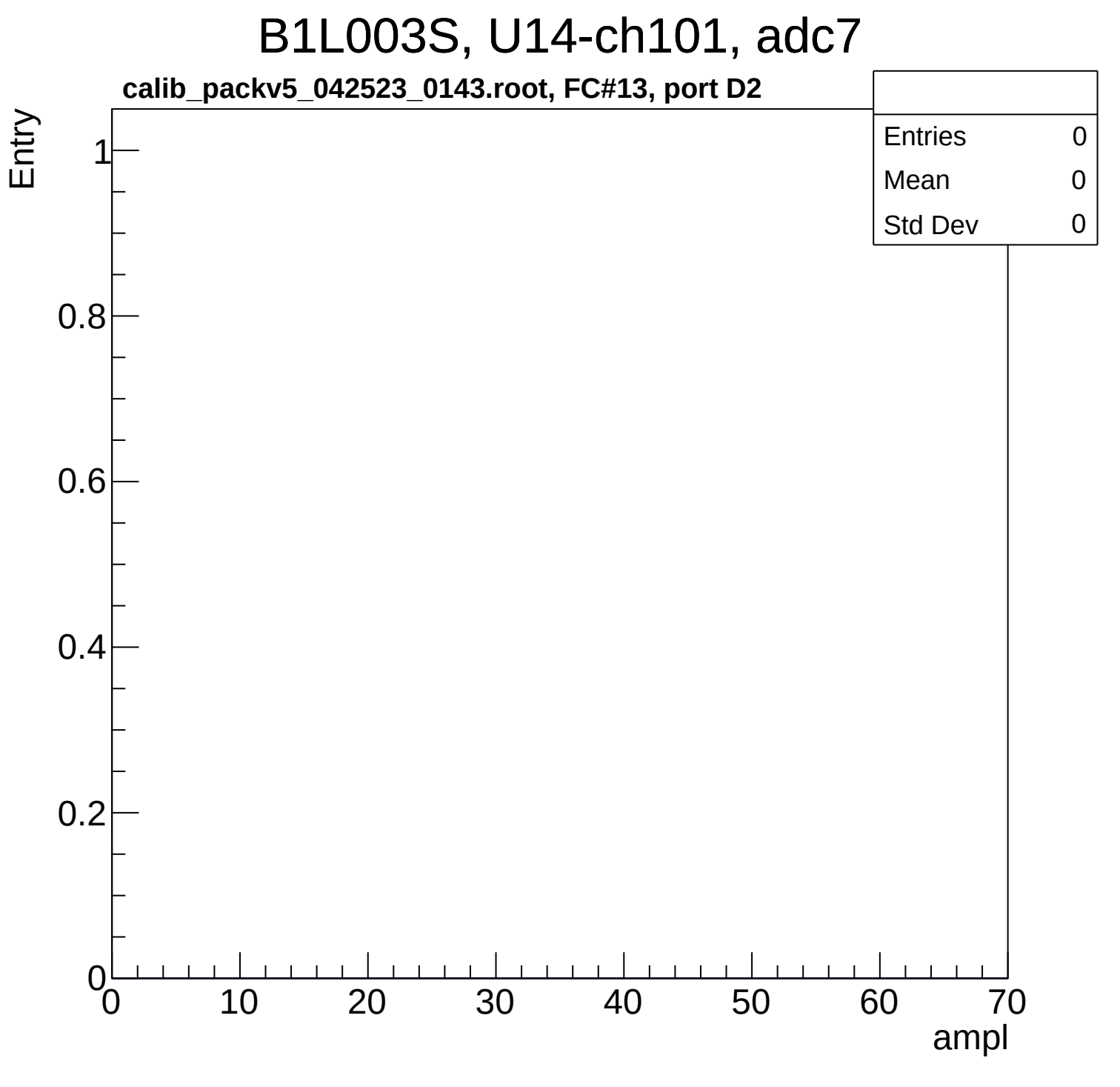

B1L003S, U14-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 43.44
Std Dev 3.027
Gaus mean : 43.5523
Gaus Width: 2.9644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

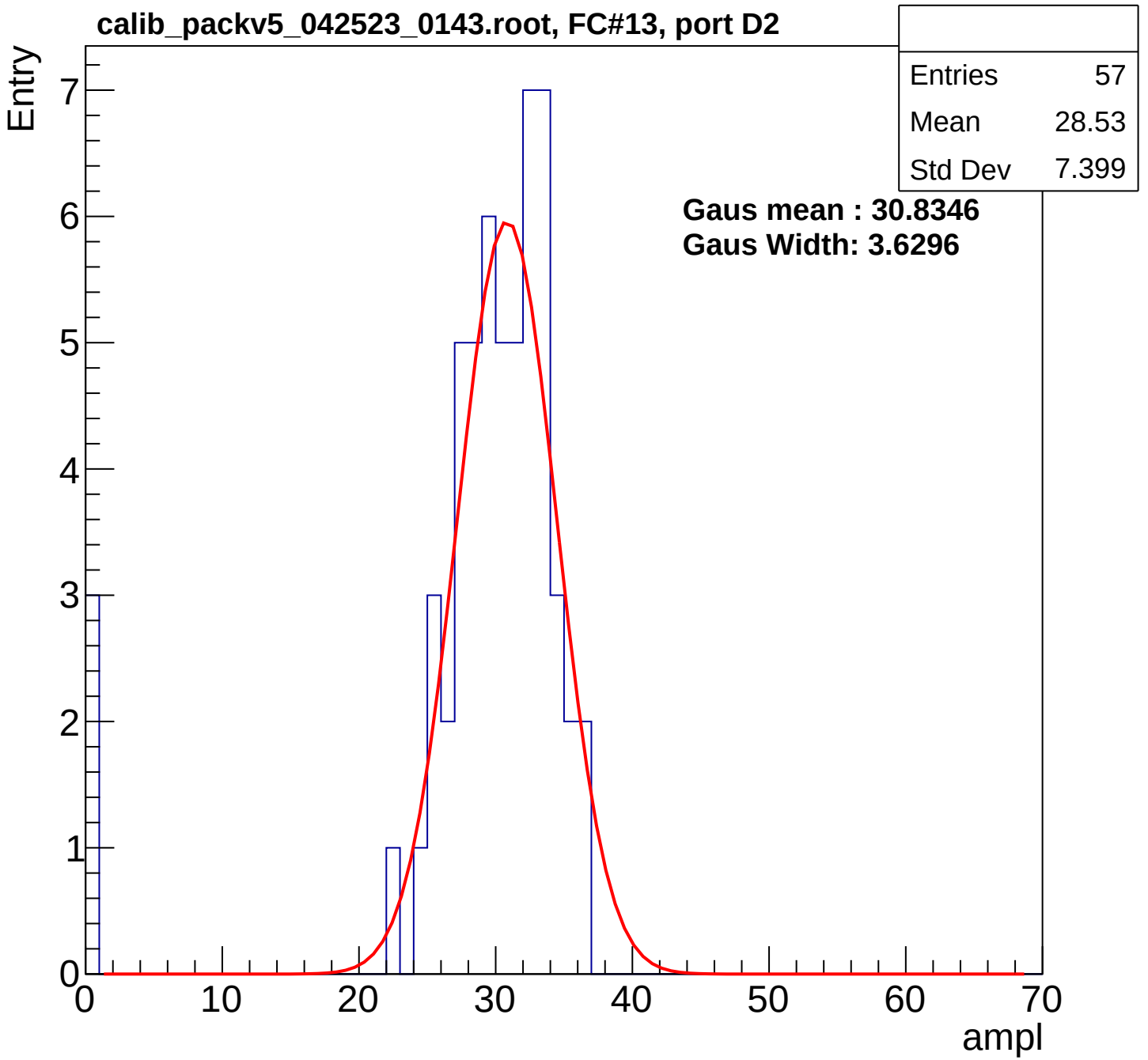

B1L003S, U14-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.28
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

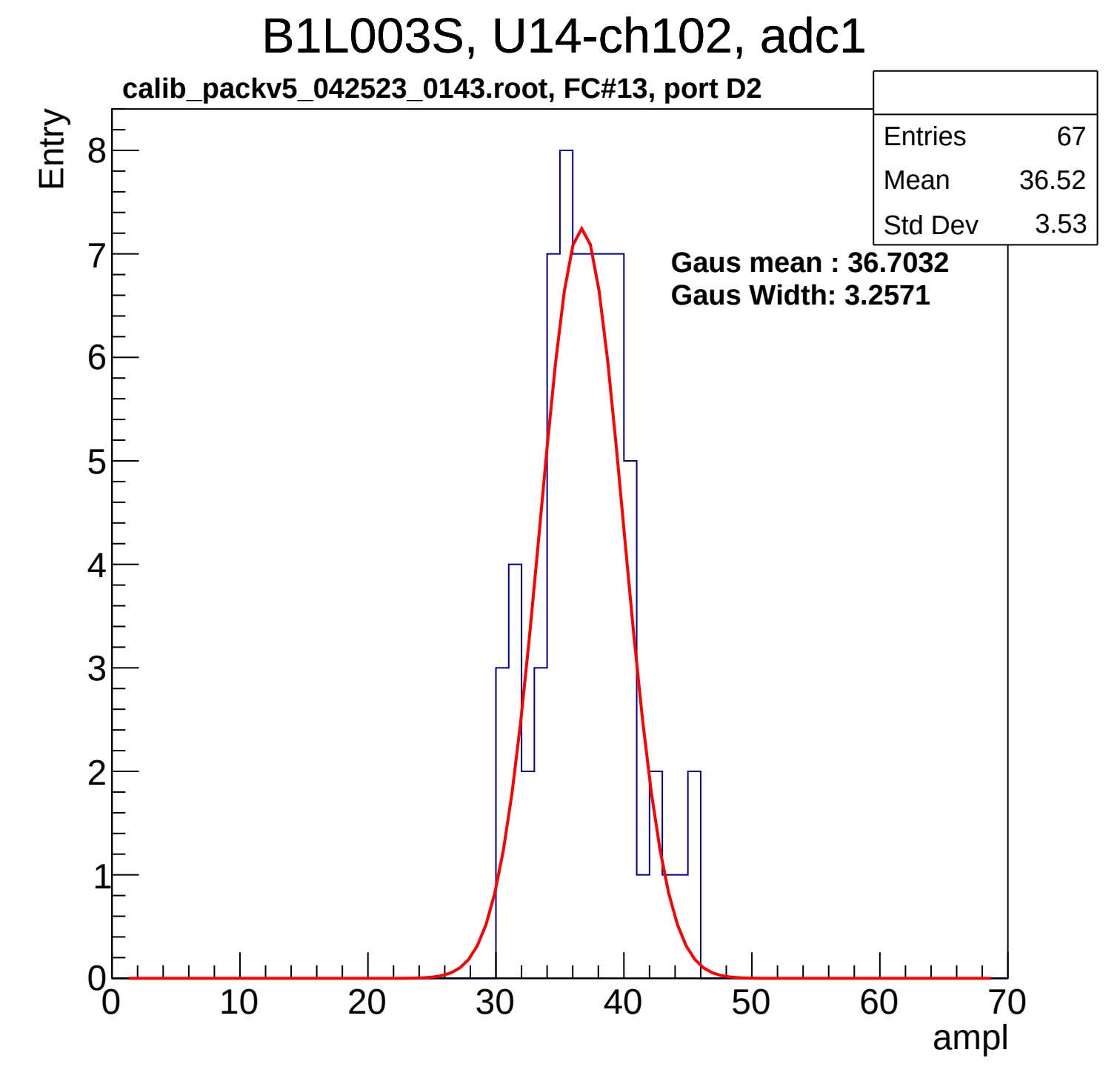

B1L003S, U14-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.83
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

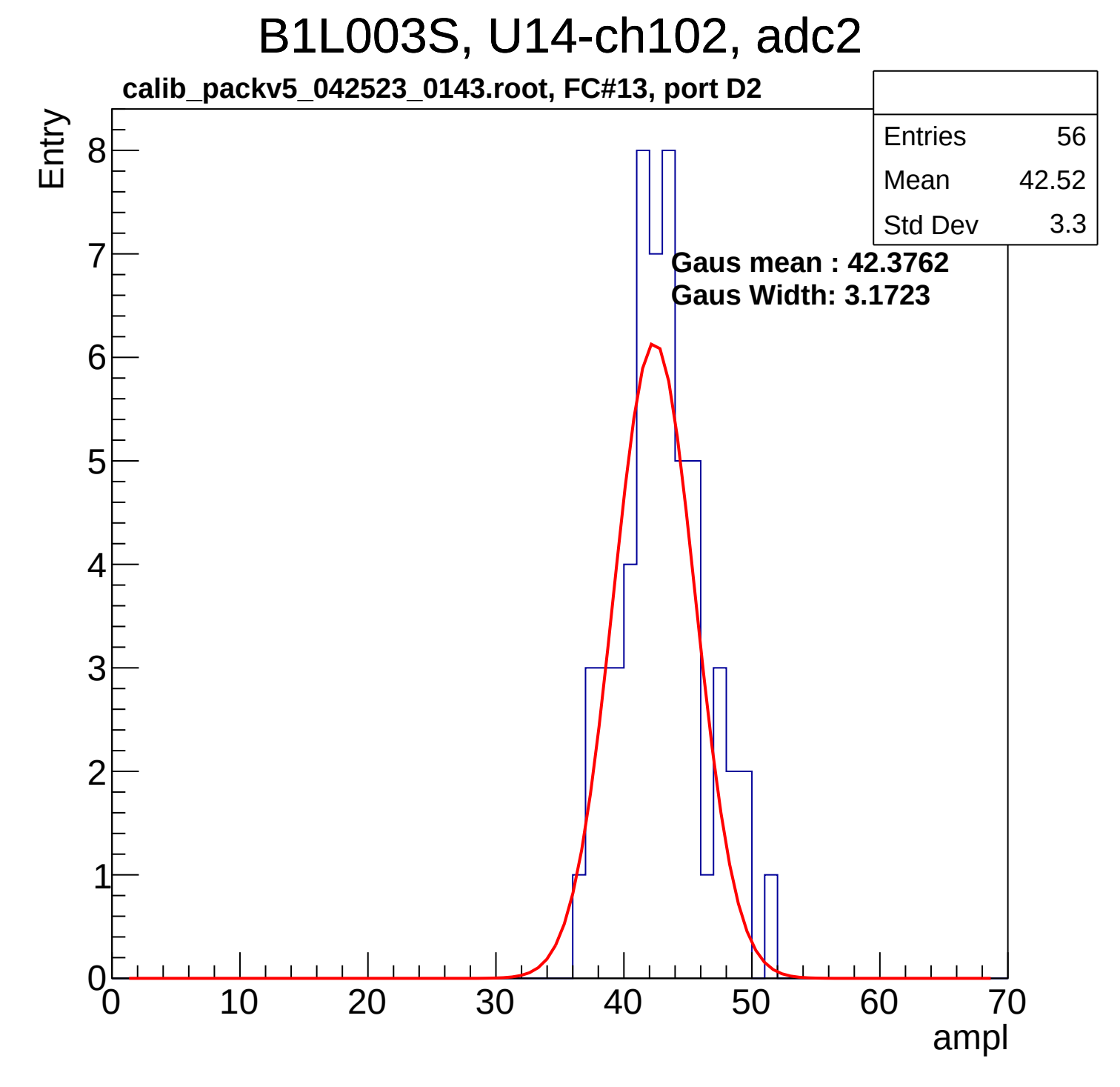

B1L003S, U14-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.52
Std Dev 8.257
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

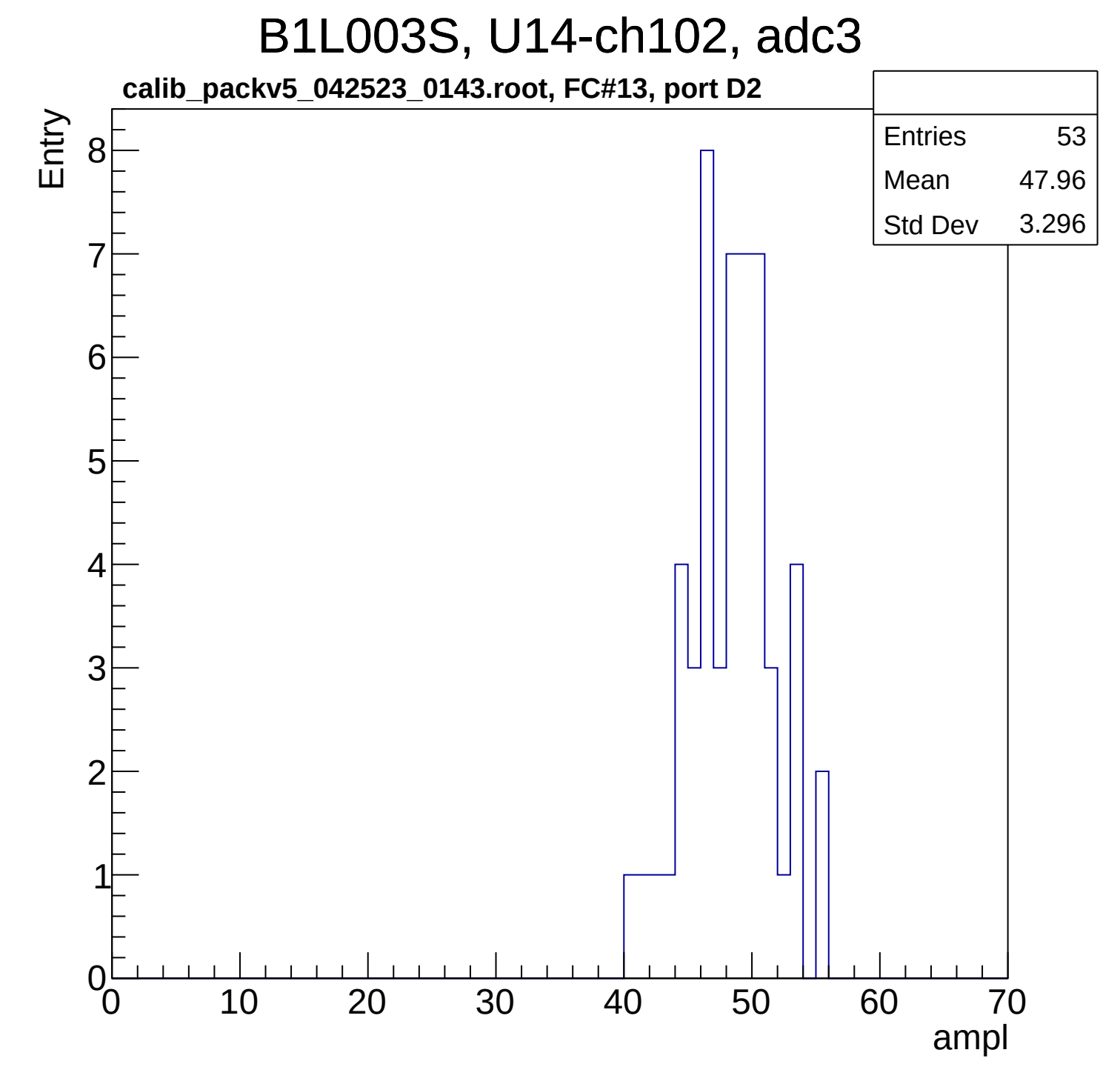

B1L003S, U14-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 0.7559
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

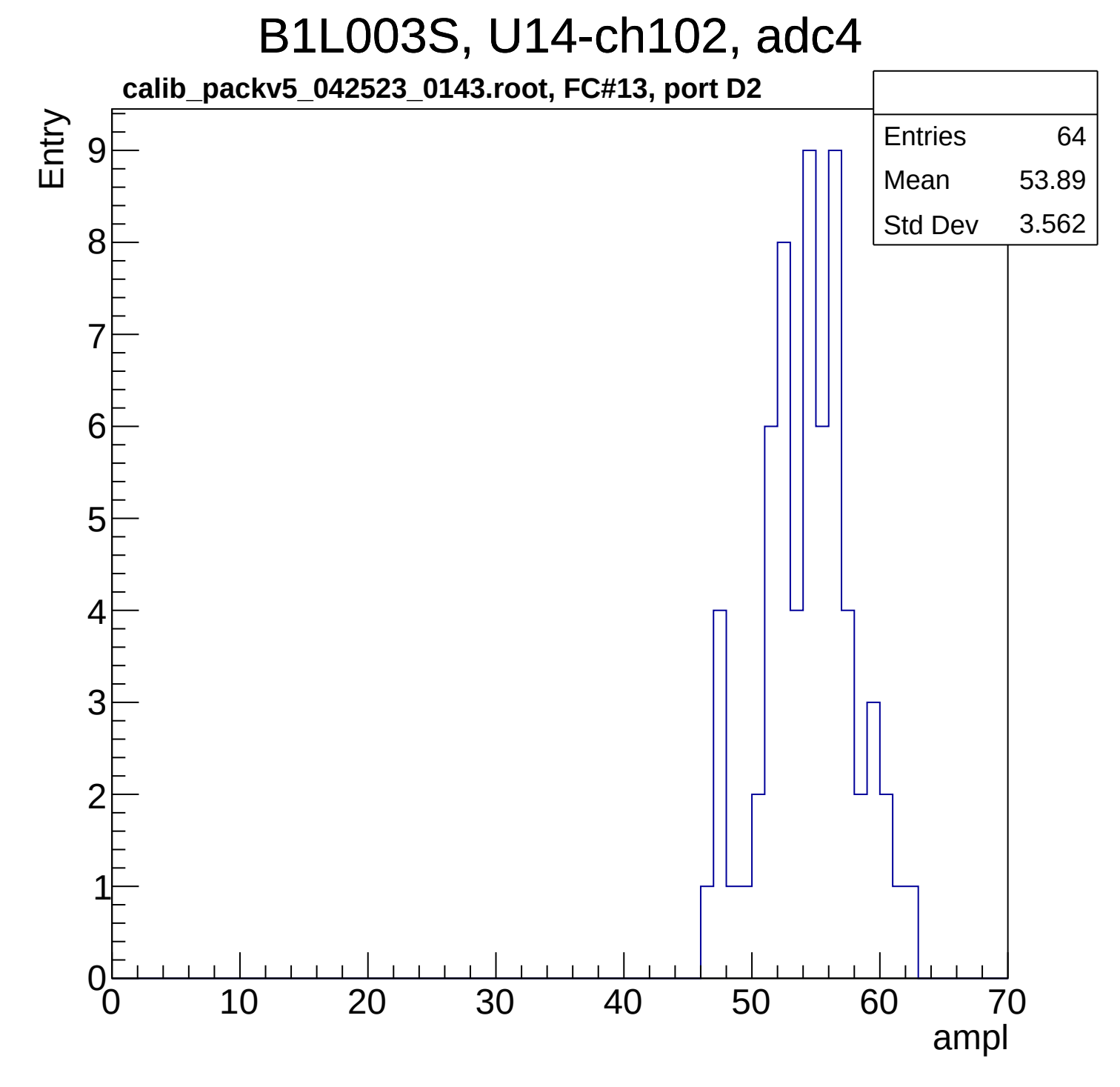

B1L003S, U14-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

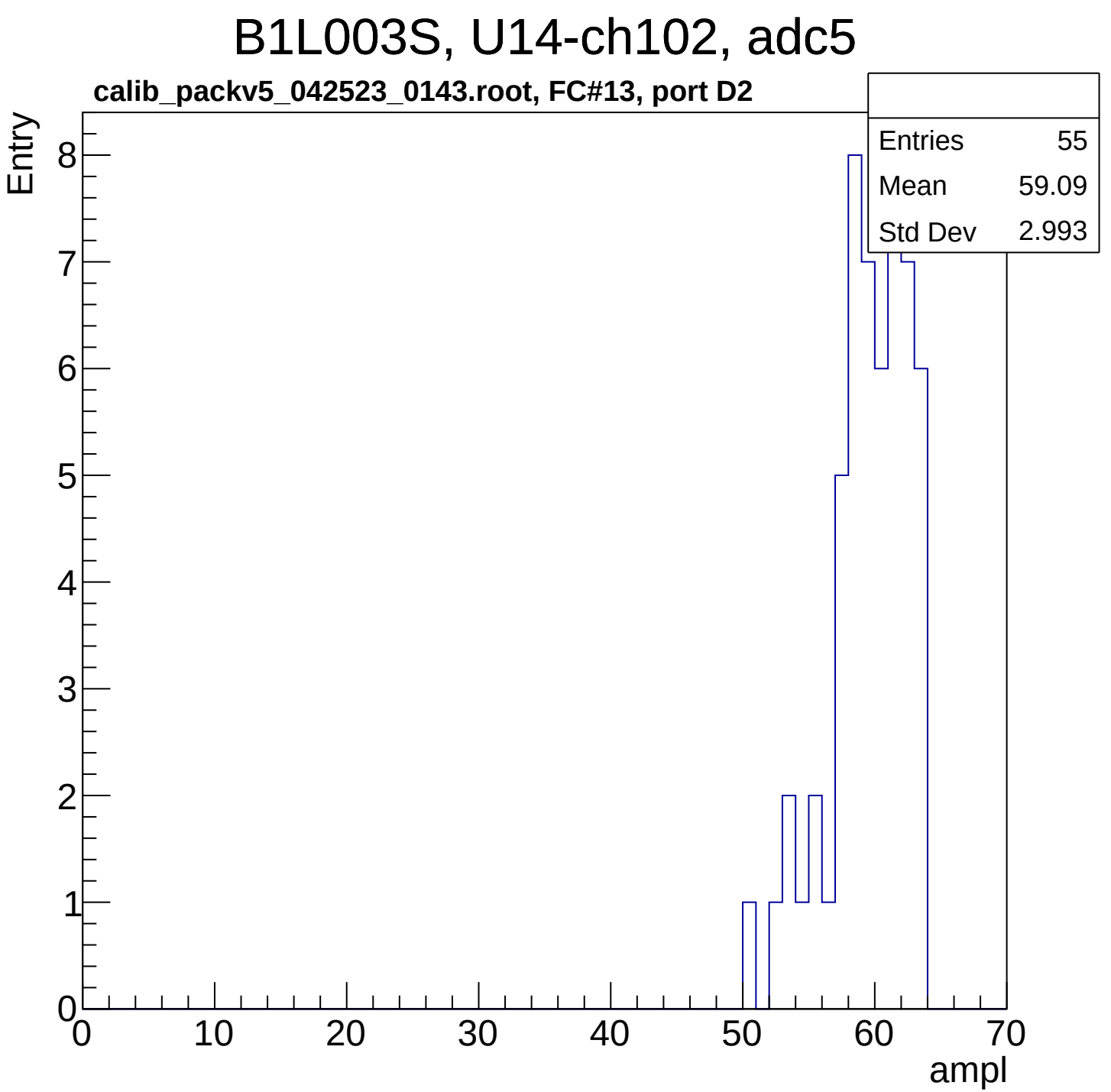

B1L003S, U14-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.53
Std Dev 7.399
Gaus mean : 30.8346
Gaus Width: 3.6296
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

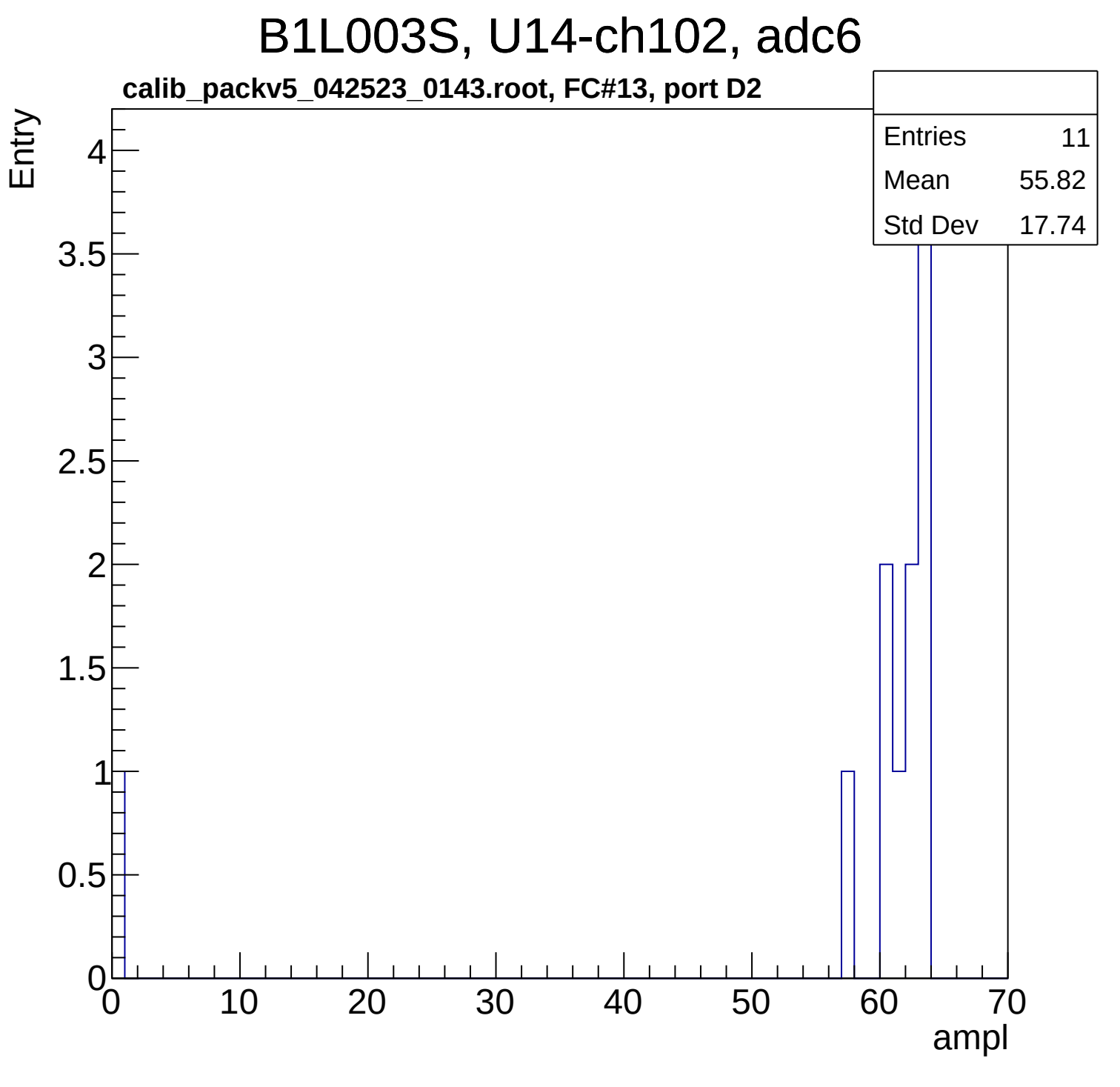

B1L003S, U14-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.52
Std Dev 3.53
Gaus mean : 36.7032
Gaus Width: 3.2571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

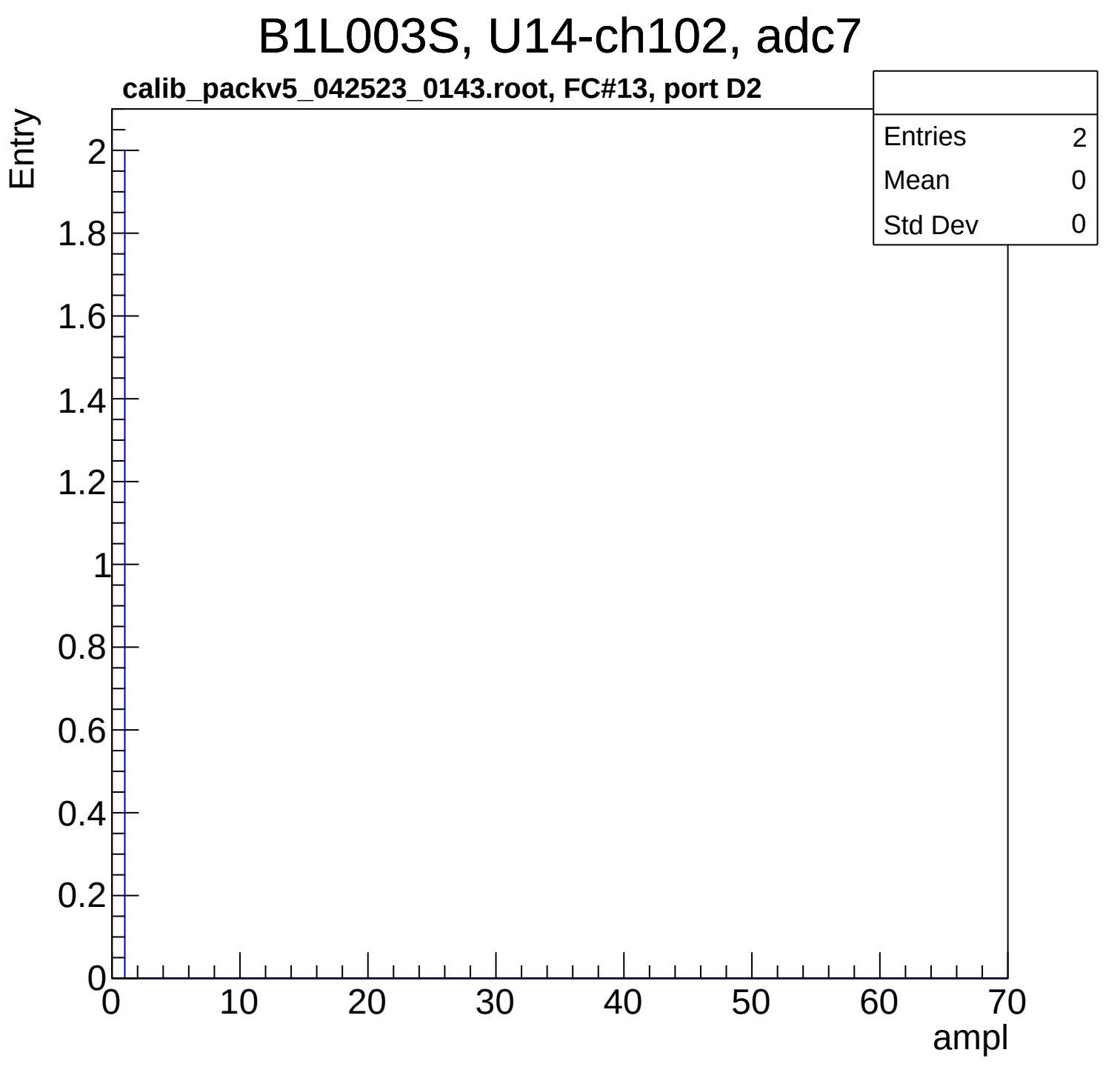

B1L003S, U14-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.52
Std Dev 3.3
Gaus mean : 42.3762
Gaus Width: 3.1723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

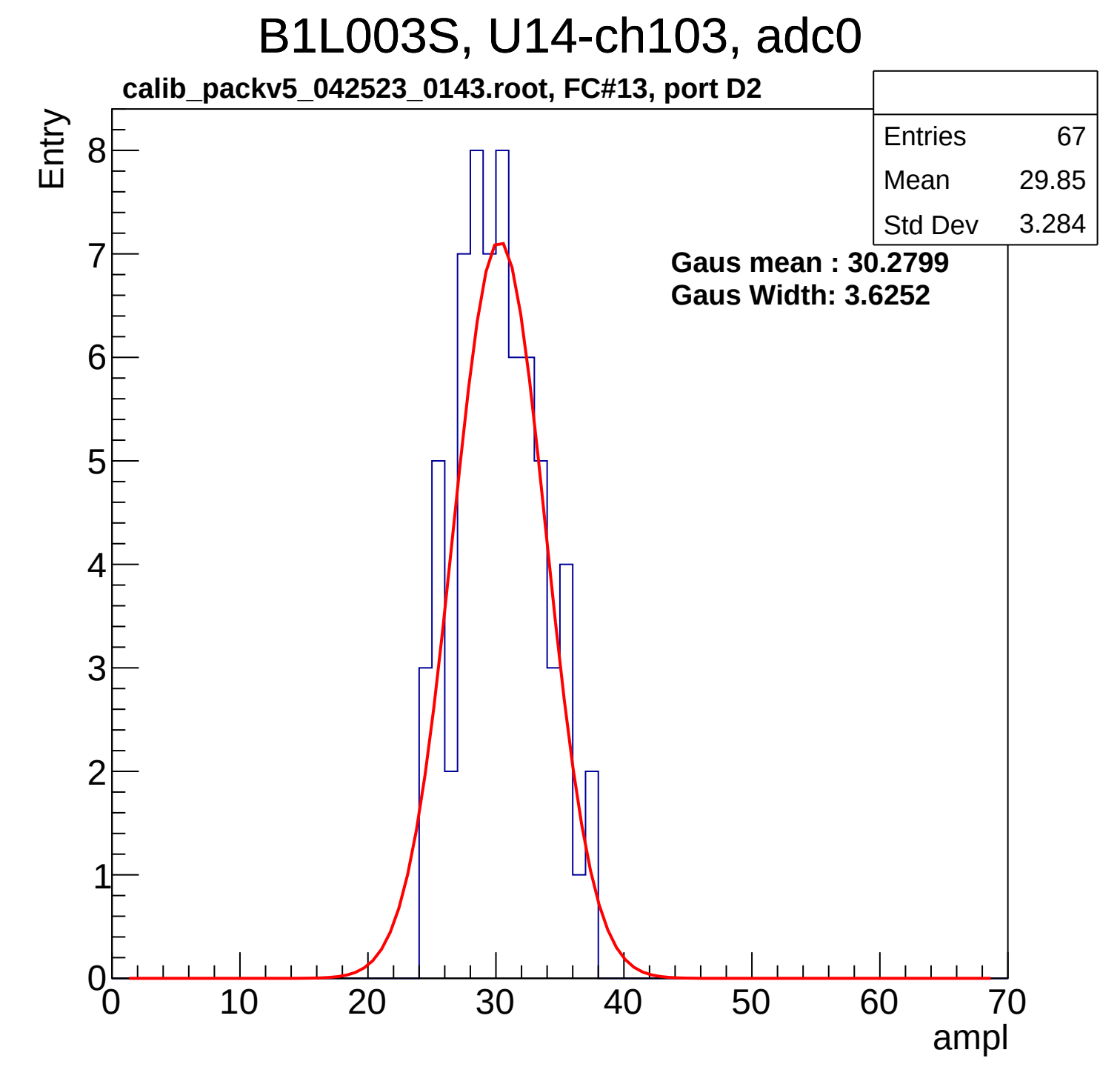

B1L003S, U14-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 47.96
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

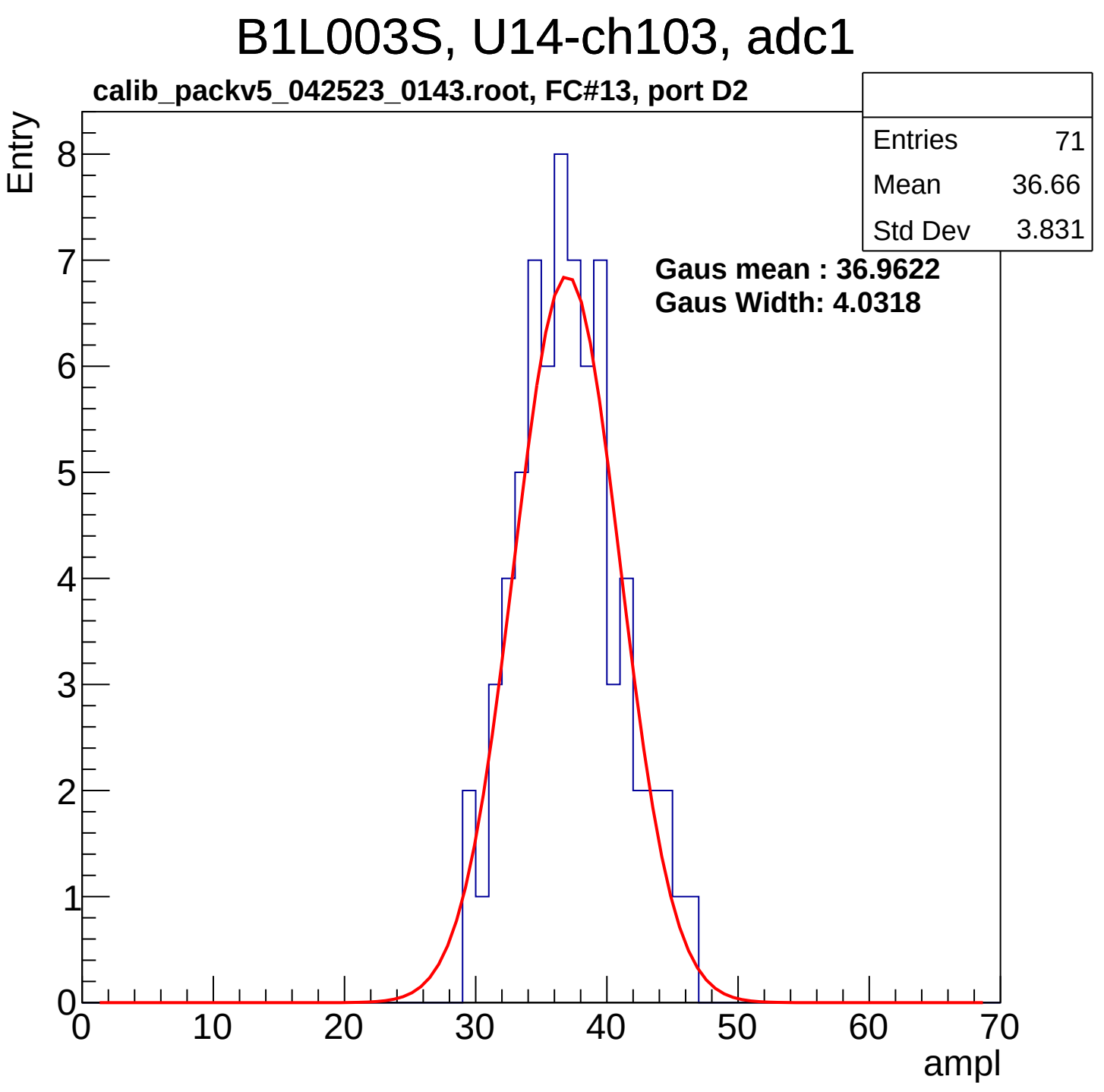

B1L003S, U14-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.89
Std Dev 3.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

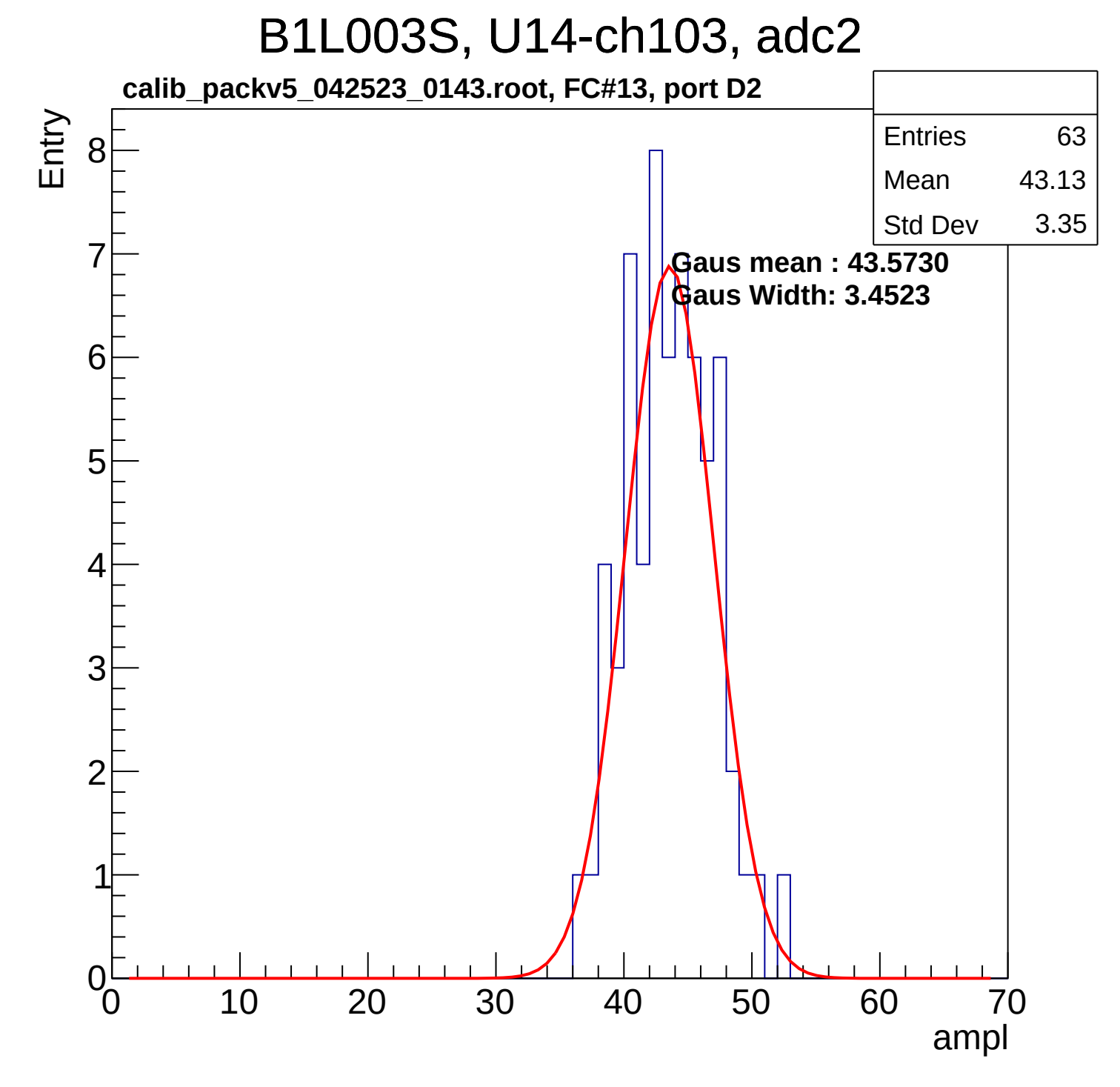

B1L003S, U14-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.09
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

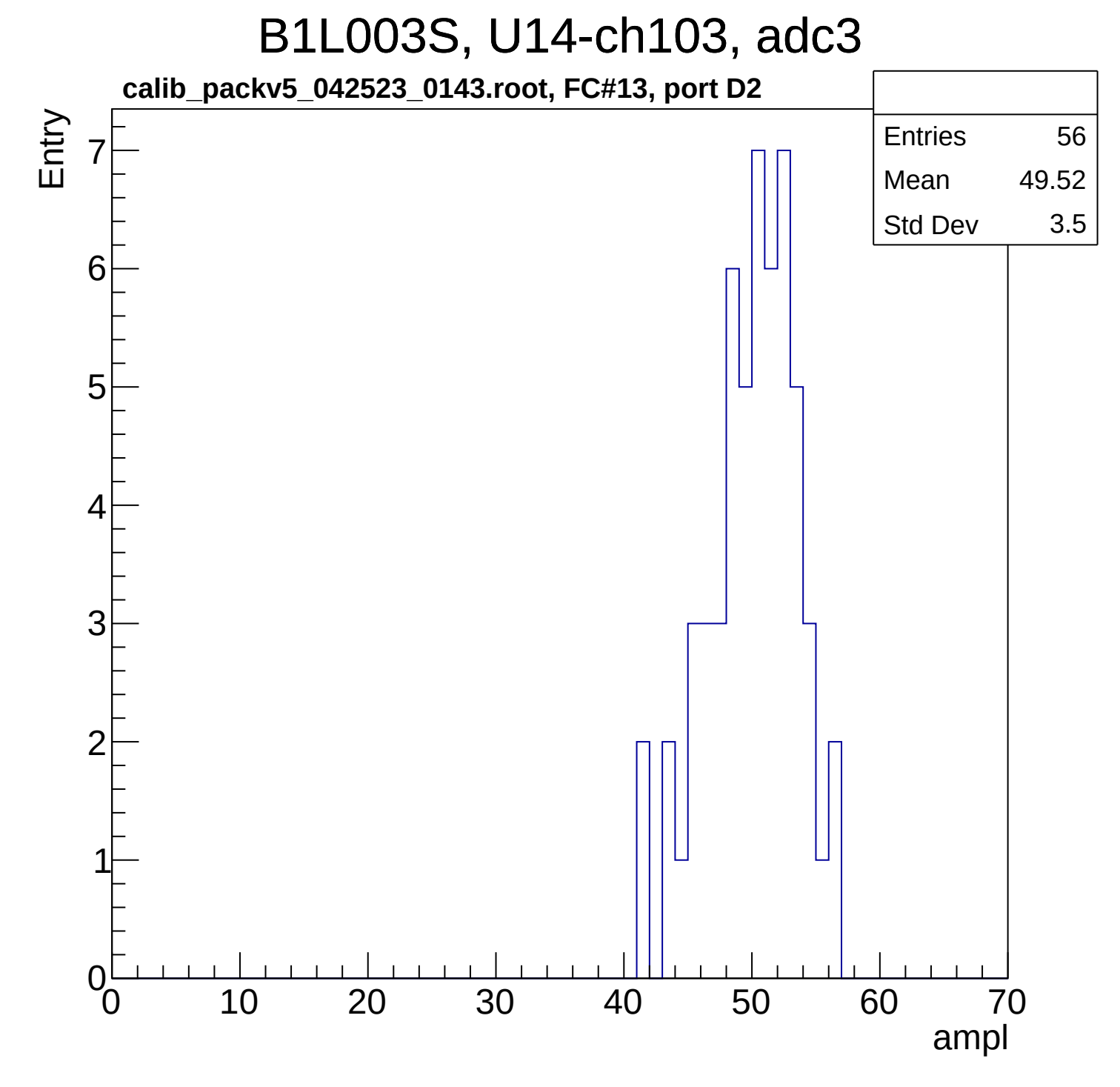

B1L003S, U14-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.82
Std Dev 17.74
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

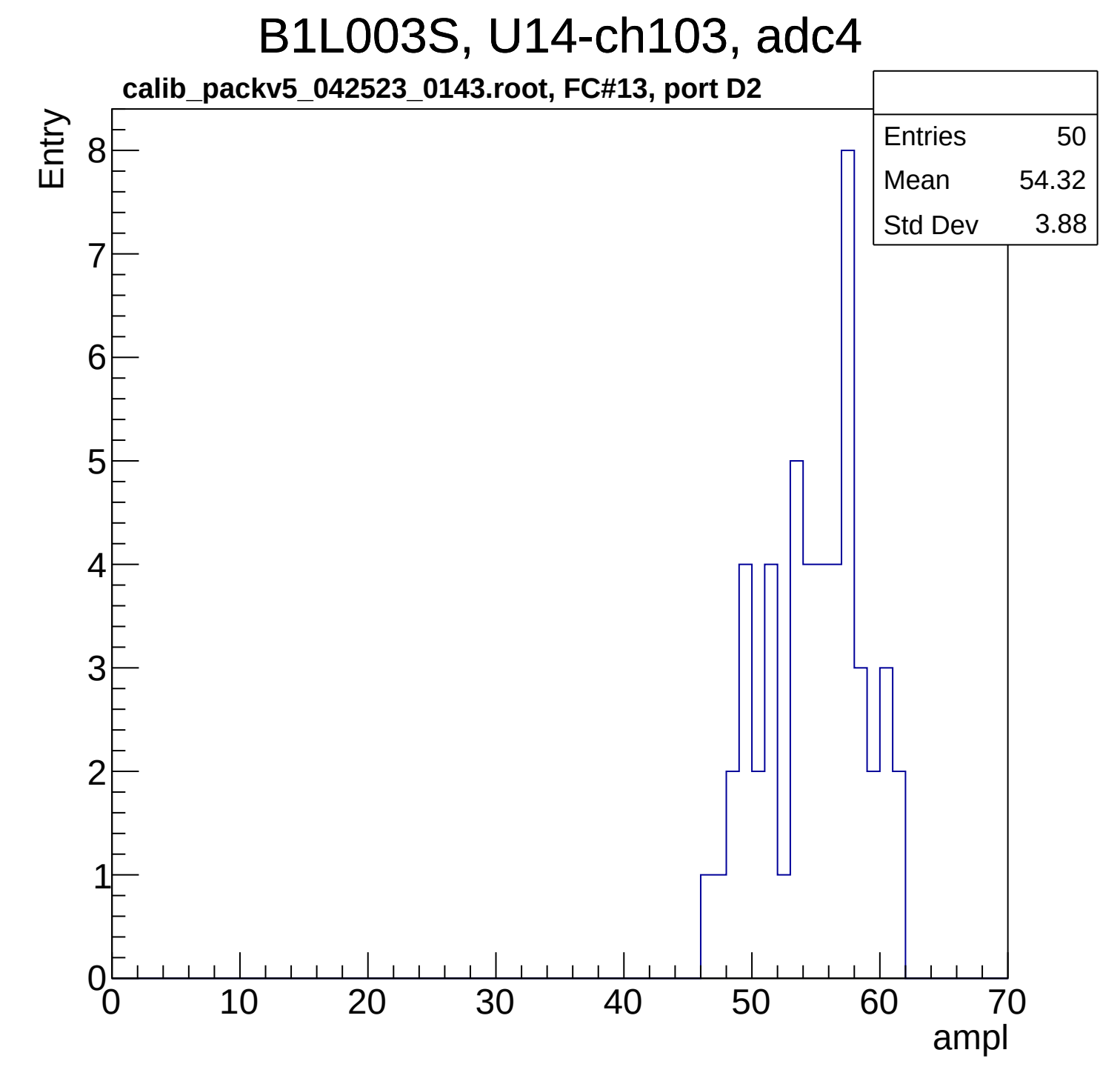

B1L003S, U14-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

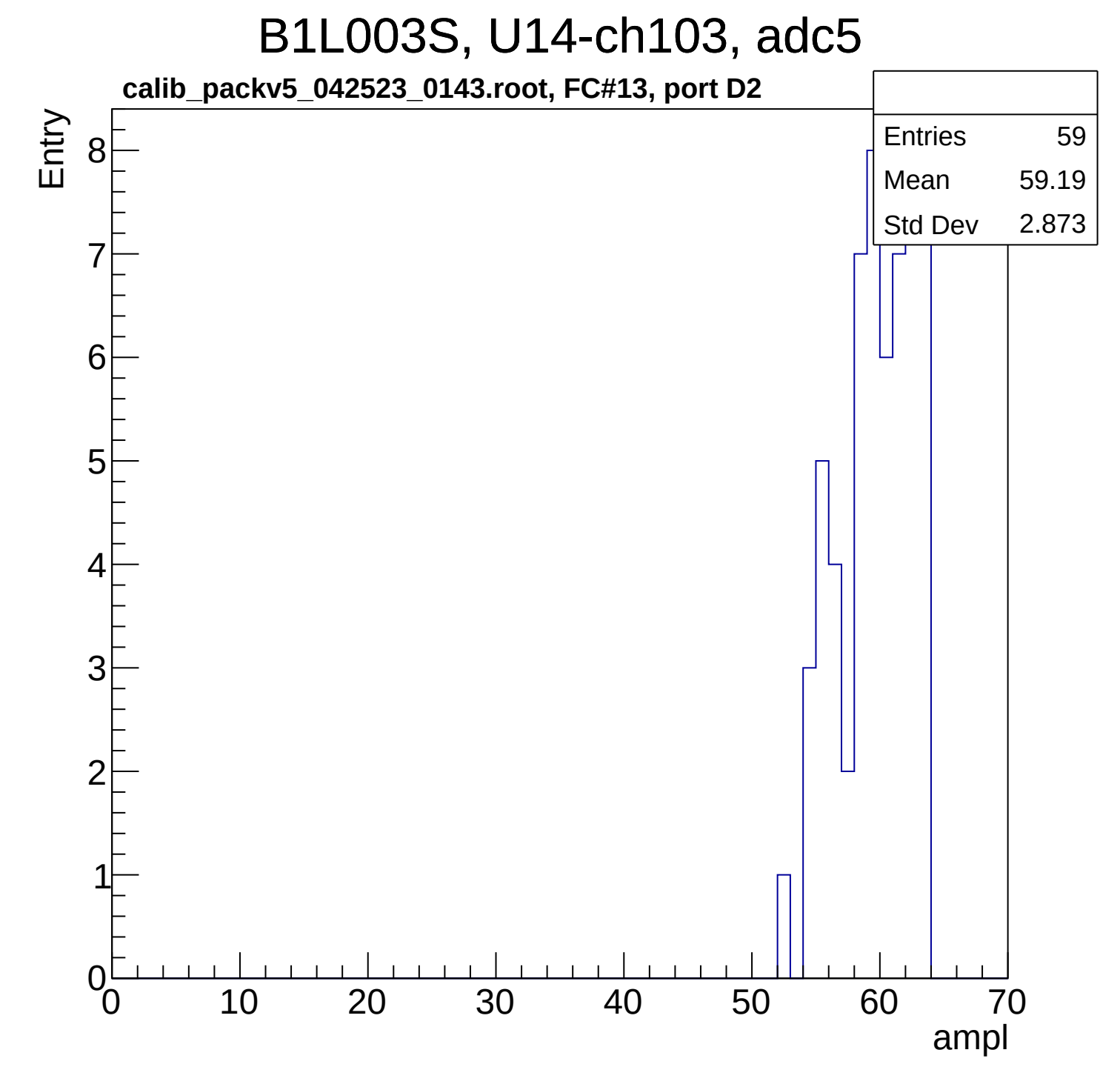

B1L003S, U14-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.85
Std Dev 3.284
Gaus mean : 30.2799
Gaus Width: 3.6252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

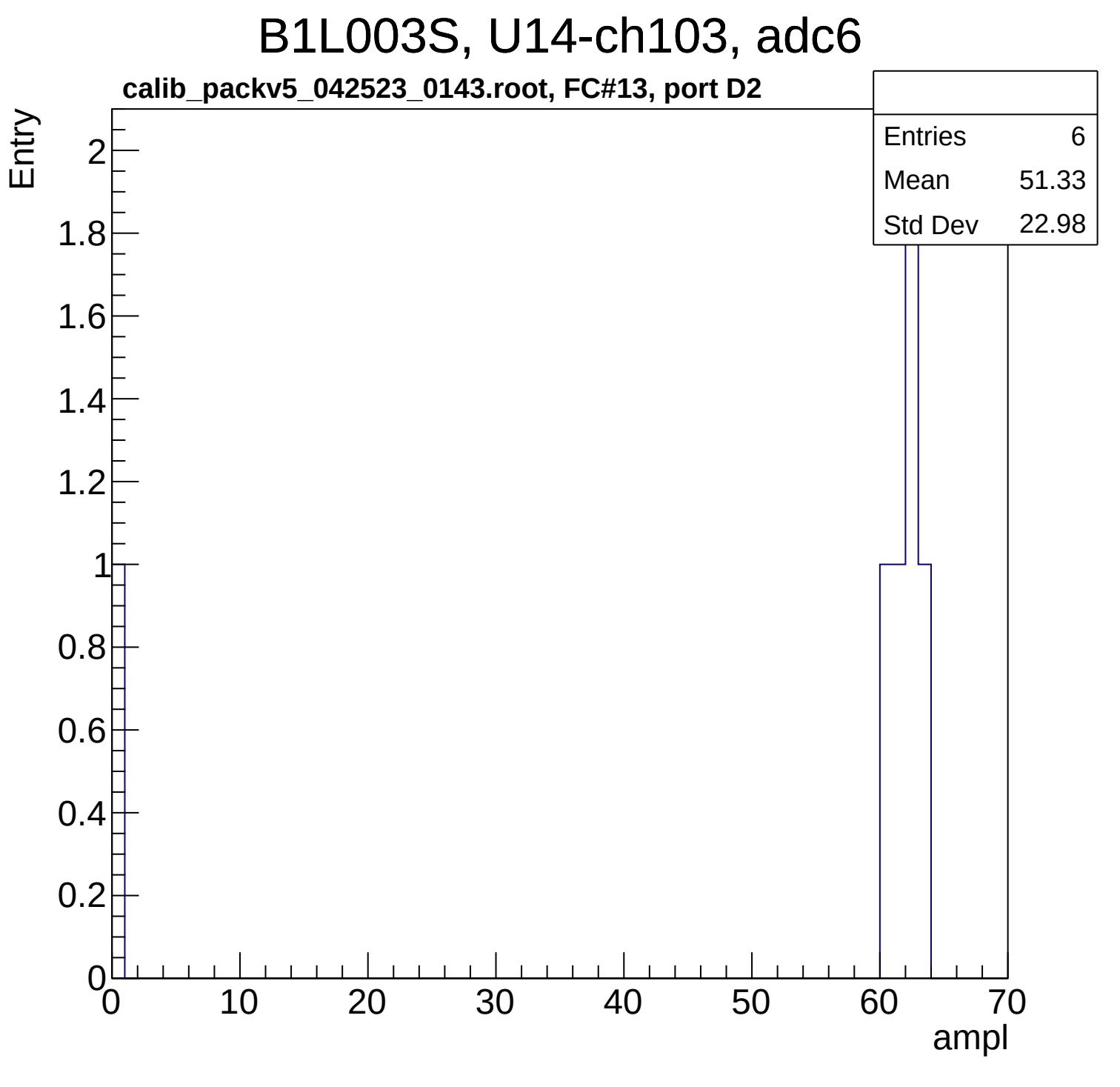

B1L003S, U14-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.66
Std Dev 3.831
Gaus mean : 36.9622
Gaus Width: 4.0318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

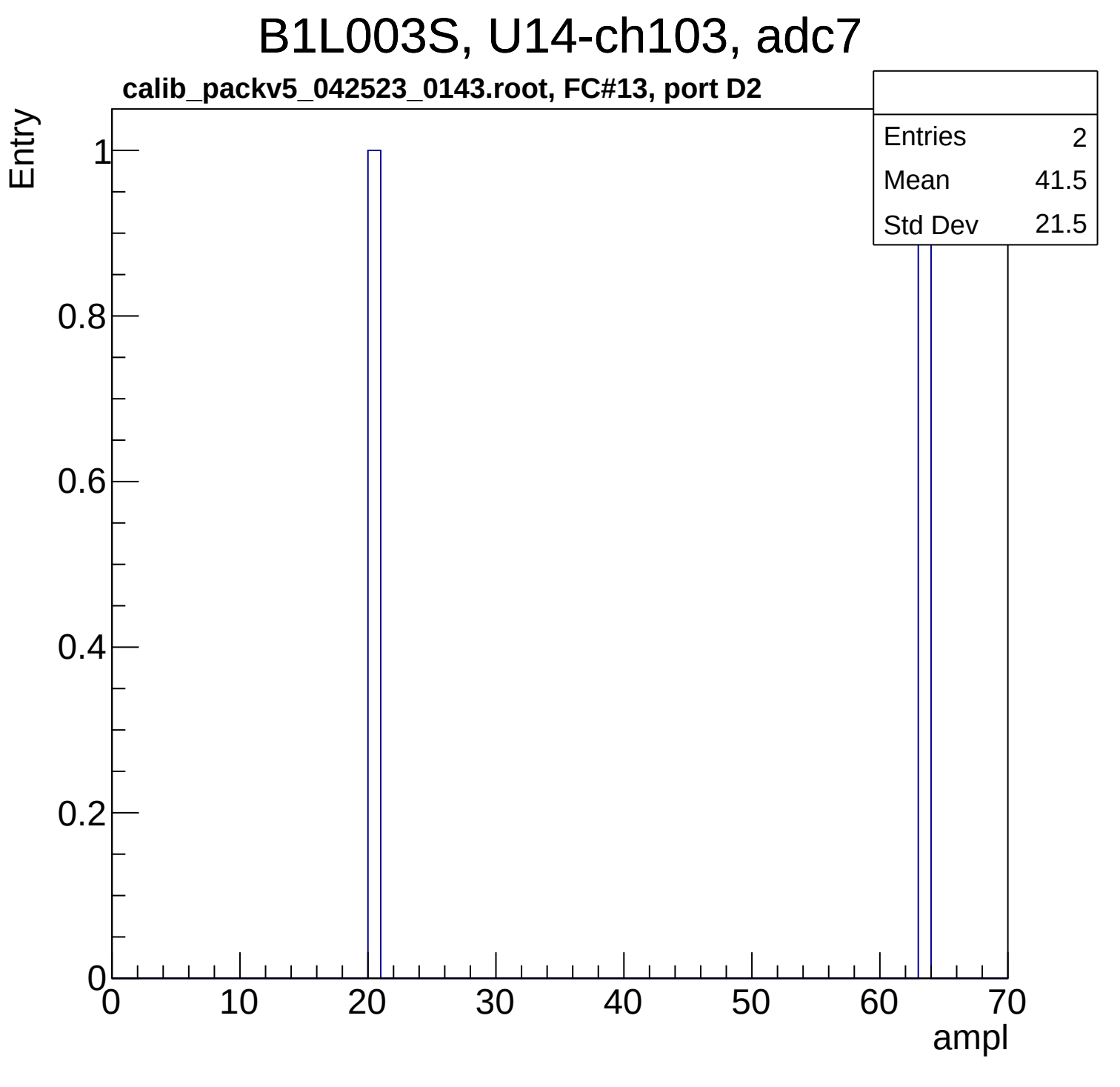

B1L003S, U14-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.13
Std Dev 3.35
Gaus mean : 43.5730
Gaus Width: 3.4523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

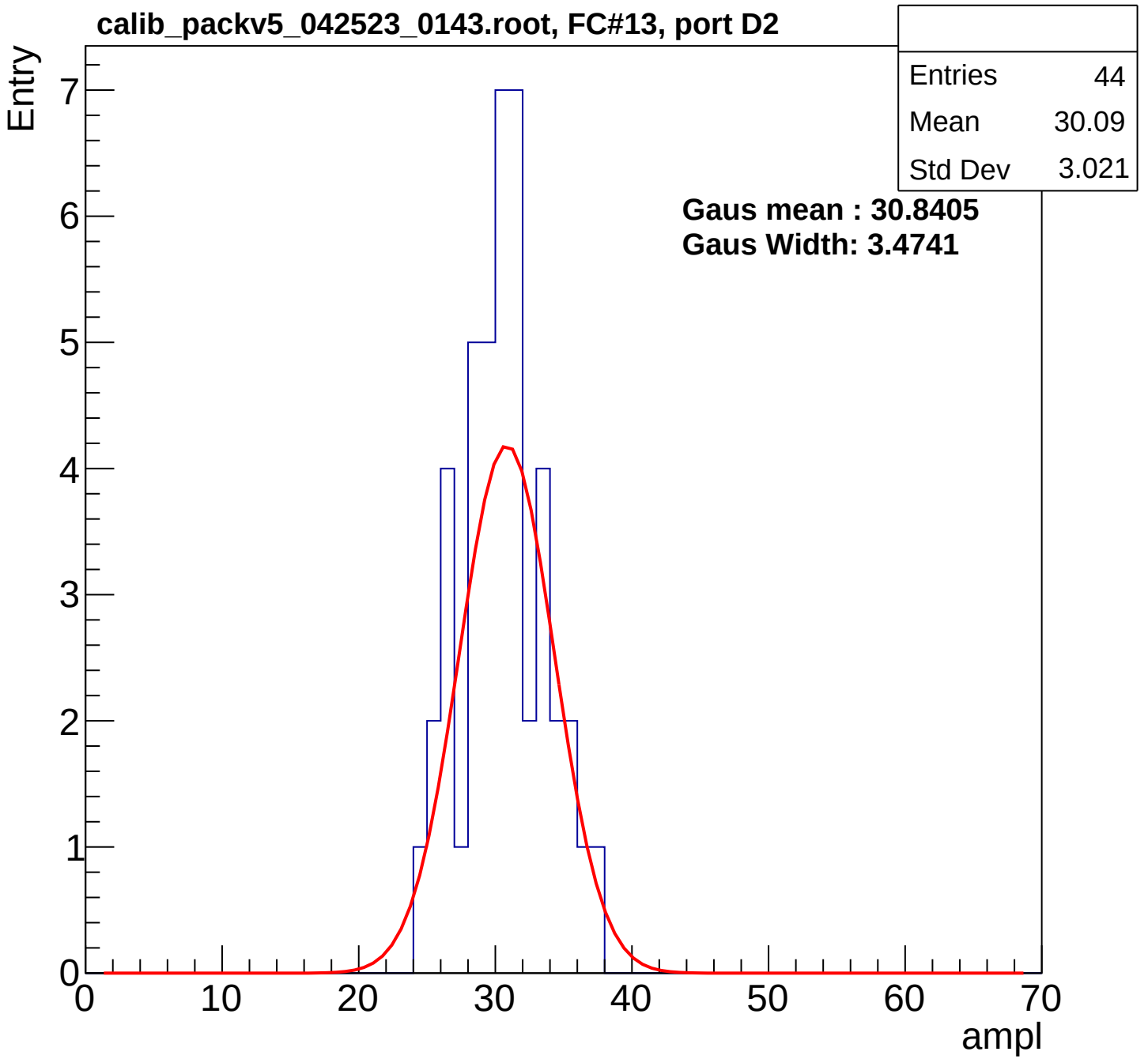

B1L003S, U14-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.52
Std Dev 3.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

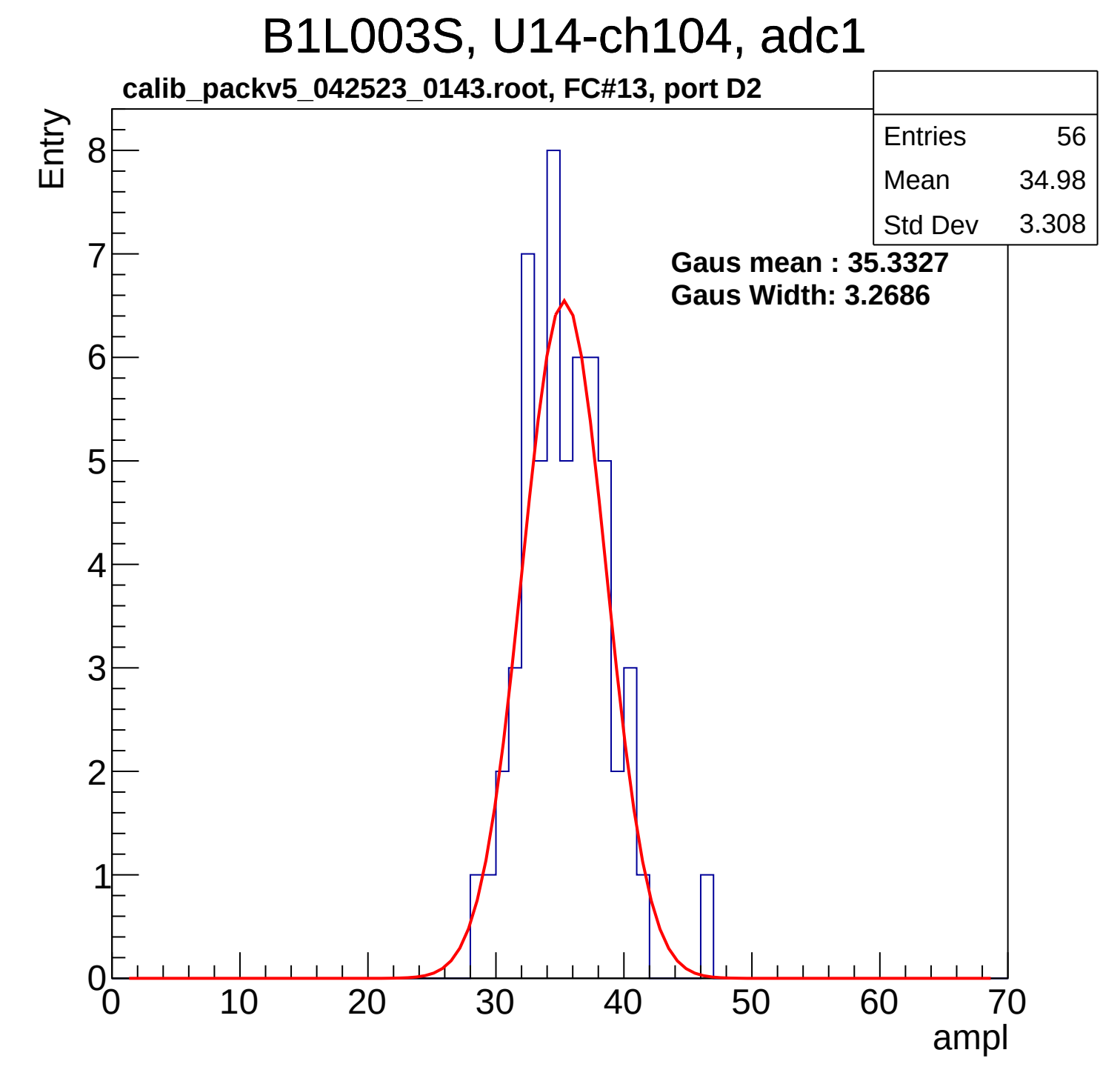

B1L003S, U14-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.32
Std Dev 3.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

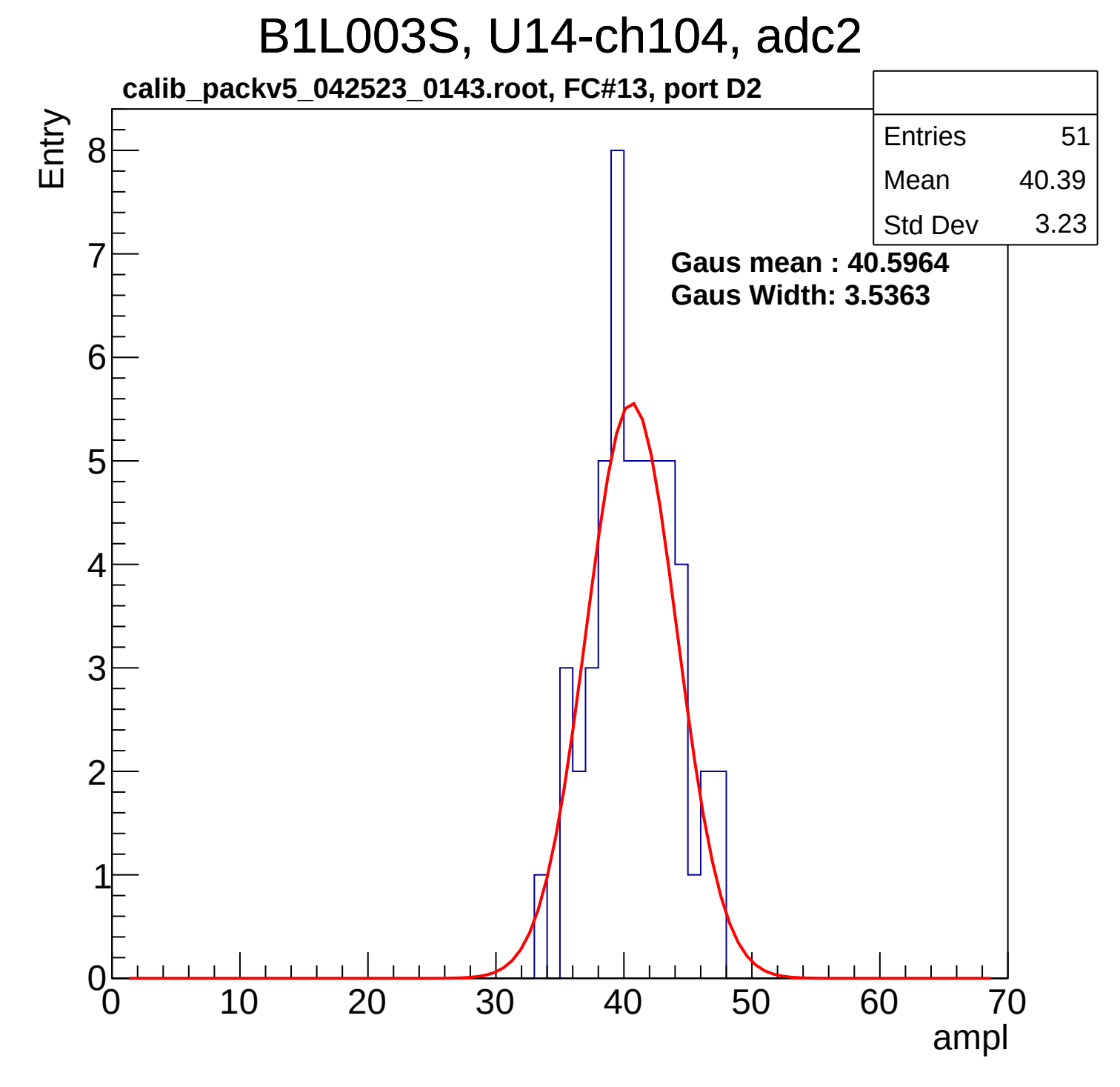

B1L003S, U14-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 59.19
Std Dev 2.873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

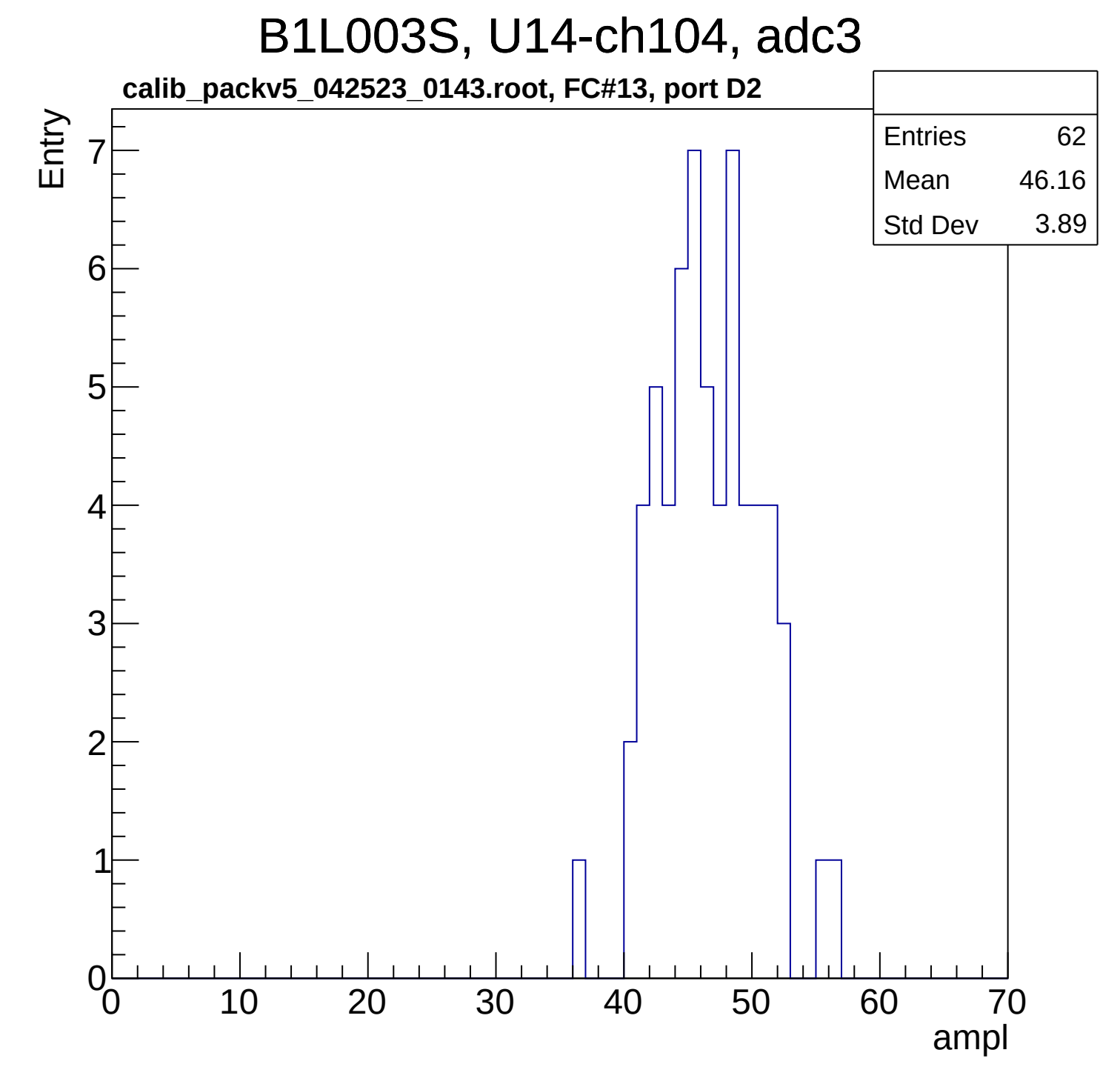

B1L003S, U14-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.33
Std Dev 22.98
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

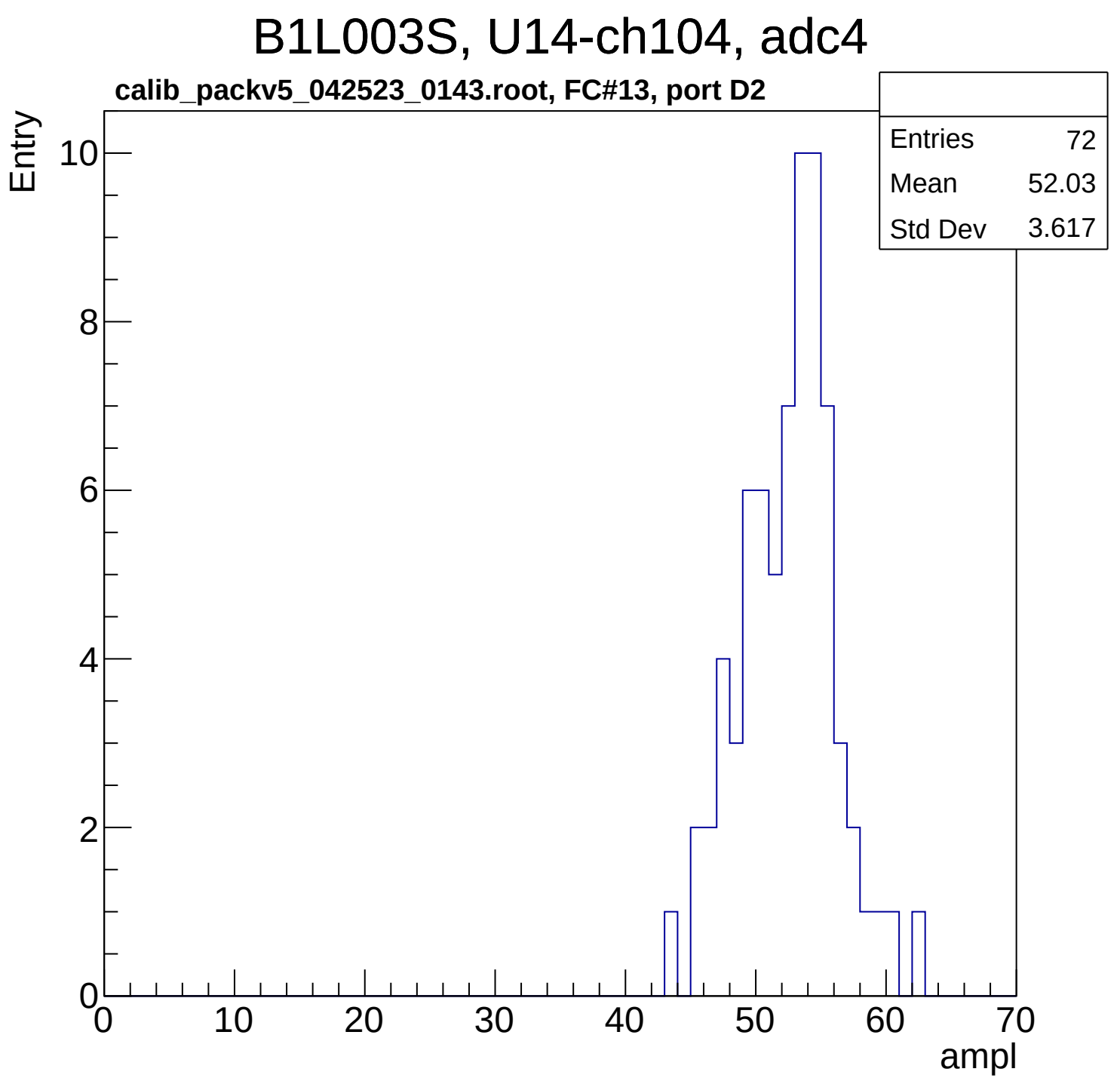

B1L003S, U14-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 41.5
Std Dev 21.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

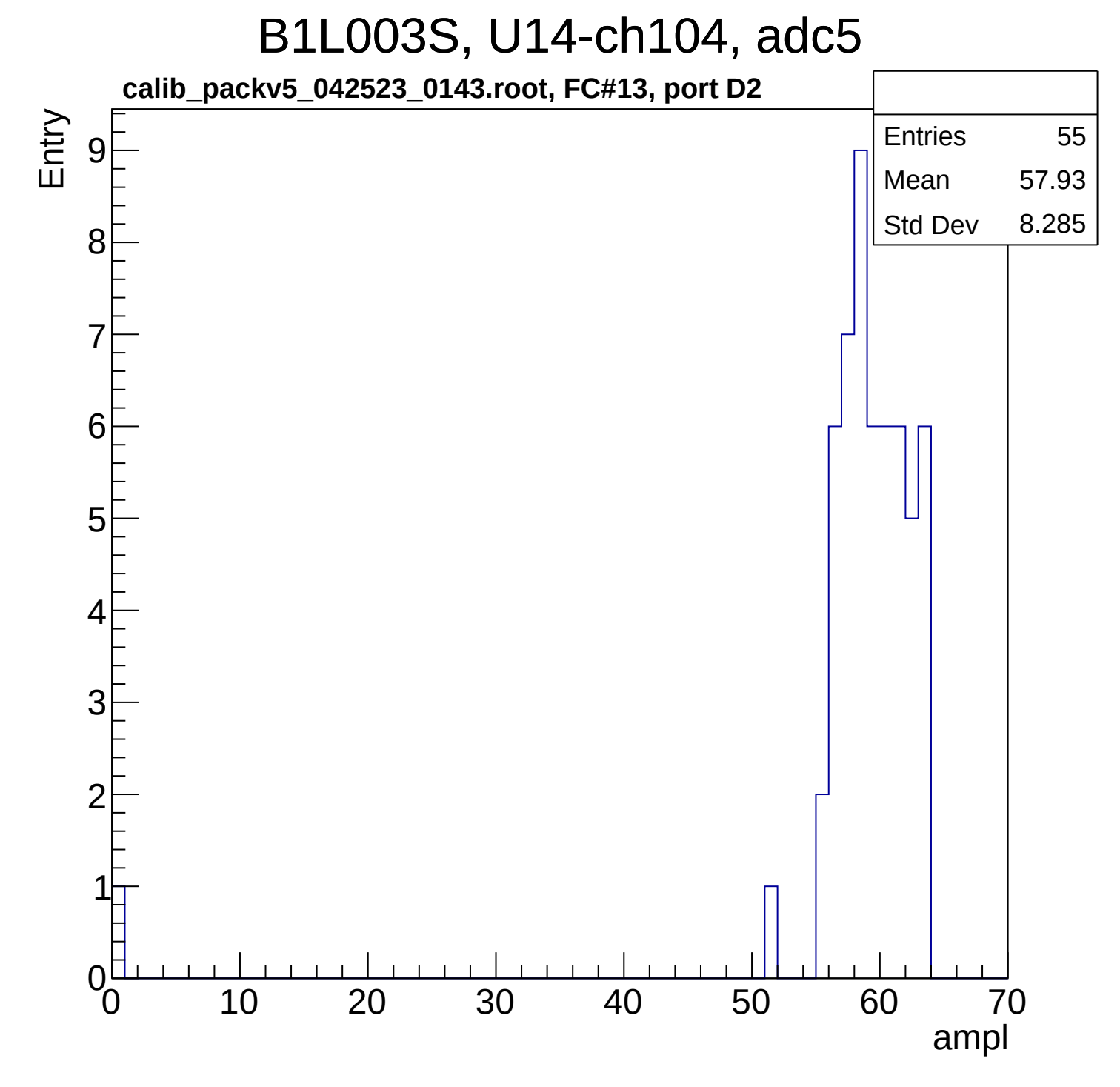

B1L003S, U14-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 30.09
Std Dev 3.021
Gaus mean : 30.8405
Gaus Width: 3.4741
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

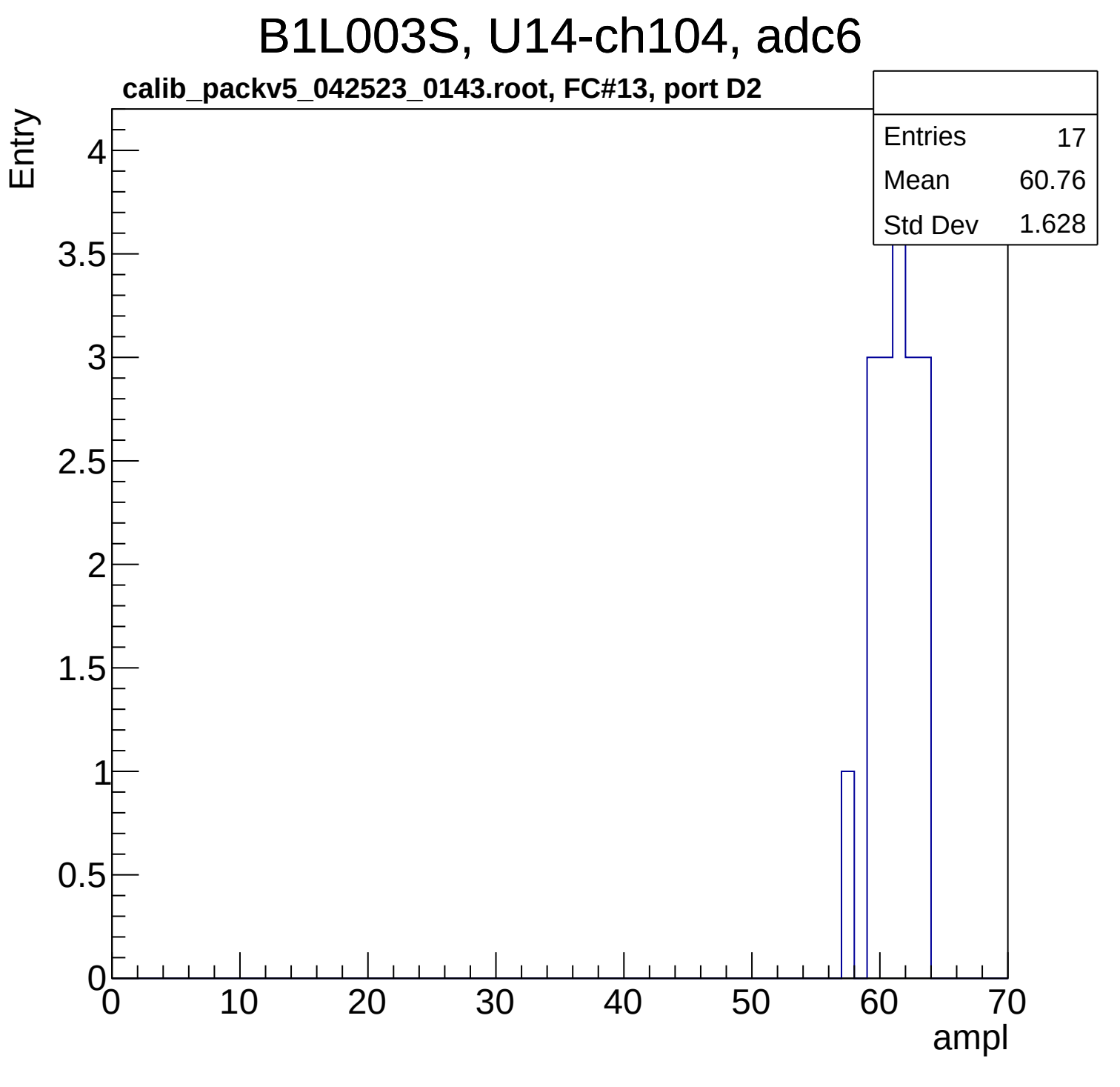

B1L003S, U14-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 34.98
Std Dev 3.308
Gaus mean : 35.3327
Gaus Width: 3.2686
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

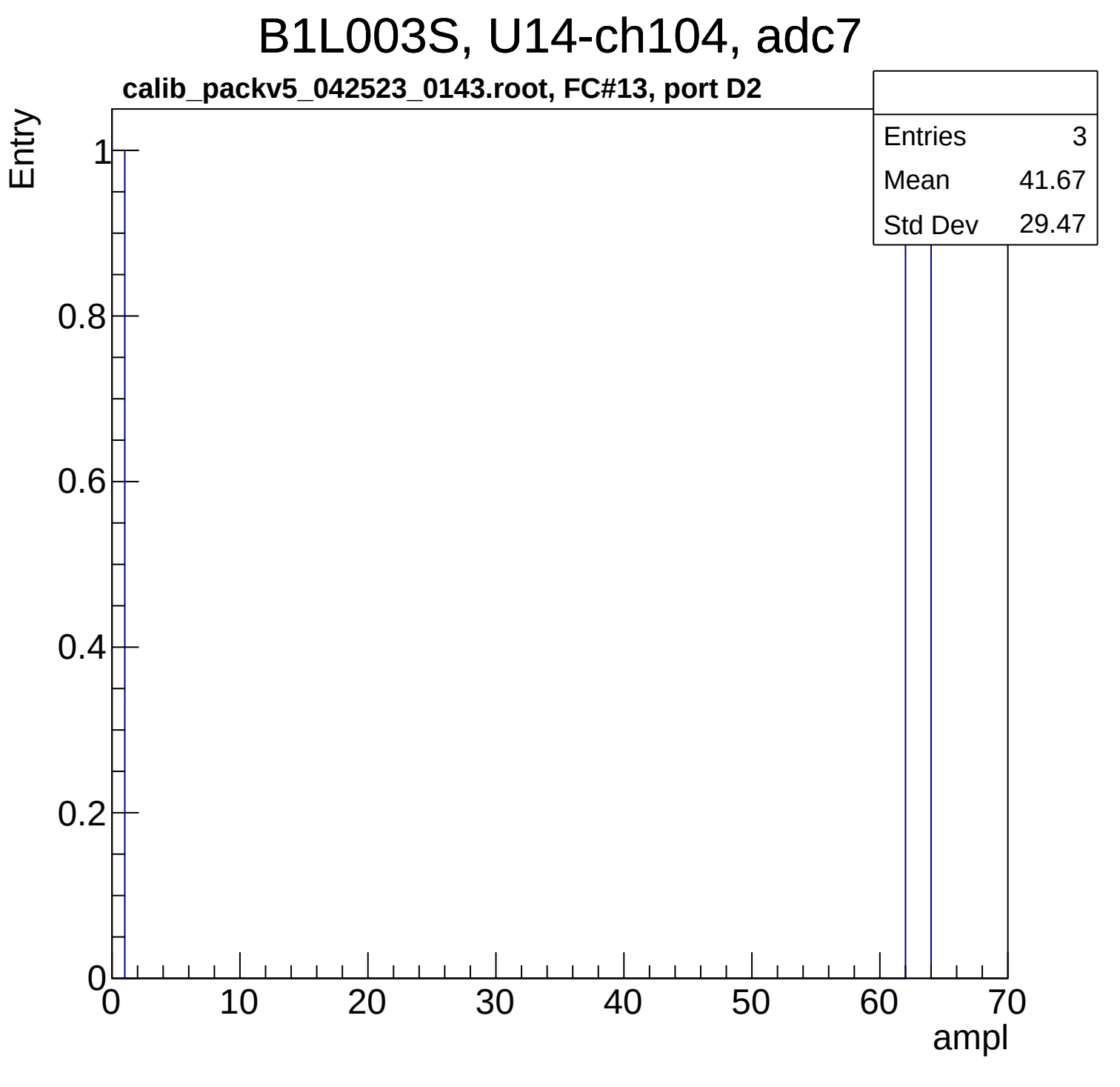

B1L003S, U14-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 40.39
Std Dev 3.23
Gaus mean : 40.5964
Gaus Width: 3.5363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

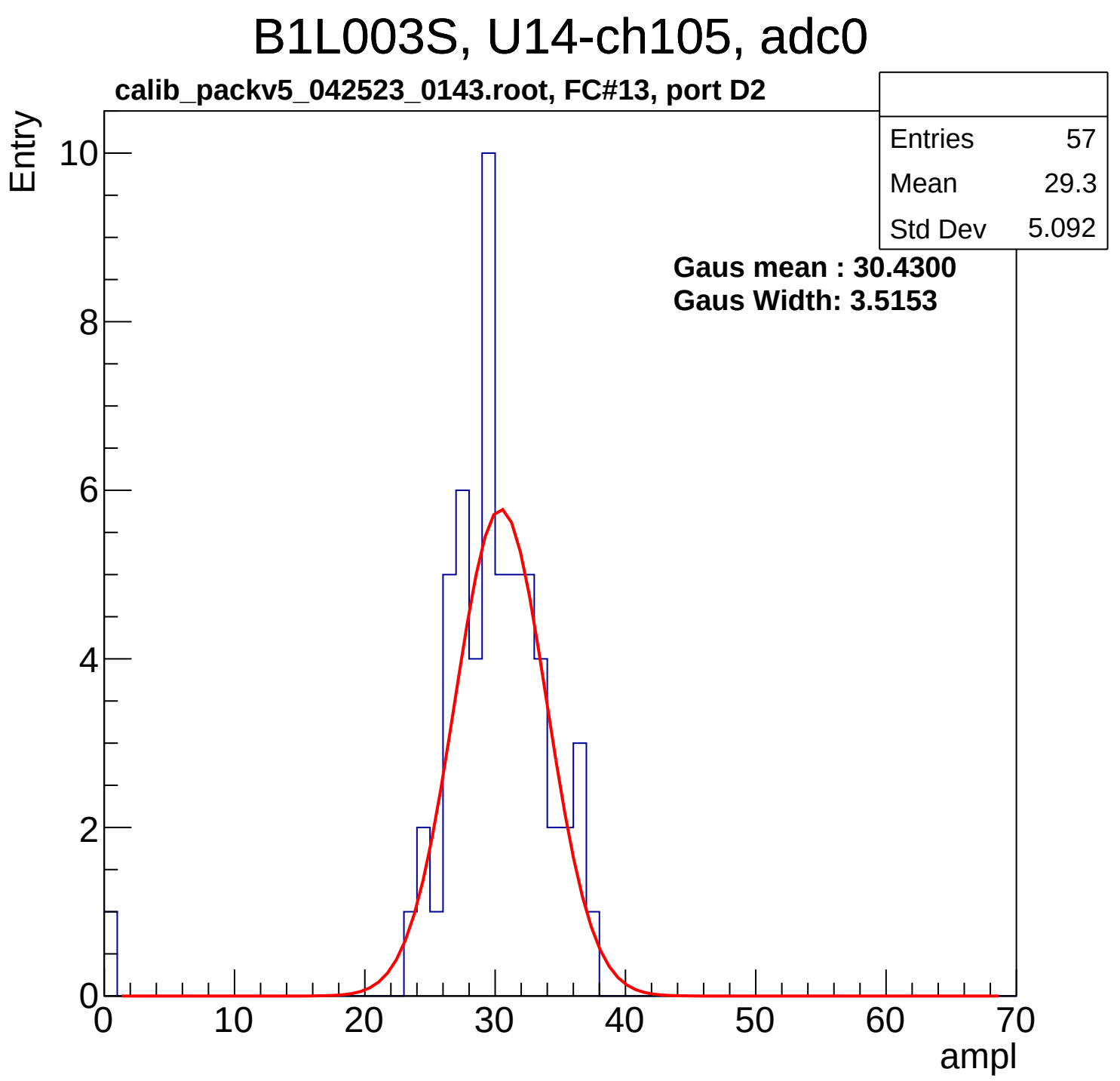

B1L003S, U14-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 46.16
Std Dev 3.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

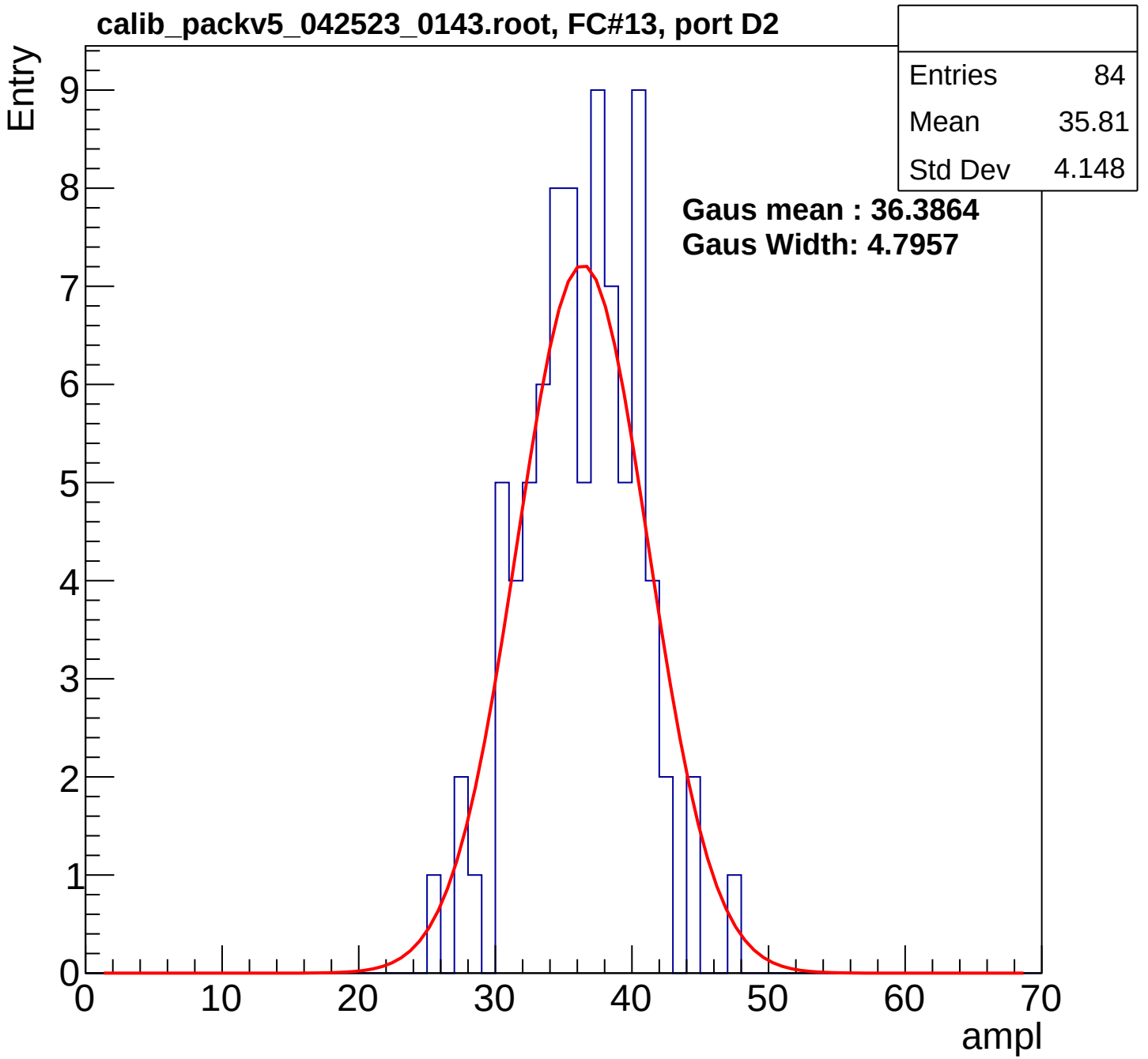

B1L003S, U14-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 52.03
Std Dev 3.617
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

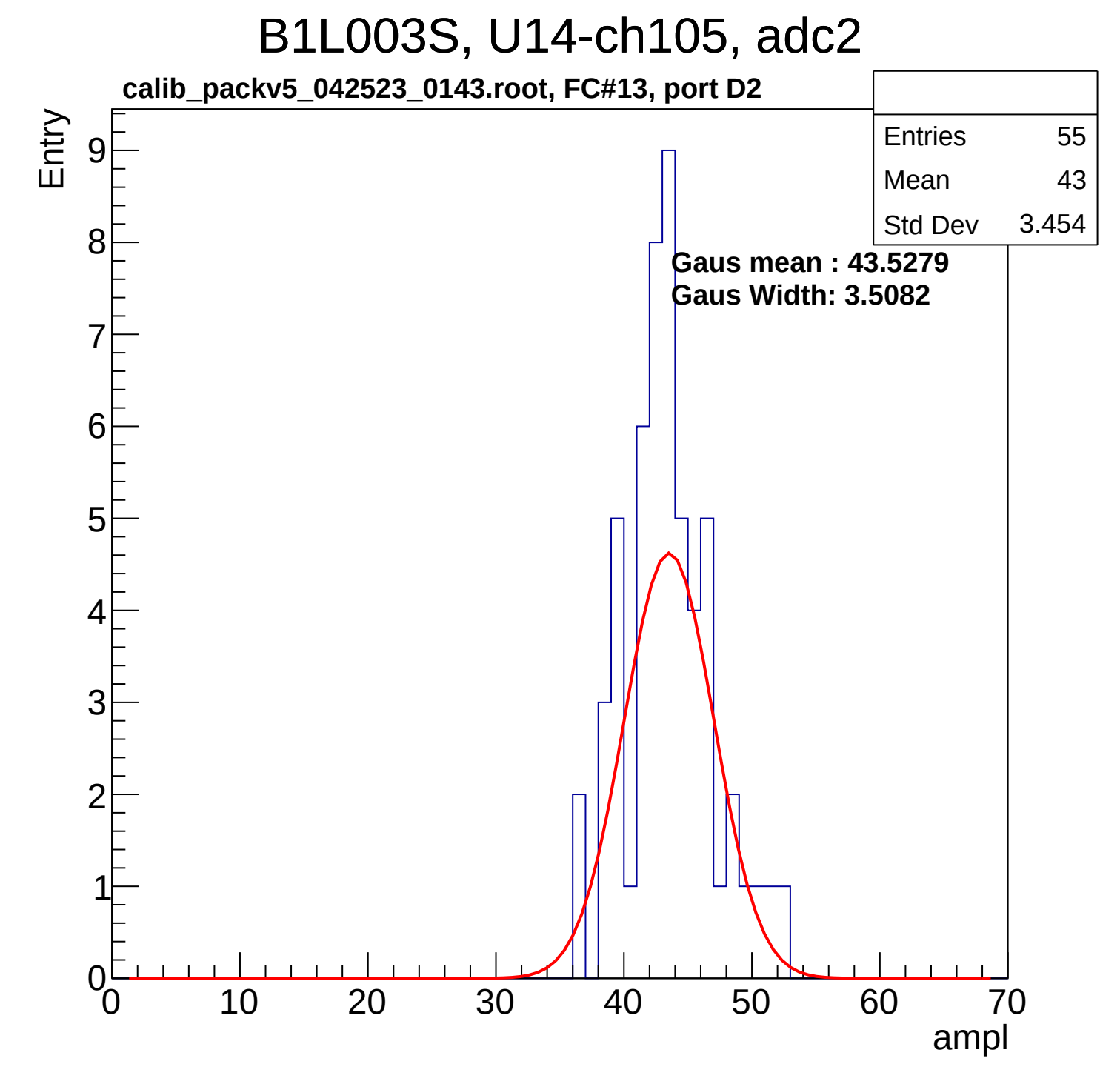

B1L003S, U14-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.93
Std Dev 8.285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

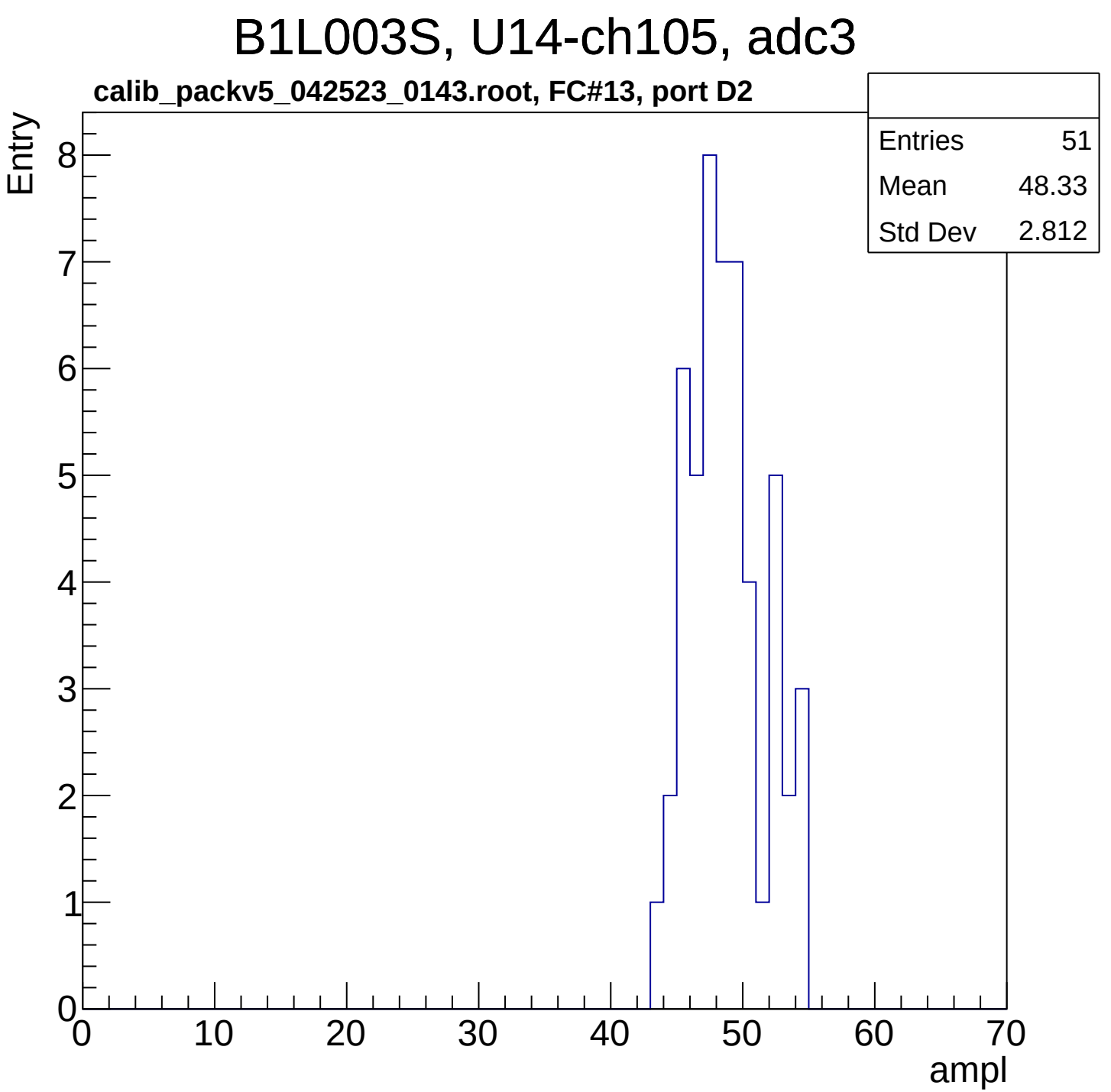

B1L003S, U14-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 60.76
Std Dev 1.628
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

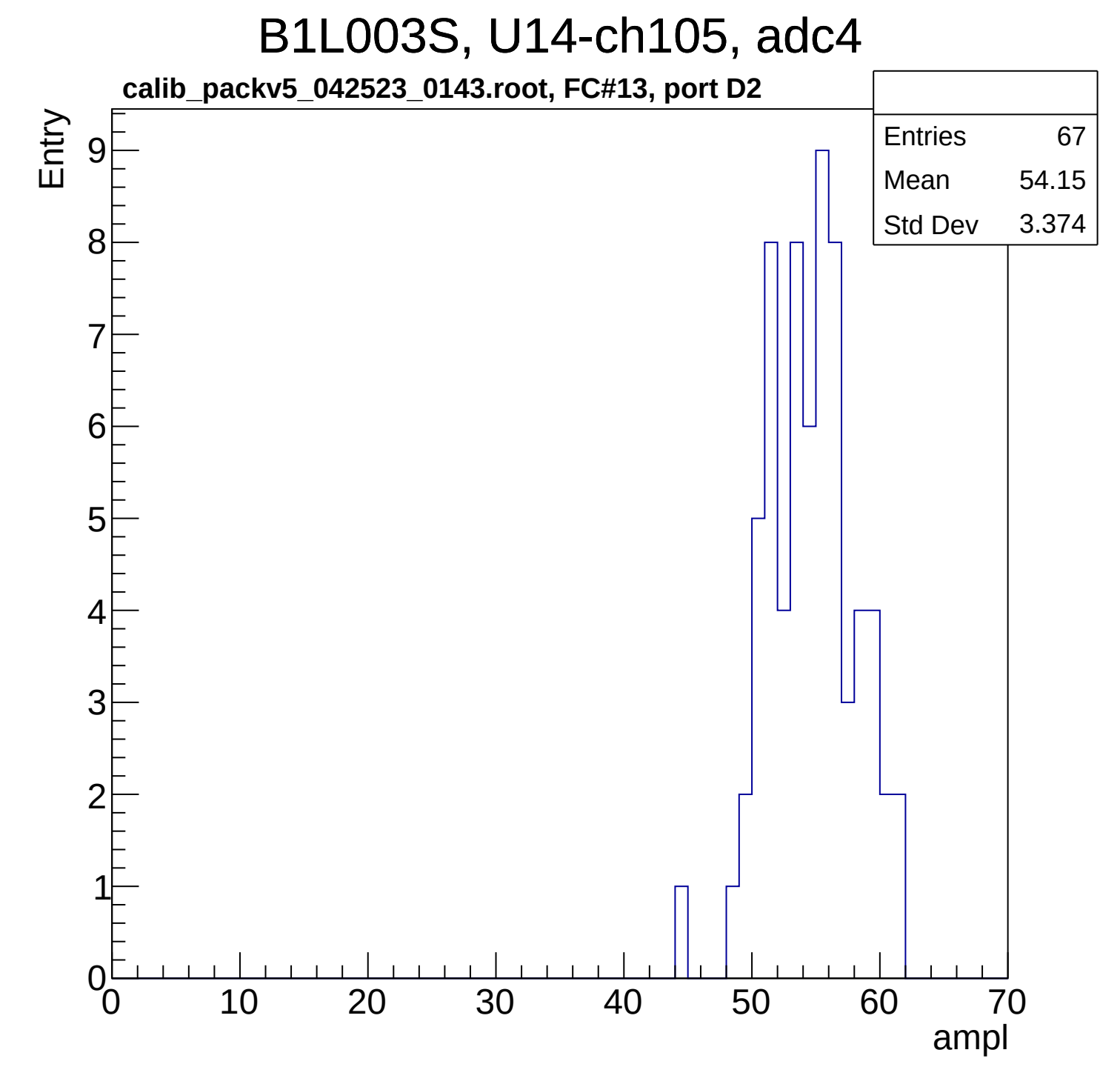

B1L003S, U14-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

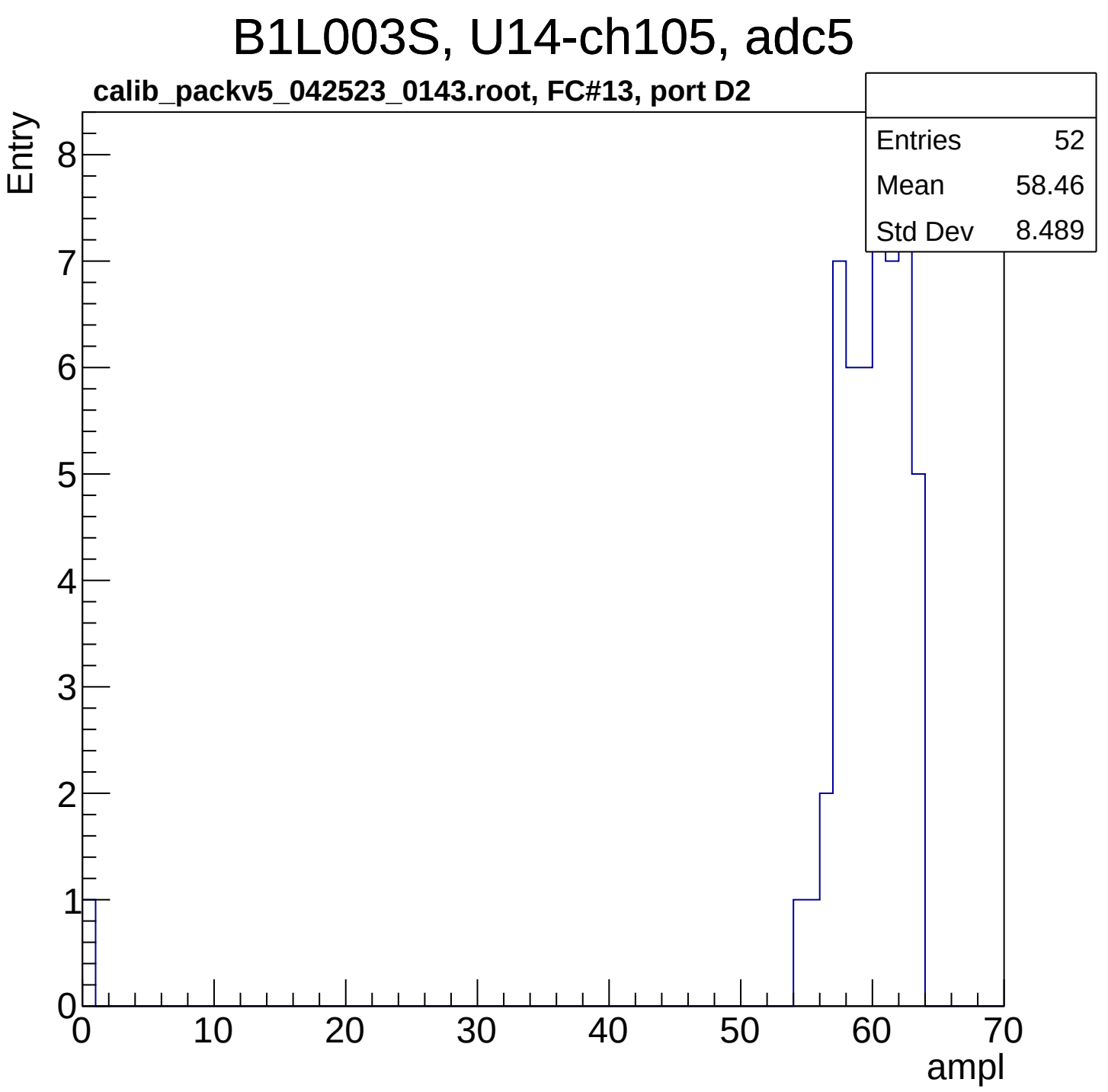

B1L003S, U14-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.3
Std Dev 5.092
Gaus mean : 30.4300
Gaus Width: 3.5153
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

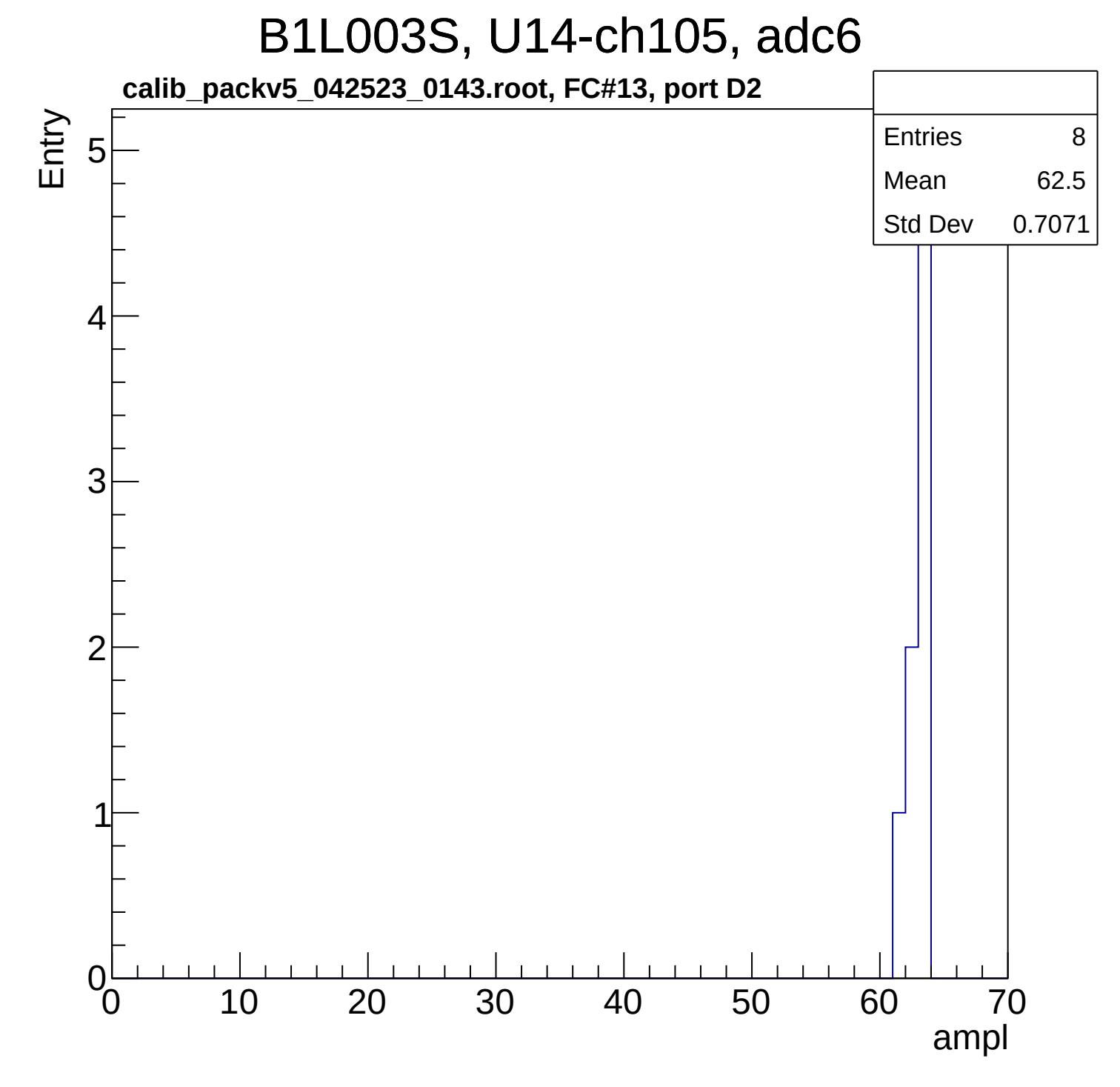

B1L003S, U14-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 35.81
Std Dev 4.148
Gaus mean : 36.3864
Gaus Width: 4.7957
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

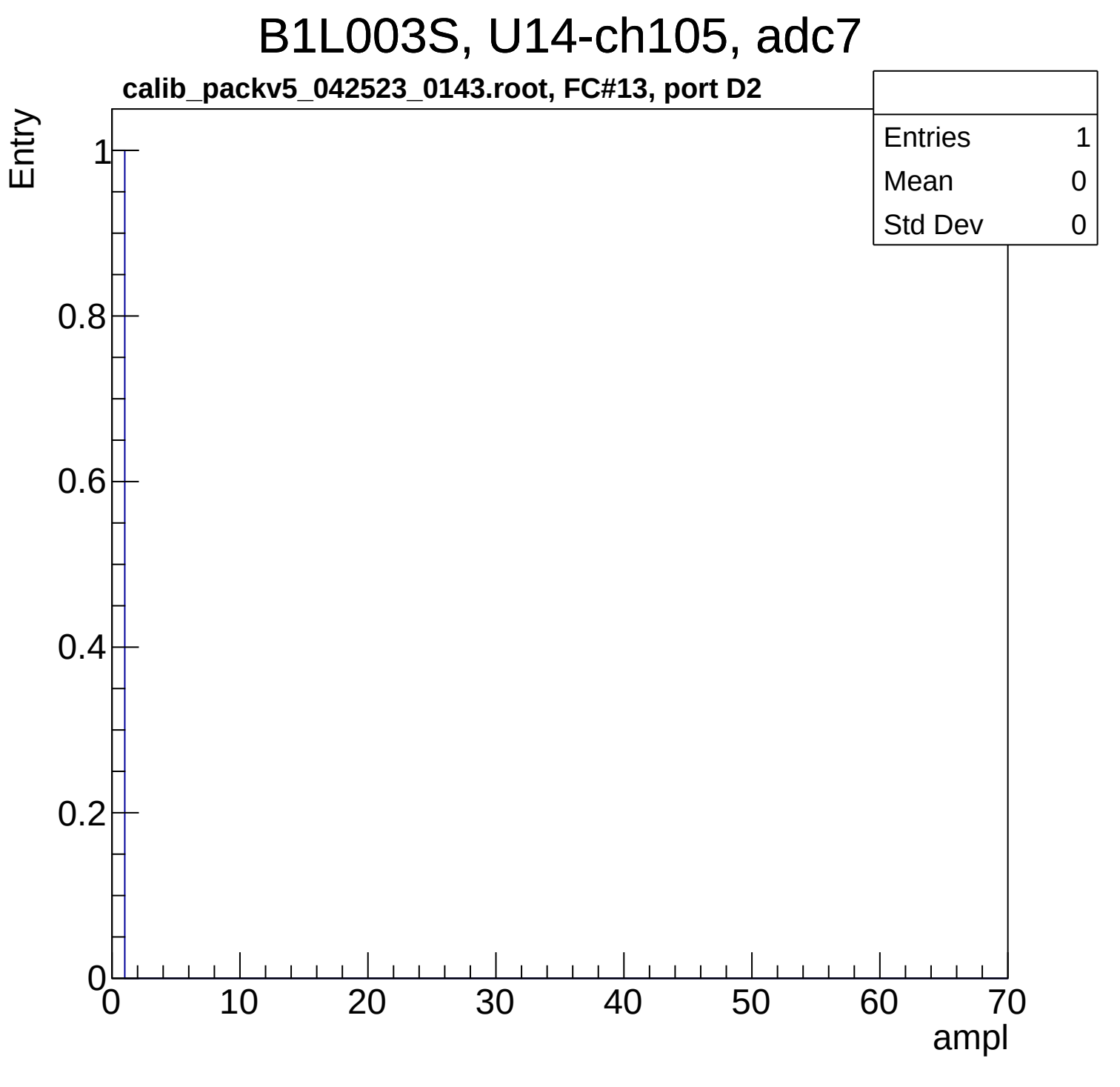

B1L003S, U14-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43
Std Dev 3.454
Gaus mean : 43.5279
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

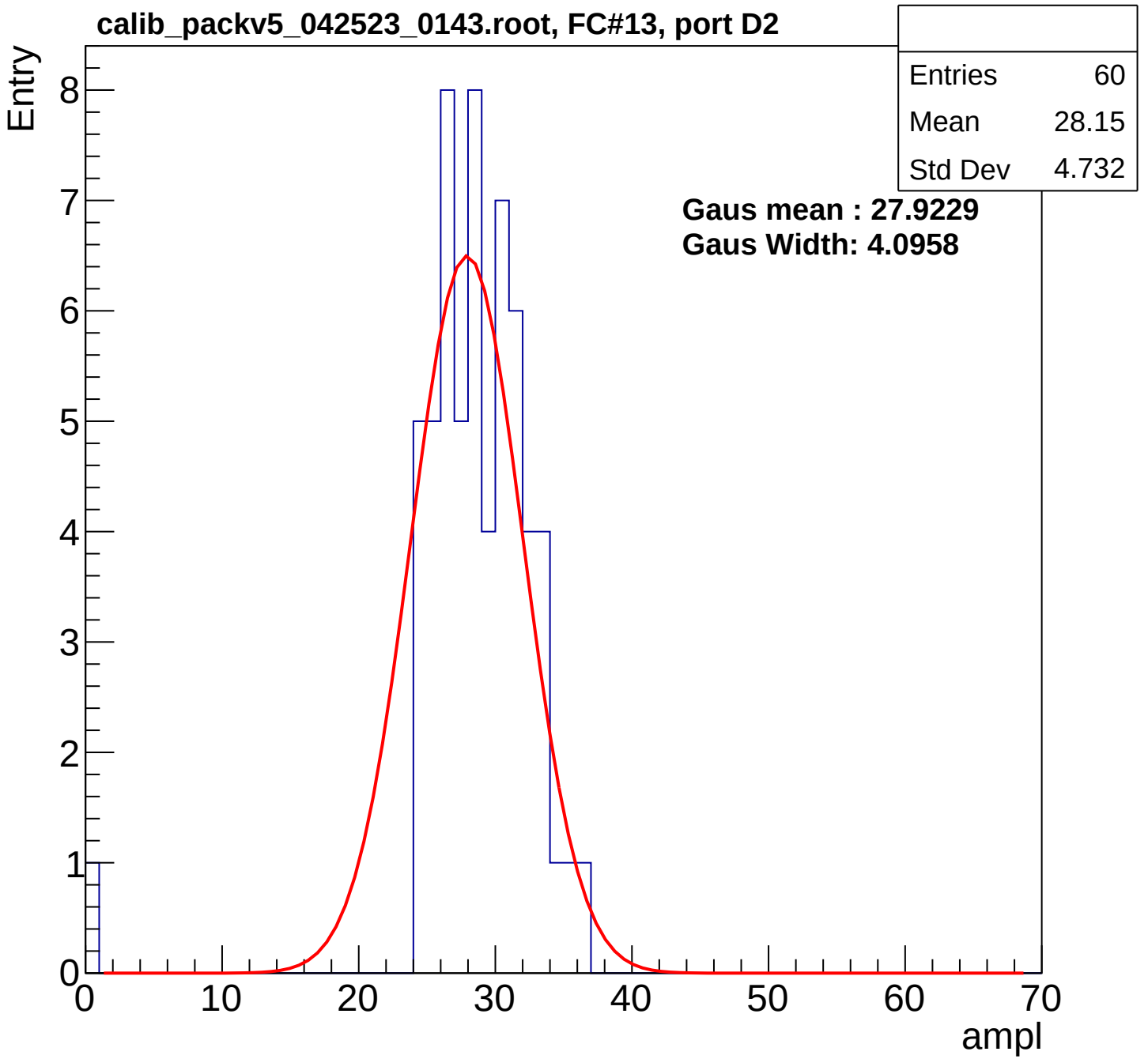

B1L003S, U14-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 48.33
Std Dev 2.812
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

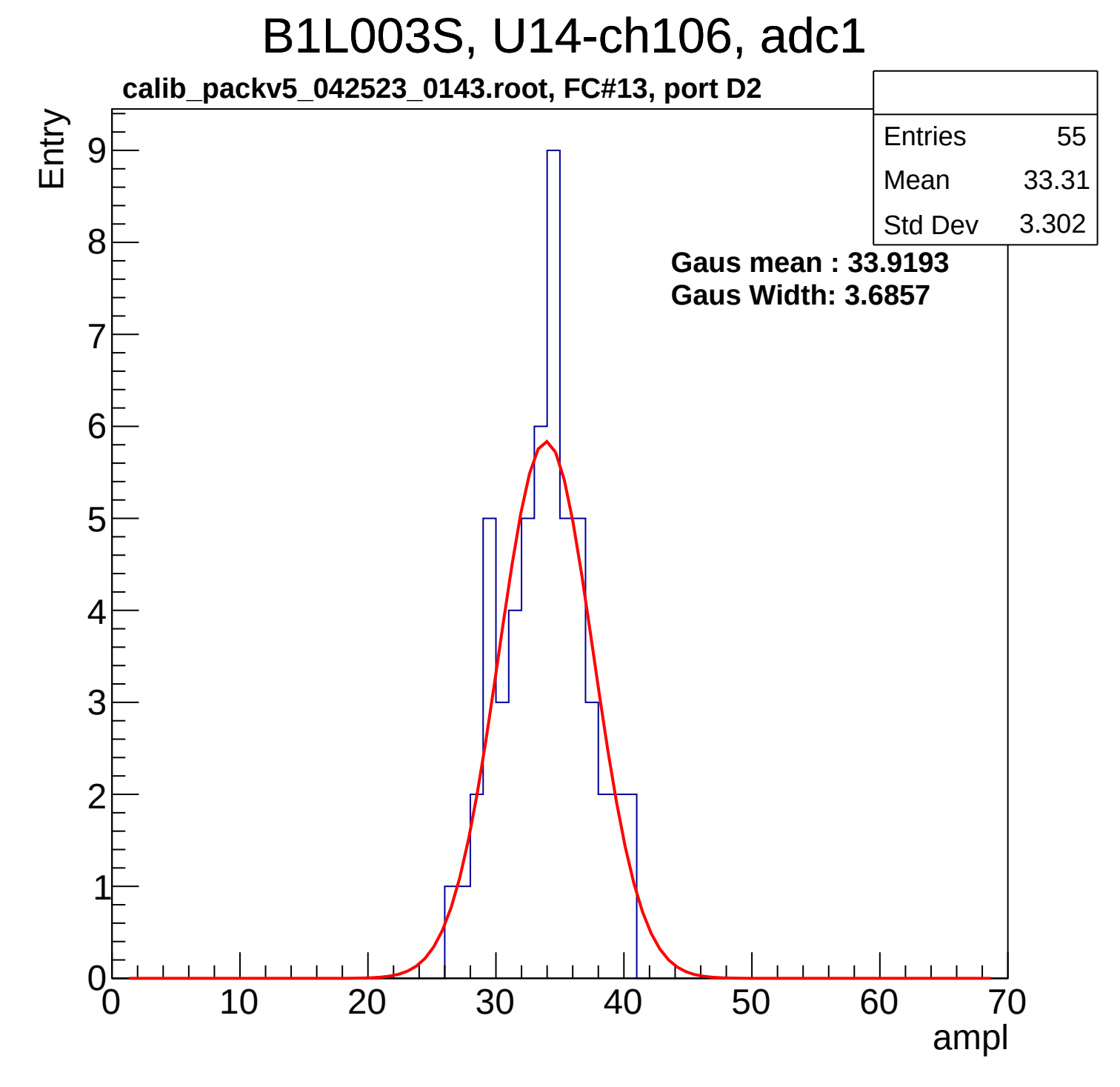

B1L003S, U14-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 54.15
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

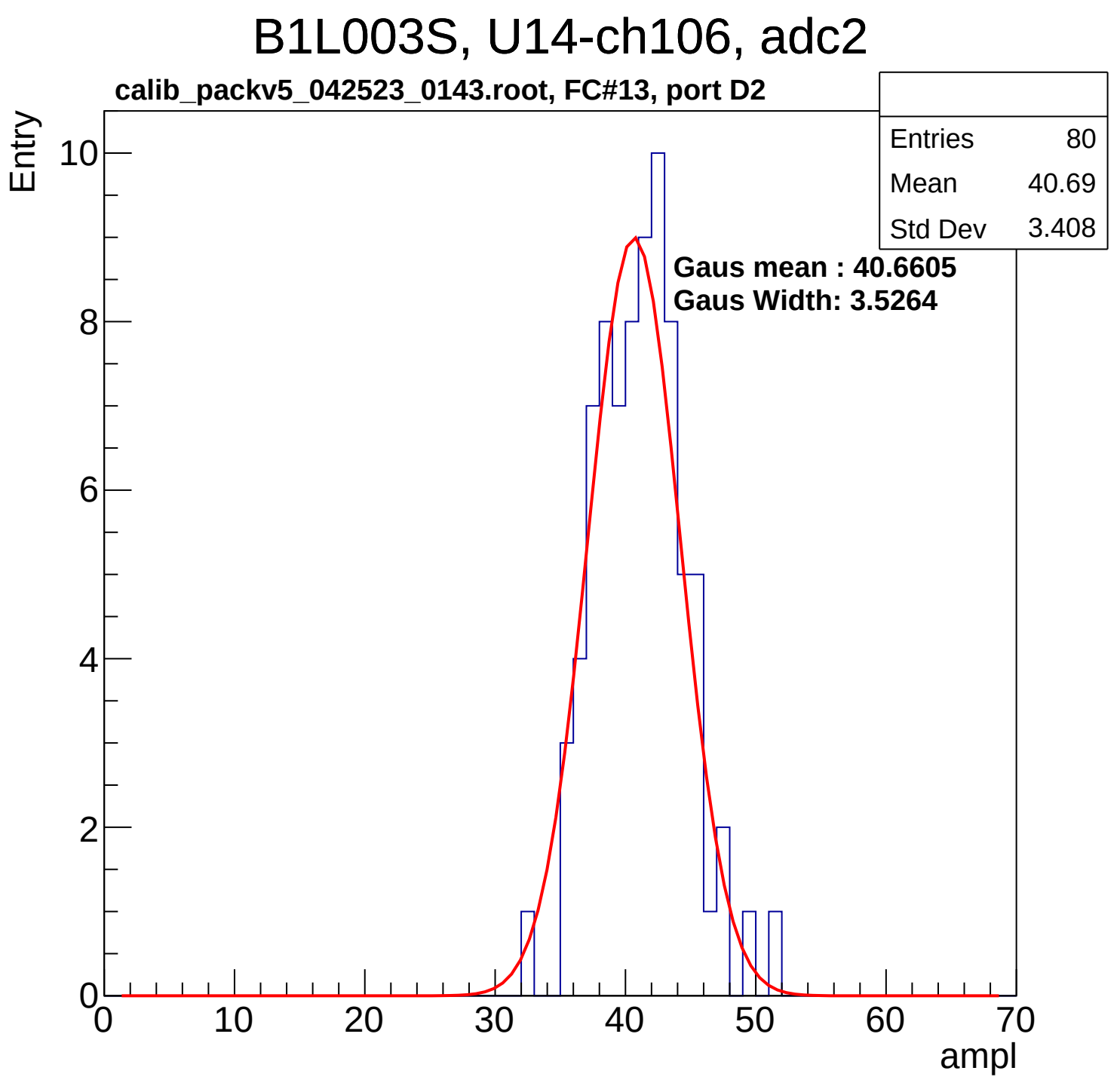

B1L003S, U14-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.46
Std Dev 8.489
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

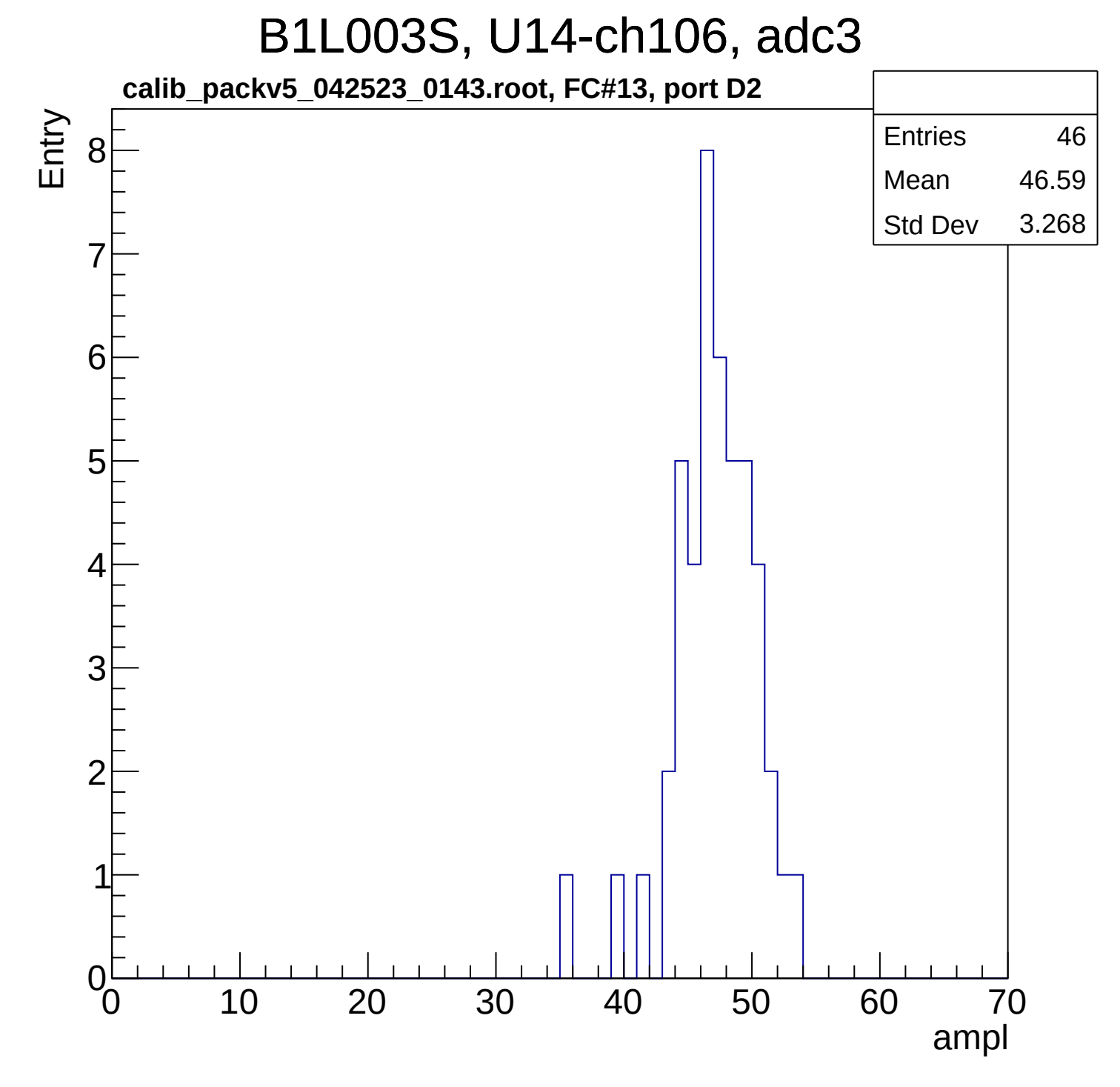

B1L003S, U14-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

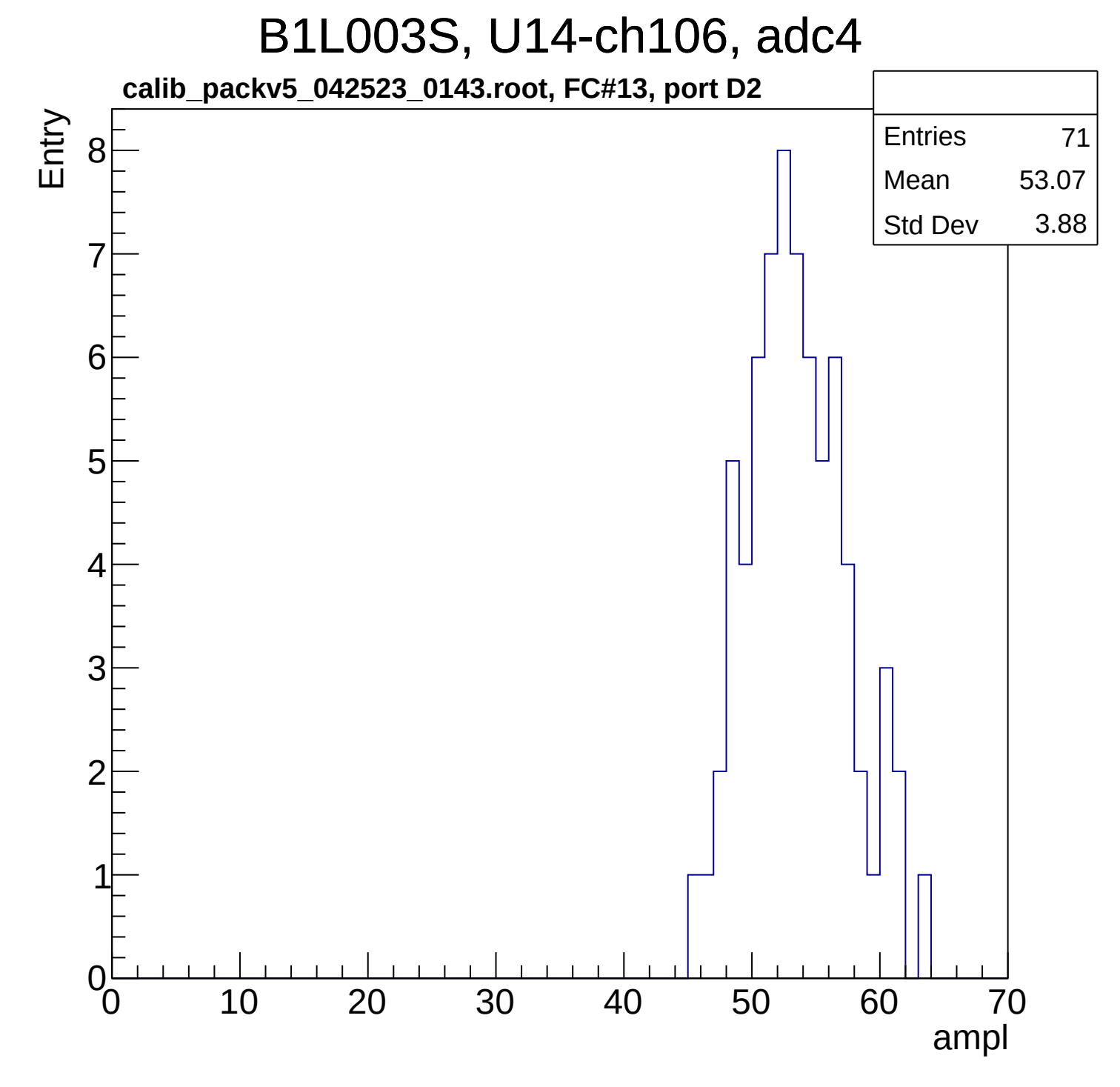

B1L003S, U14-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

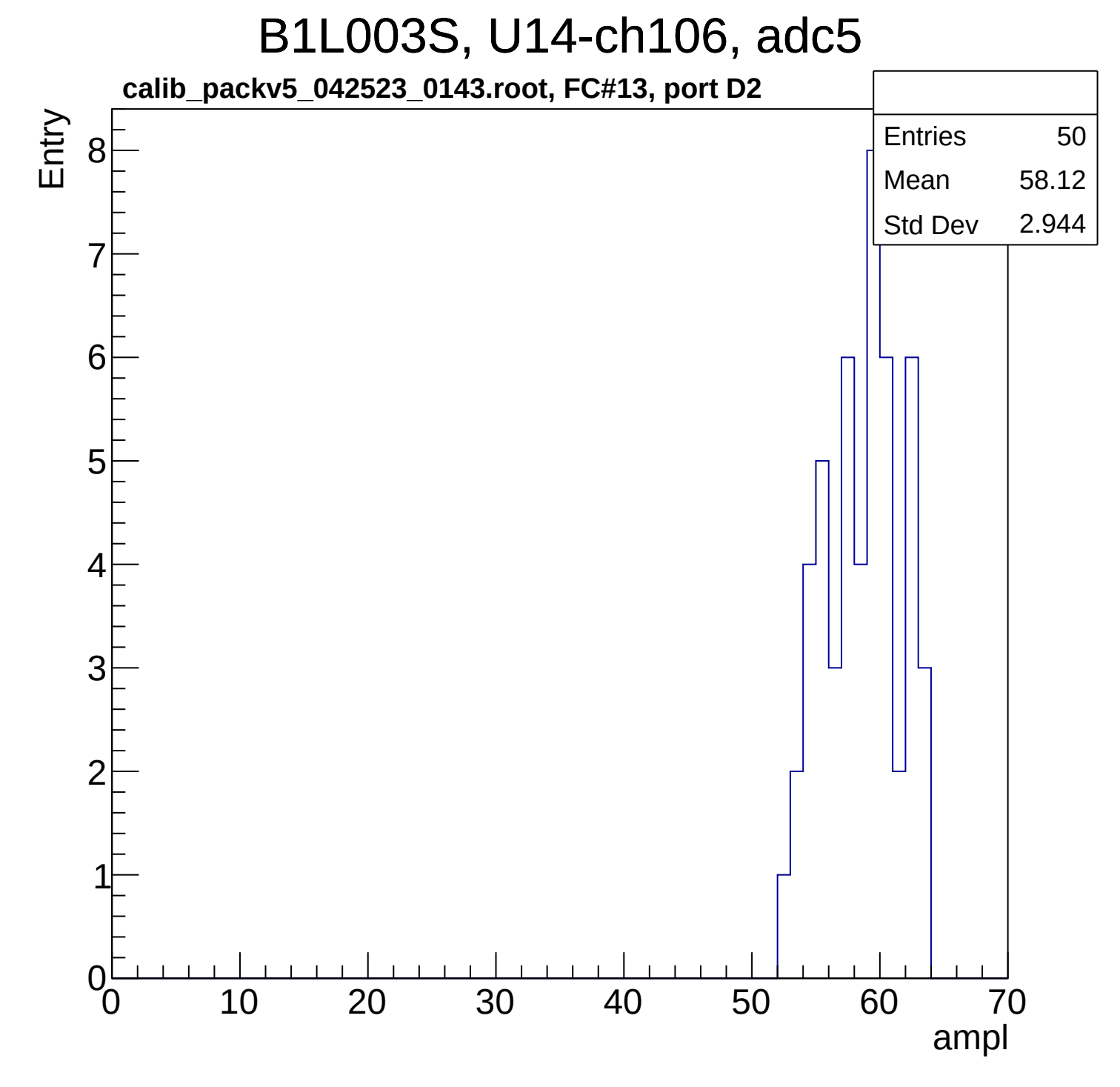

B1L003S, U14-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.15
Std Dev 4.732
Gaus mean : 27.9229
Gaus Width: 4.0958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

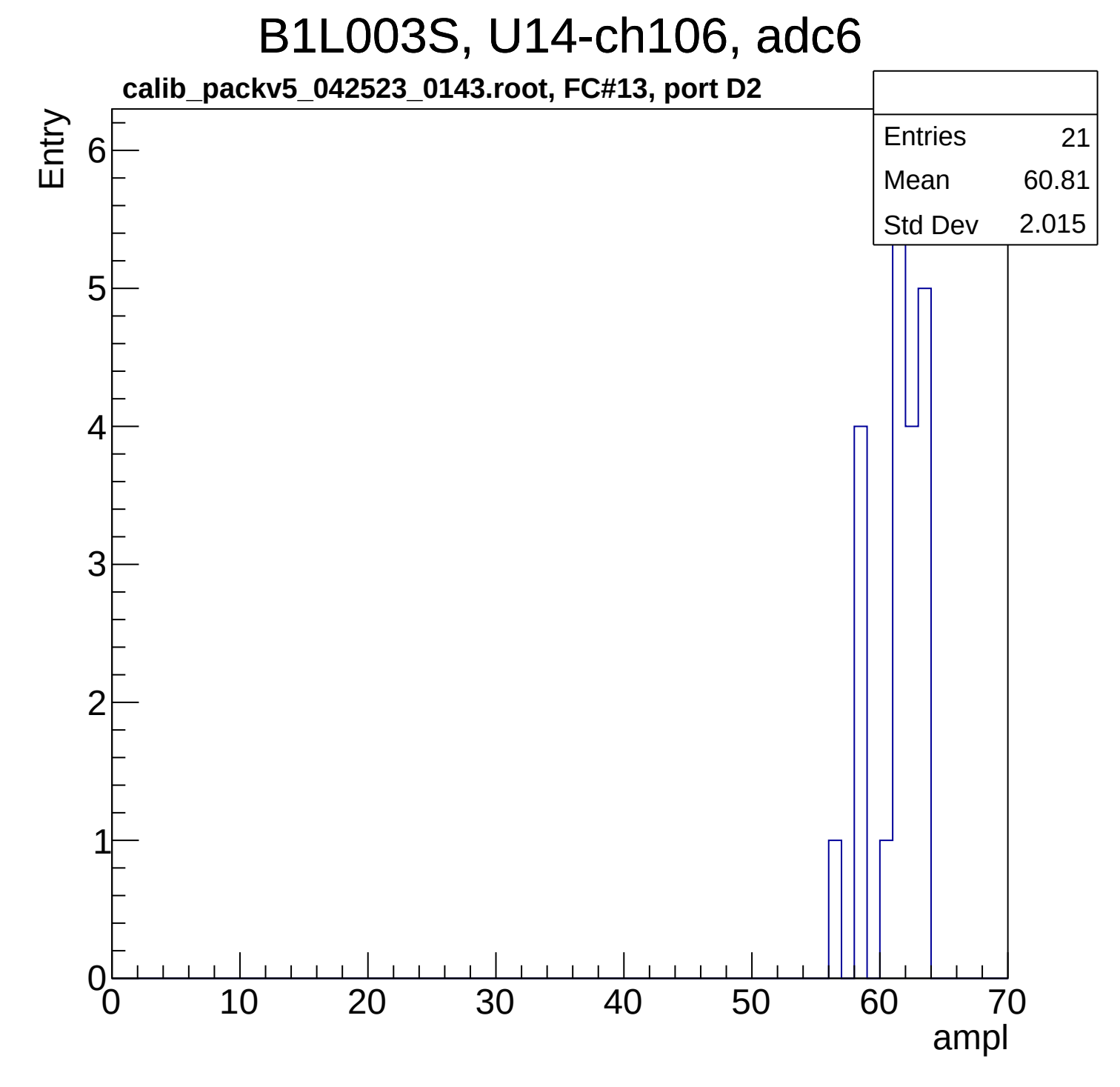

B1L003S, U14-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 33.31
Std Dev 3.302
Gaus mean : 33.9193
Gaus Width: 3.6857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

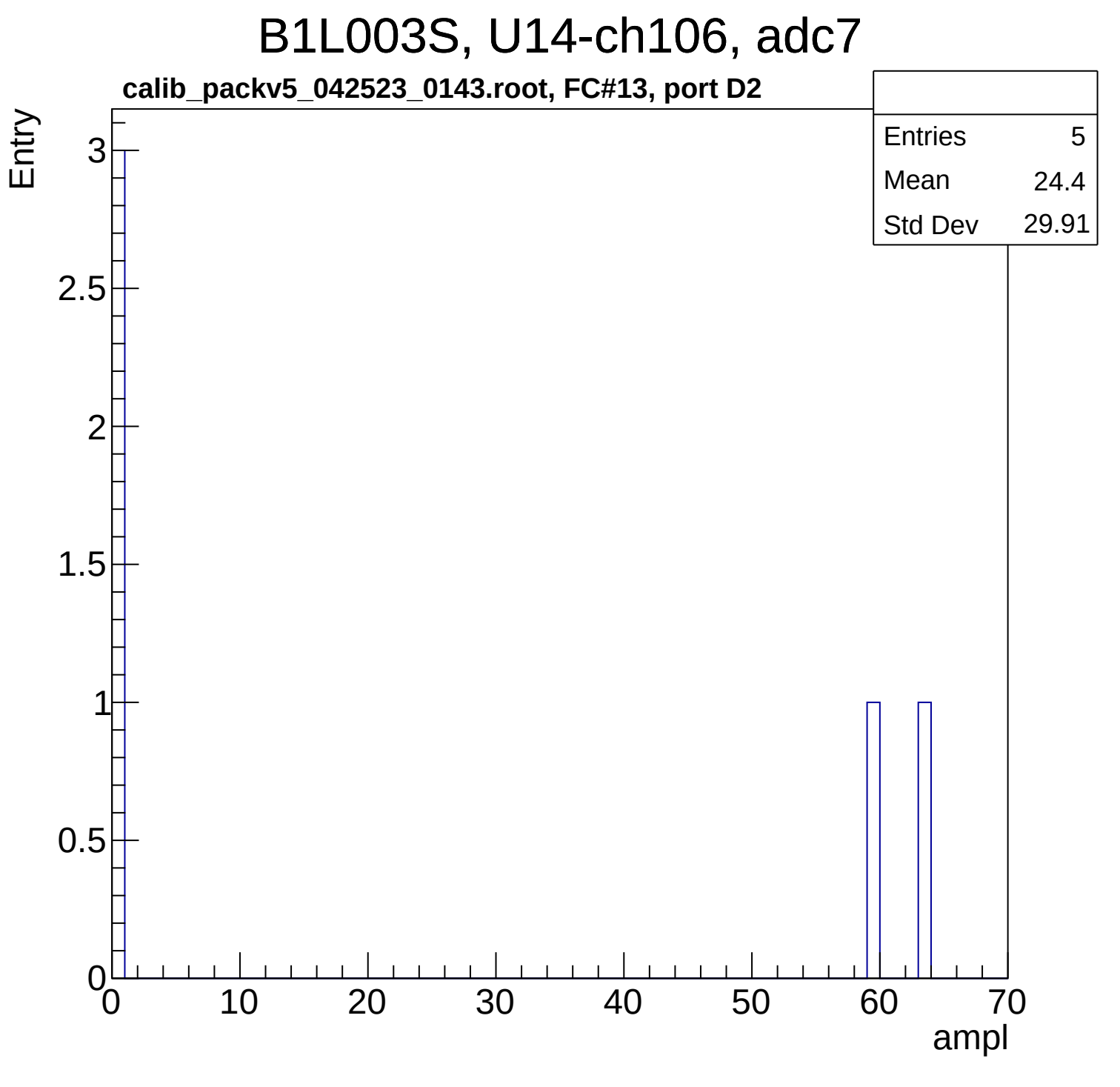

B1L003S, U14-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 40.69
Std Dev 3.408
Gaus mean : 40.6605
Gaus Width: 3.5264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

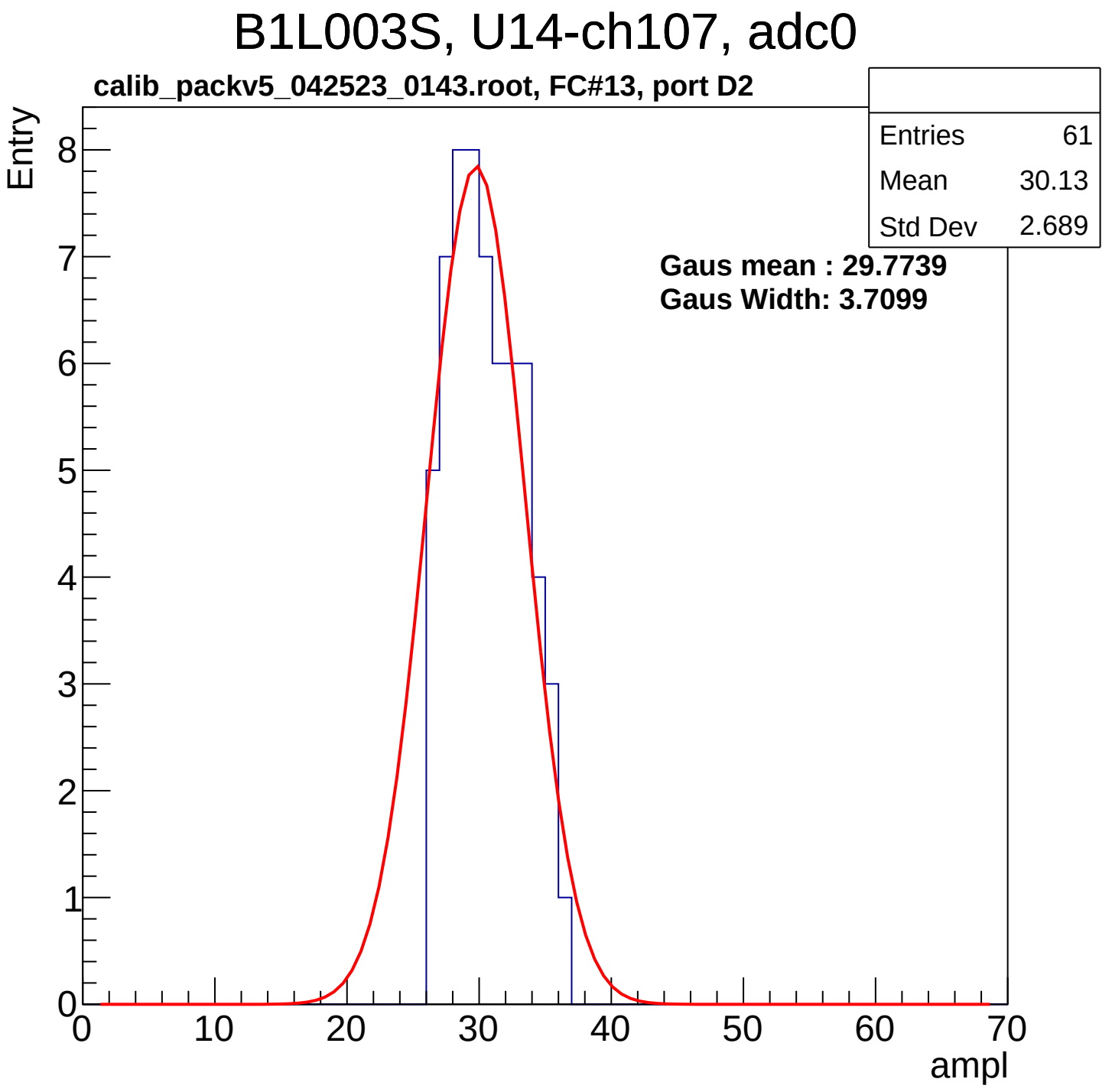

B1L003S, U14-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 46.59
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

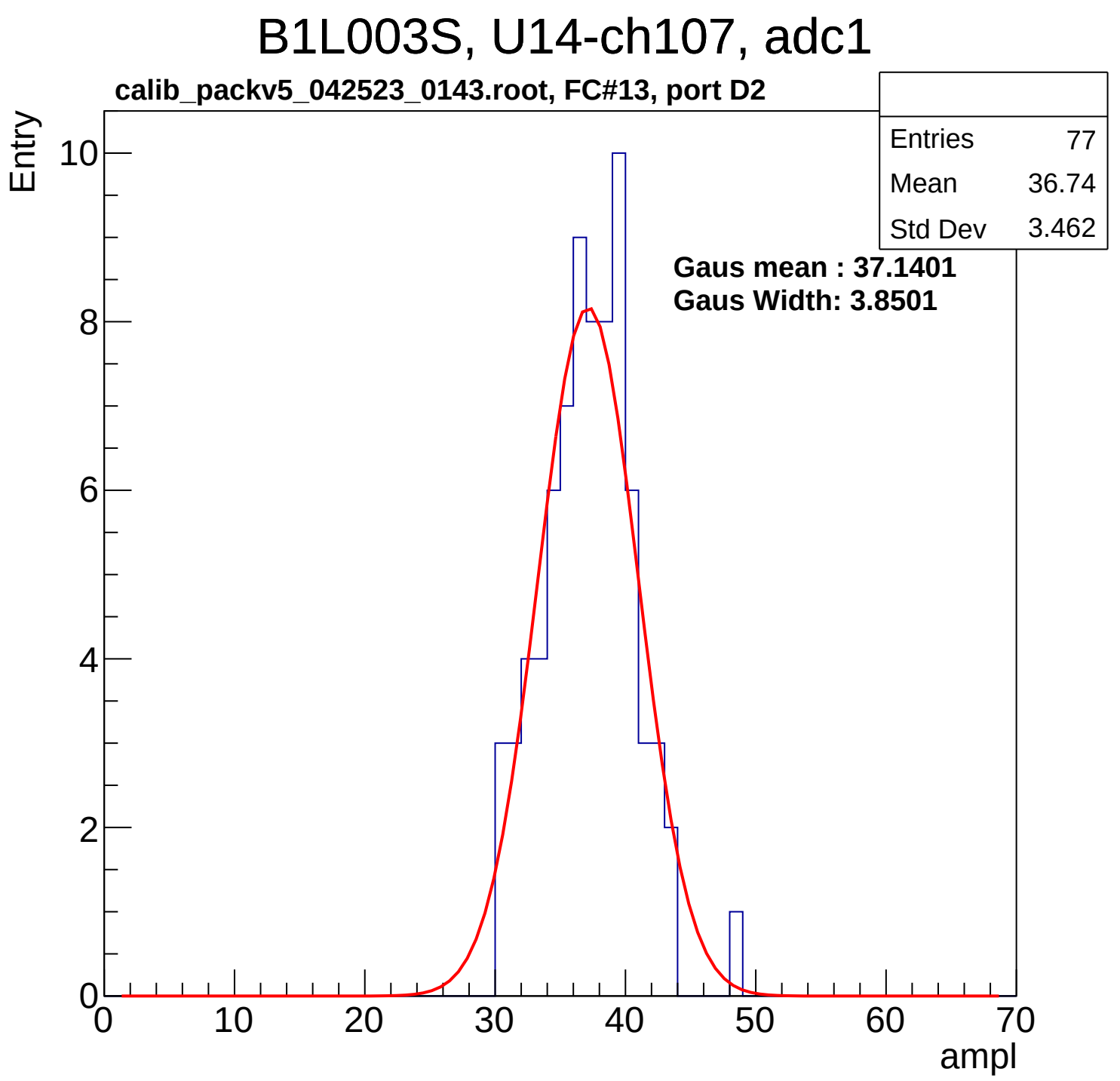

B1L003S, U14-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 53.07
Std Dev 3.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

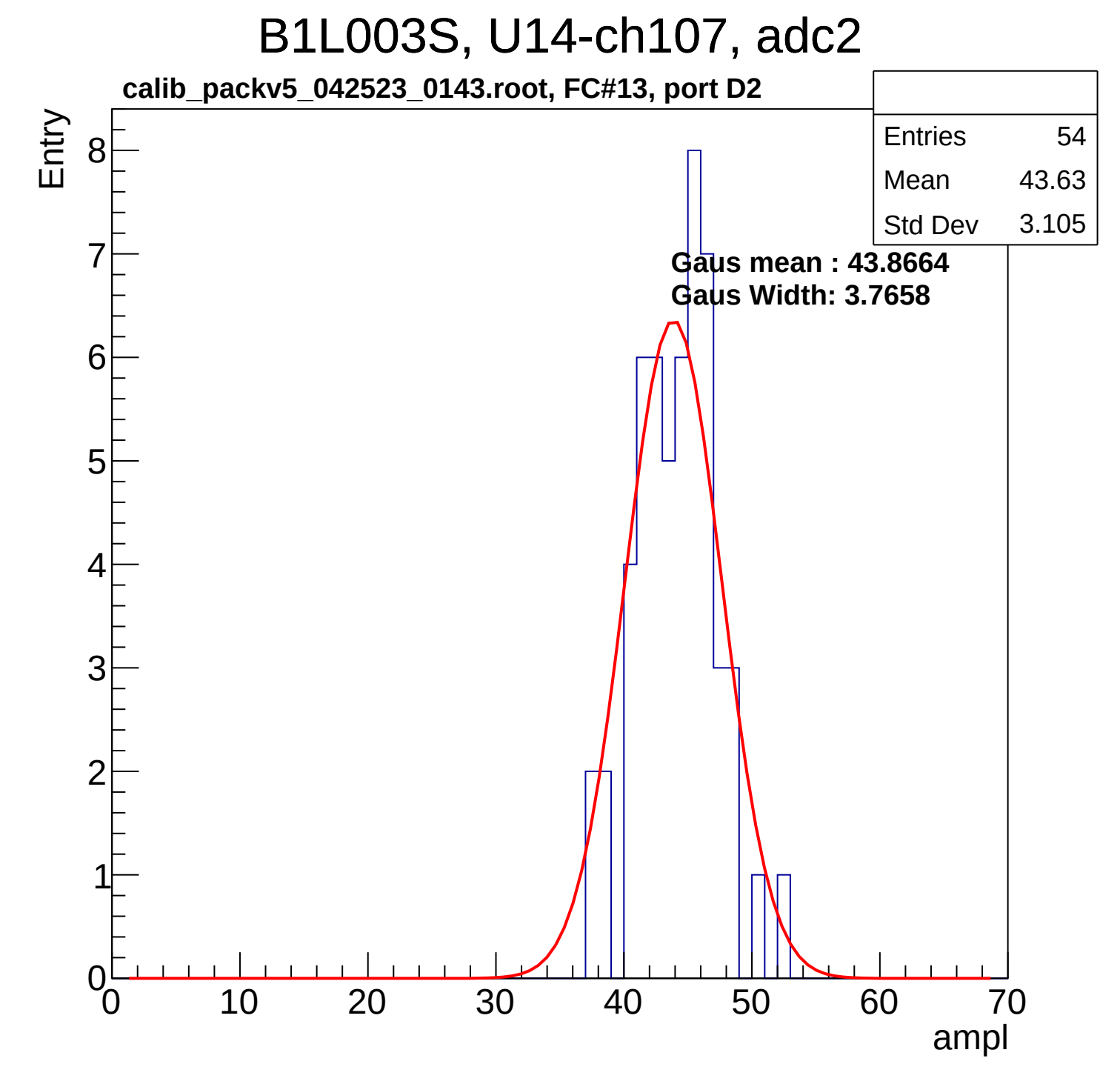

B1L003S, U14-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.12
Std Dev 2.944
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

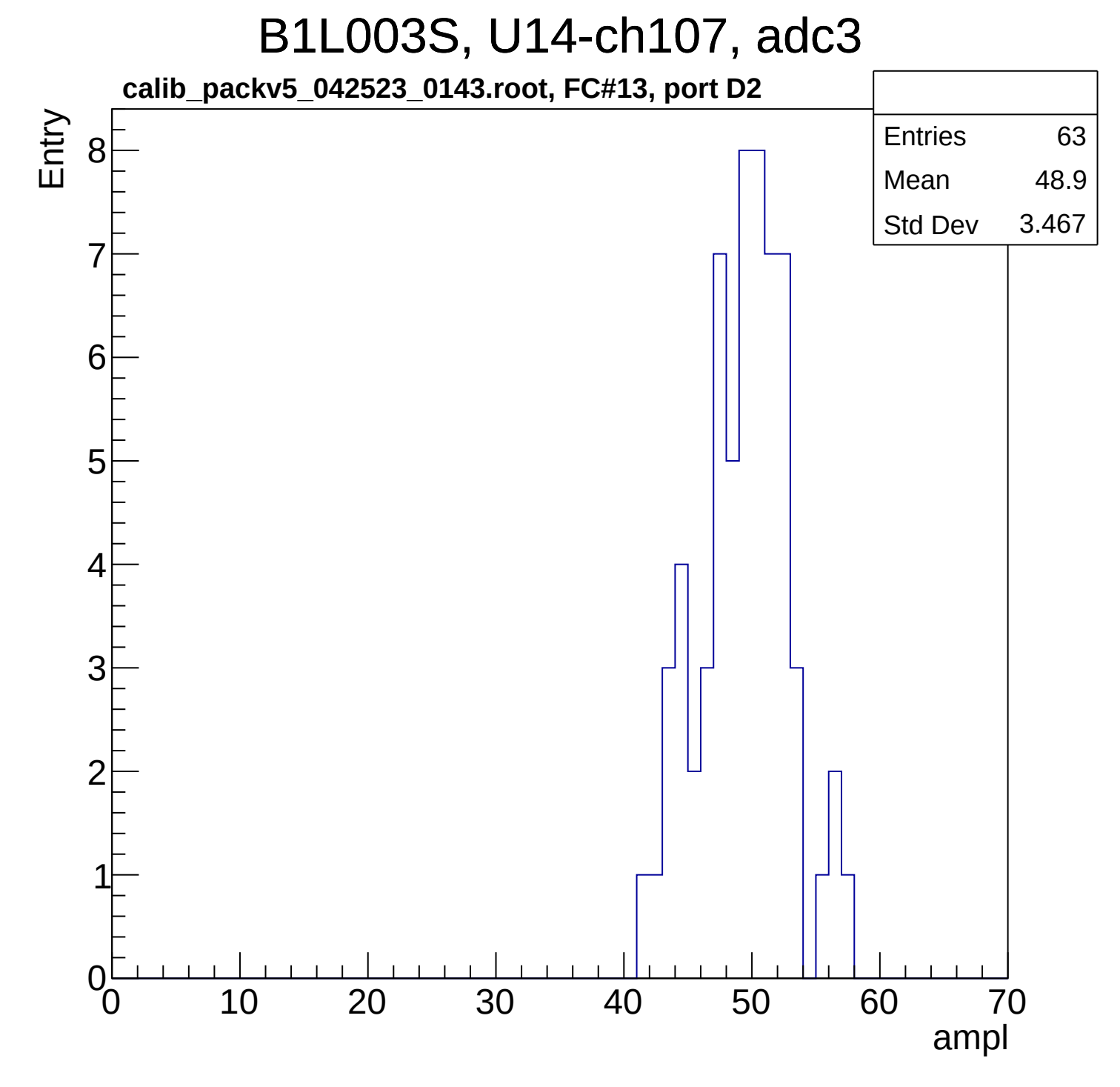

B1L003S, U14-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 60.81
Std Dev 2.015
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

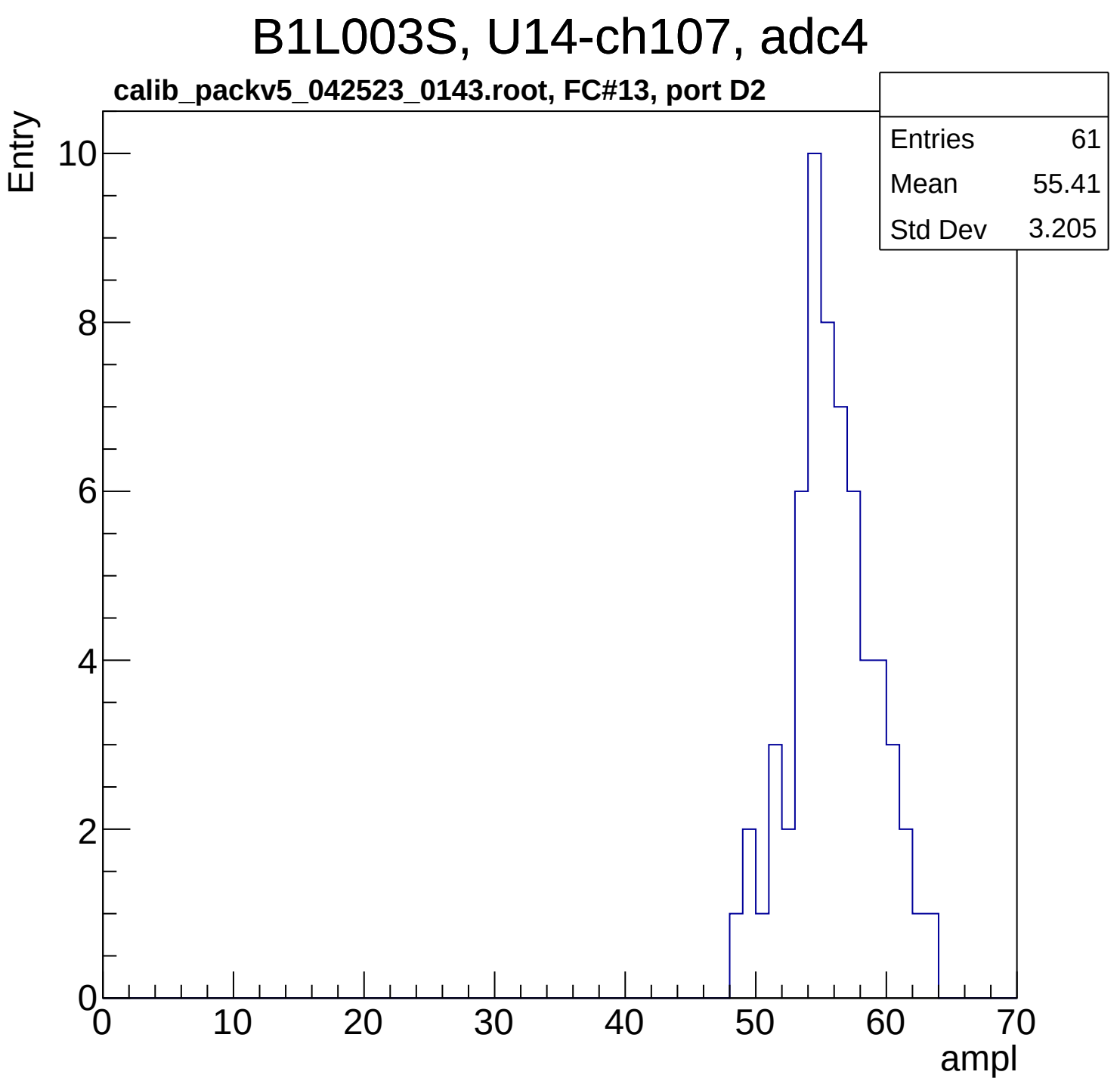

B1L003S, U14-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 24.4
Std Dev 29.91
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

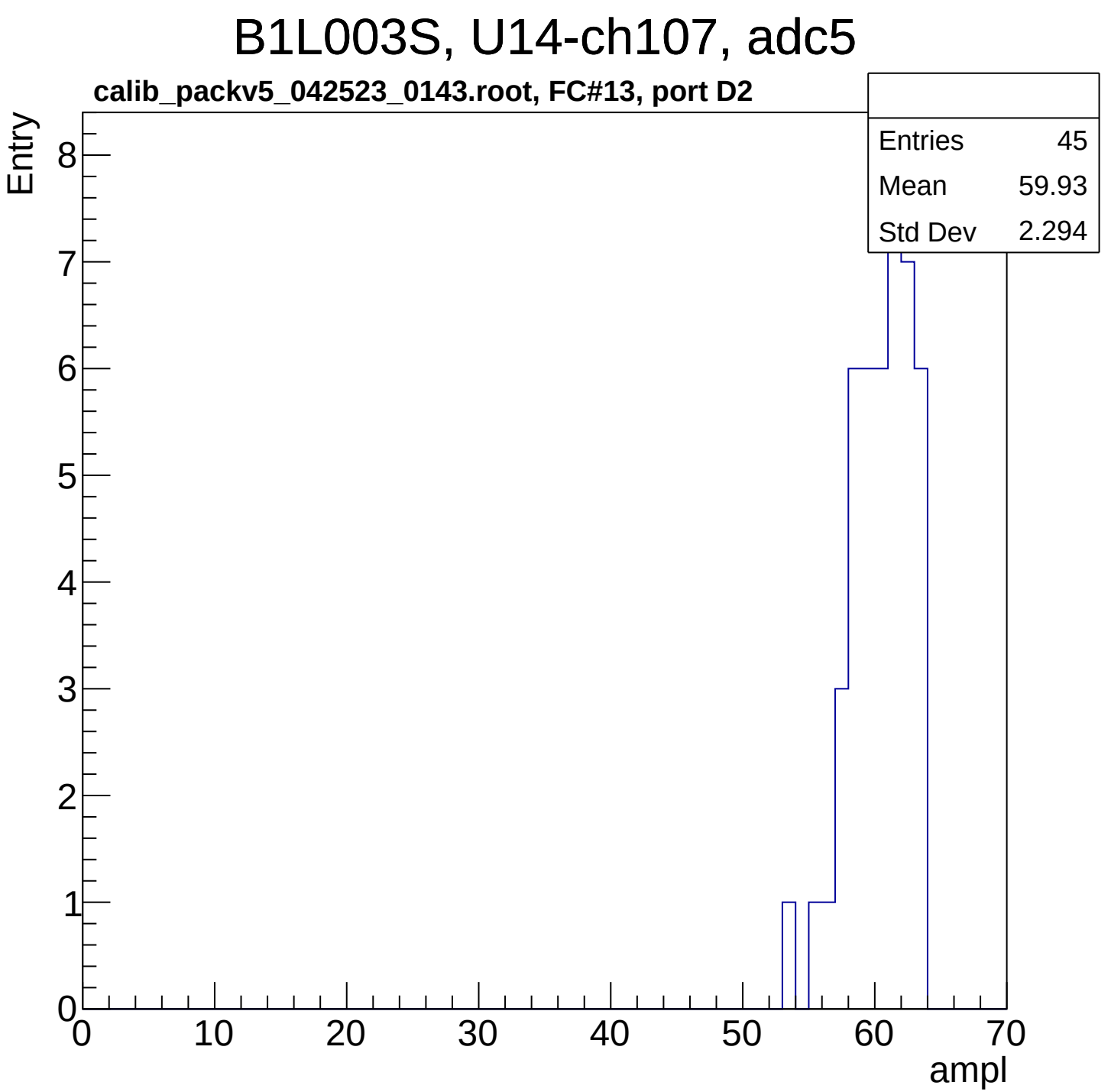

B1L003S, U14-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 30.13
Std Dev 2.689
Gaus mean : 29.7739
Gaus Width: 3.7099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

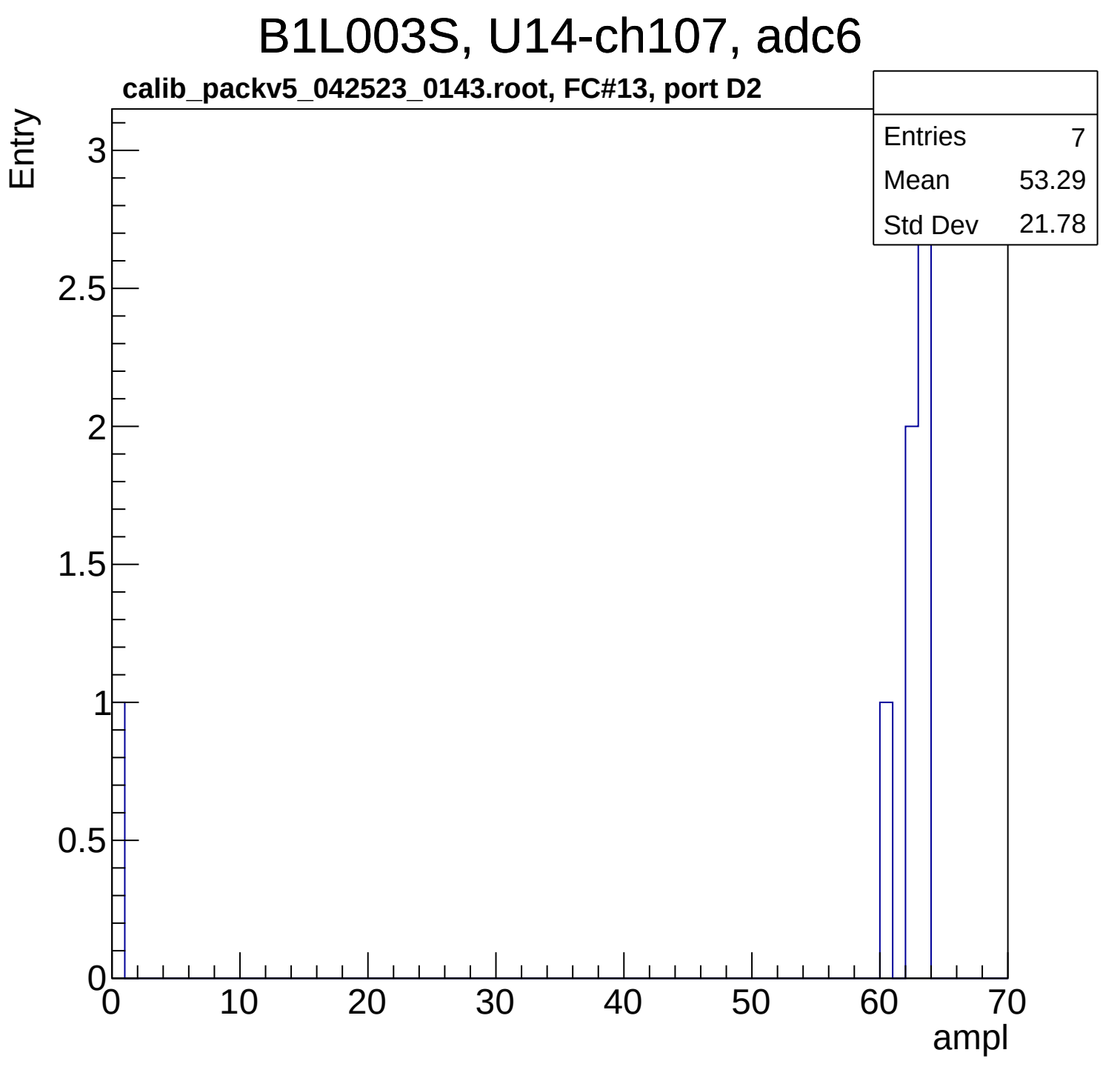

B1L003S, U14-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.74
Std Dev 3.462
Gaus mean : 37.1401
Gaus Width: 3.8501
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

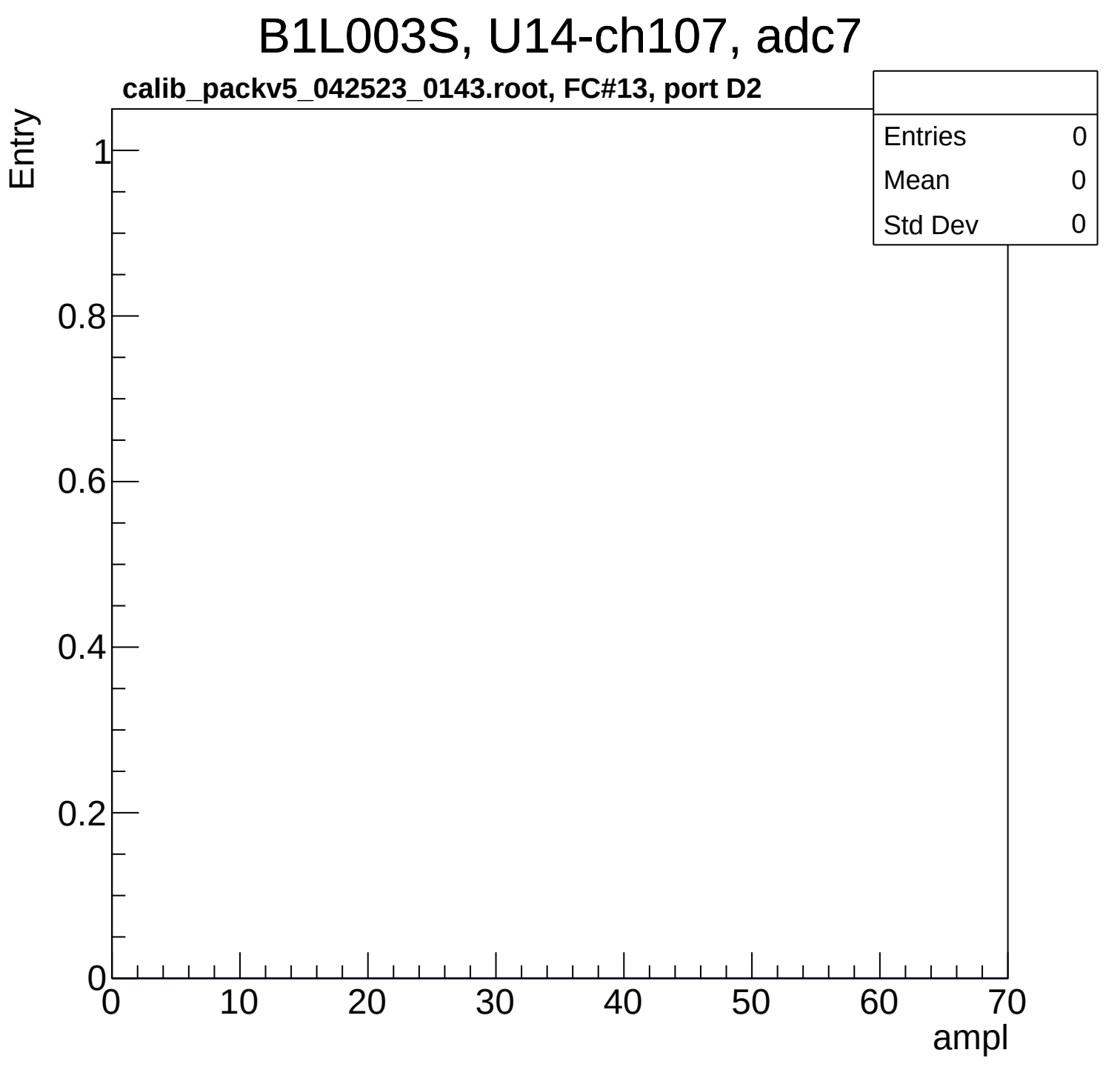

B1L003S, U14-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.63
Std Dev 3.105
Gaus mean : 43.8664
Gaus Width: 3.7658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

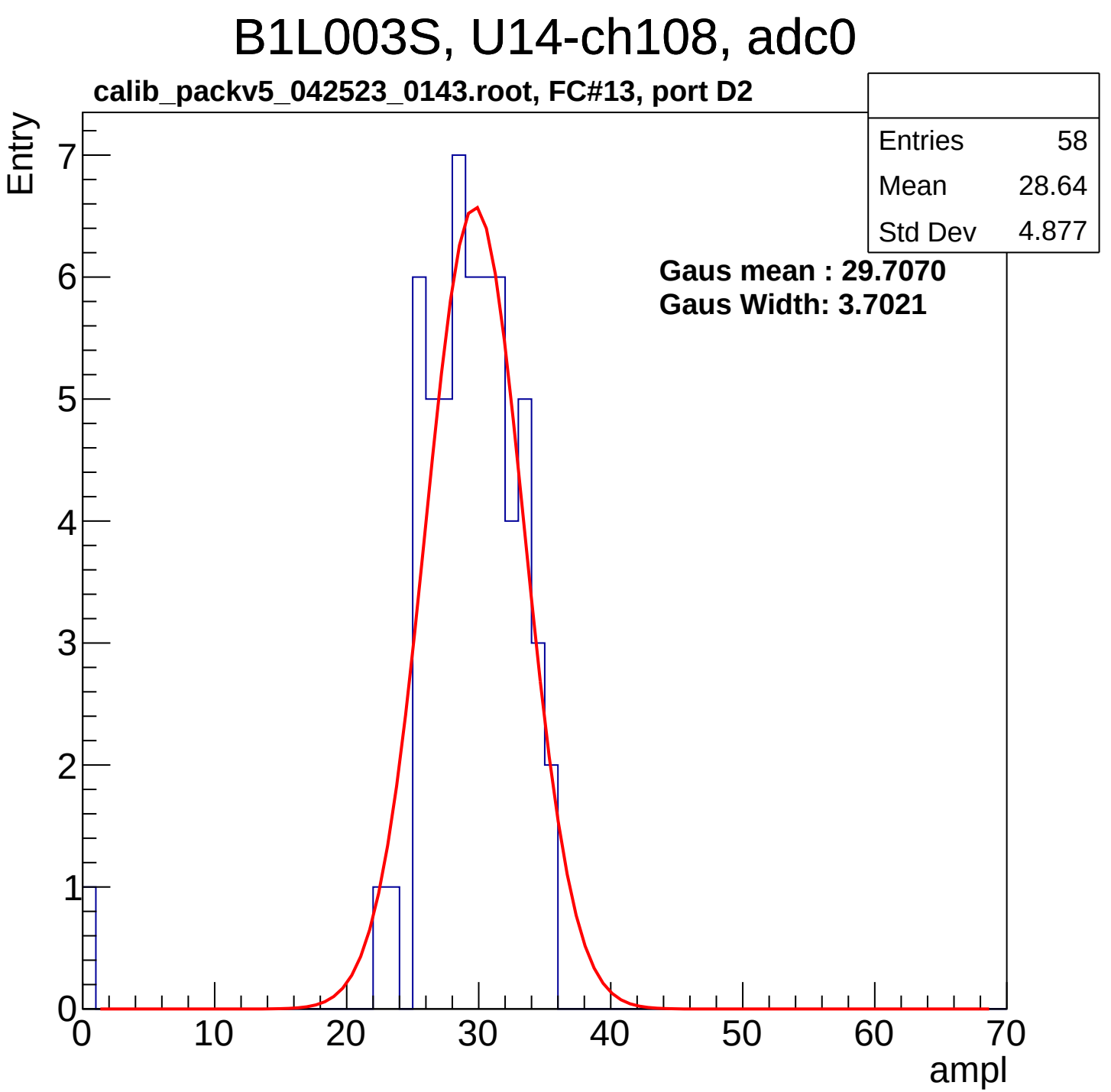

B1L003S, U14-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.9
Std Dev 3.467
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

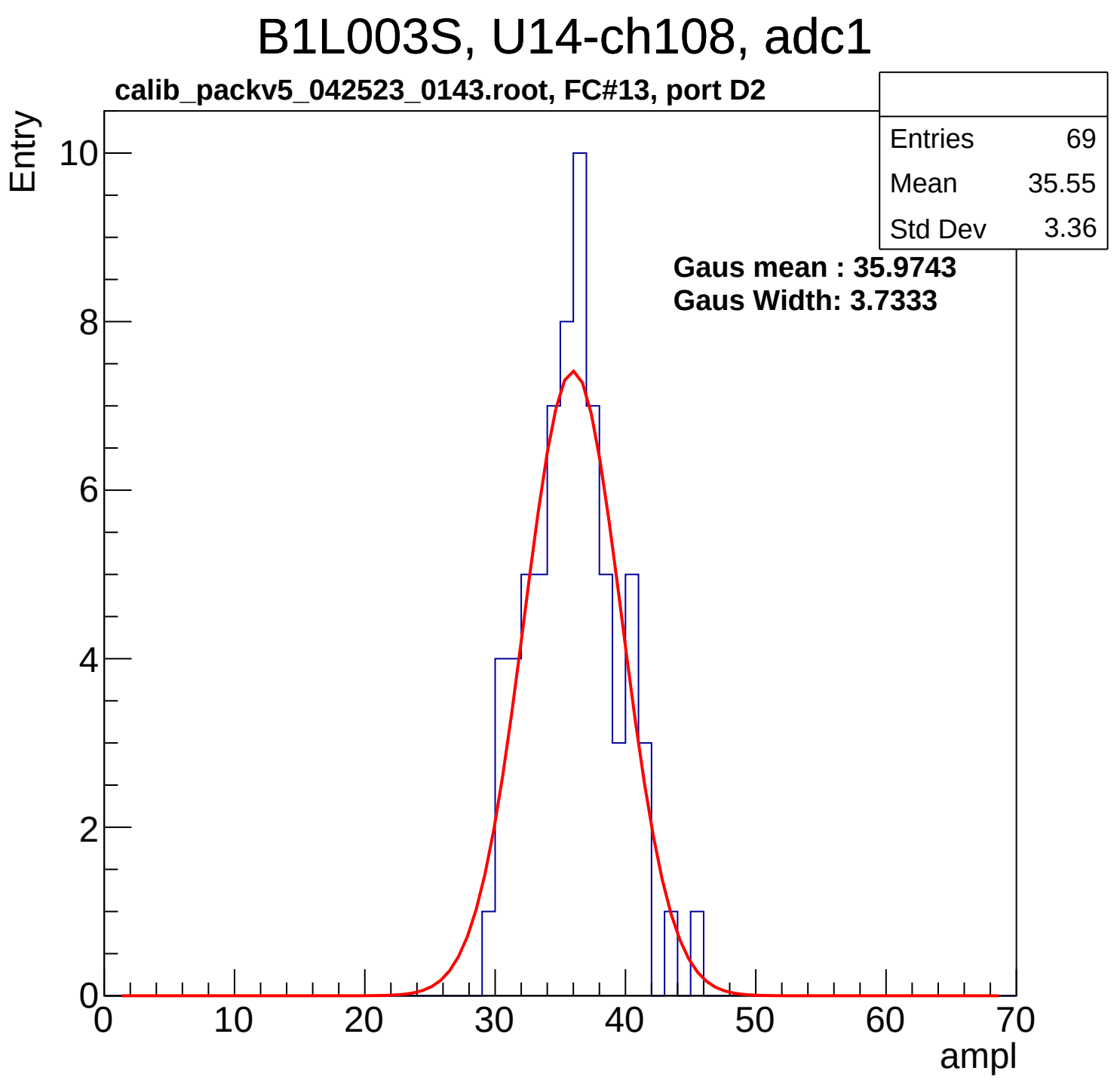

B1L003S, U14-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.41
Std Dev 3.205
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

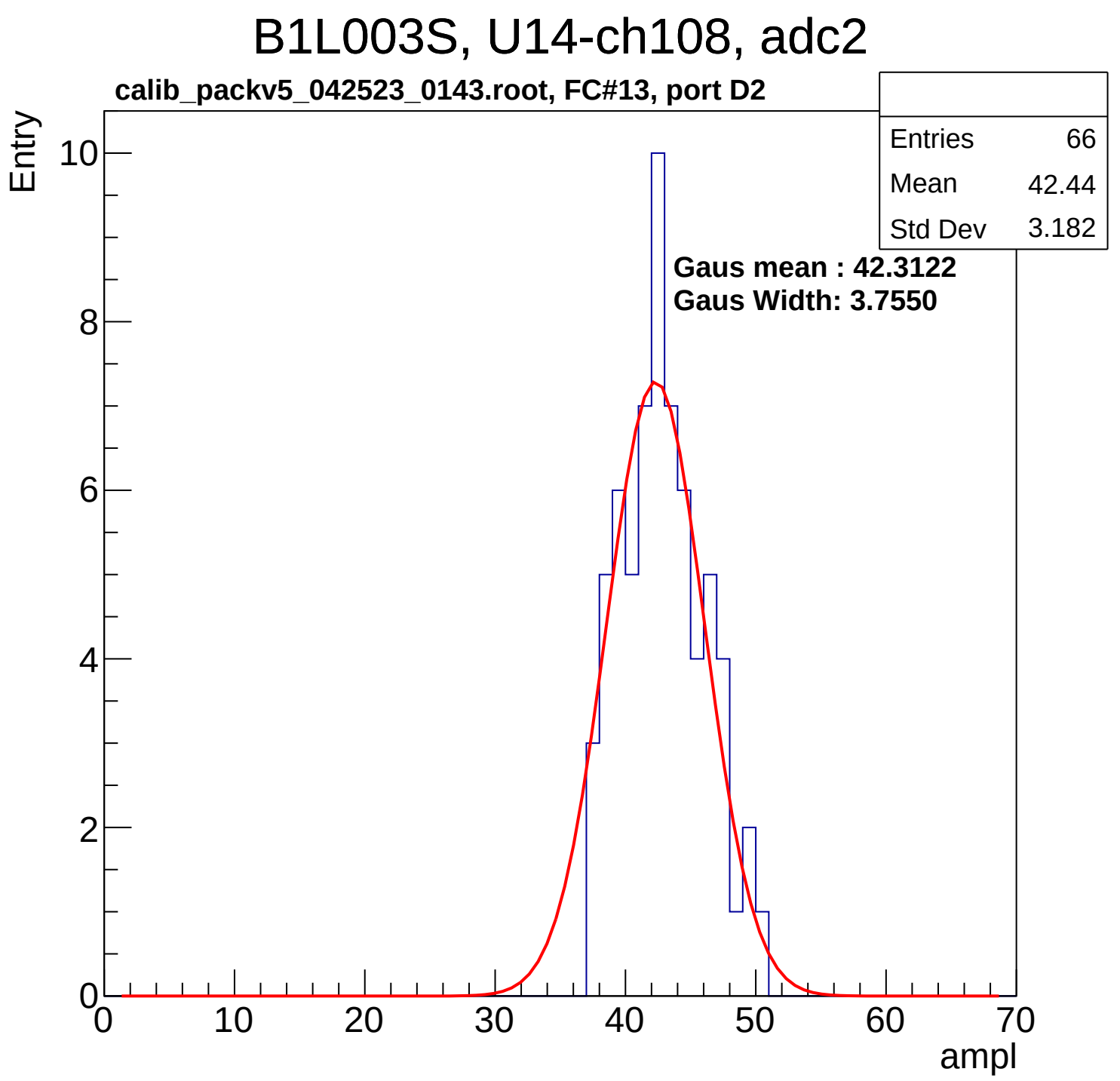

B1L003S, U14-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.93
Std Dev 2.294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

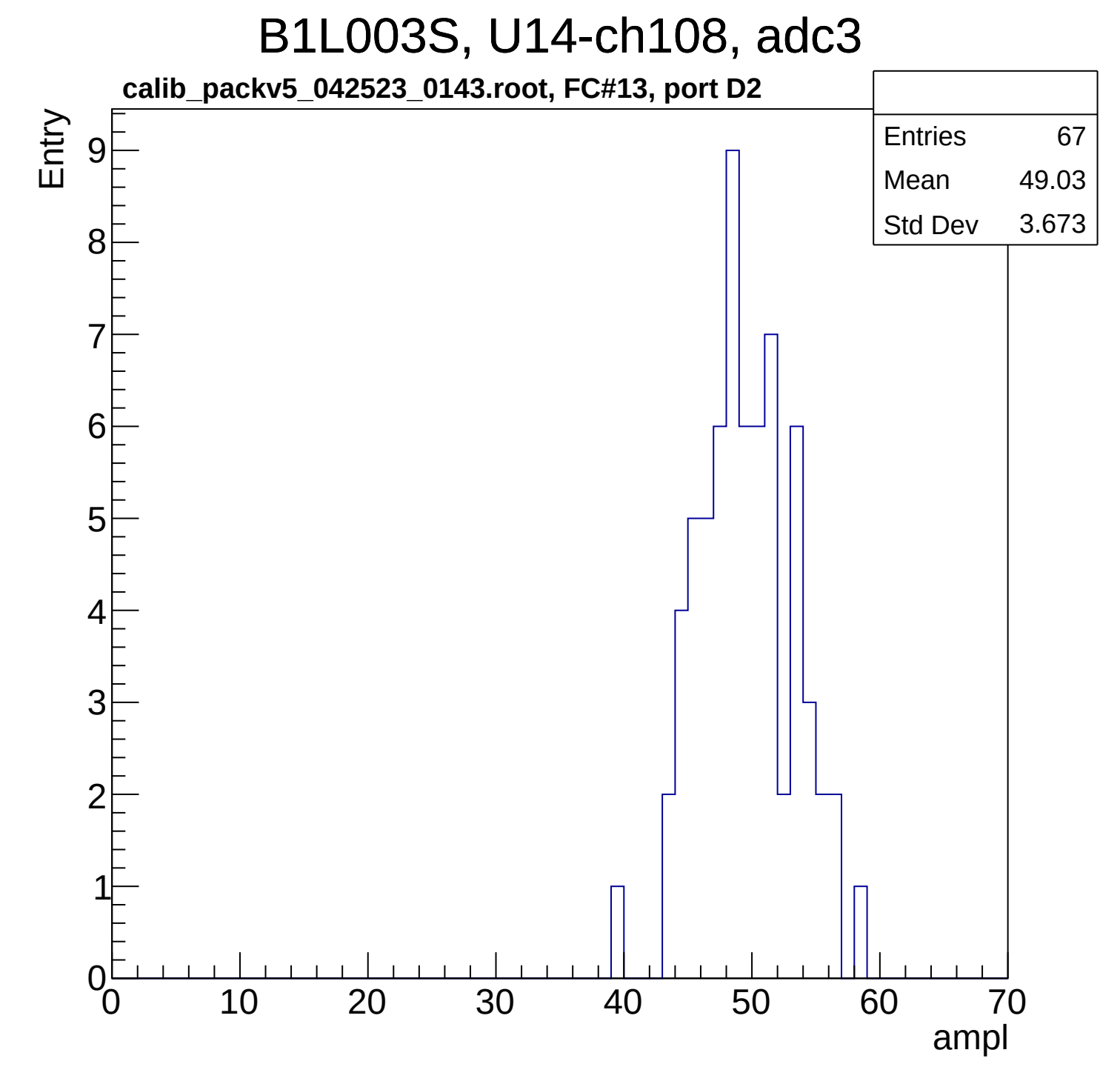

B1L003S, U14-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.29
Std Dev 21.78
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

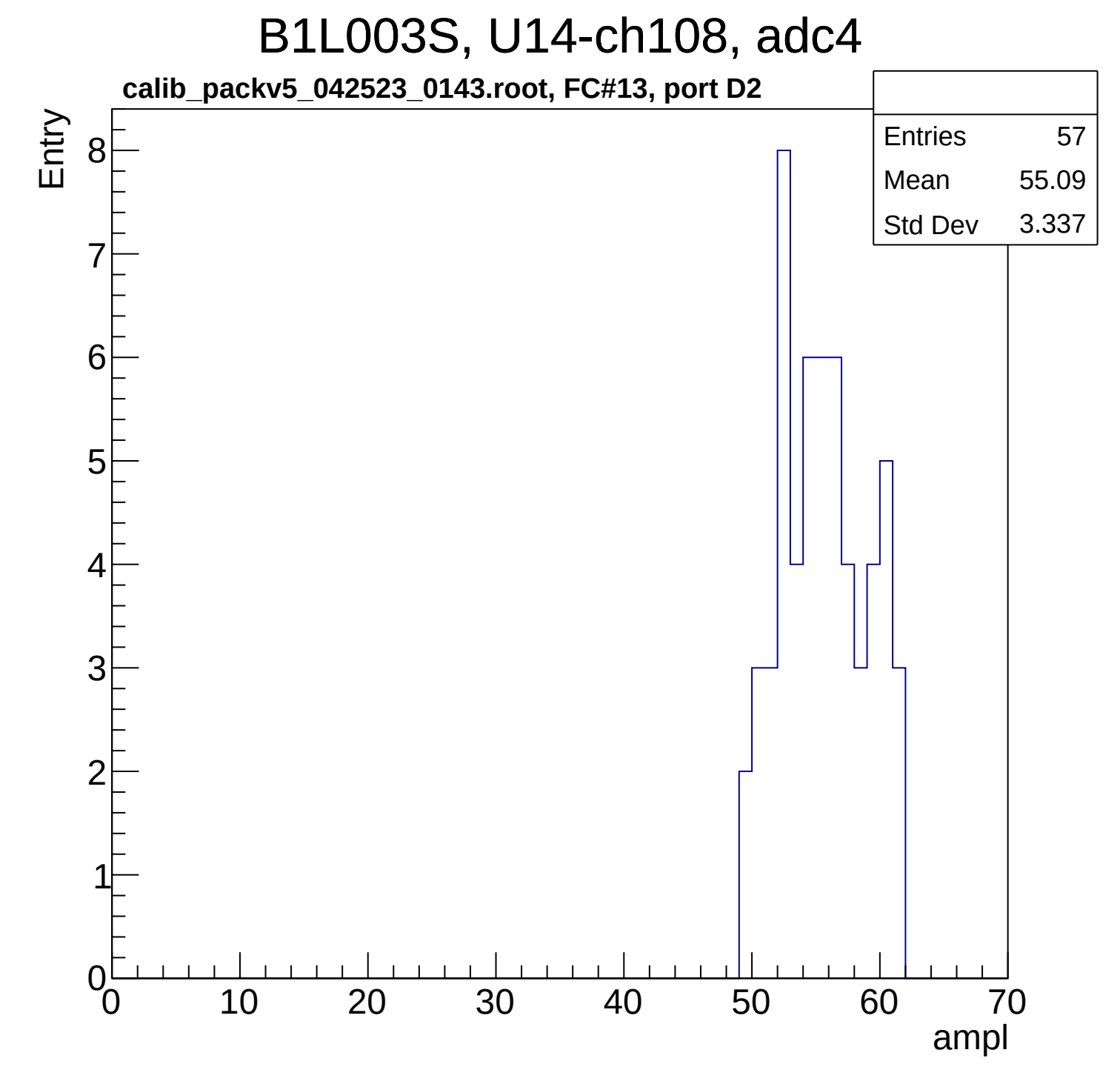

B1L003S, U14-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

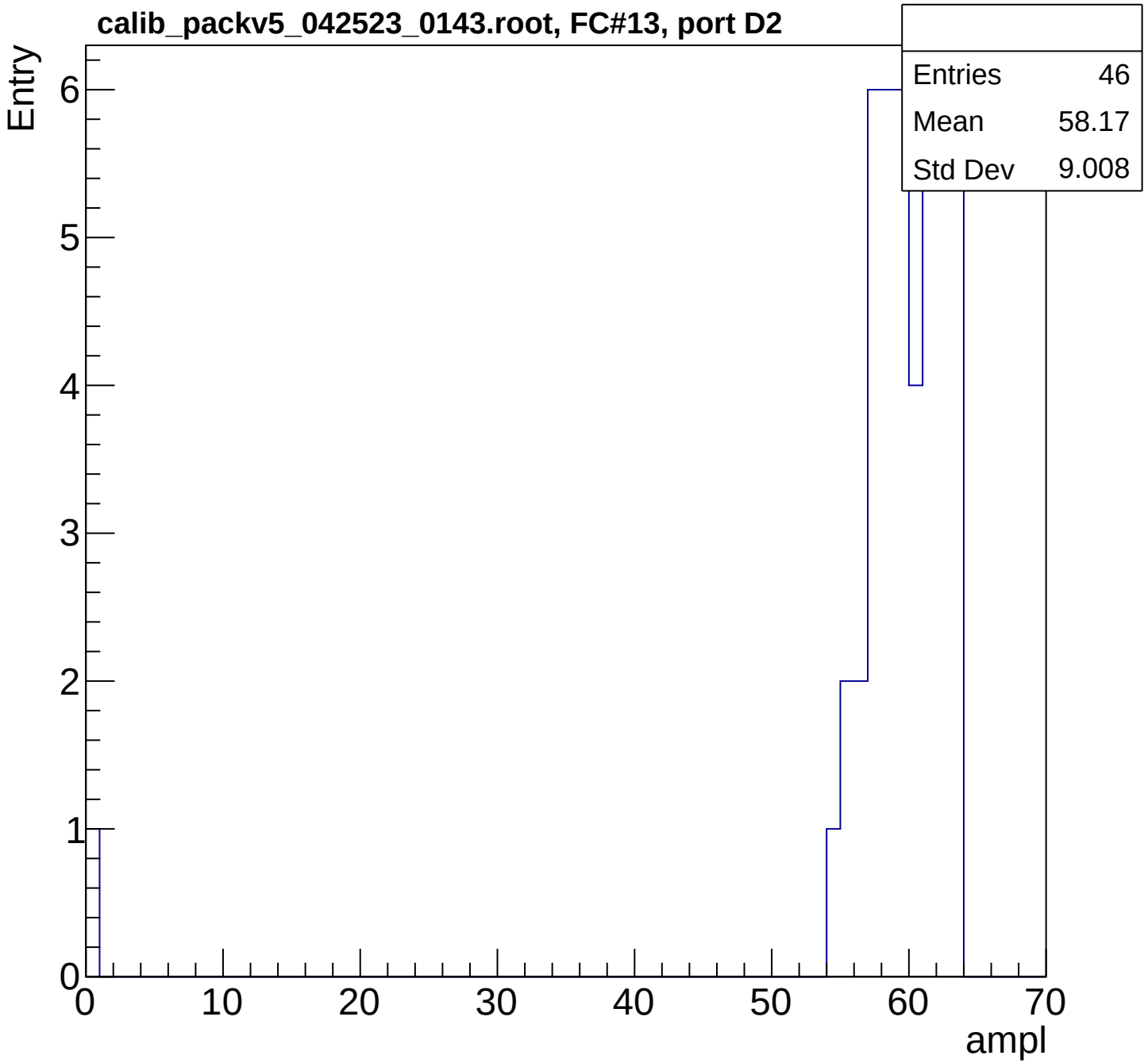

B1L003S, U14-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.64
Std Dev 4.877
Gaus mean : 29.7070
Gaus Width: 3.7021
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

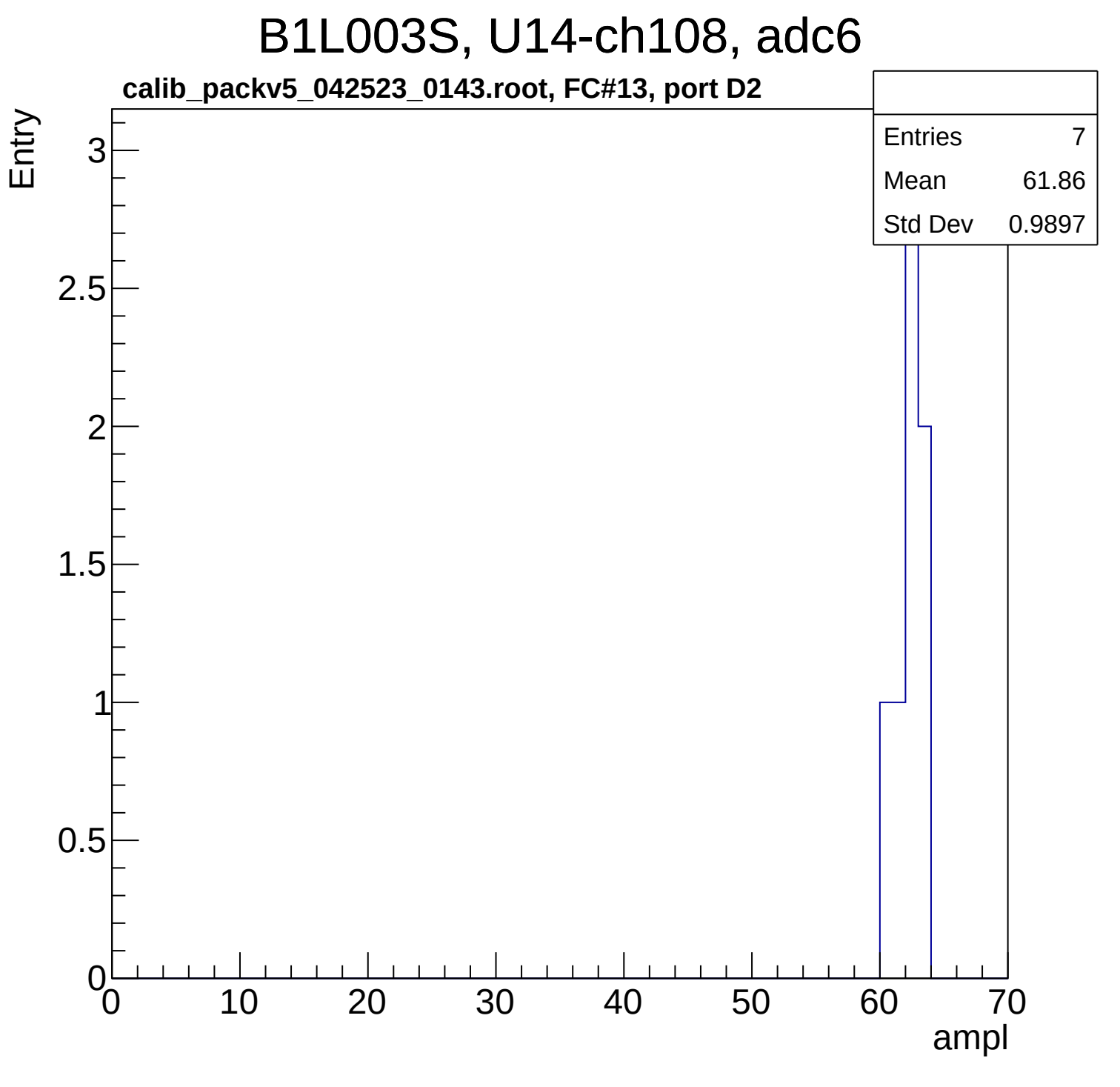

B1L003S, U14-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.55
Std Dev 3.36
Gaus mean : 35.9743
Gaus Width: 3.7333
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

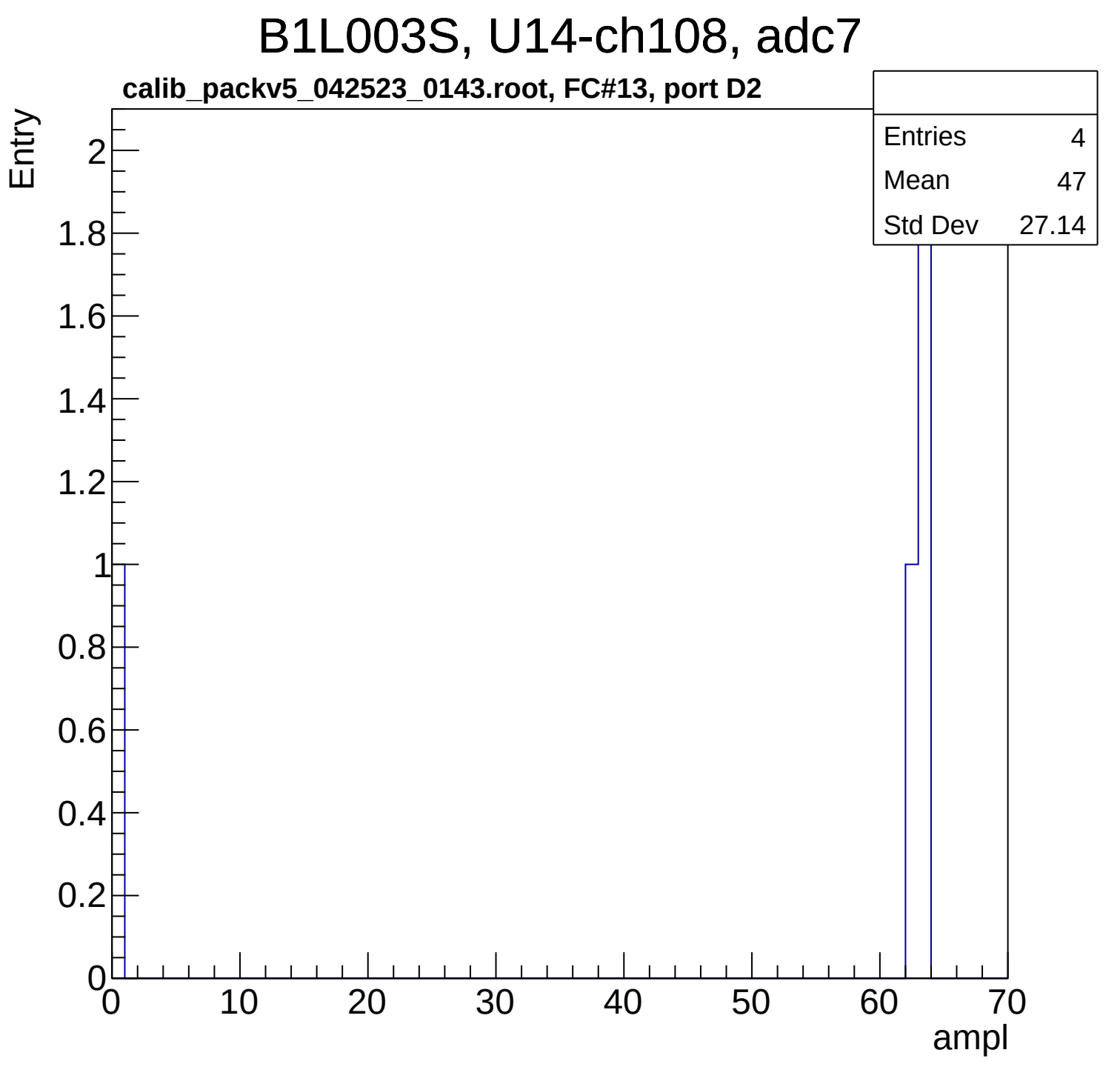

B1L003S, U14-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.44
Std Dev 3.182
Gaus mean : 42.3122
Gaus Width: 3.7550
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

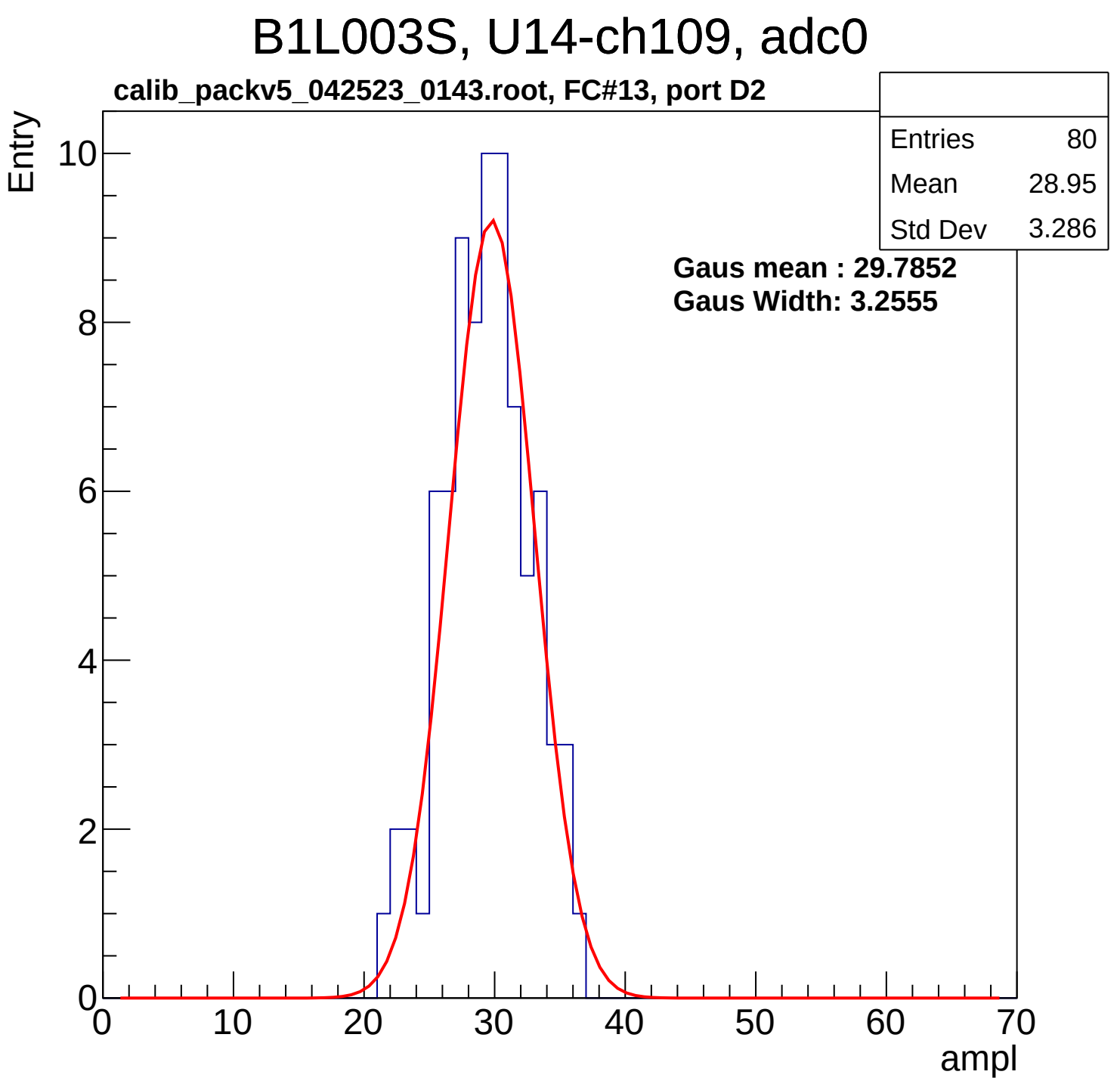

B1L003S, U14-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.03
Std Dev 3.673
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

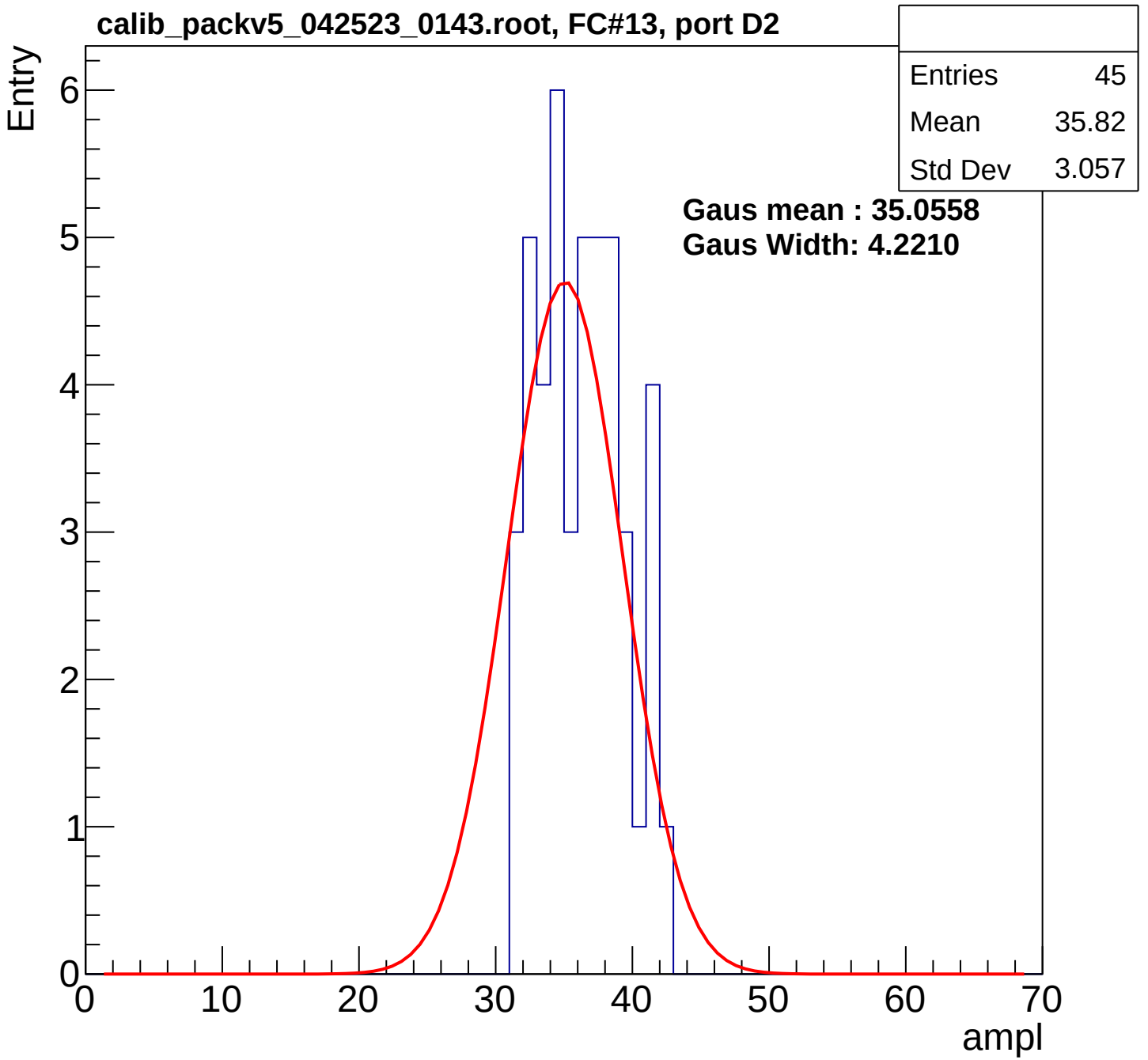

B1L003S, U14-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.09
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

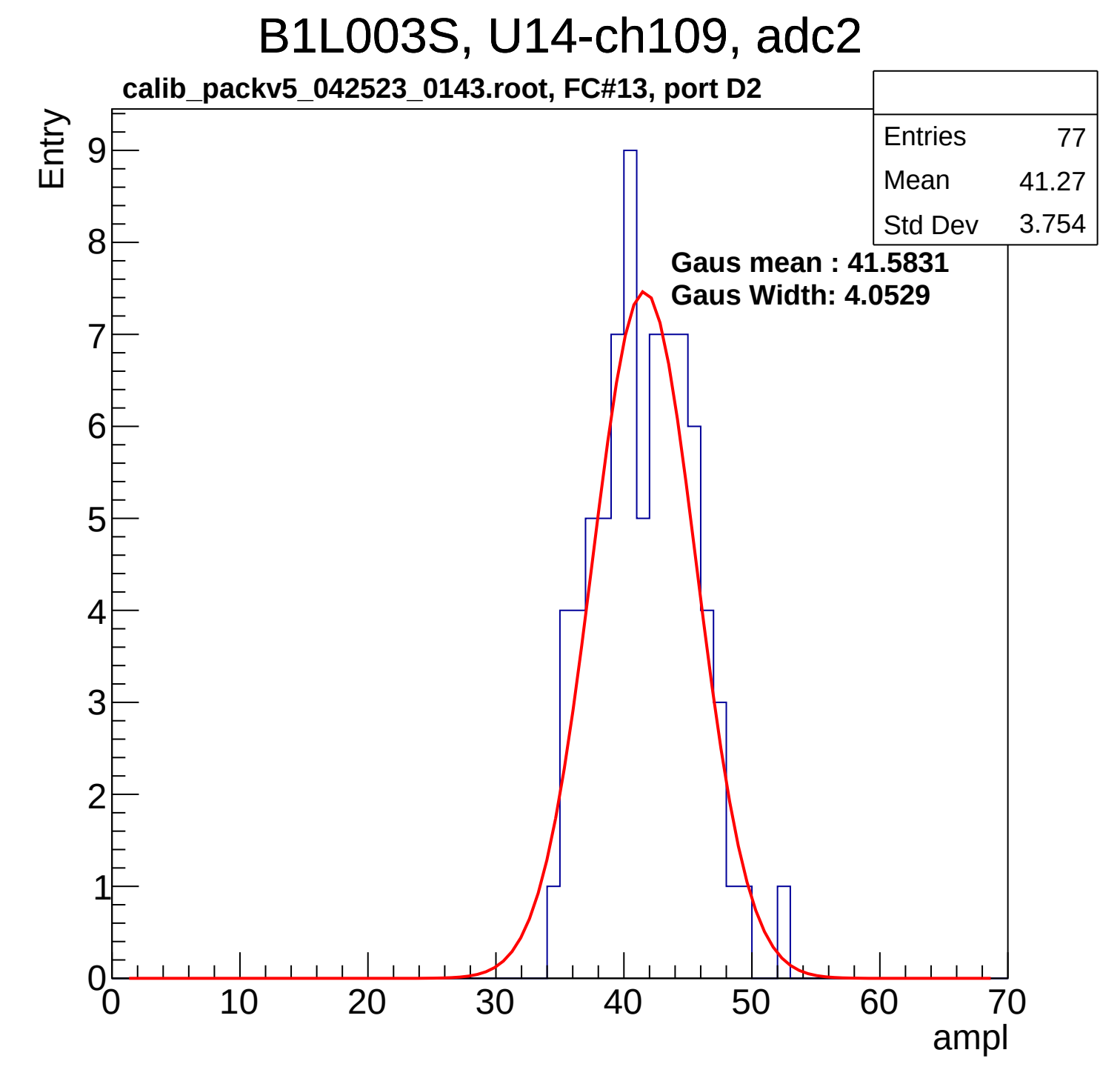

B1L003S, U14-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.17
Std Dev 9.008
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

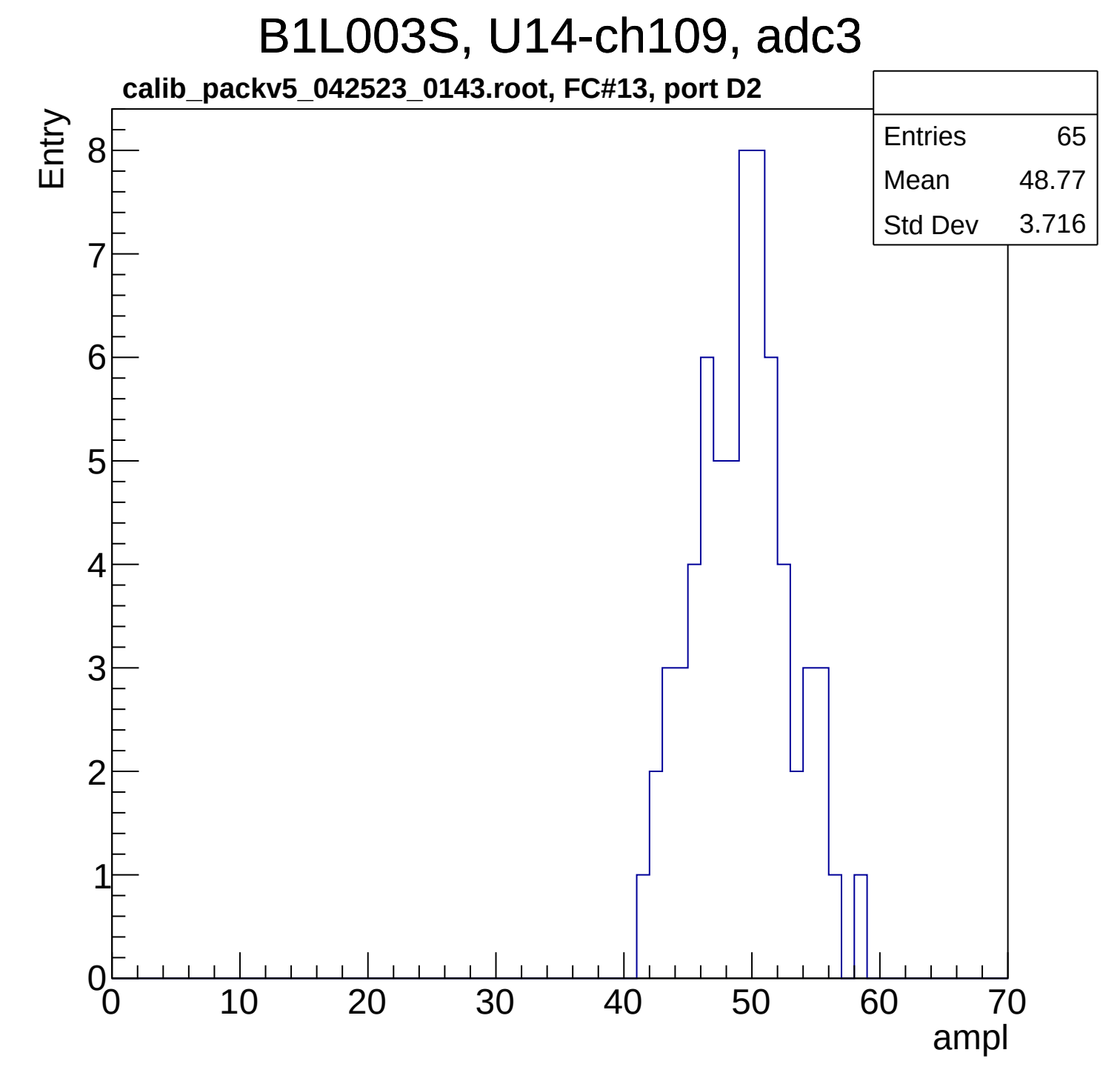

B1L003S, U14-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

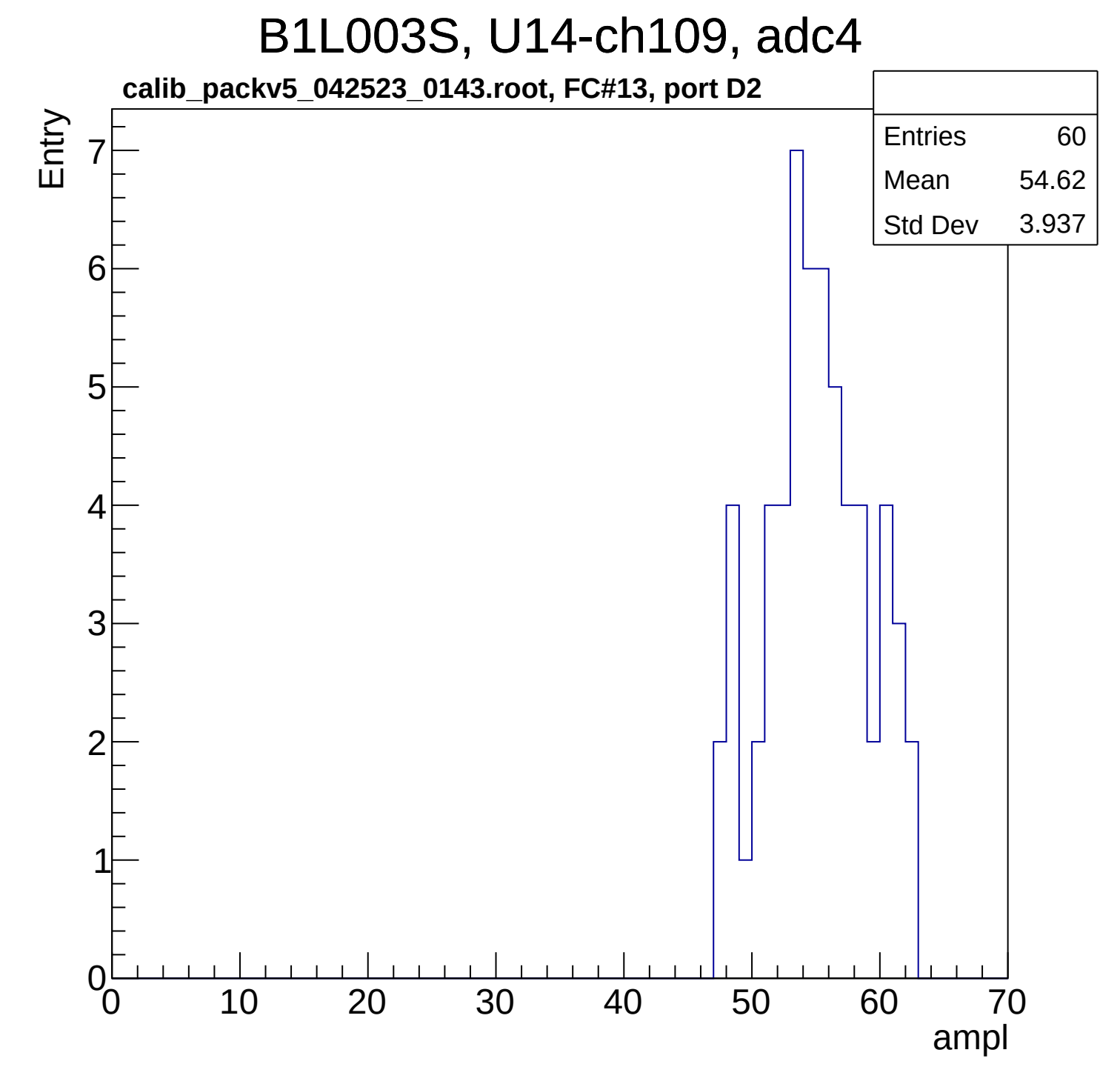

B1L003S, U14-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

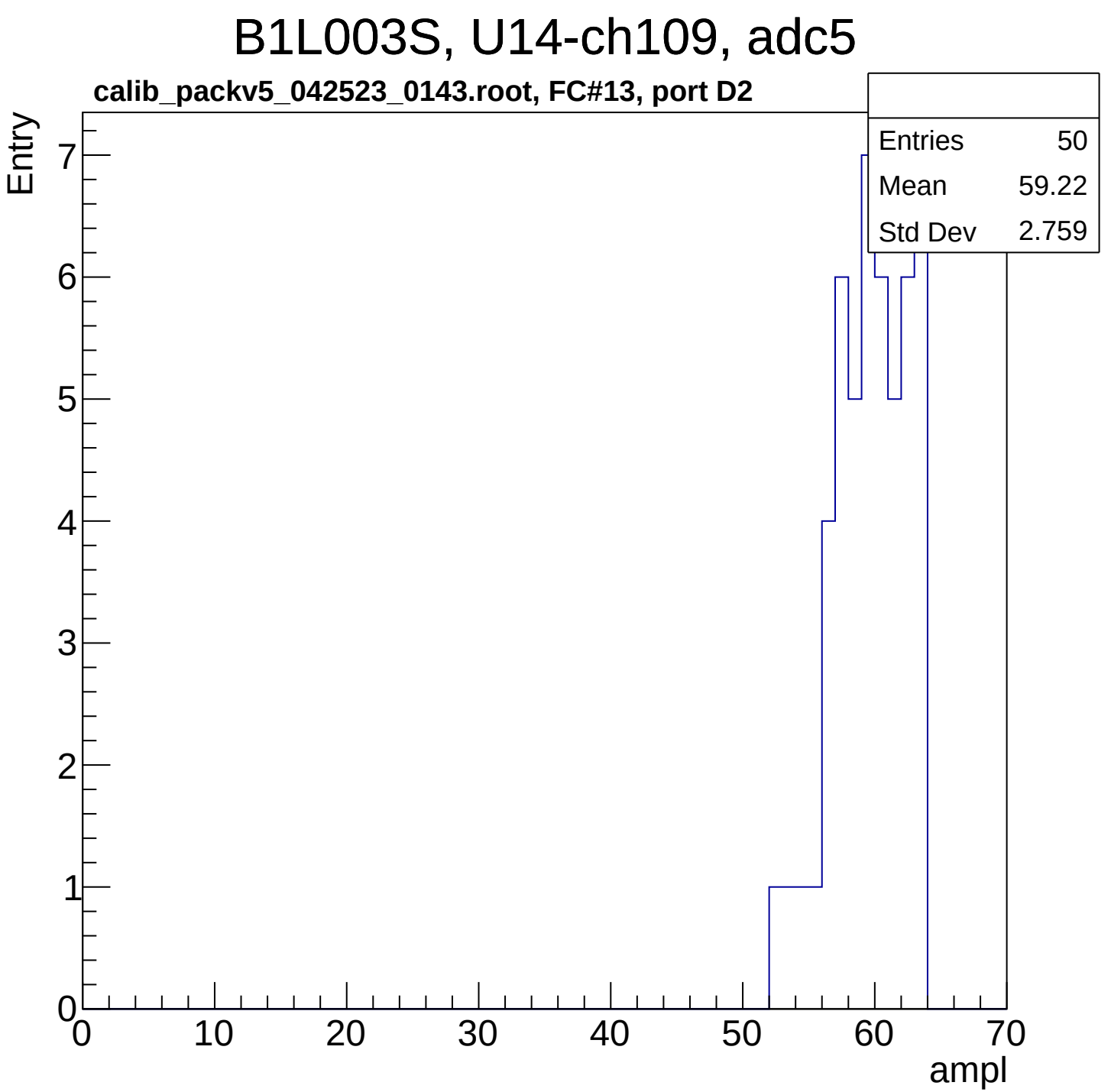

B1L003S, U14-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 28.95
Std Dev 3.286
Gaus mean : 29.7852
Gaus Width: 3.2555
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

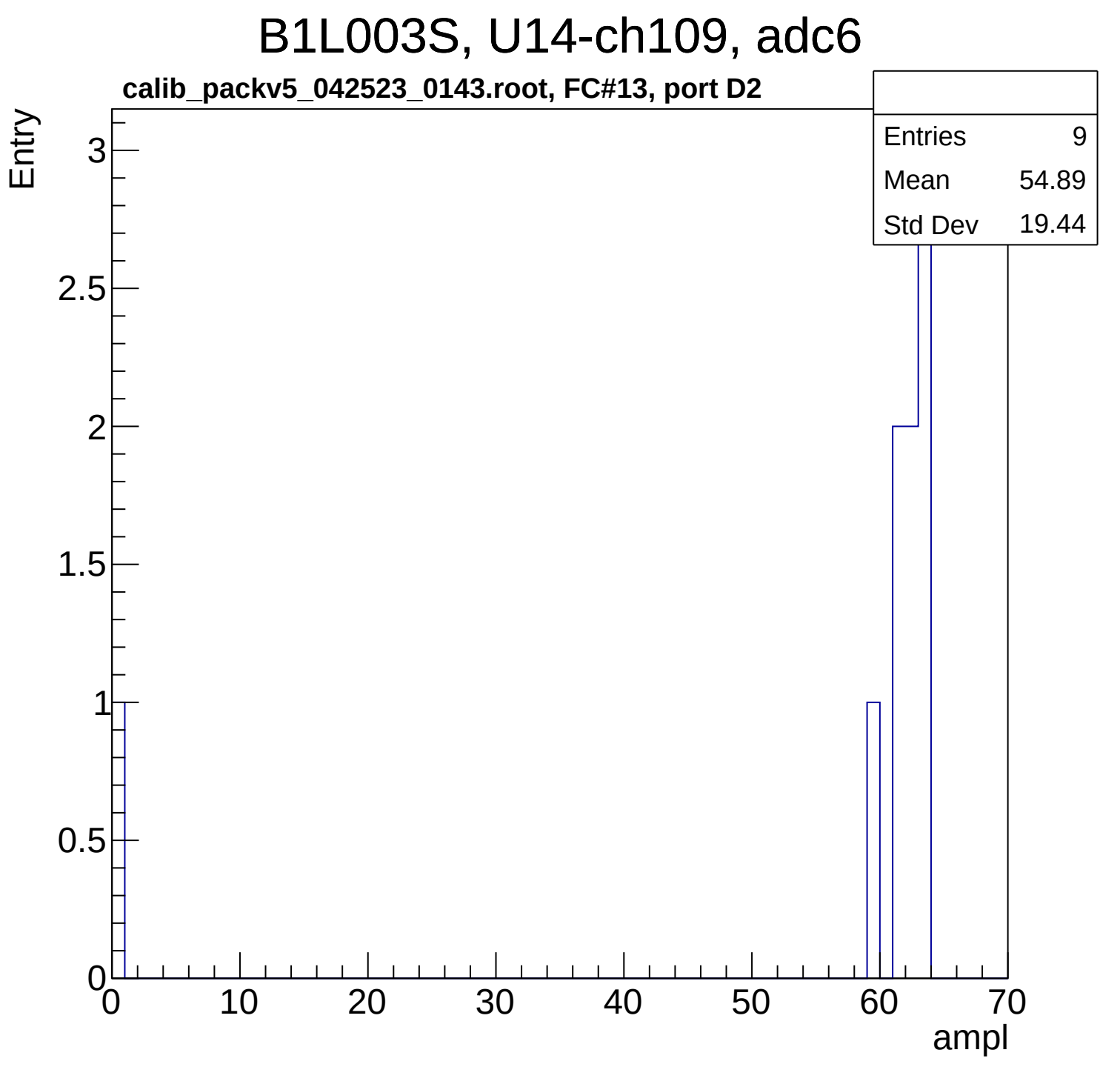

B1L003S, U14-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 35.82
Std Dev 3.057
Gaus mean : 35.0558
Gaus Width: 4.2210
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

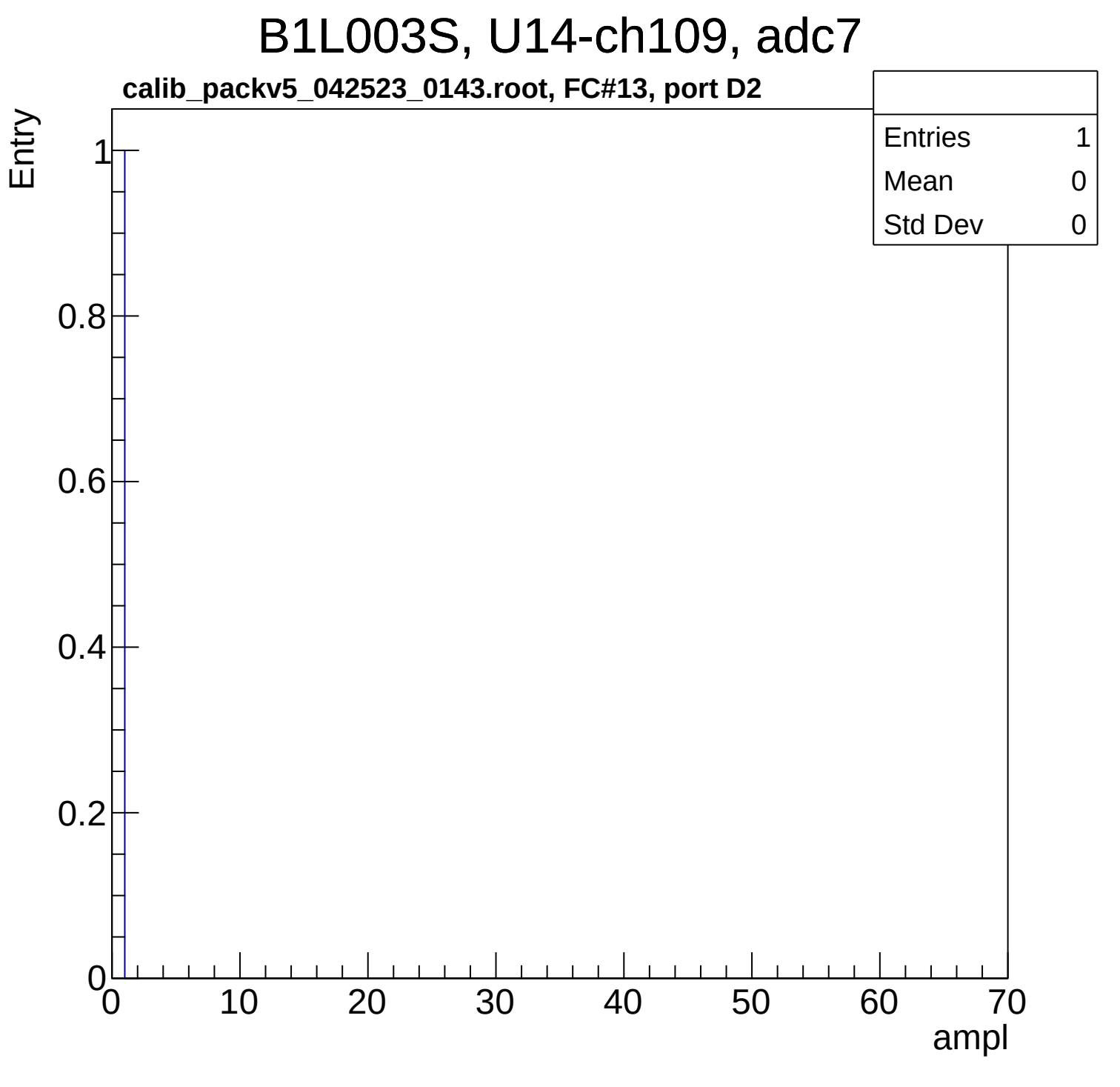

B1L003S, U14-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 41.27
Std Dev 3.754
Gaus mean : 41.5831
Gaus Width: 4.0529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

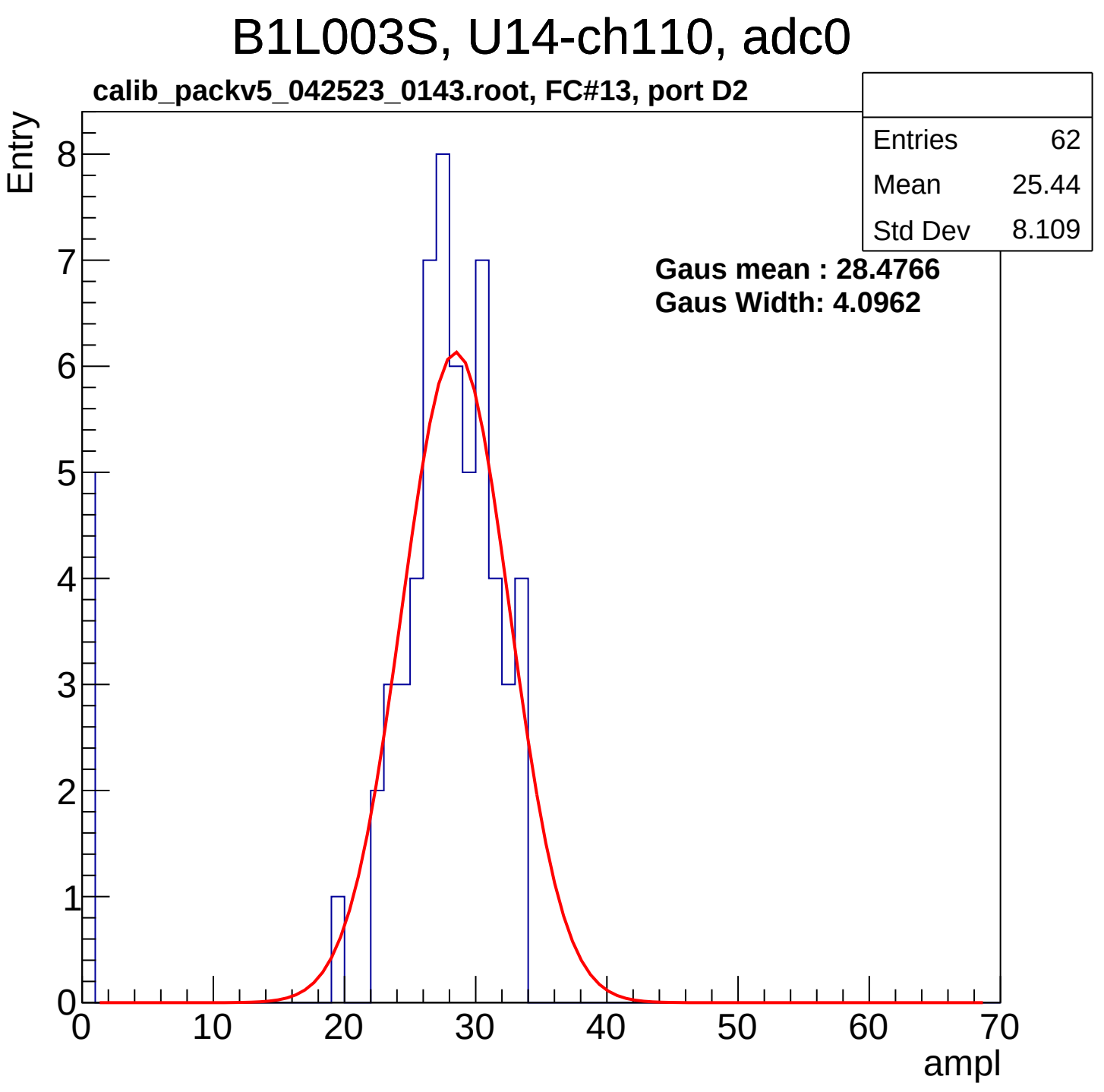

B1L003S, U14-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.77
Std Dev 3.716
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

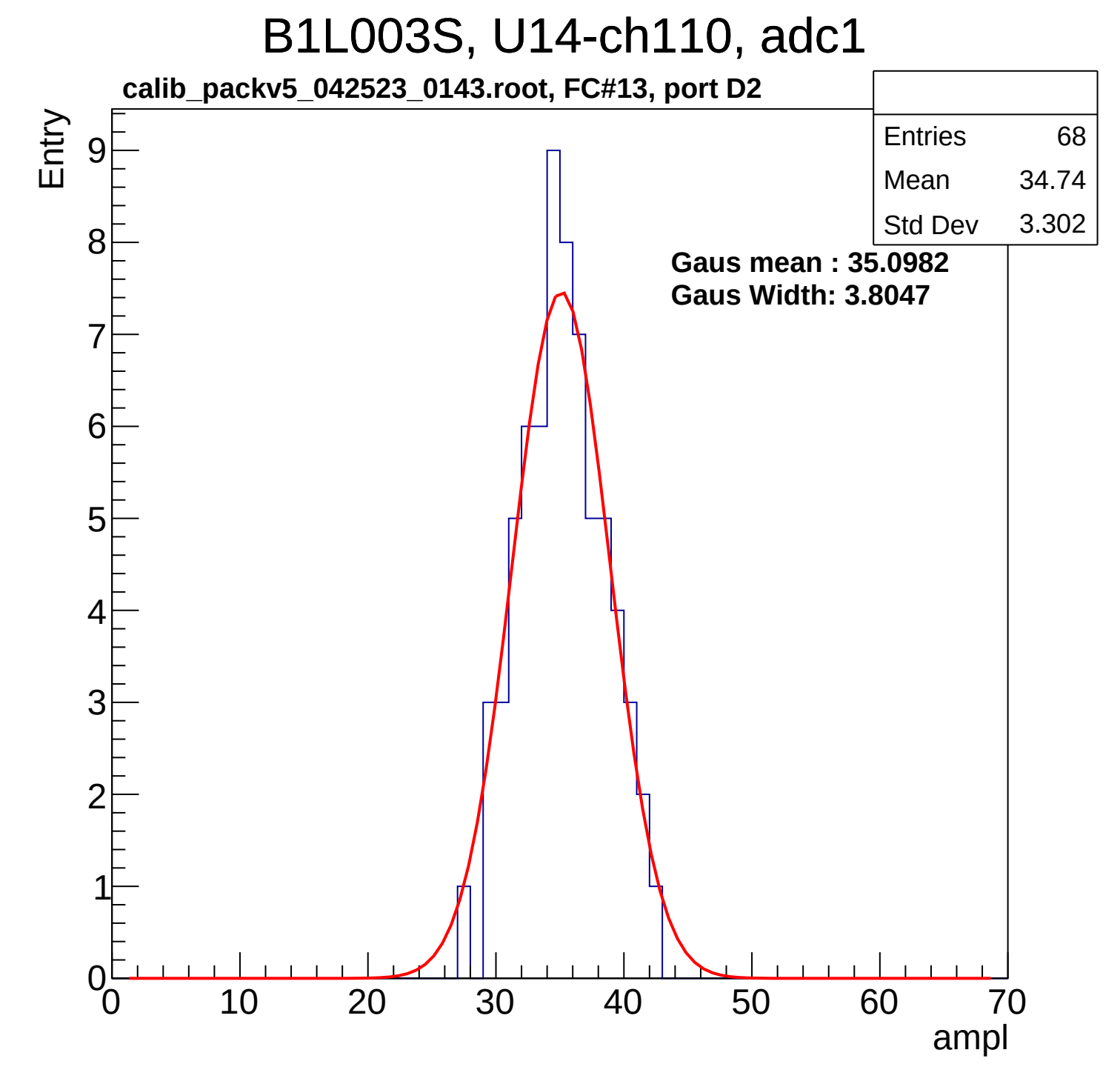

B1L003S, U14-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.62
Std Dev 3.937
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

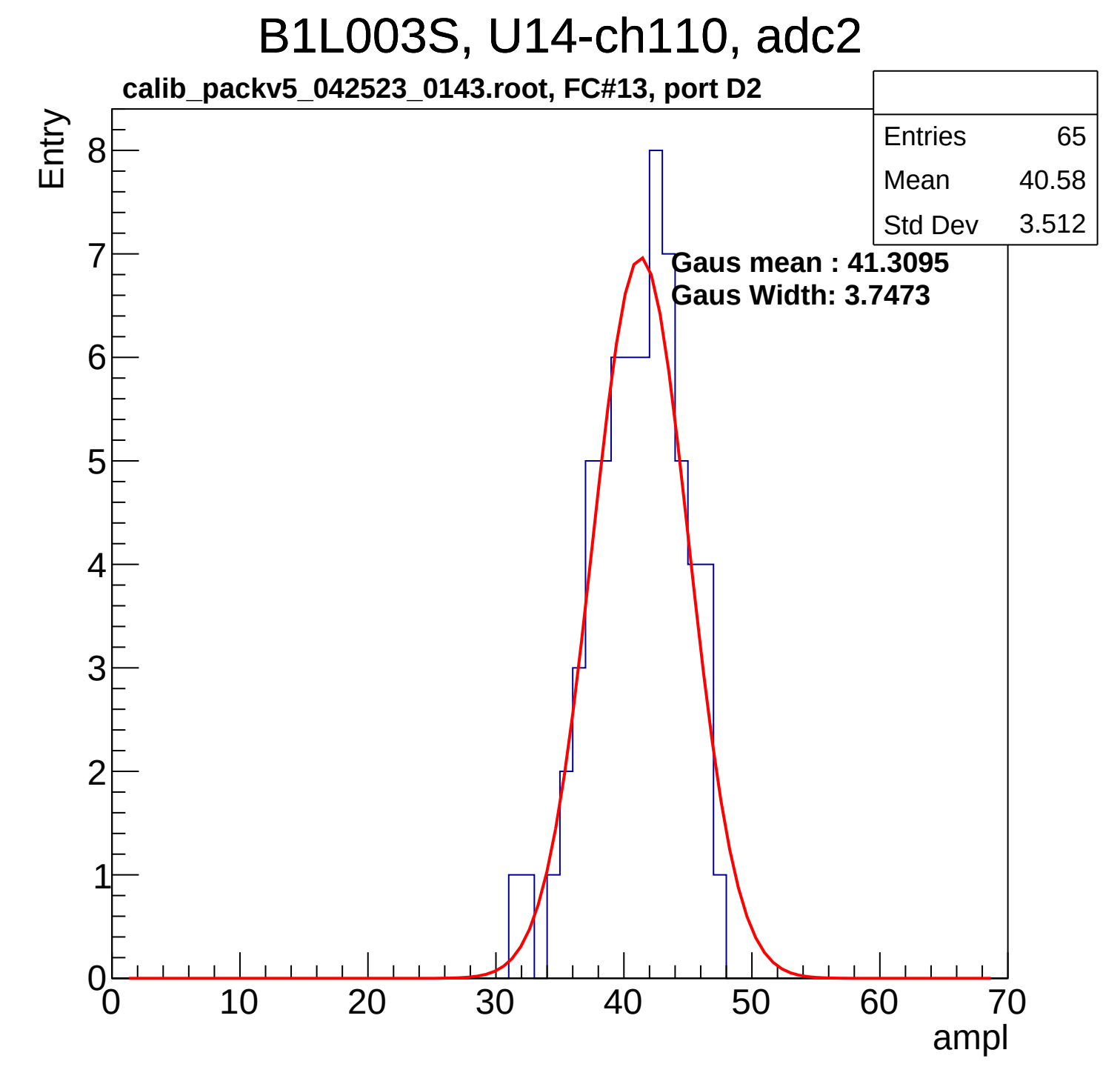

B1L003S, U14-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.22
Std Dev 2.759
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

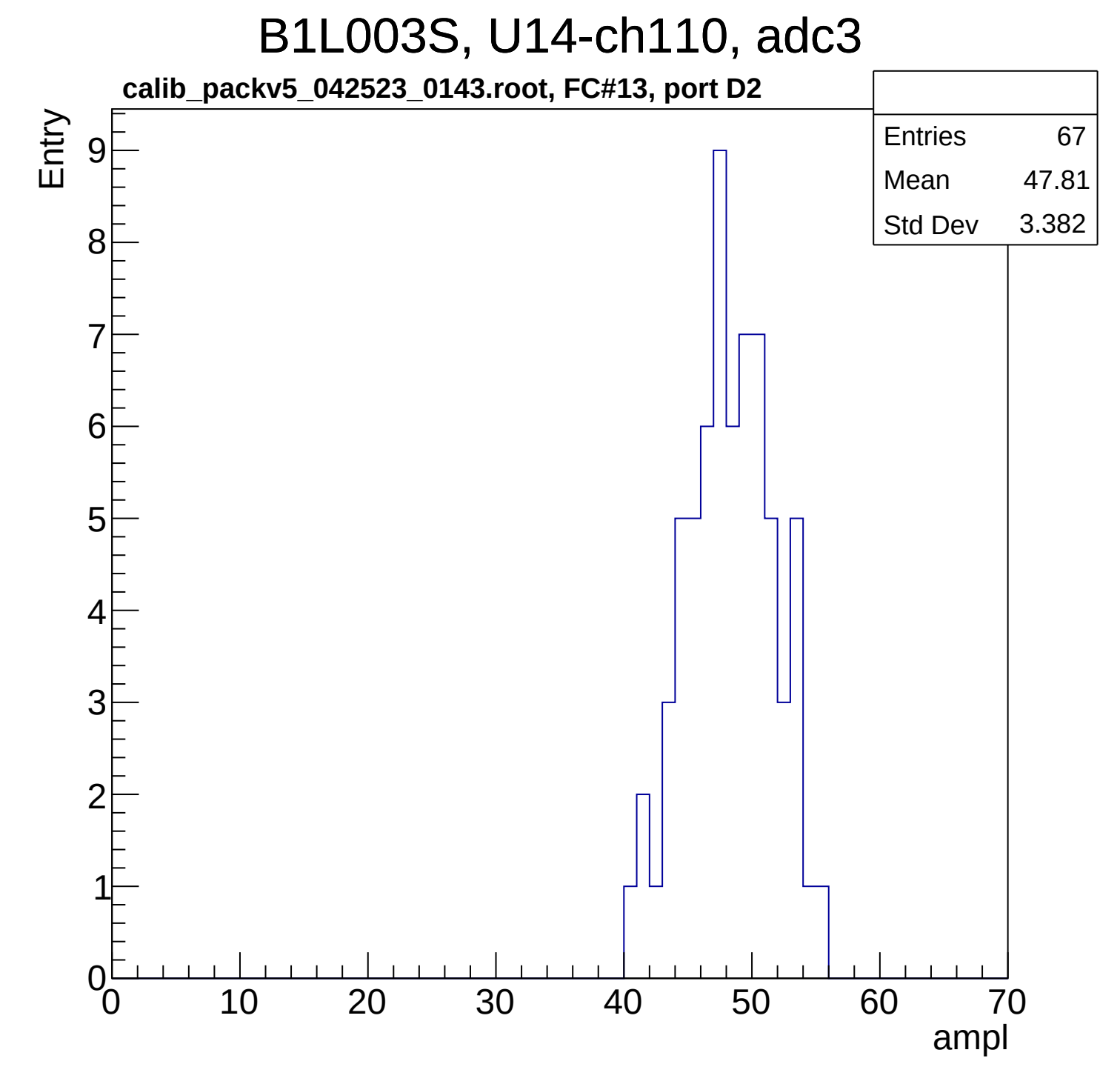

B1L003S, U14-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

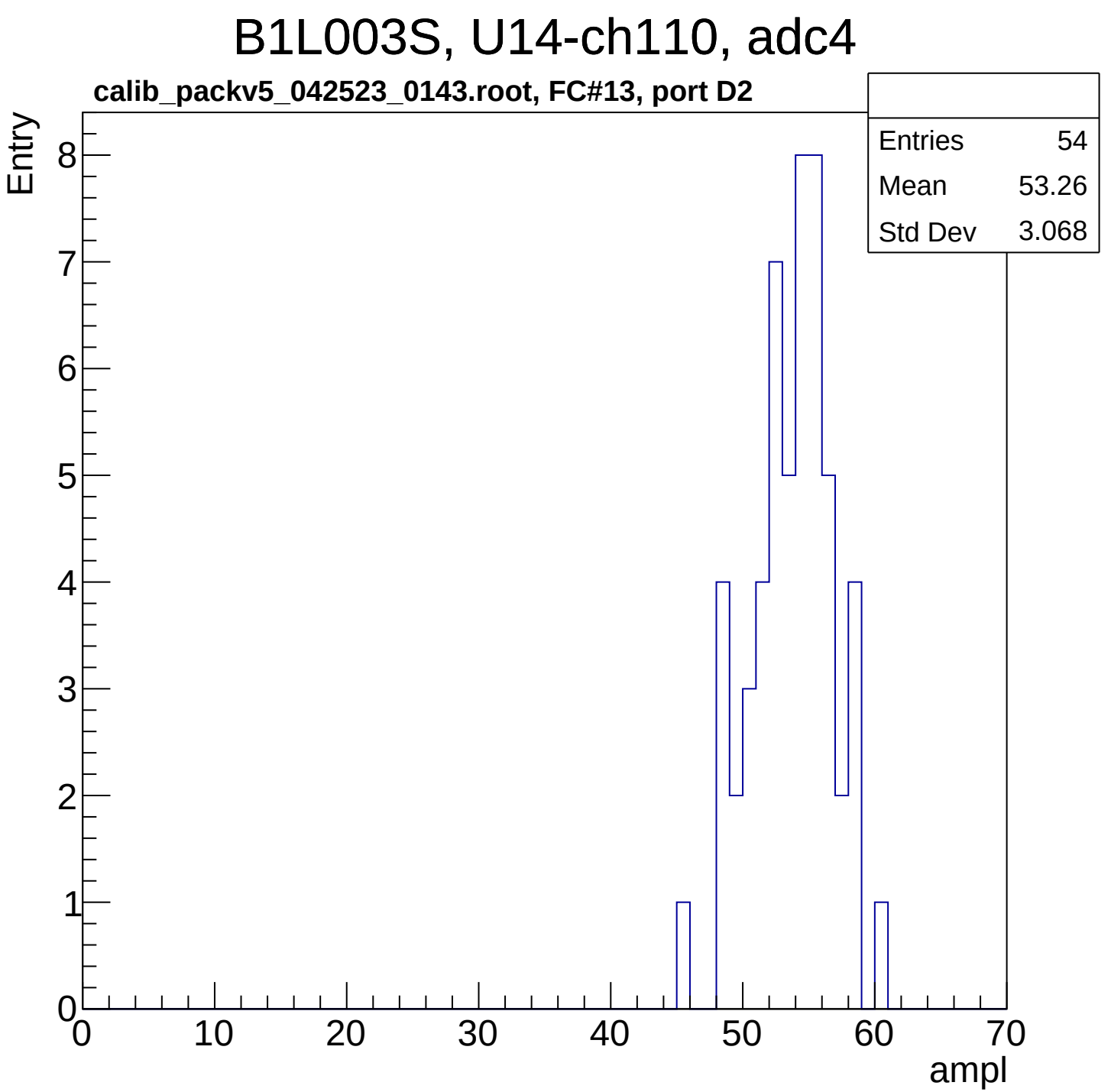

B1L003S, U14-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

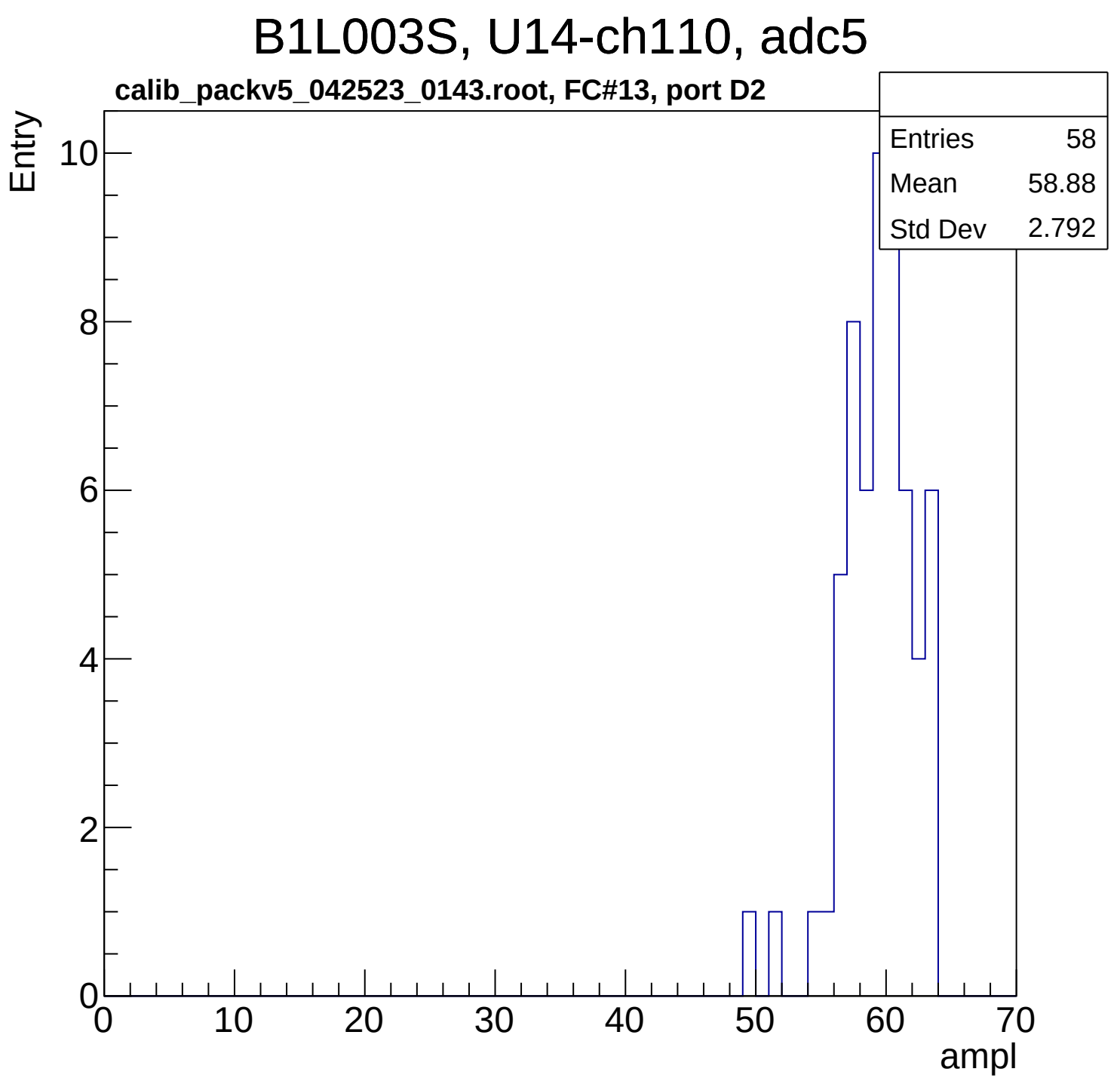

B1L003S, U14-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 25.44
Std Dev 8.109
Gaus mean : 28.4766
Gaus Width: 4.0962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

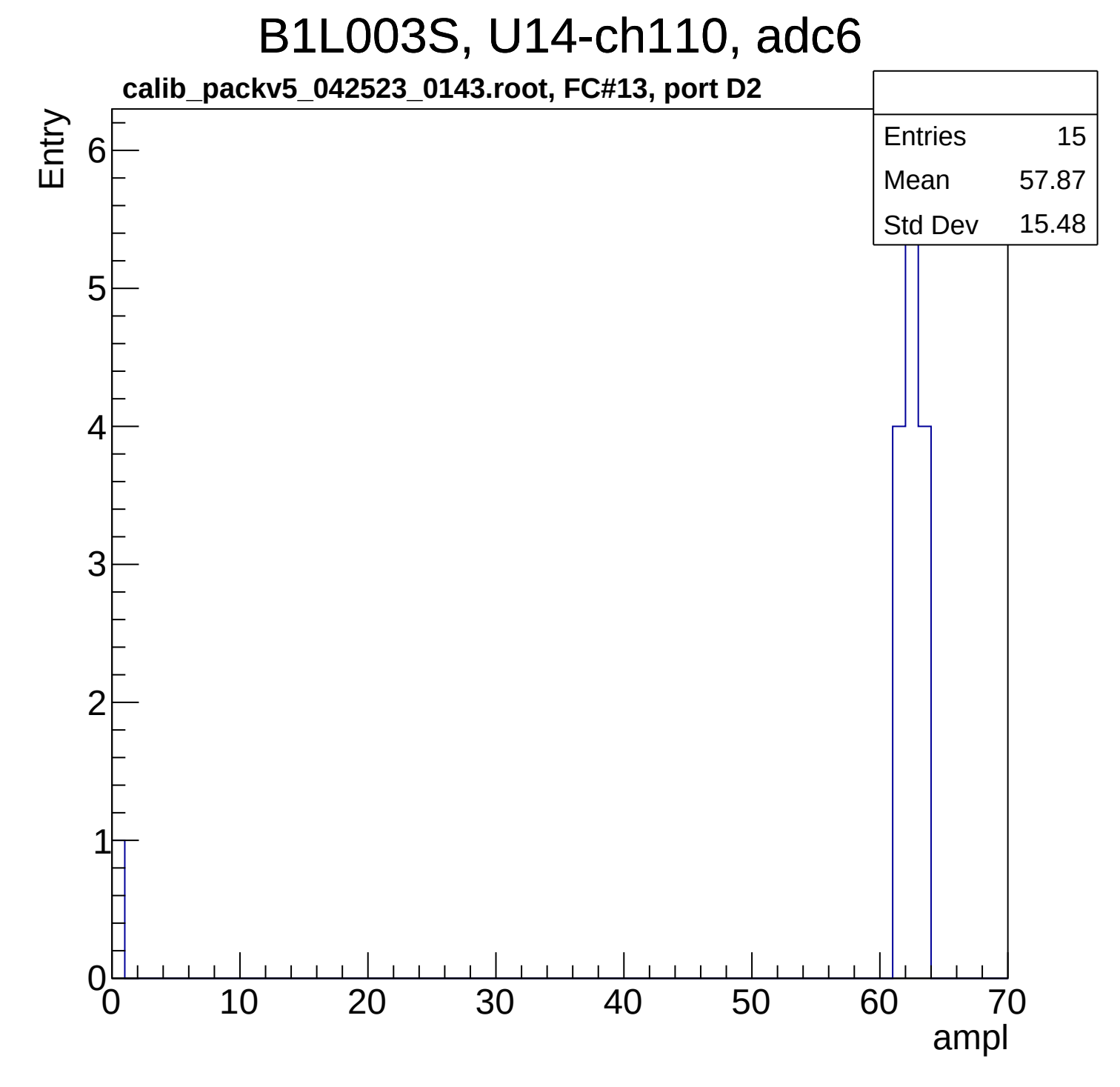

B1L003S, U14-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 34.74
Std Dev 3.302
Gaus mean : 35.0982
Gaus Width: 3.8047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

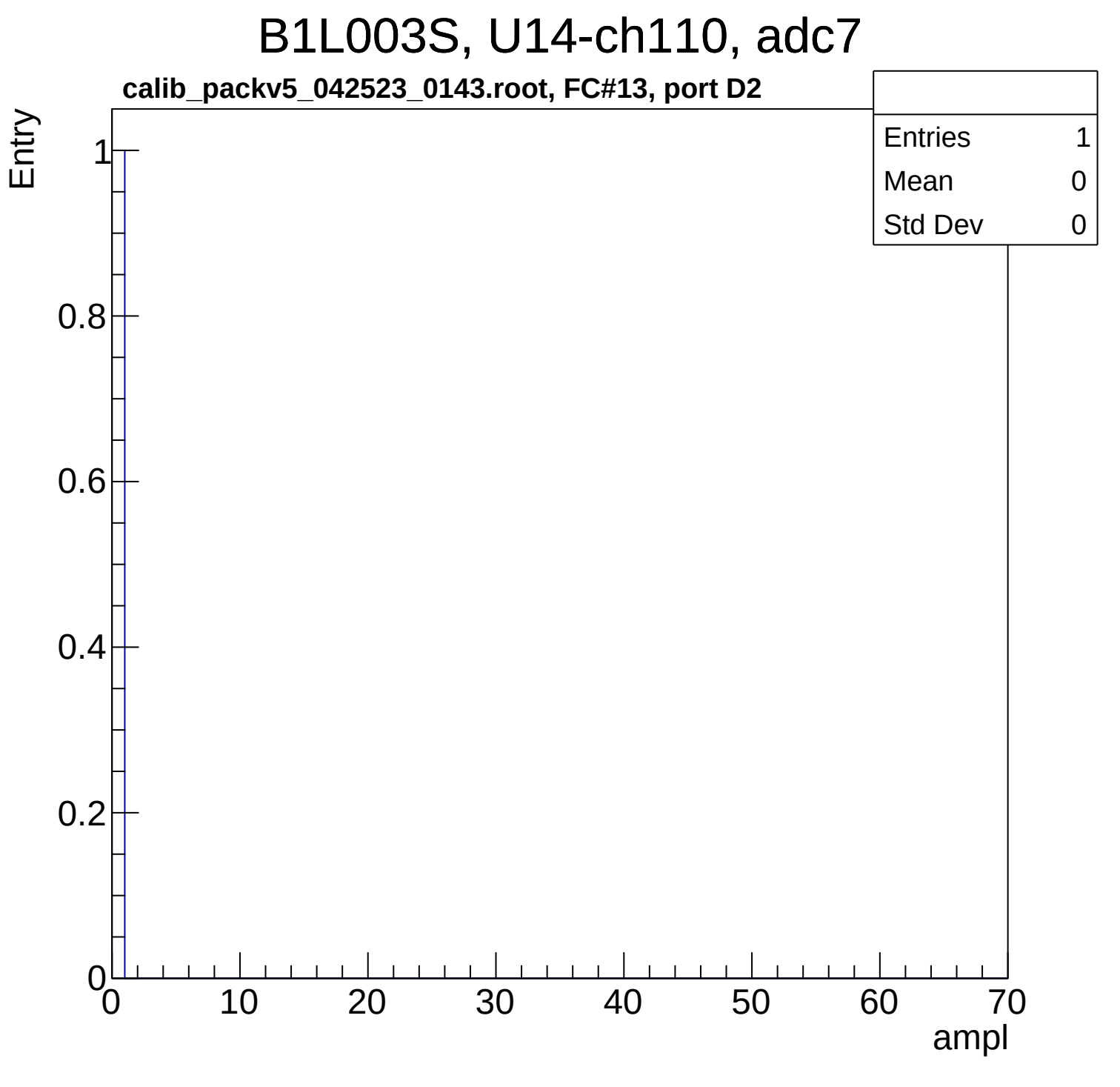

B1L003S, U14-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 40.58
Std Dev 3.512
Gaus mean : 41.3095
Gaus Width: 3.7473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

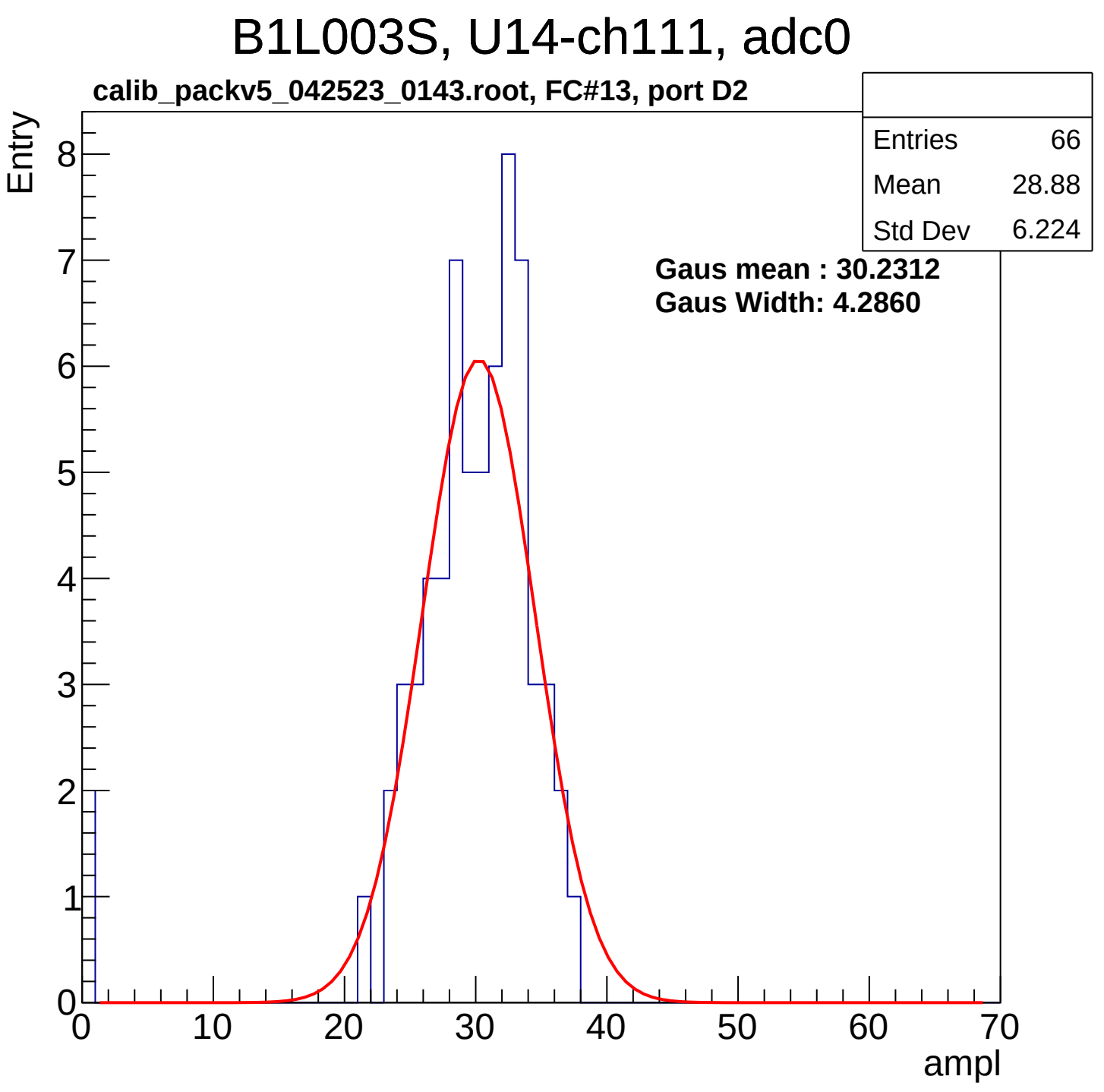

B1L003S, U14-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.81
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

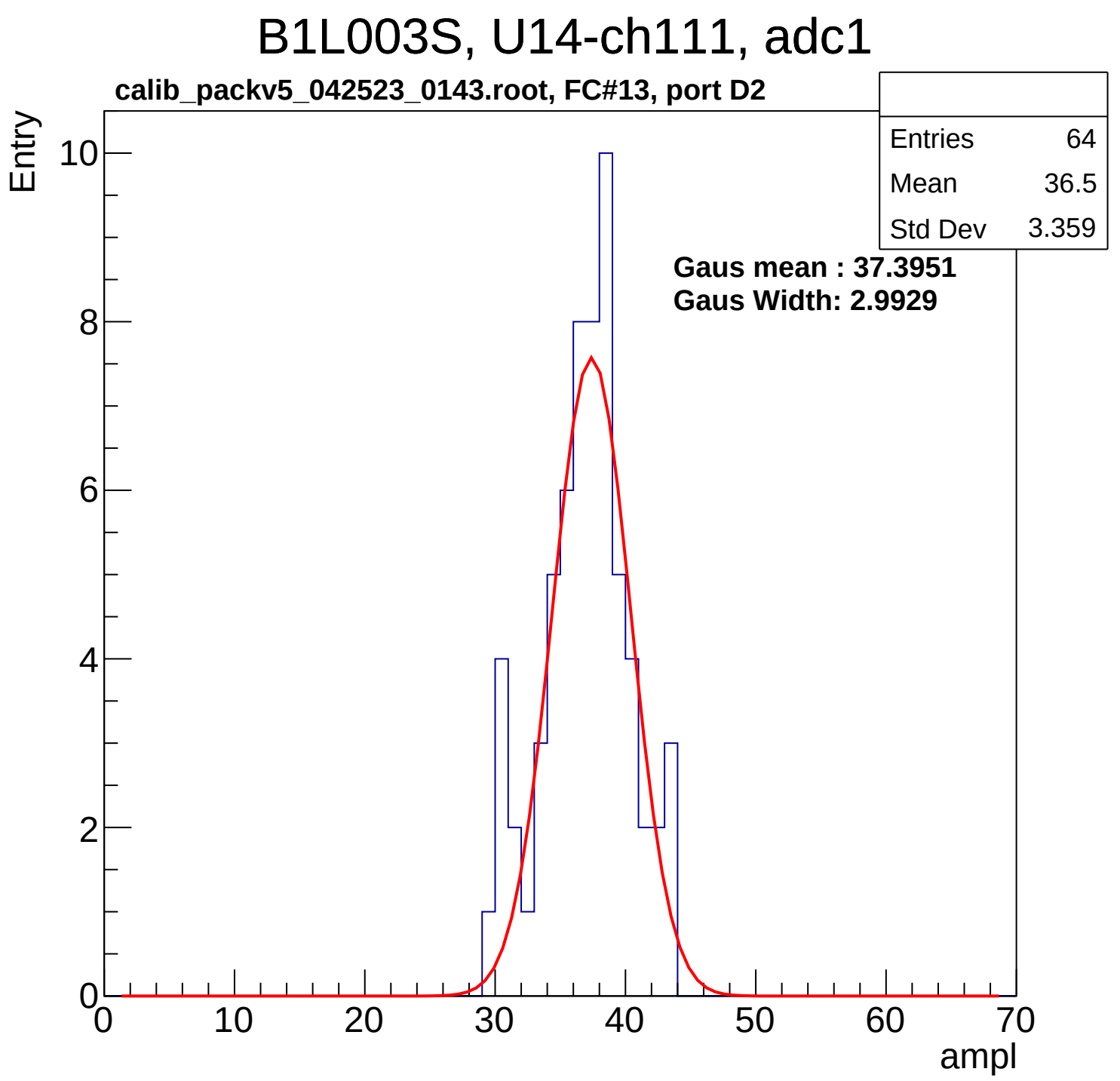

B1L003S, U14-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.26
Std Dev 3.068
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

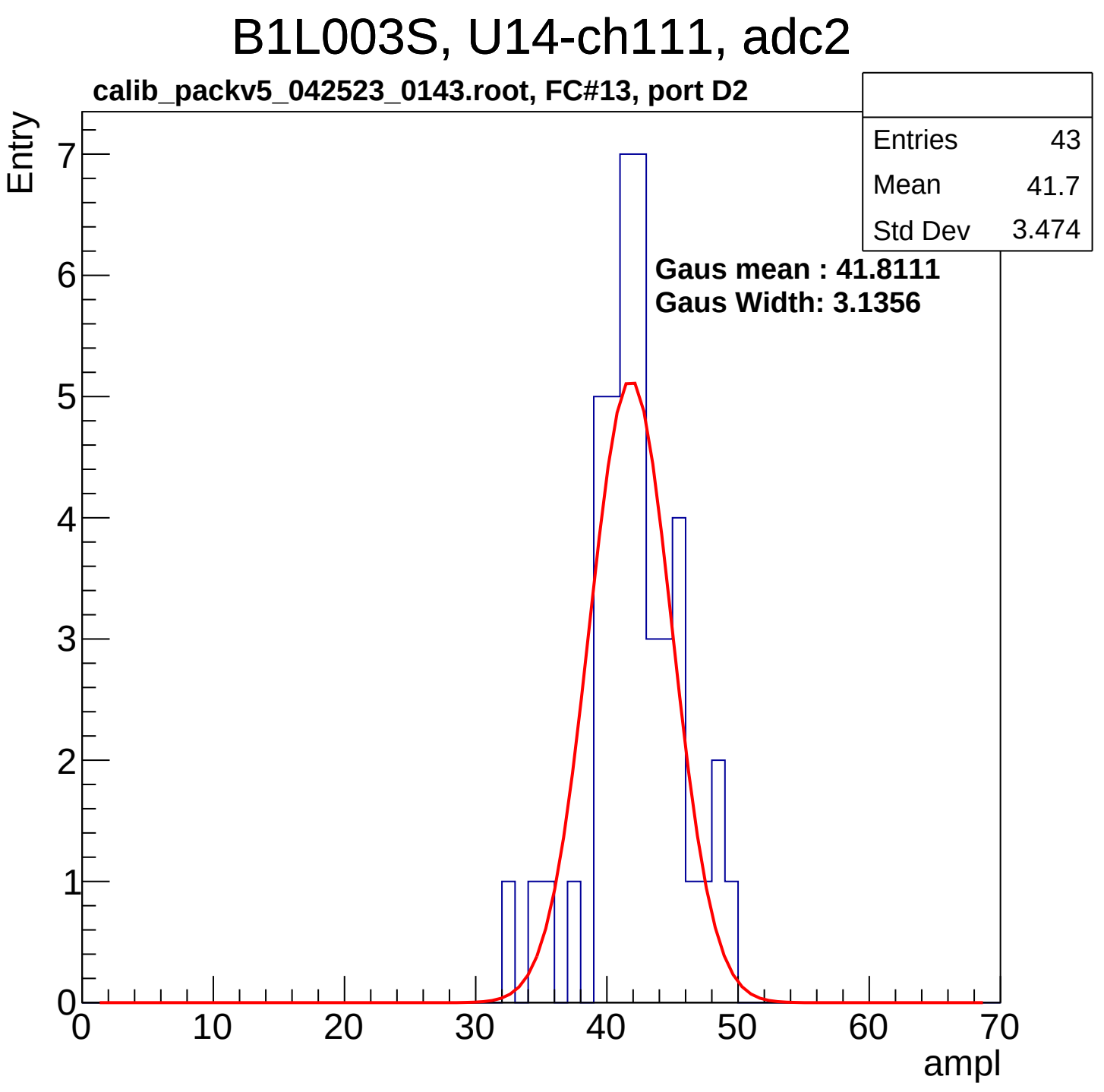

B1L003S, U14-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.88
Std Dev 2.792
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

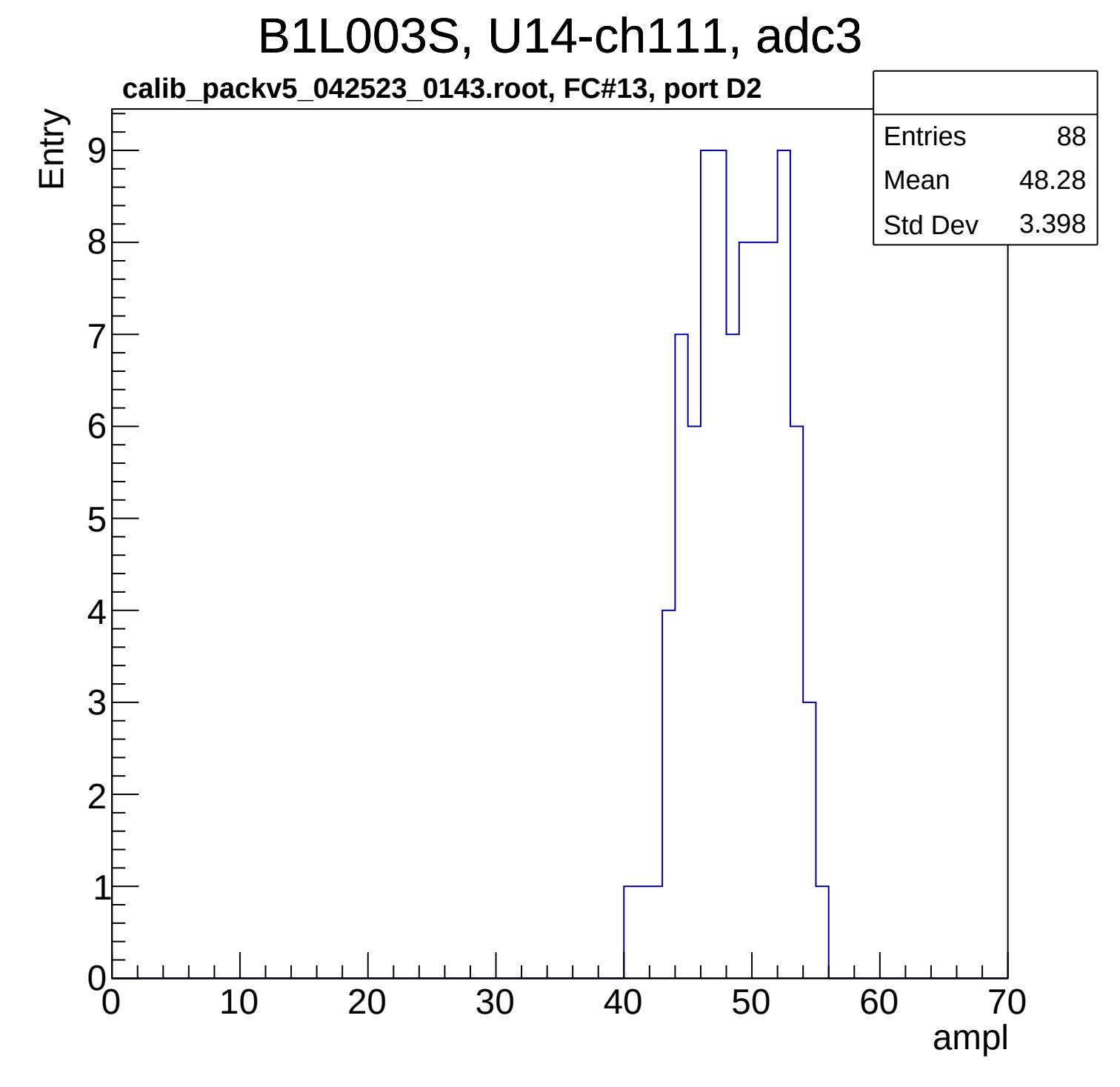

B1L003S, U14-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.87
Std Dev 15.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

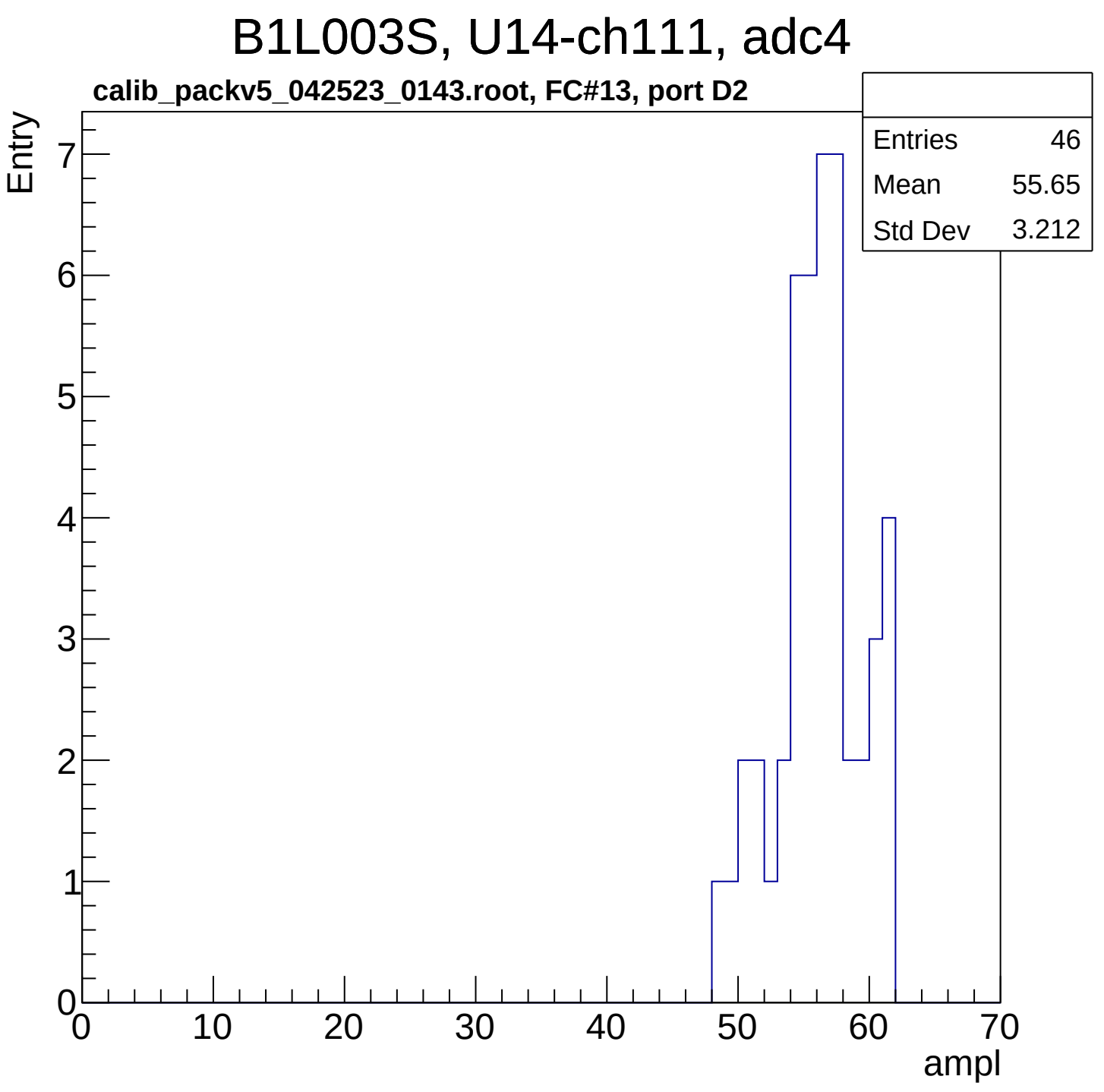

B1L003S, U14-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

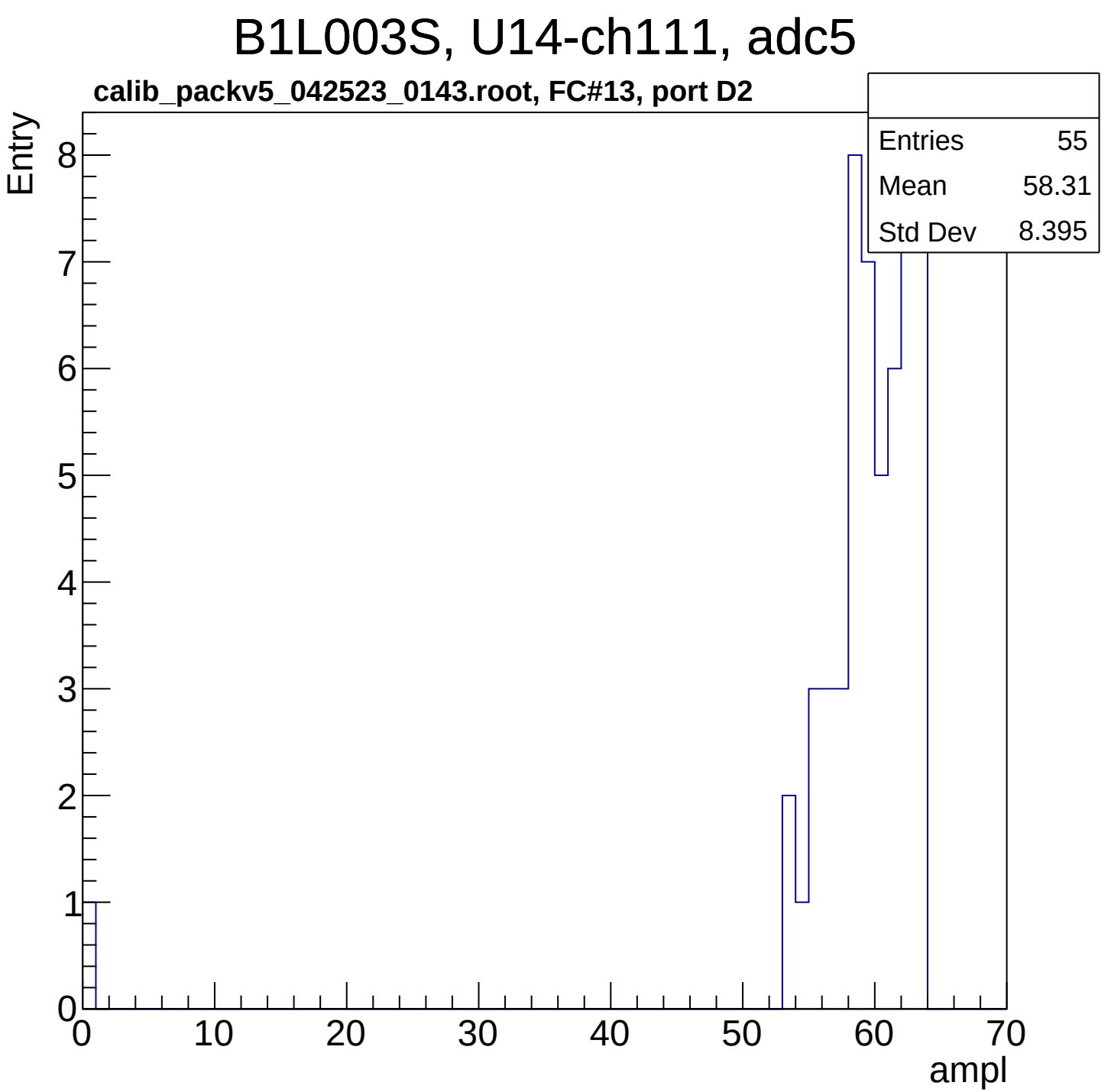

B1L003S, U14-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.88
Std Dev 6.224
Gaus mean : 30.2312
Gaus Width: 4.2860
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

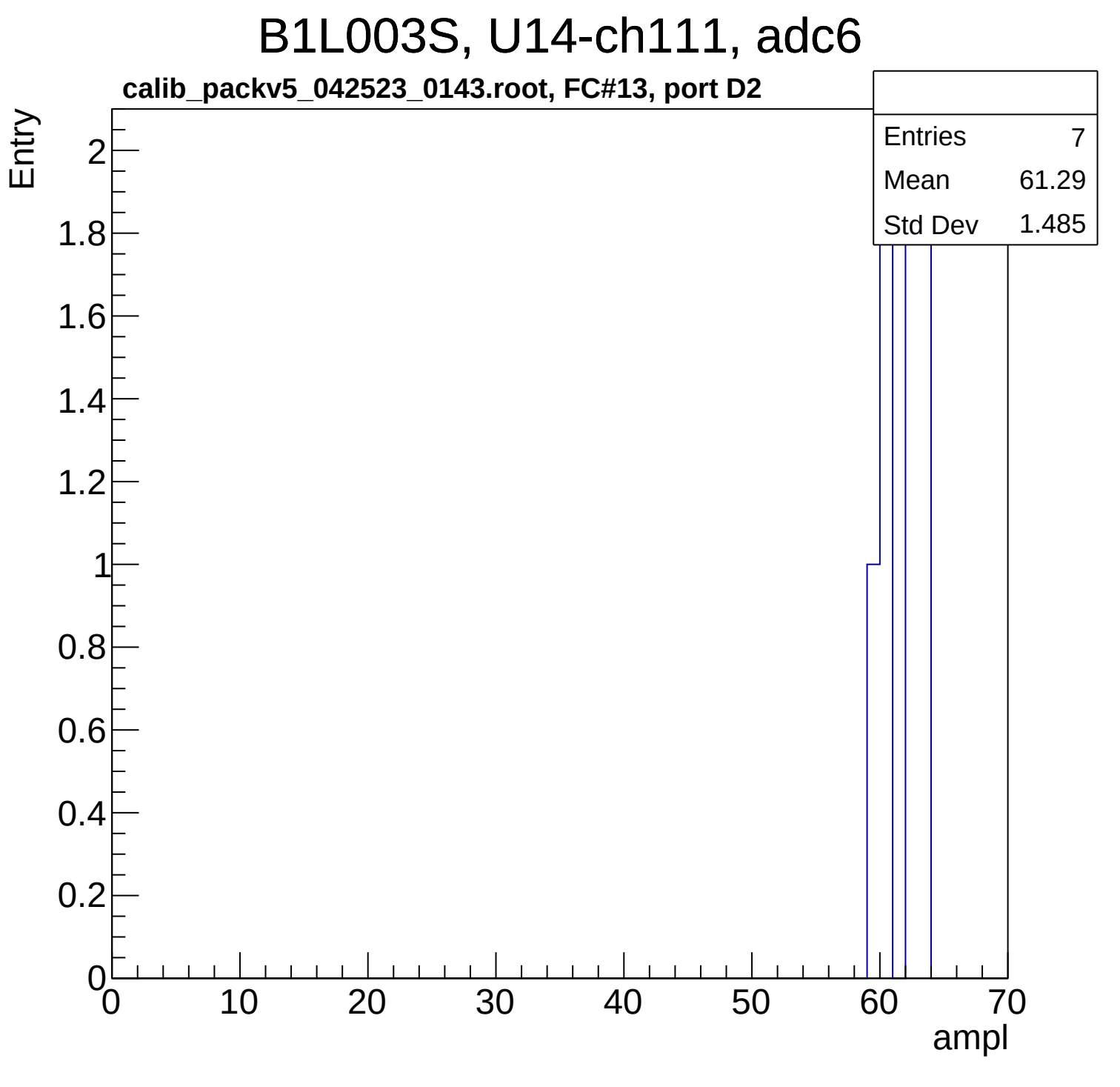

B1L003S, U14-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.5
Std Dev 3.359
Gaus mean : 37.3951
Gaus Width: 2.9929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

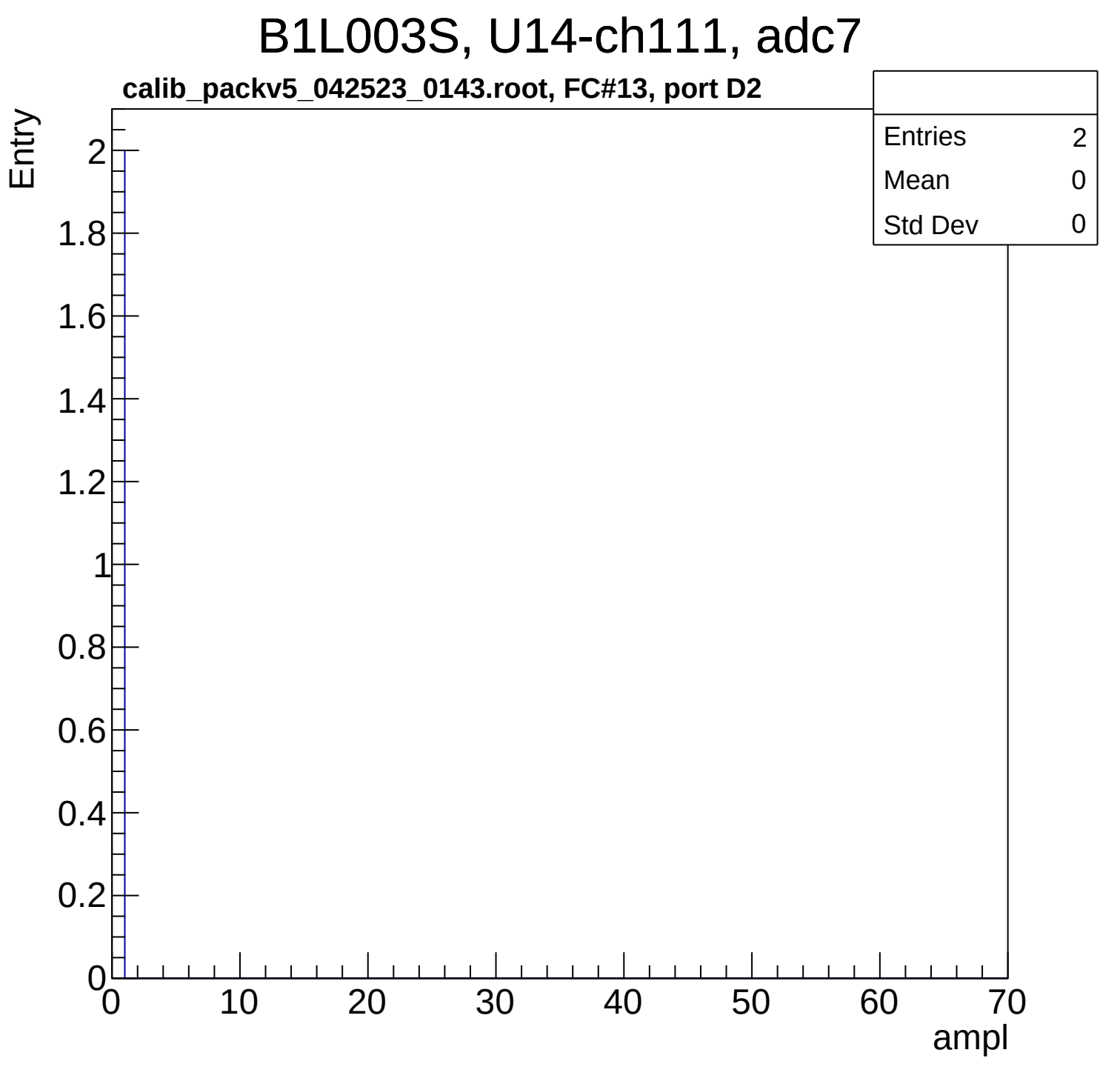

B1L003S, U14-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 41.7
Std Dev 3.474
Gaus mean : 41.8111
Gaus Width: 3.1356
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

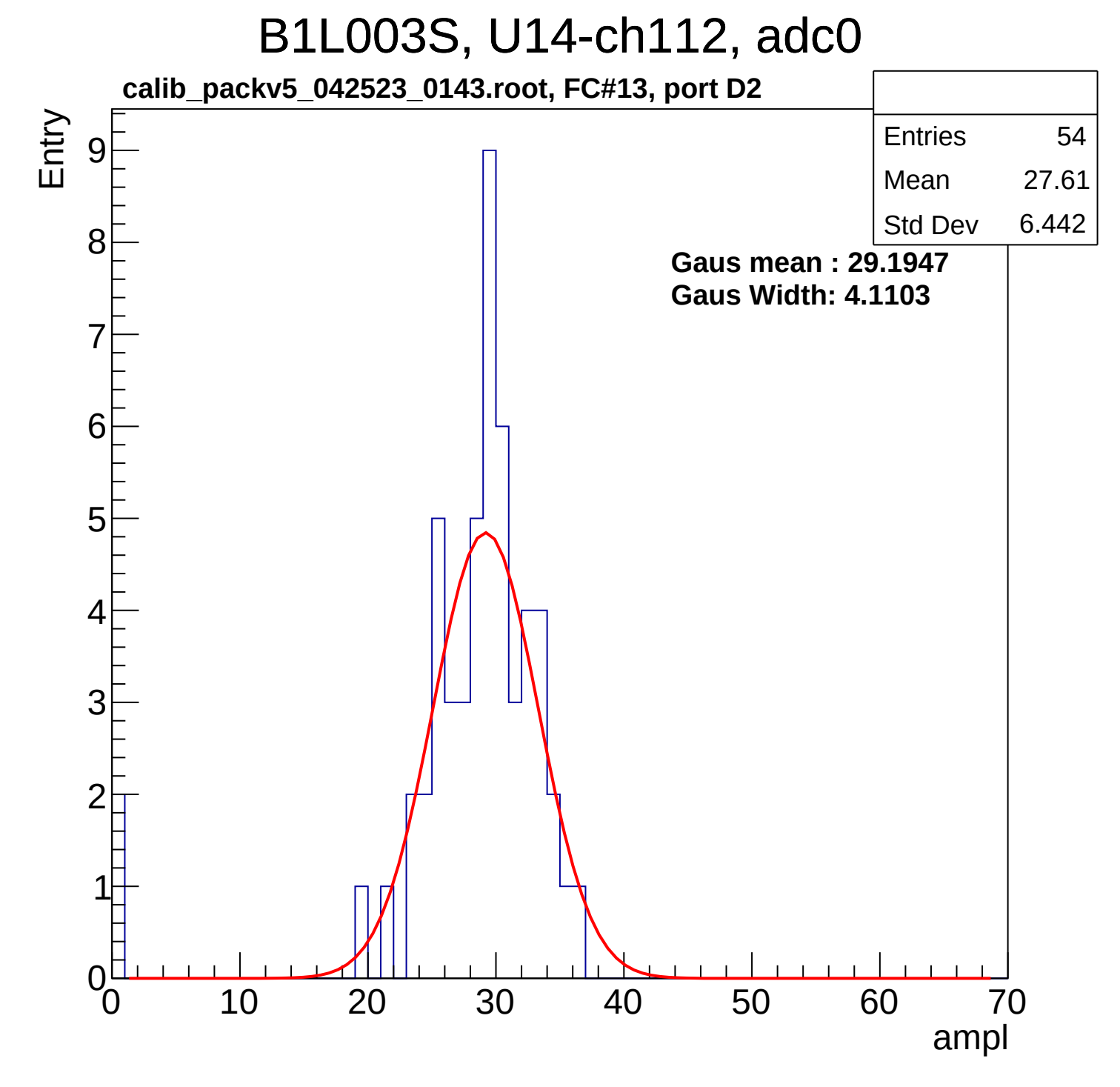

B1L003S, U14-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 88
Mean 48.28
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

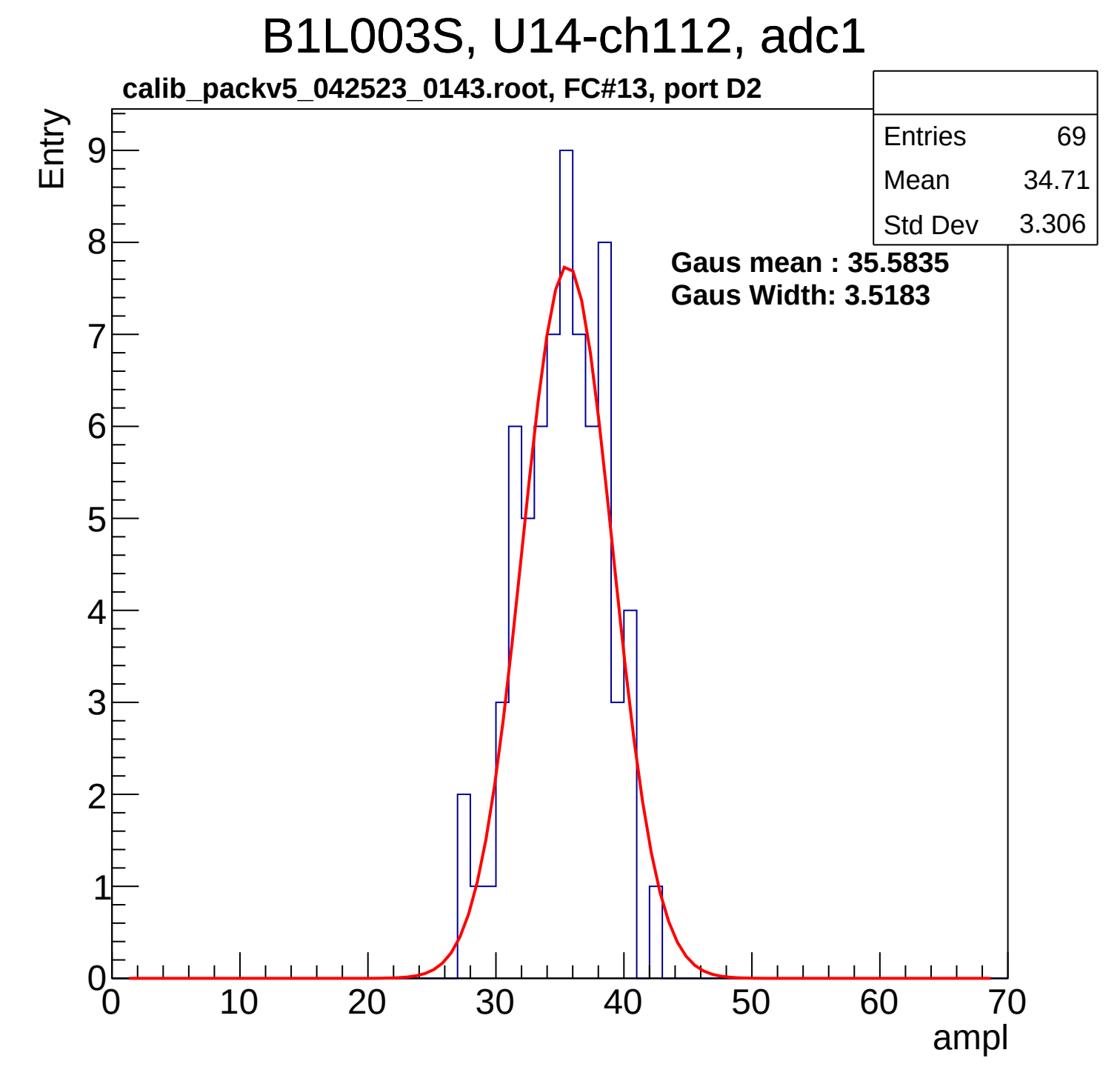

B1L003S, U14-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.65
Std Dev 3.212
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

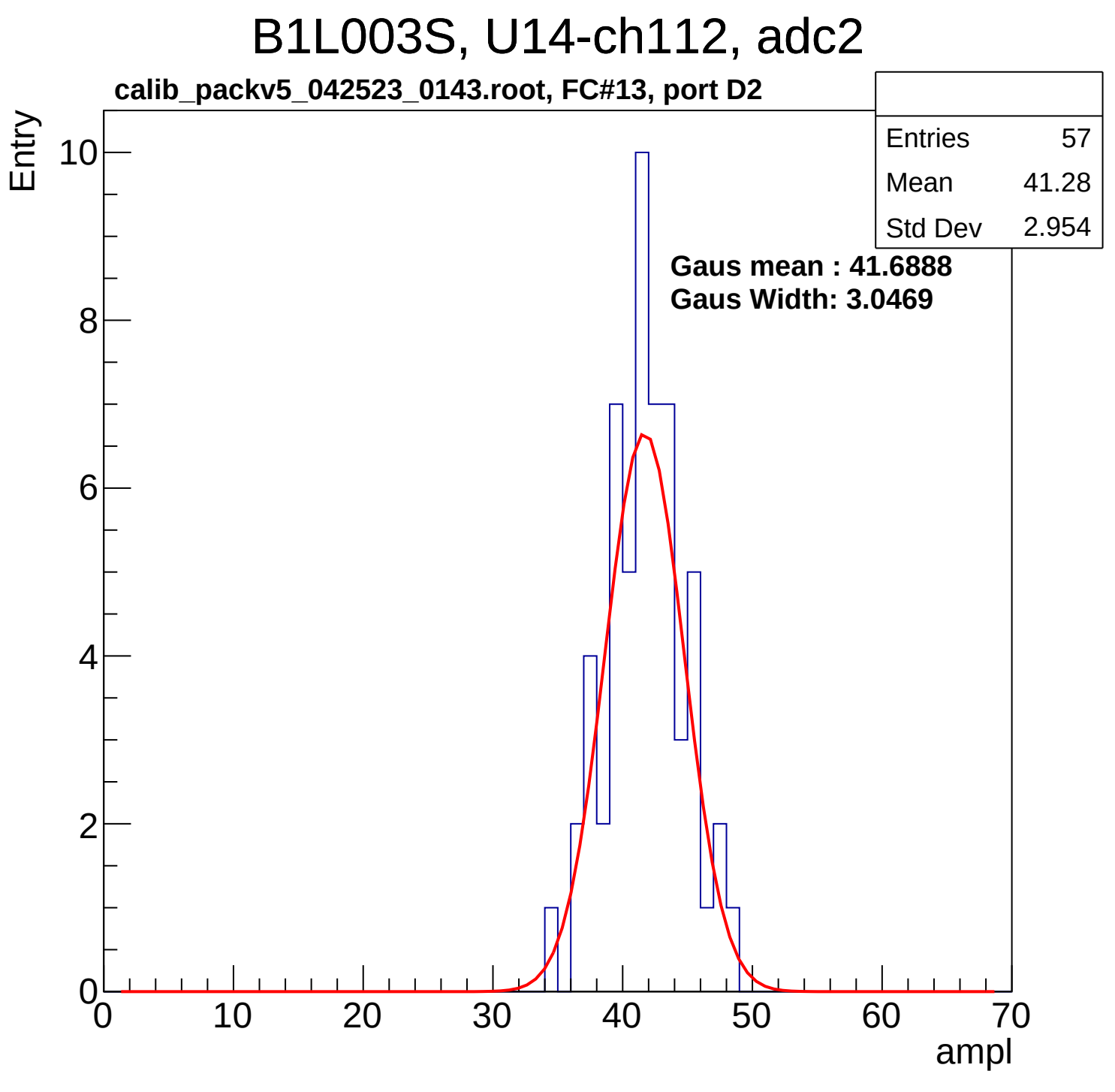

B1L003S, U14-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.31
Std Dev 8.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

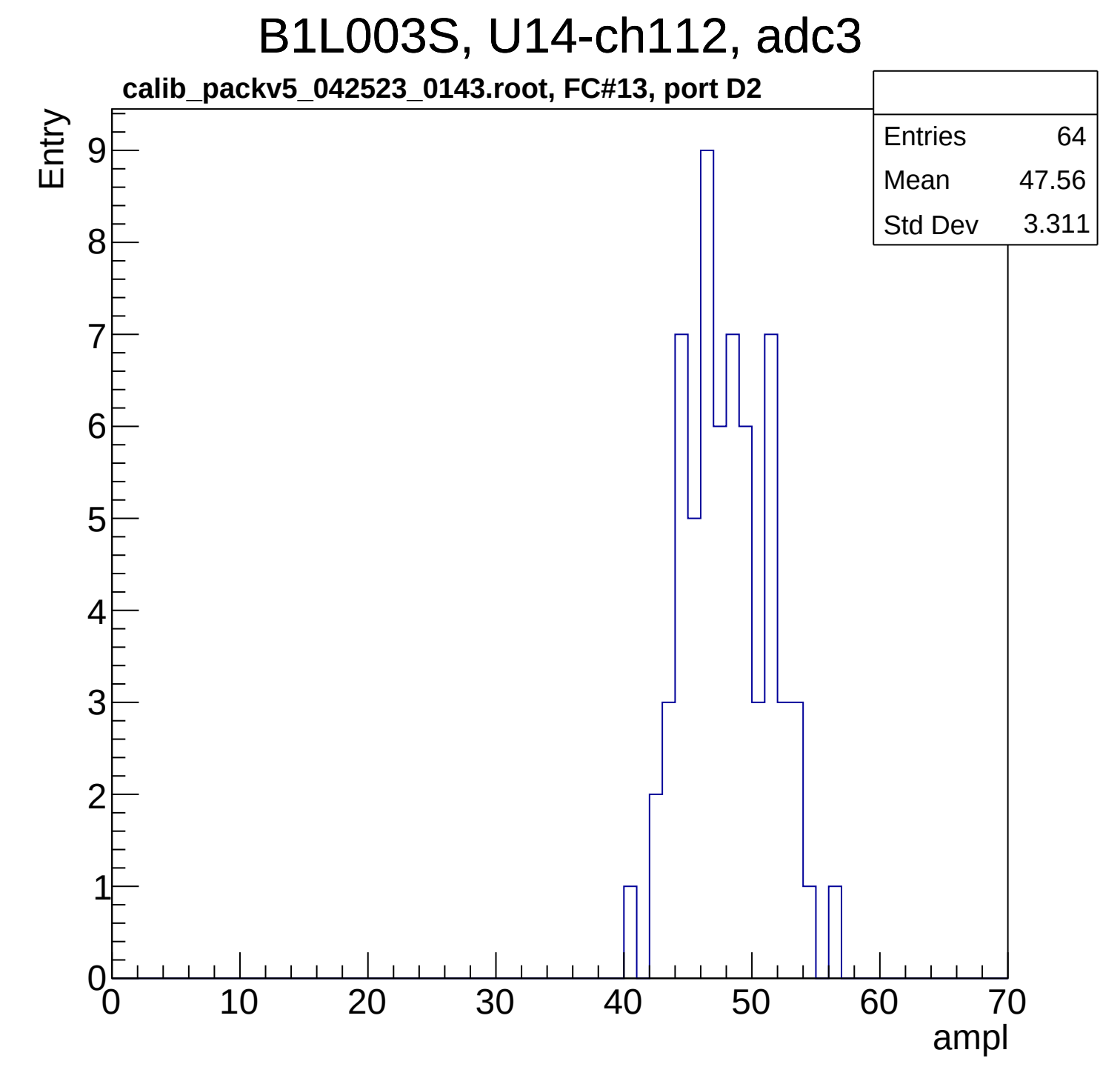

B1L003S, U14-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.29
Std Dev 1.485
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

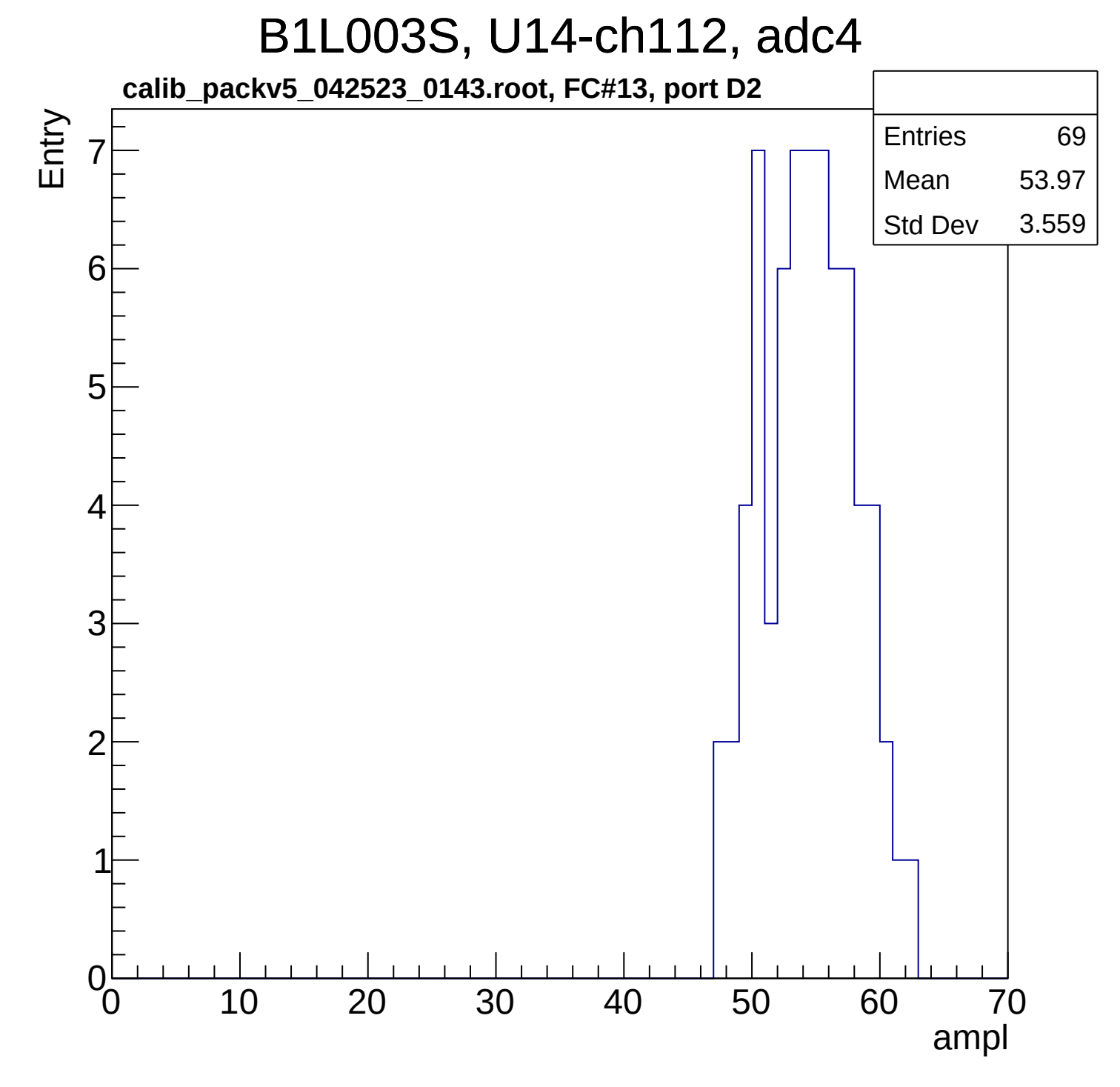

B1L003S, U14-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

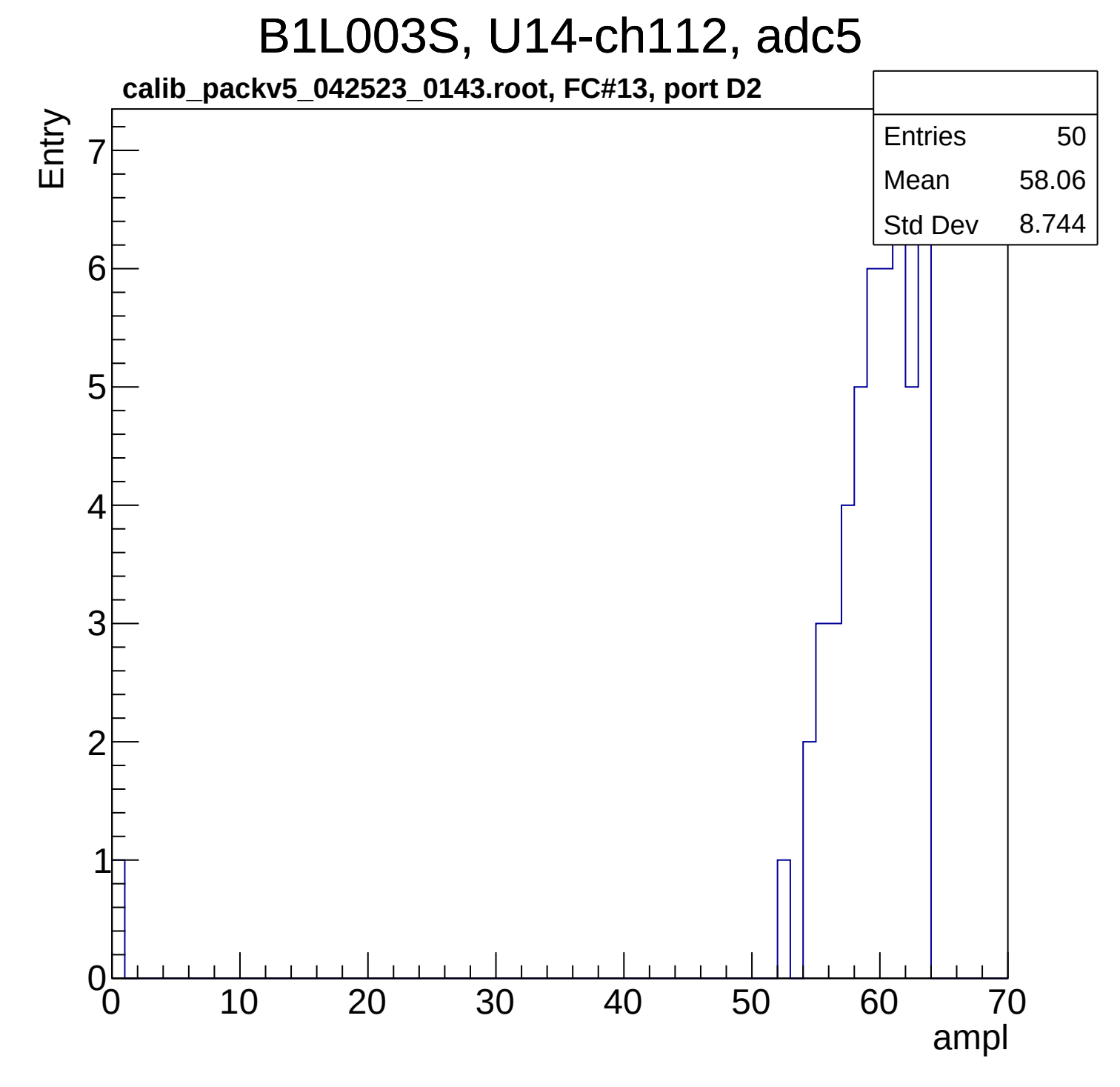

B1L003S, U14-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 27.61
Std Dev 6.442
Gaus mean : 29.1947
Gaus Width: 4.1103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

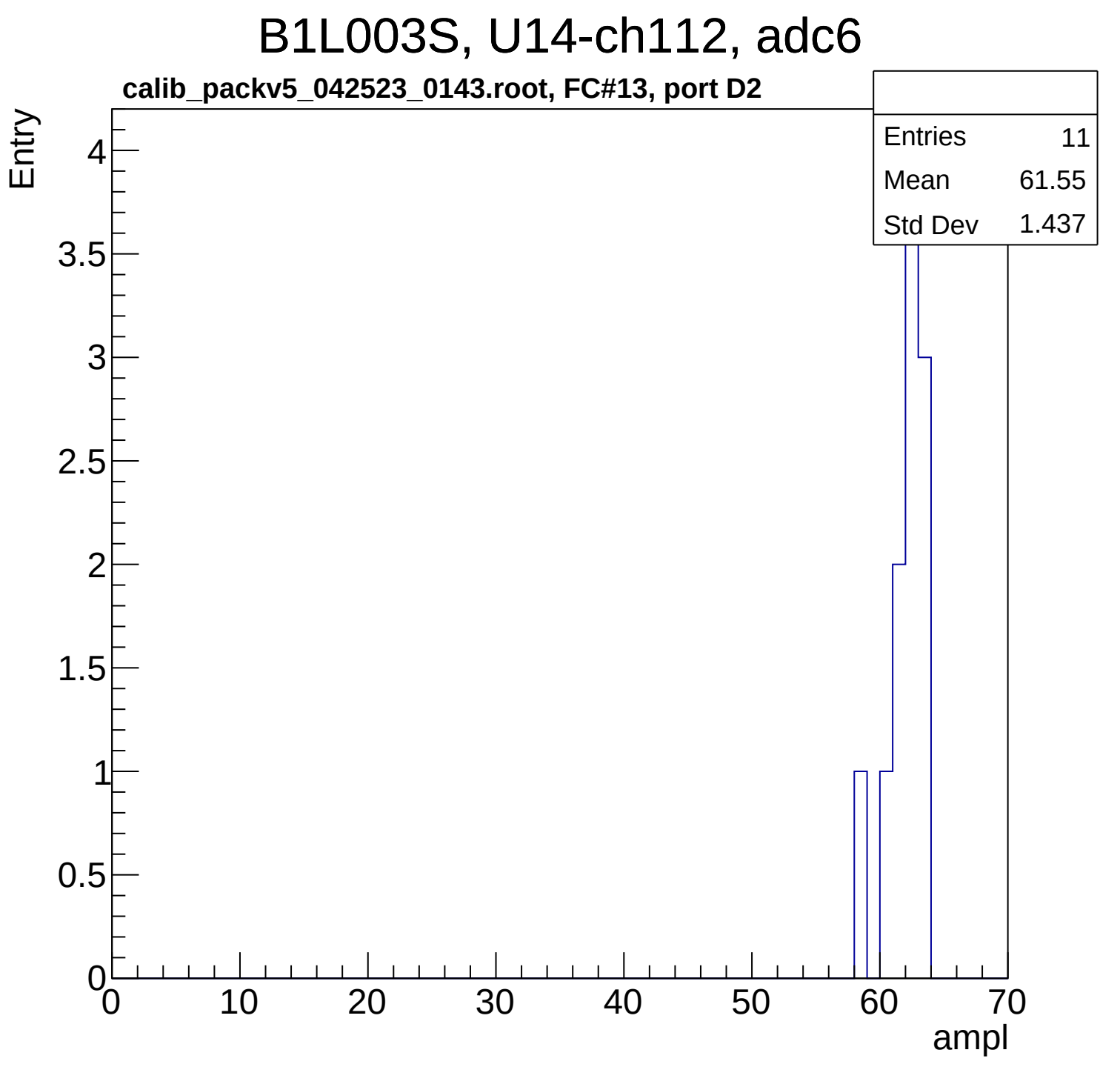

B1L003S, U14-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 34.71
Std Dev 3.306
Gaus mean : 35.5835
Gaus Width: 3.5183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

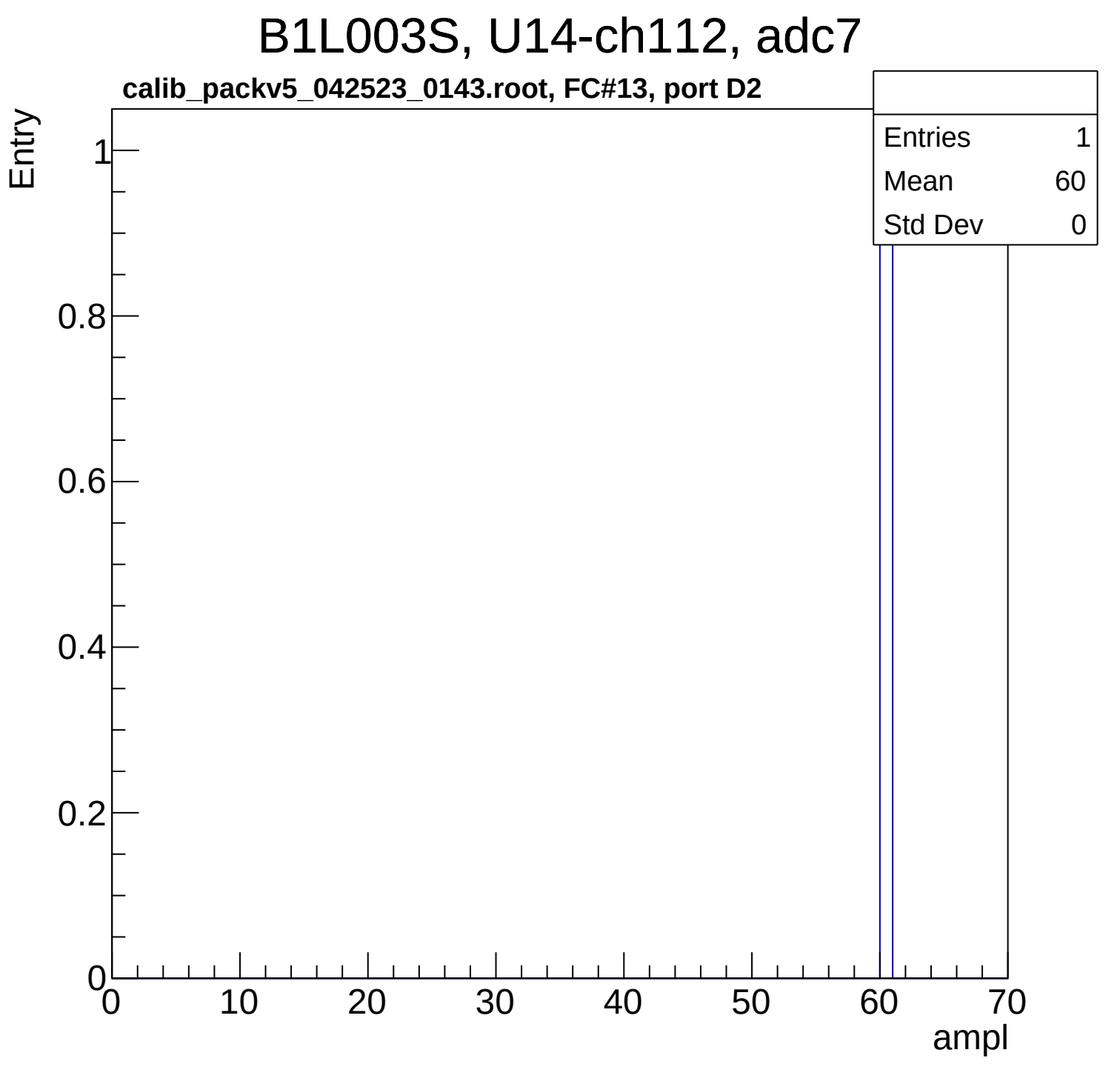

B1L003S, U14-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.28
Std Dev 2.954
Gaus mean : 41.6888
Gaus Width: 3.0469
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

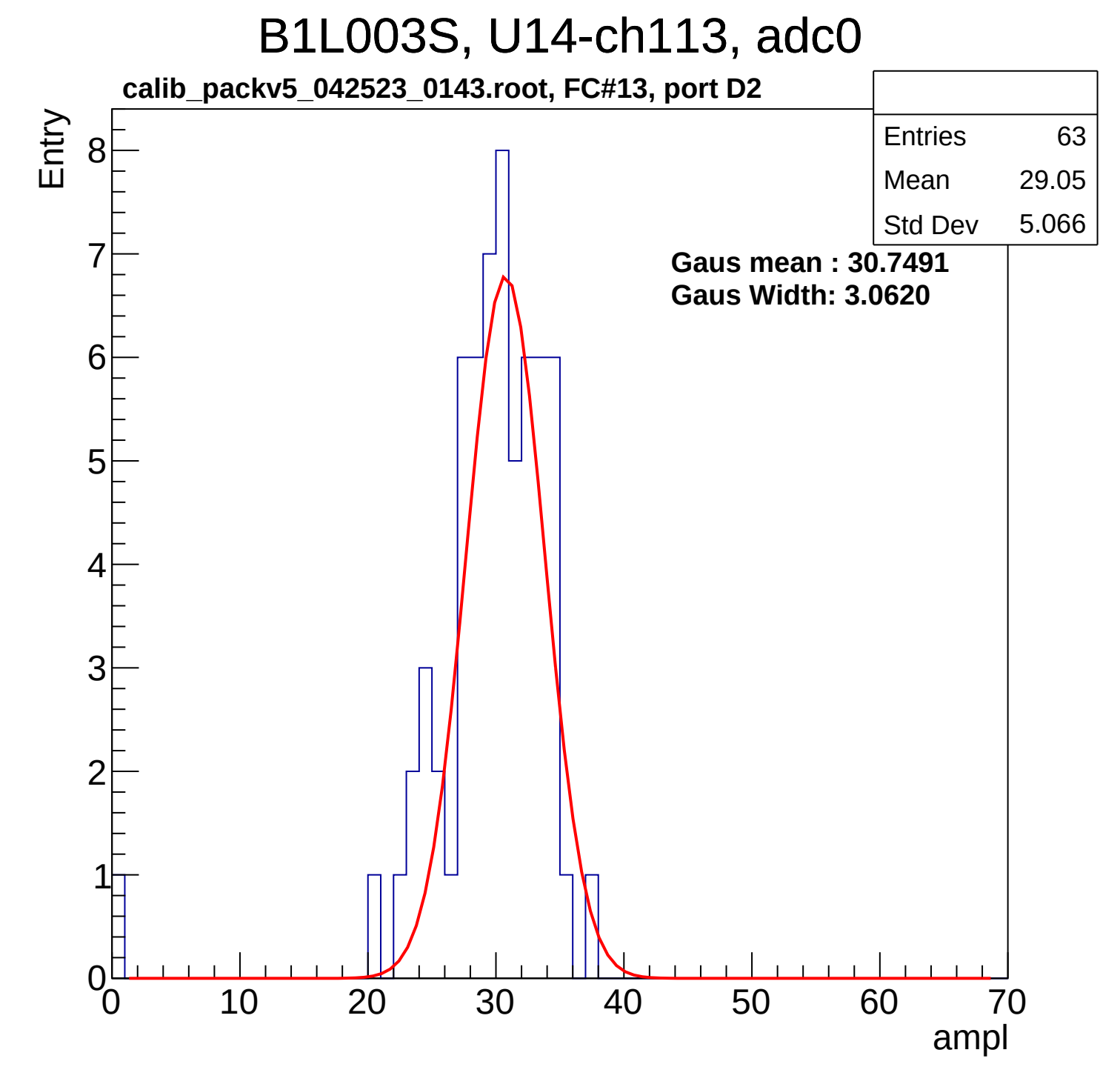

B1L003S, U14-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.56
Std Dev 3.311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

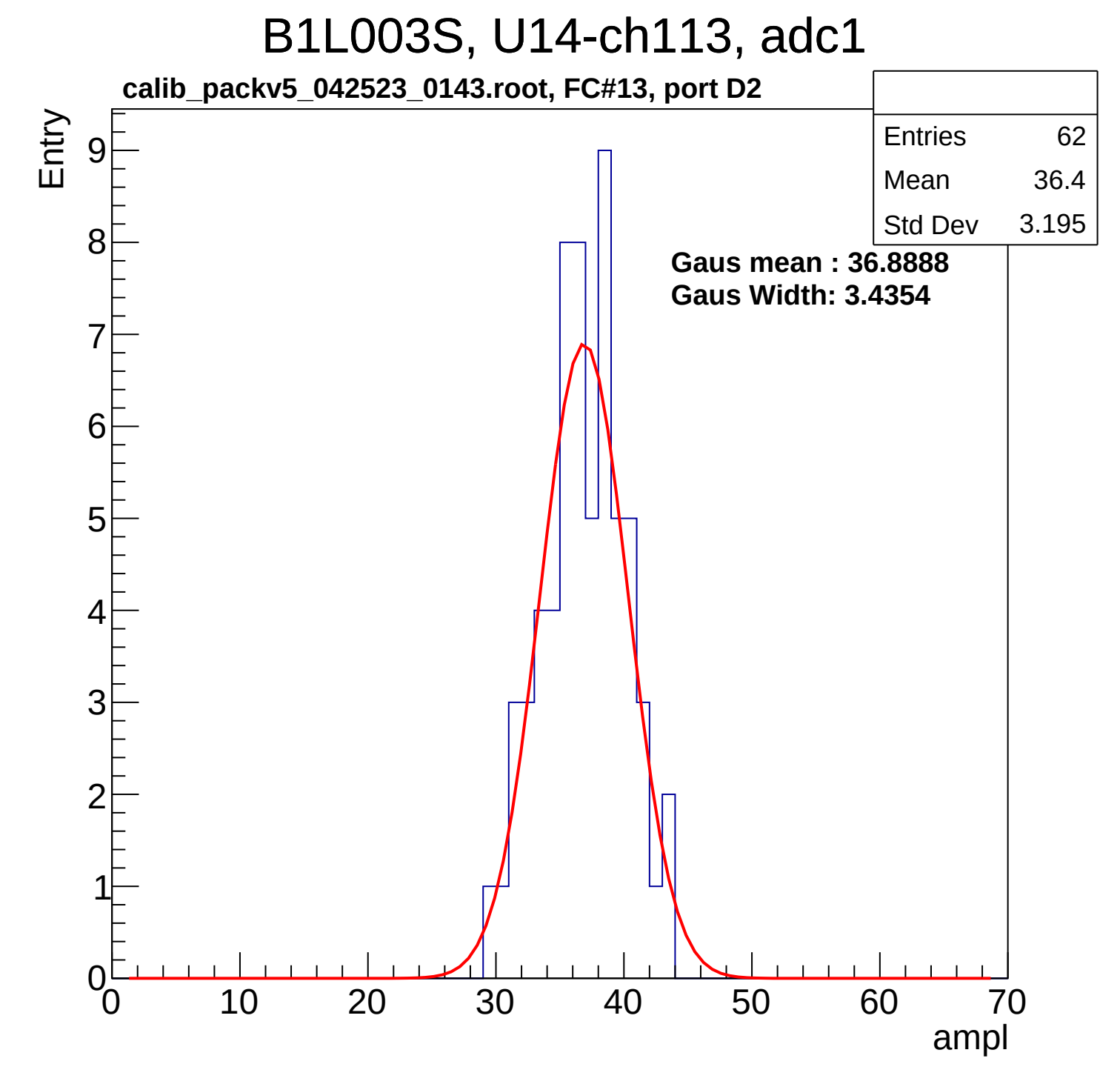

B1L003S, U14-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 53.97
Std Dev 3.559
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

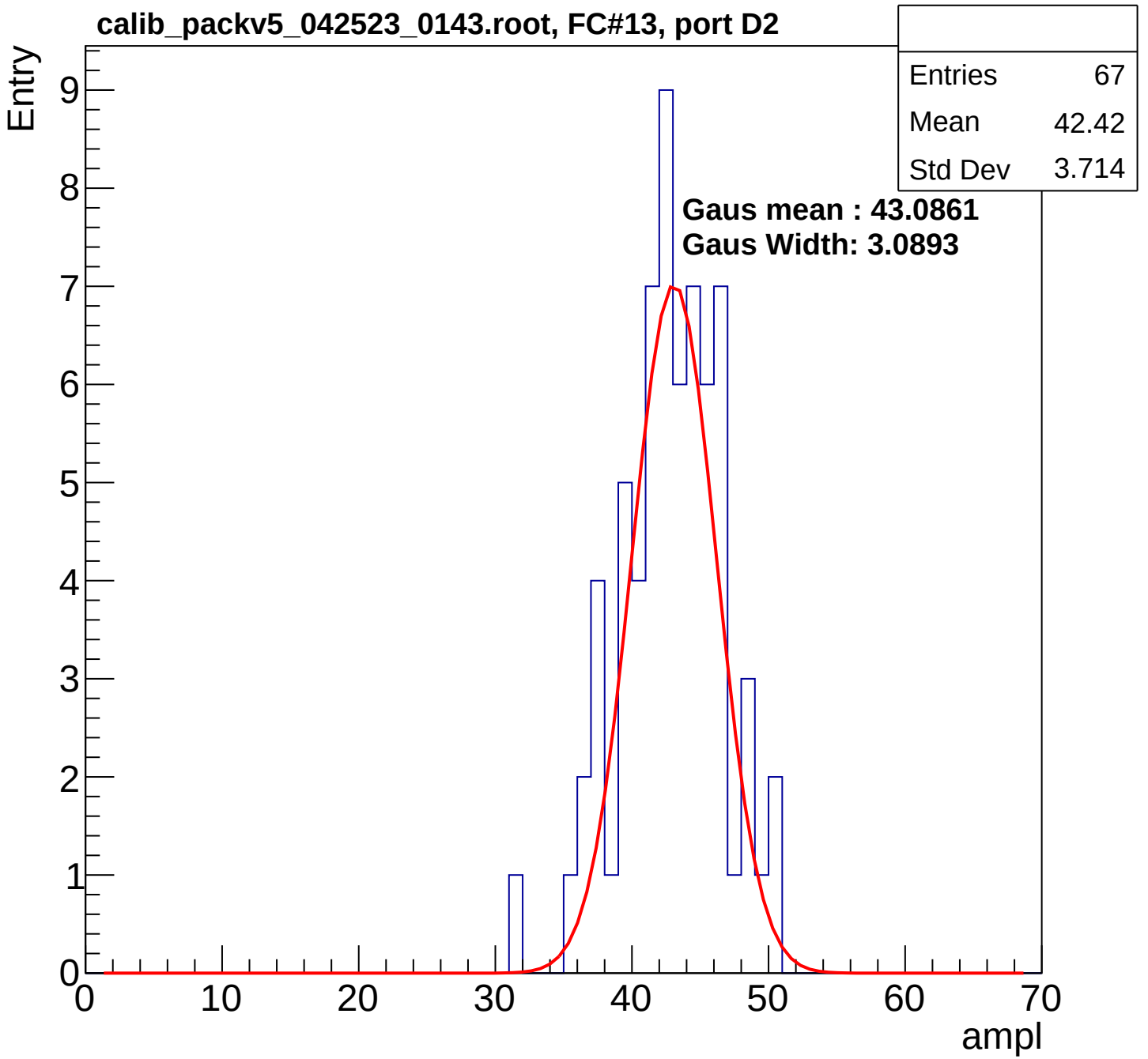

B1L003S, U14-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.06
Std Dev 8.744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

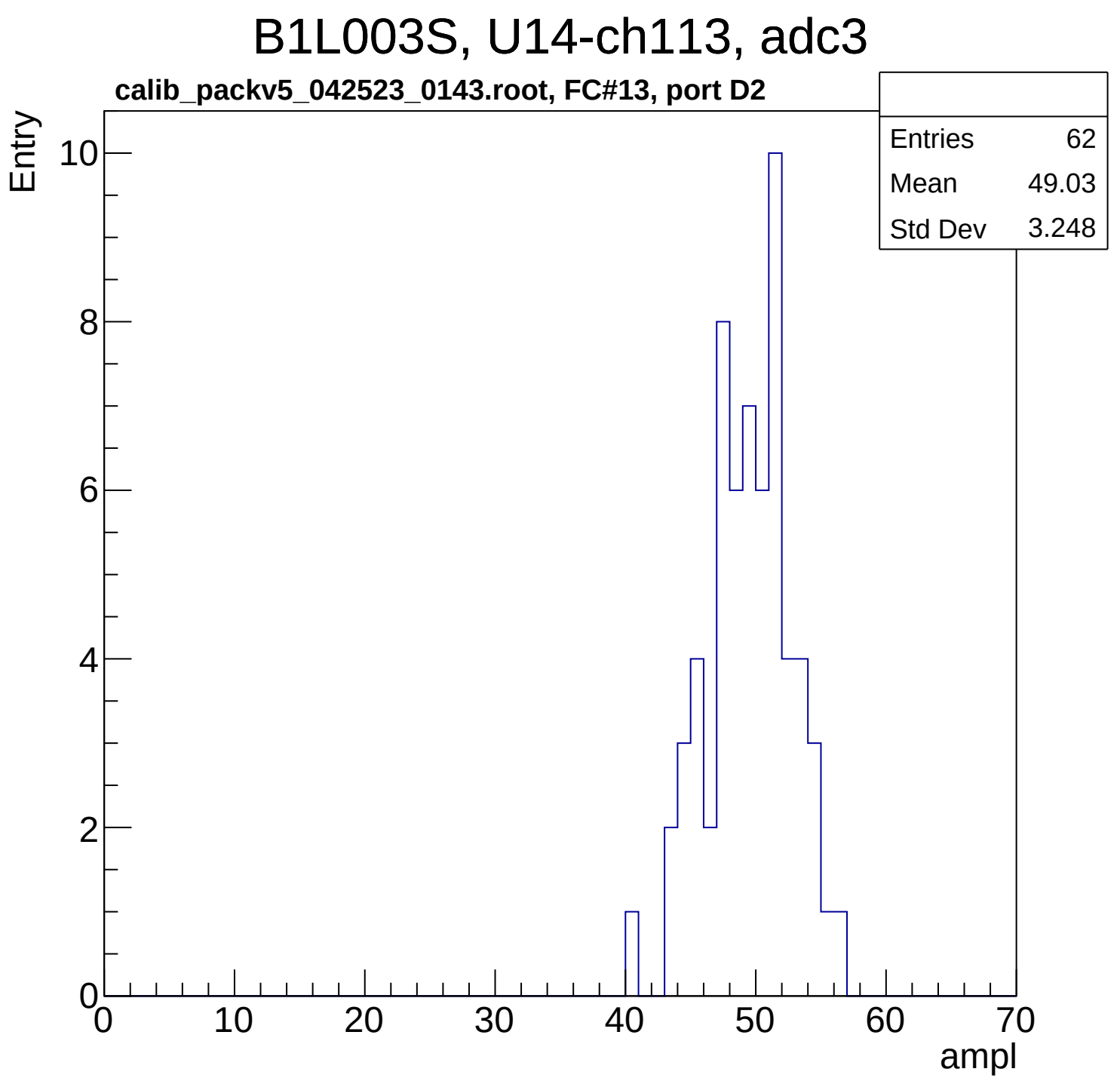

B1L003S, U14-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.55
Std Dev 1.437
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

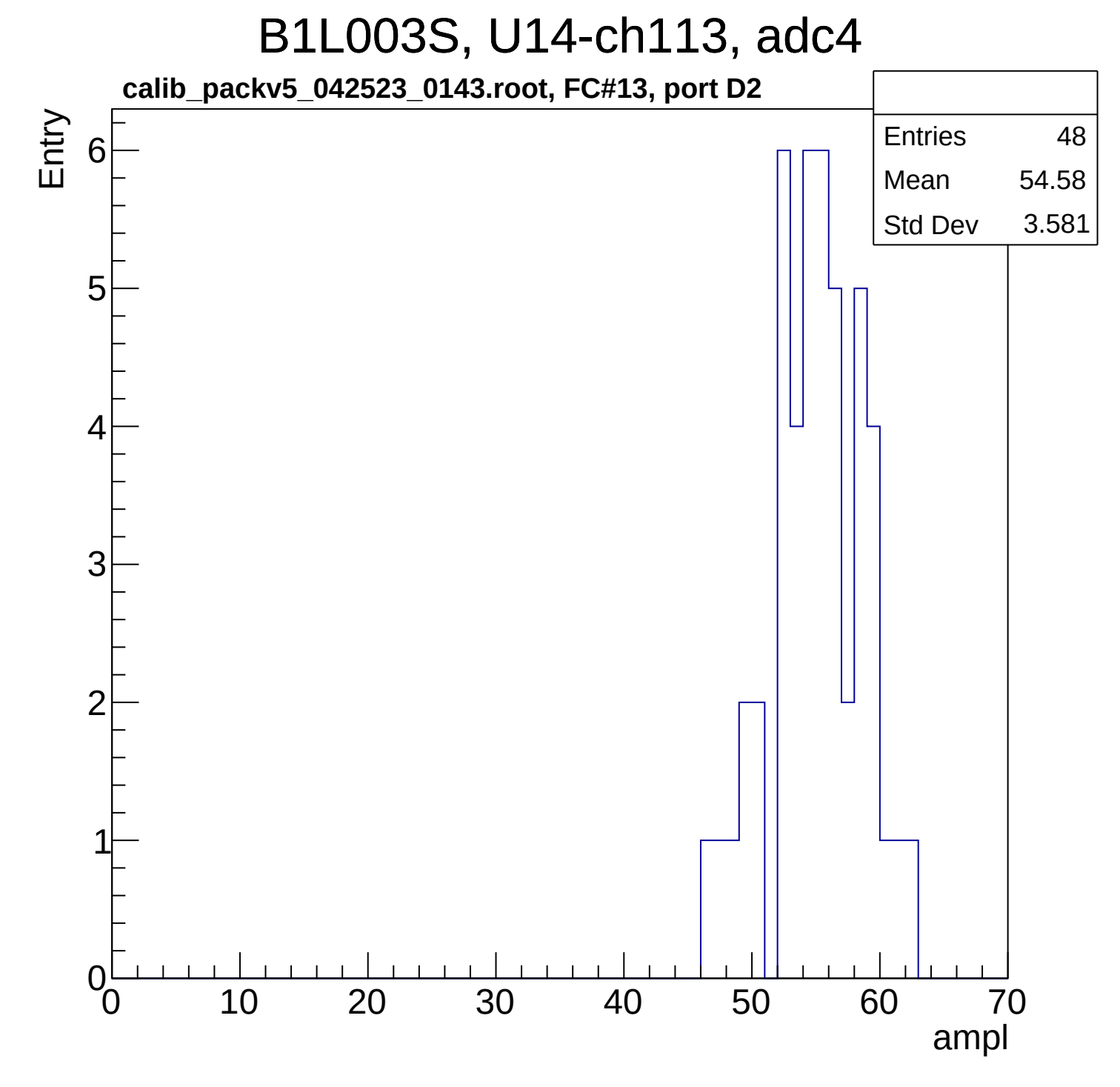

B1L003S, U14-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

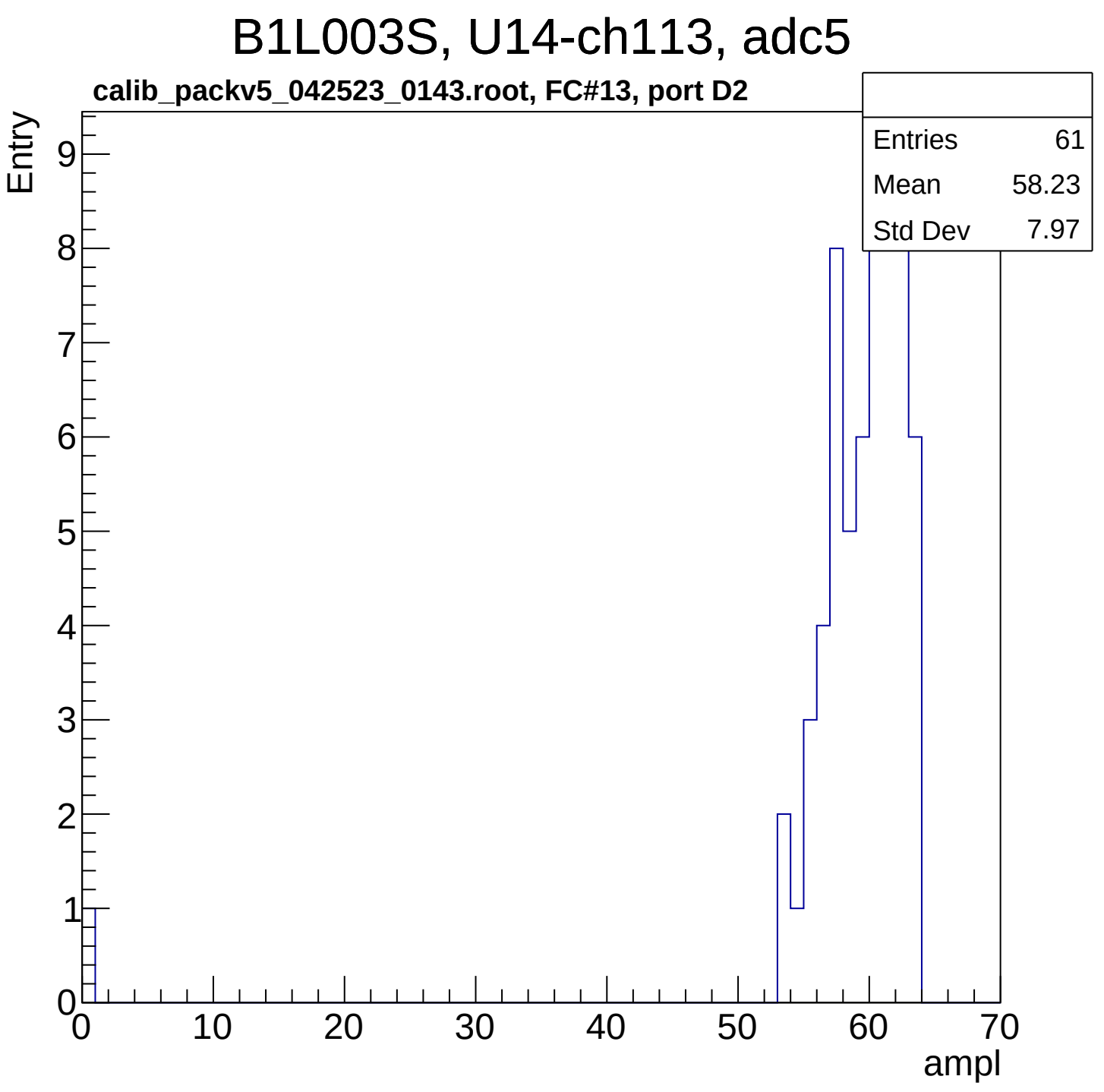

B1L003S, U14-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.05
Std Dev 5.066
Gaus mean : 30.7491
Gaus Width: 3.0620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

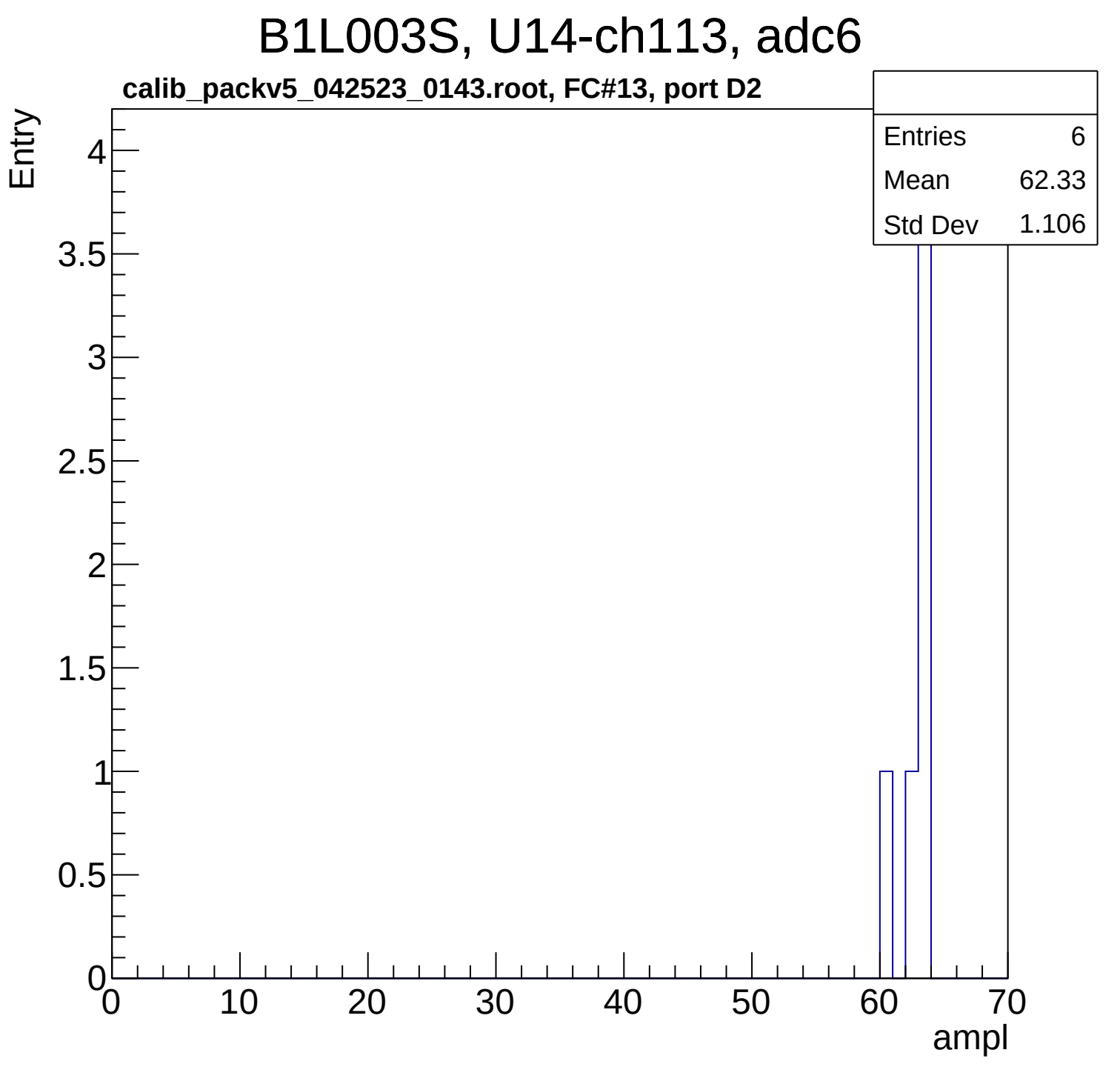

B1L003S, U14-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.4
Std Dev 3.195
Gaus mean : 36.8888
Gaus Width: 3.4354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

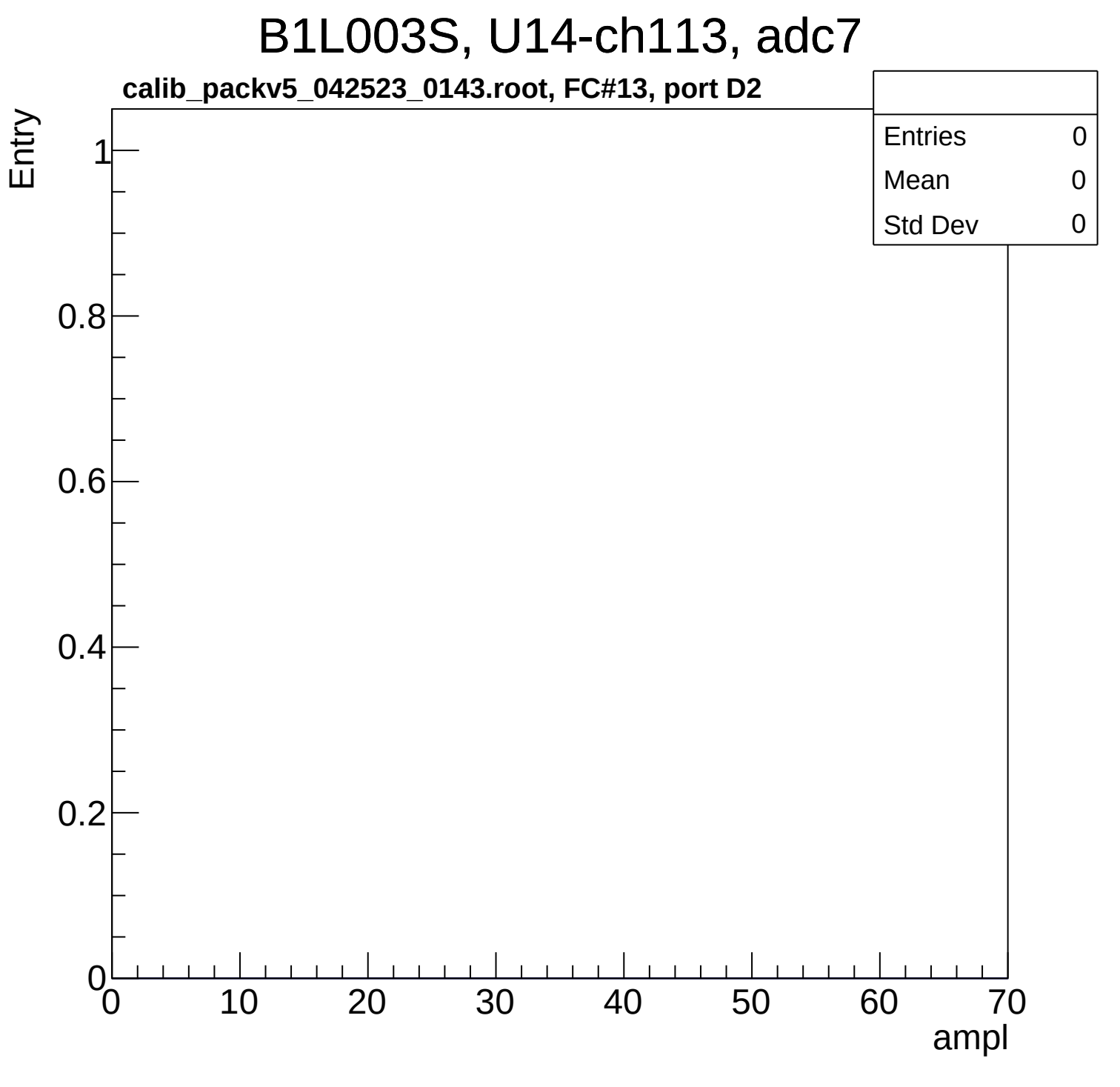

B1L003S, U14-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.42
Std Dev 3.714
Gaus mean : 43.0861
Gaus Width: 3.0893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

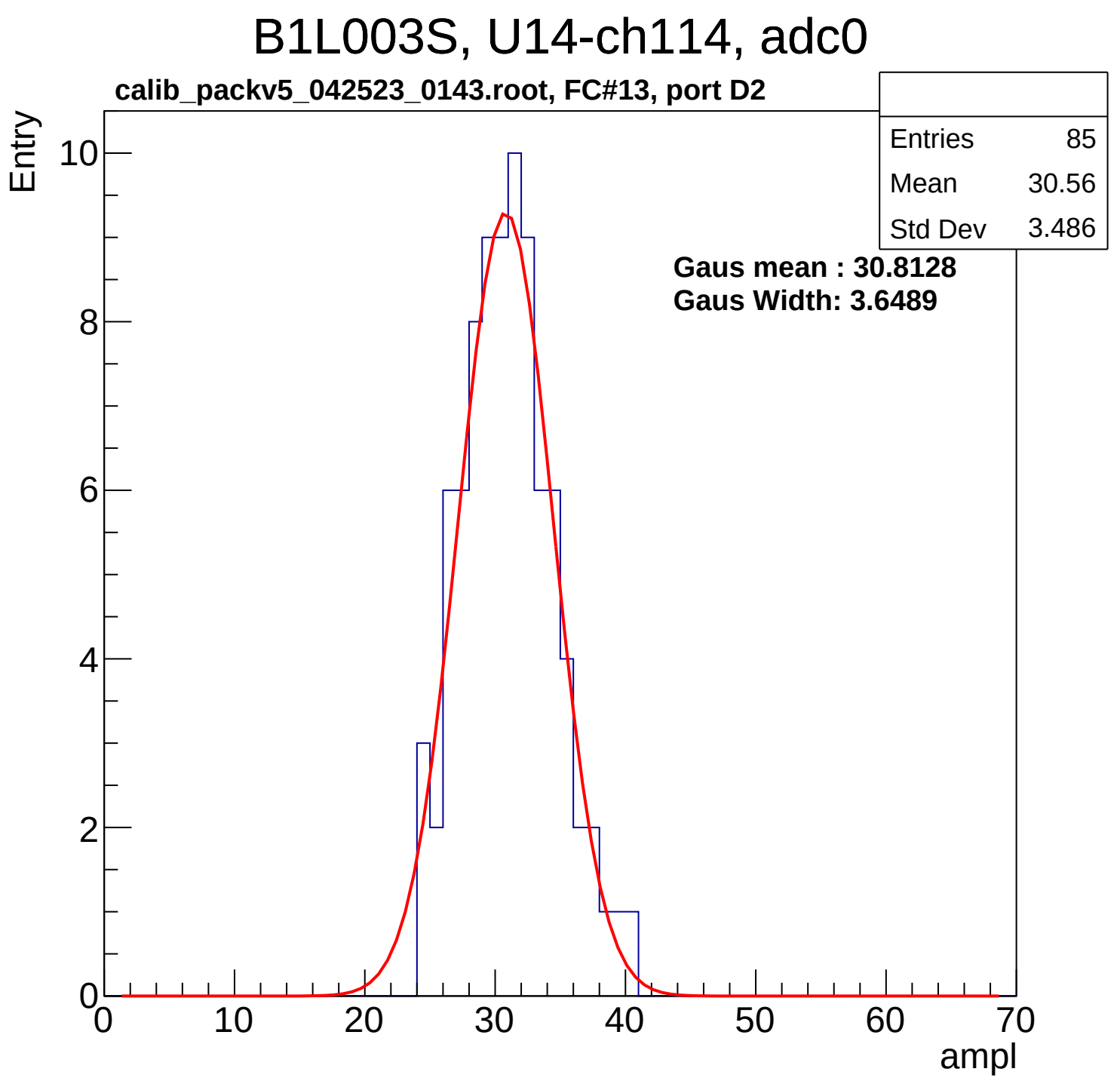

B1L003S, U14-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 49.03
Std Dev 3.248
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

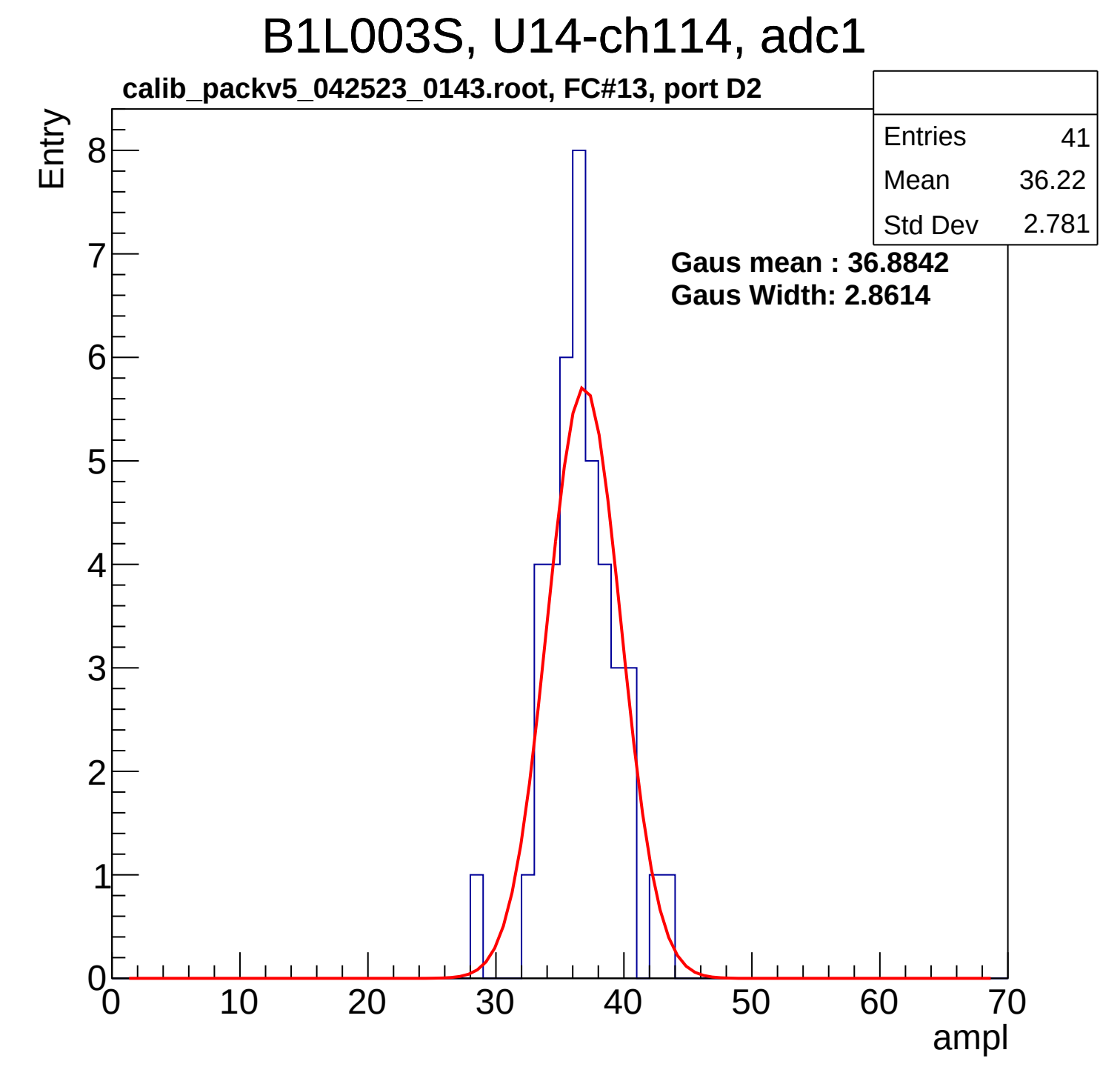

B1L003S, U14-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.58
Std Dev 3.581
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

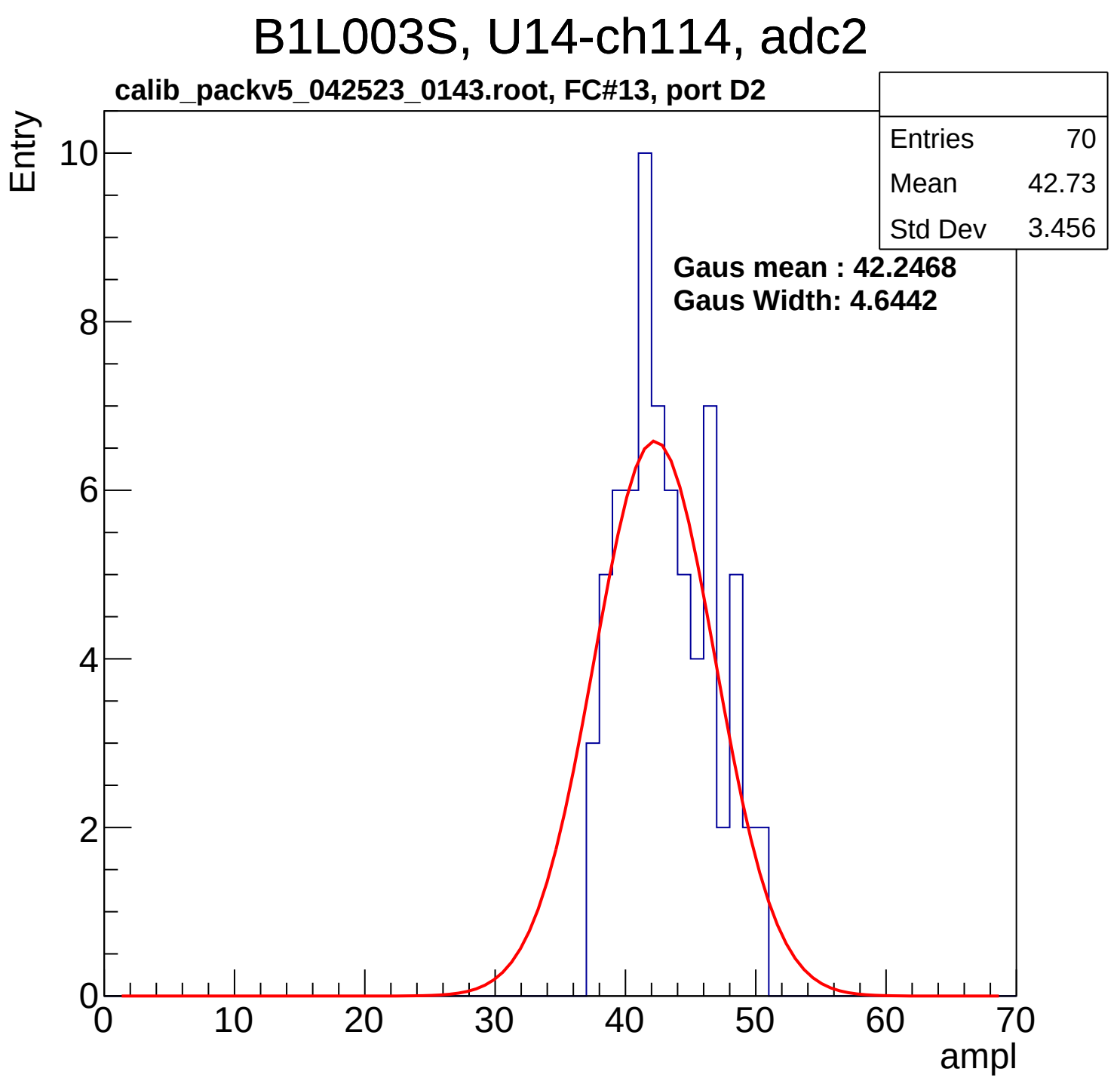

B1L003S, U14-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.23
Std Dev 7.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

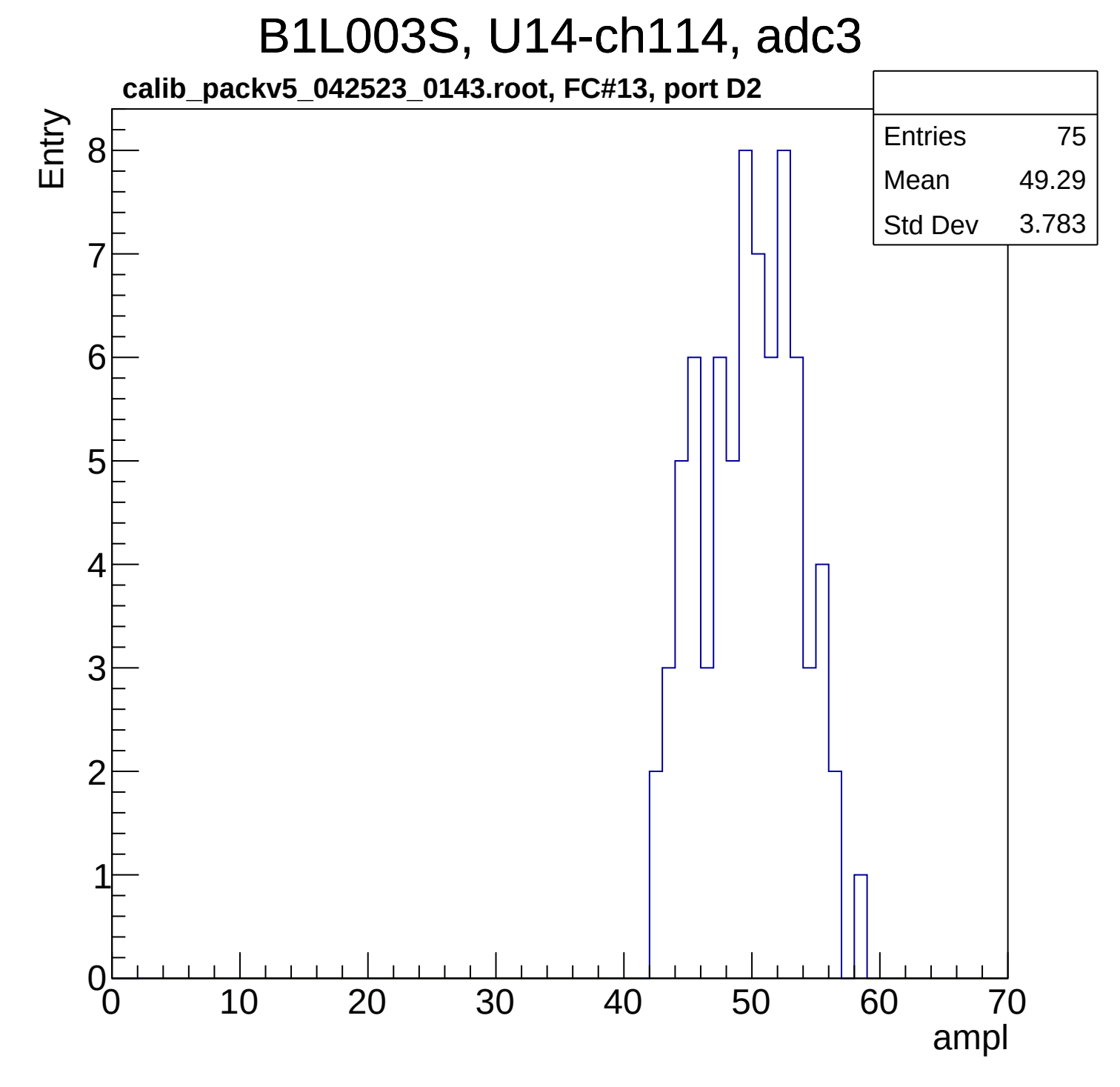

B1L003S, U14-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 1.106
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

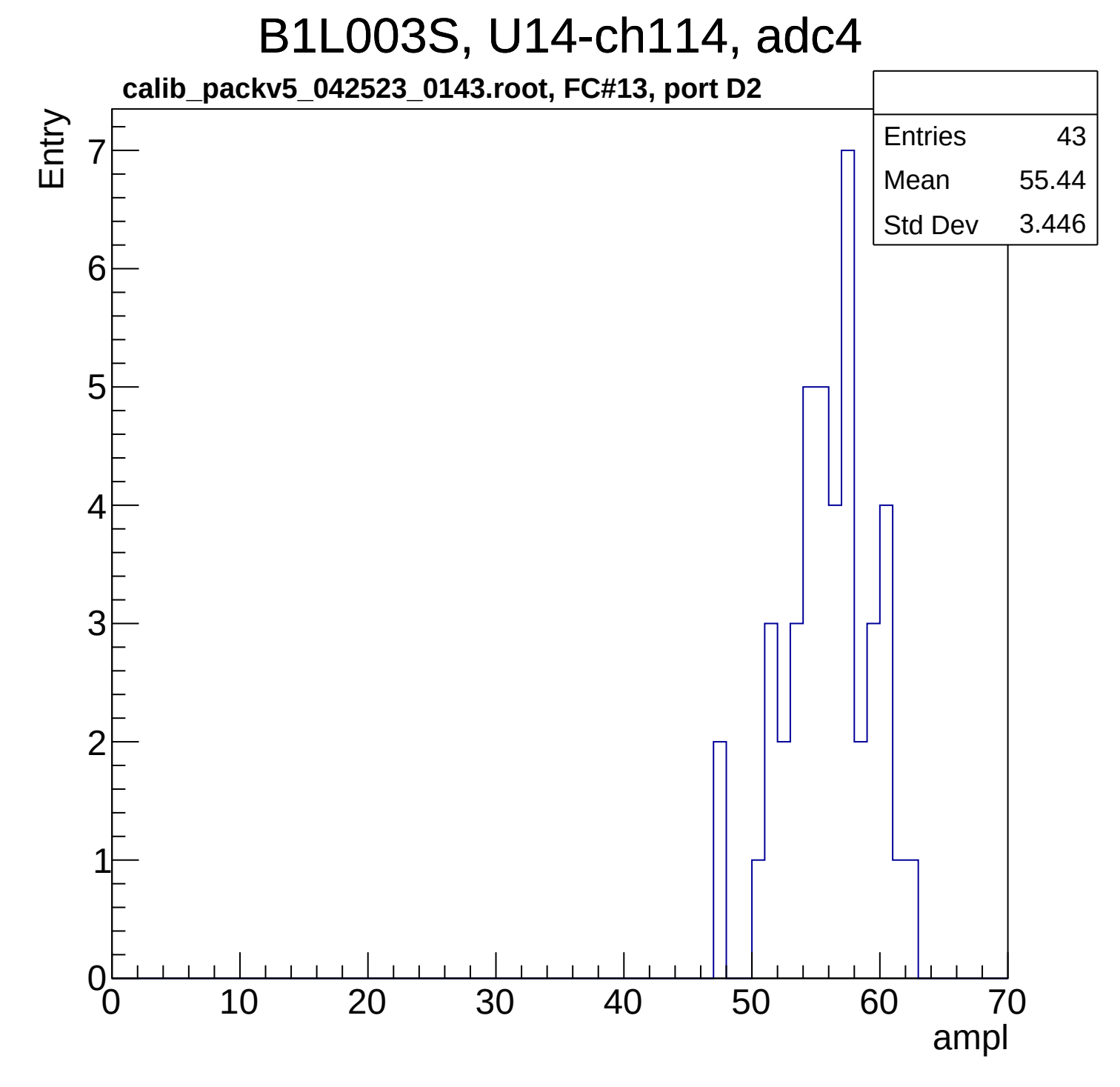

B1L003S, U14-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

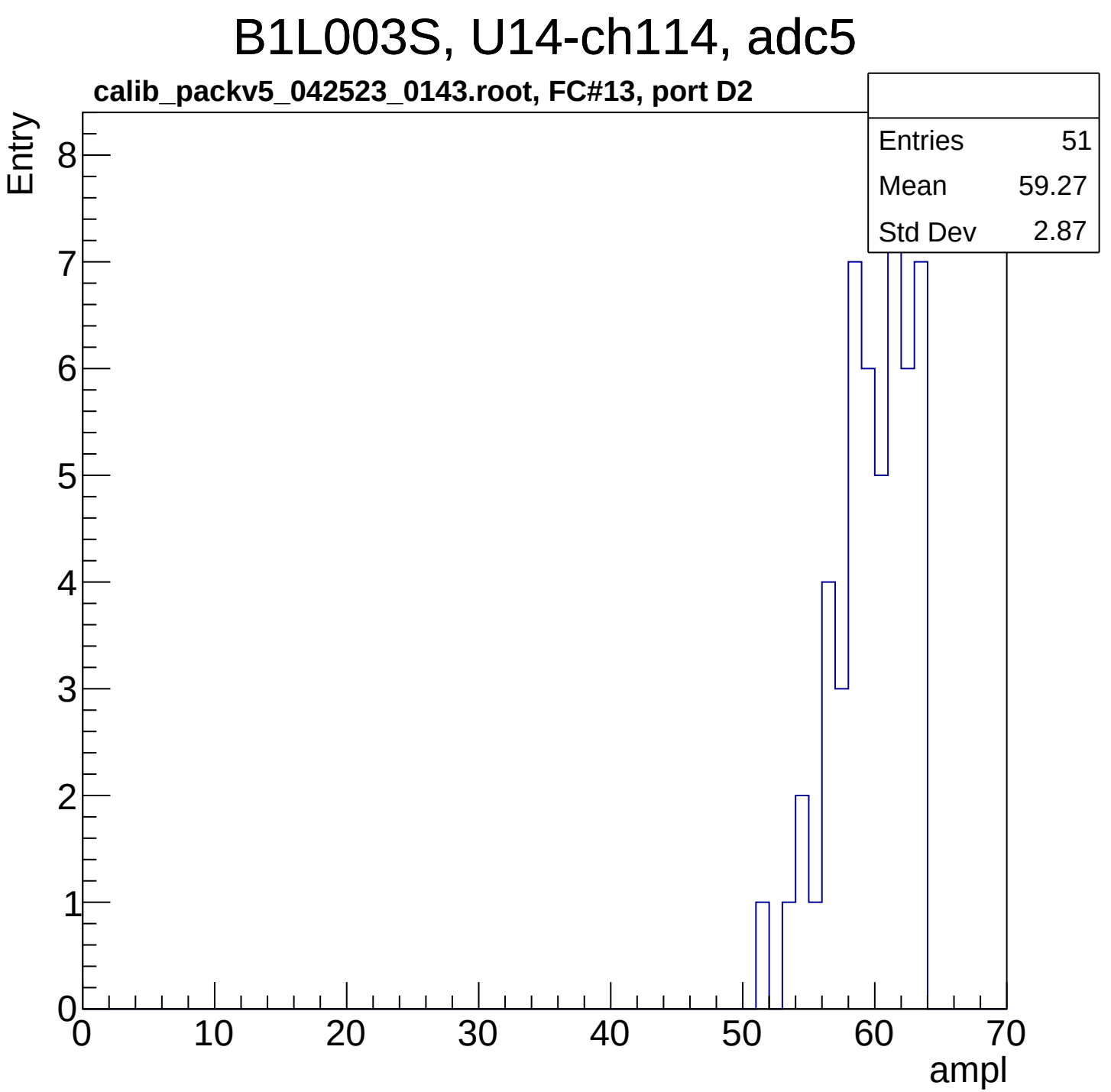

B1L003S, U14-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 30.56
Std Dev 3.486
Gaus mean : 30.8128
Gaus Width: 3.6489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

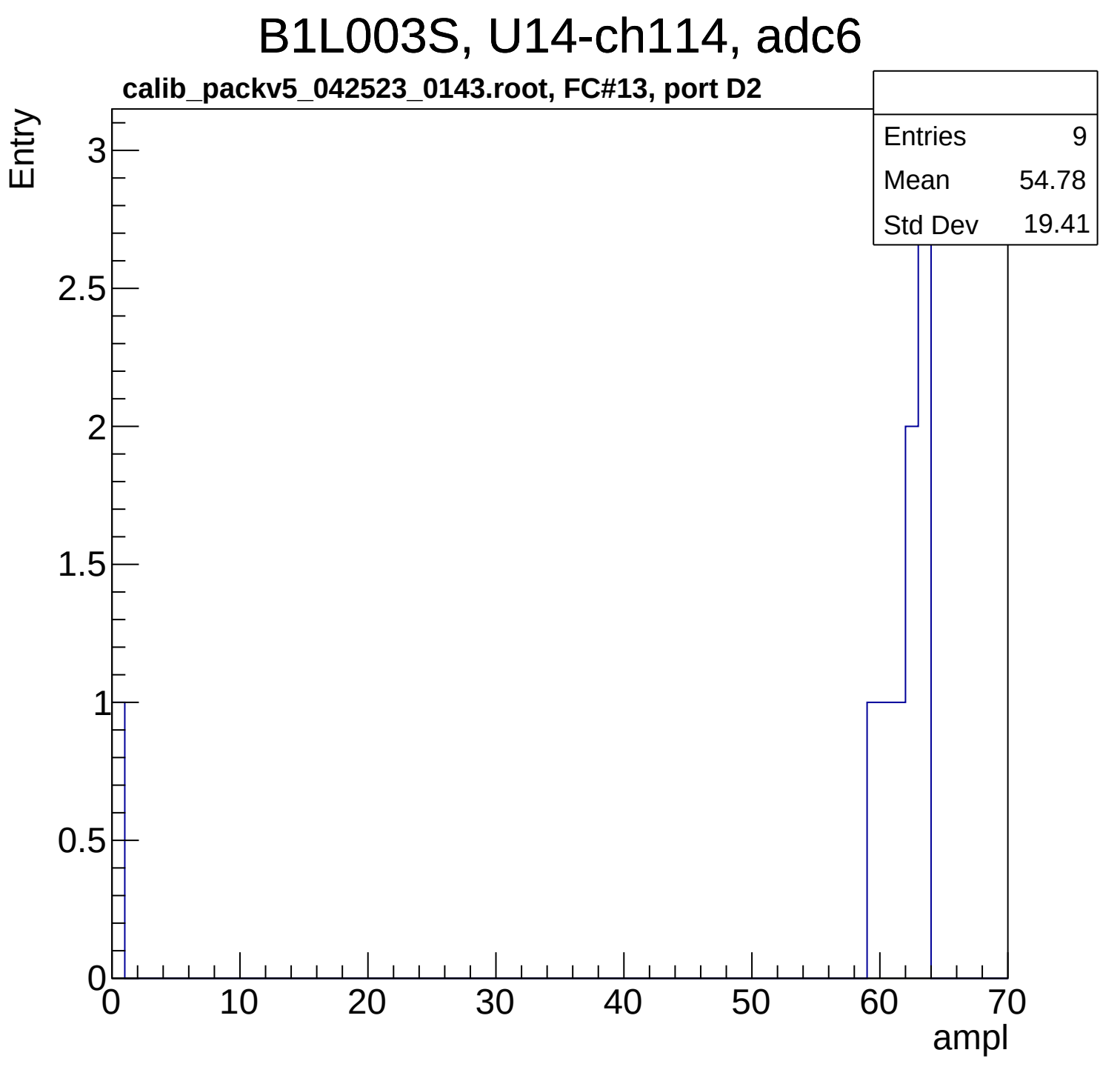

B1L003S, U14-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 36.22
Std Dev 2.781
Gaus mean : 36.8842
Gaus Width: 2.8614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

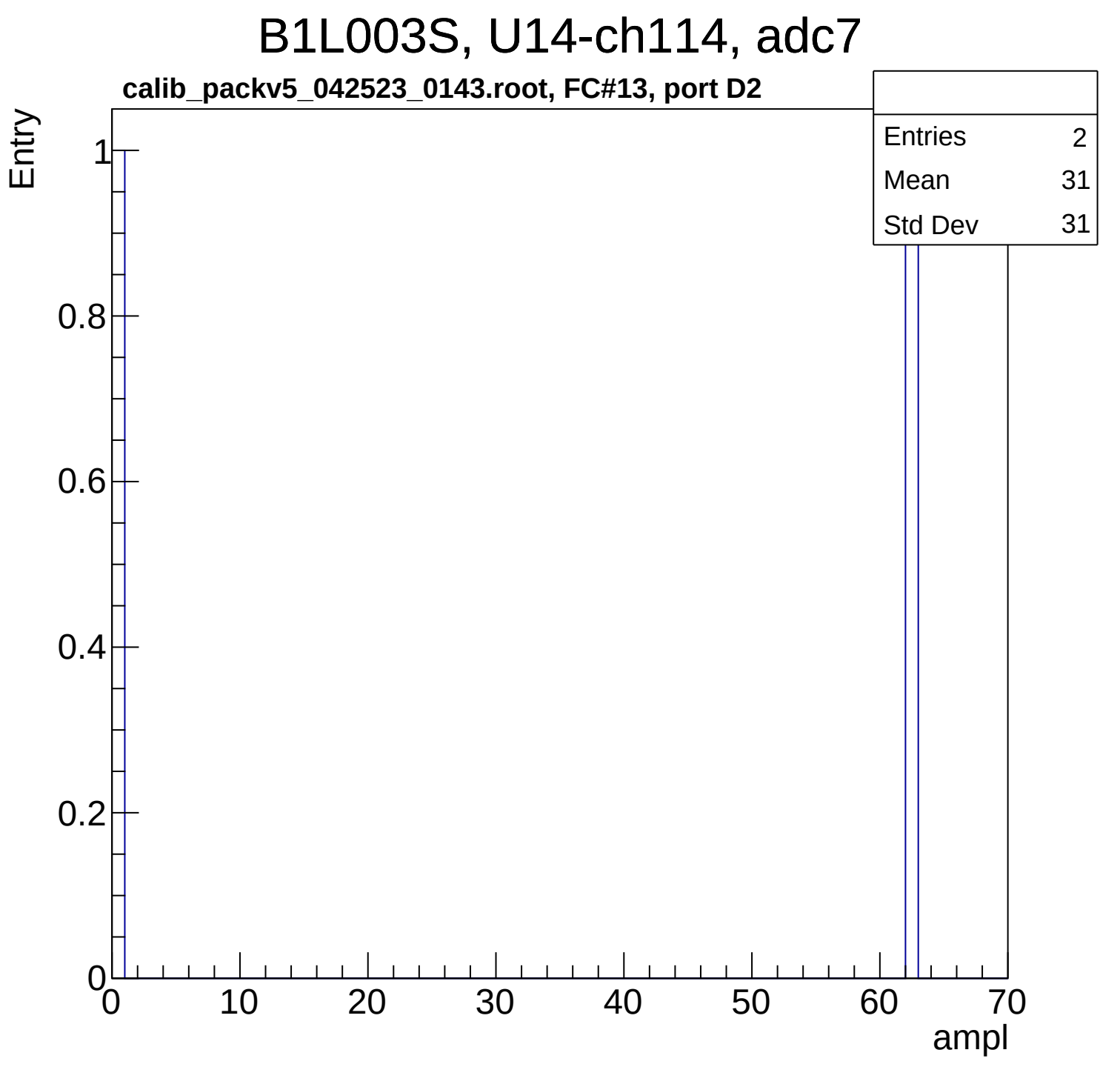

B1L003S, U14-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.73
Std Dev 3.456
Gaus mean : 42.2468
Gaus Width: 4.6442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

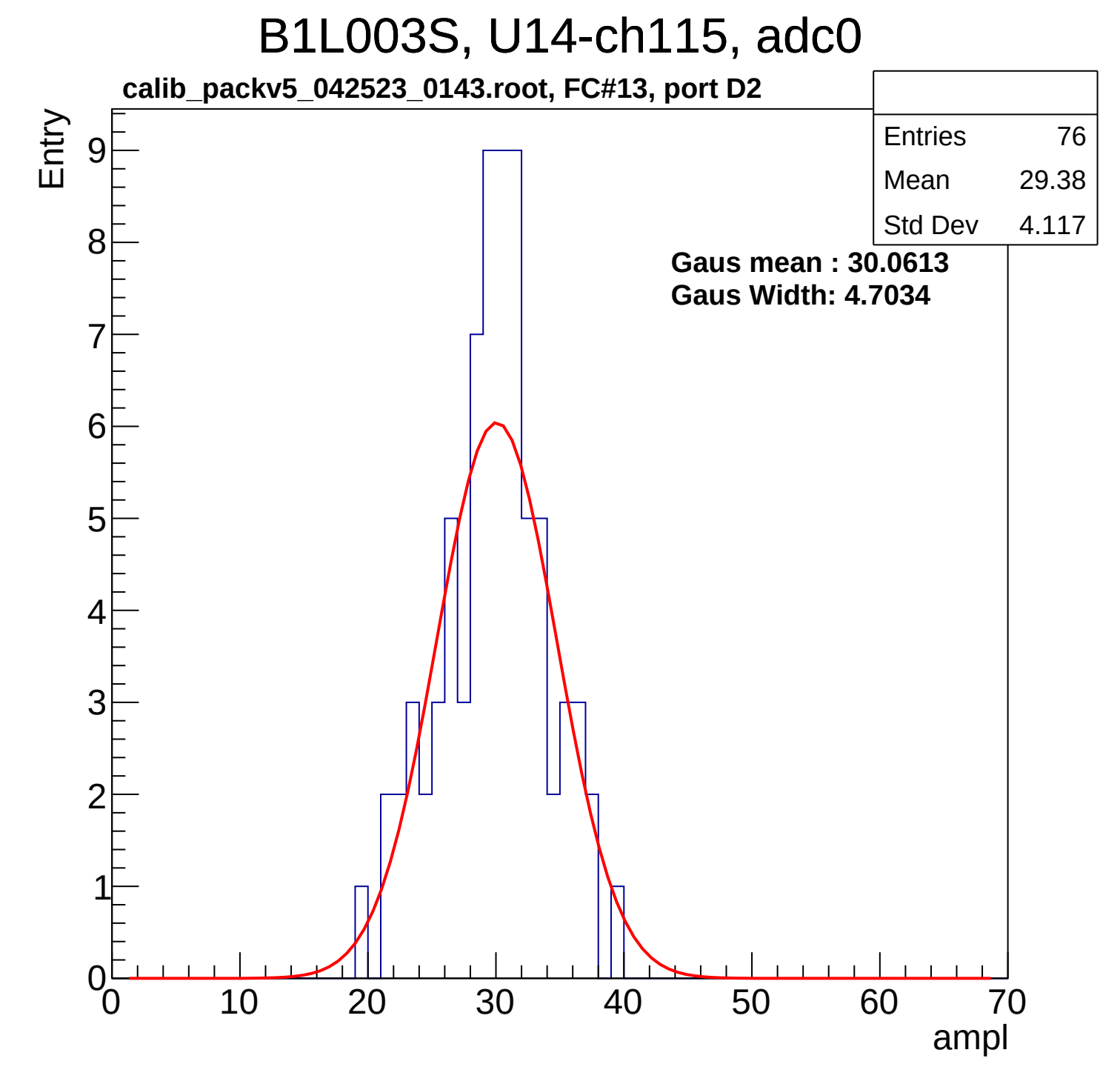

B1L003S, U14-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.29
Std Dev 3.783
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

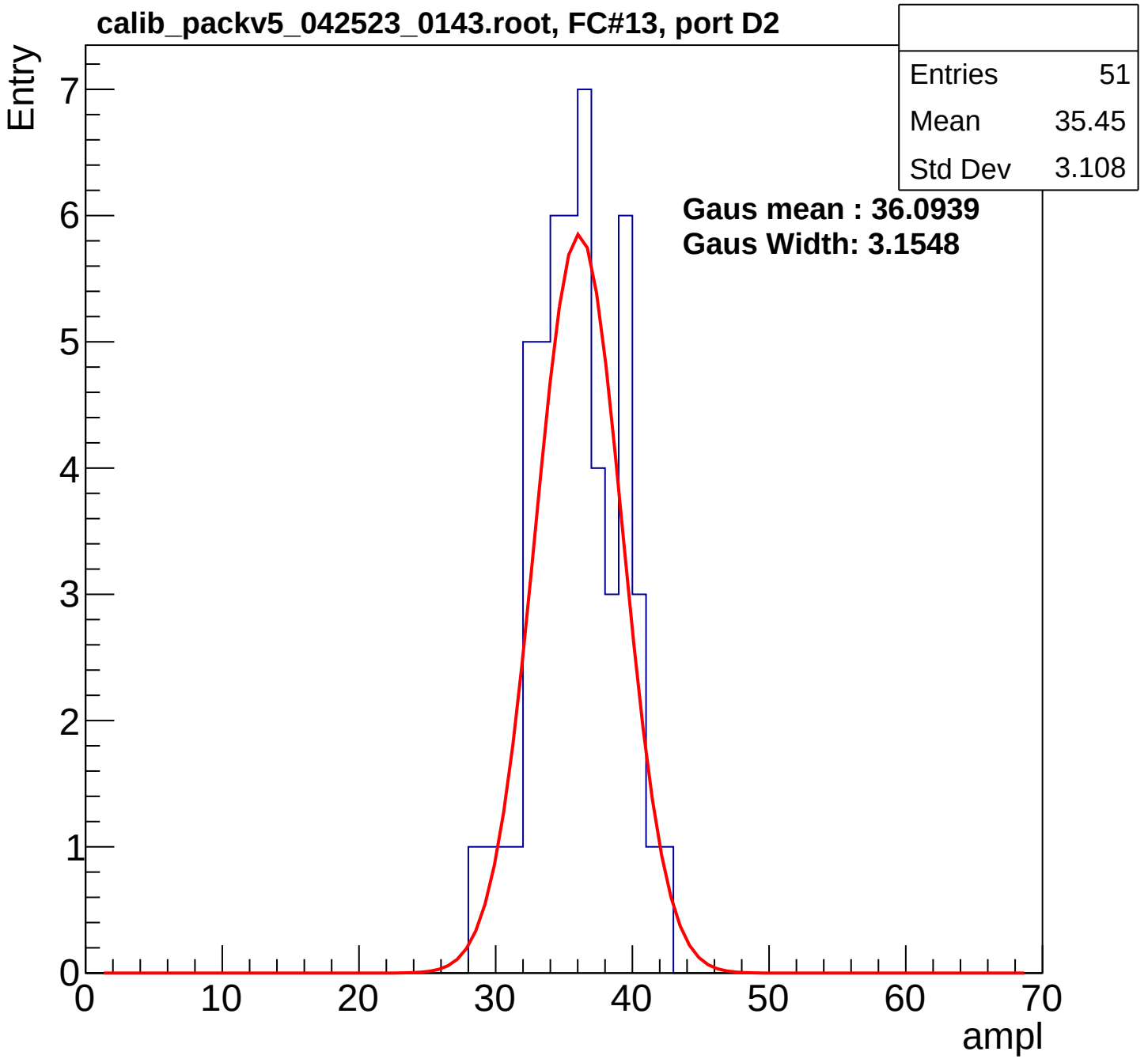

B1L003S, U14-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 55.44
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

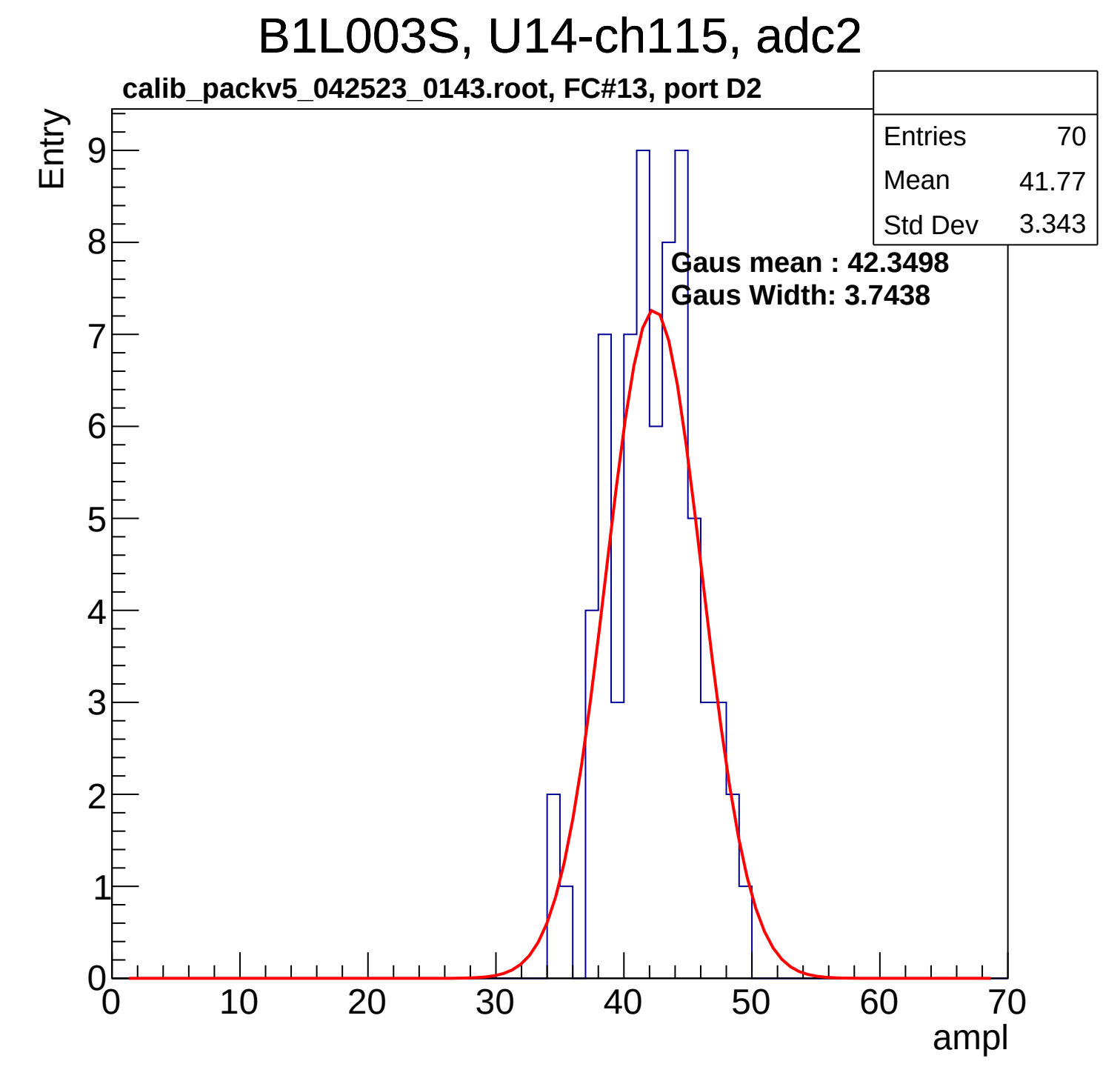

B1L003S, U14-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.27
Std Dev 2.87
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

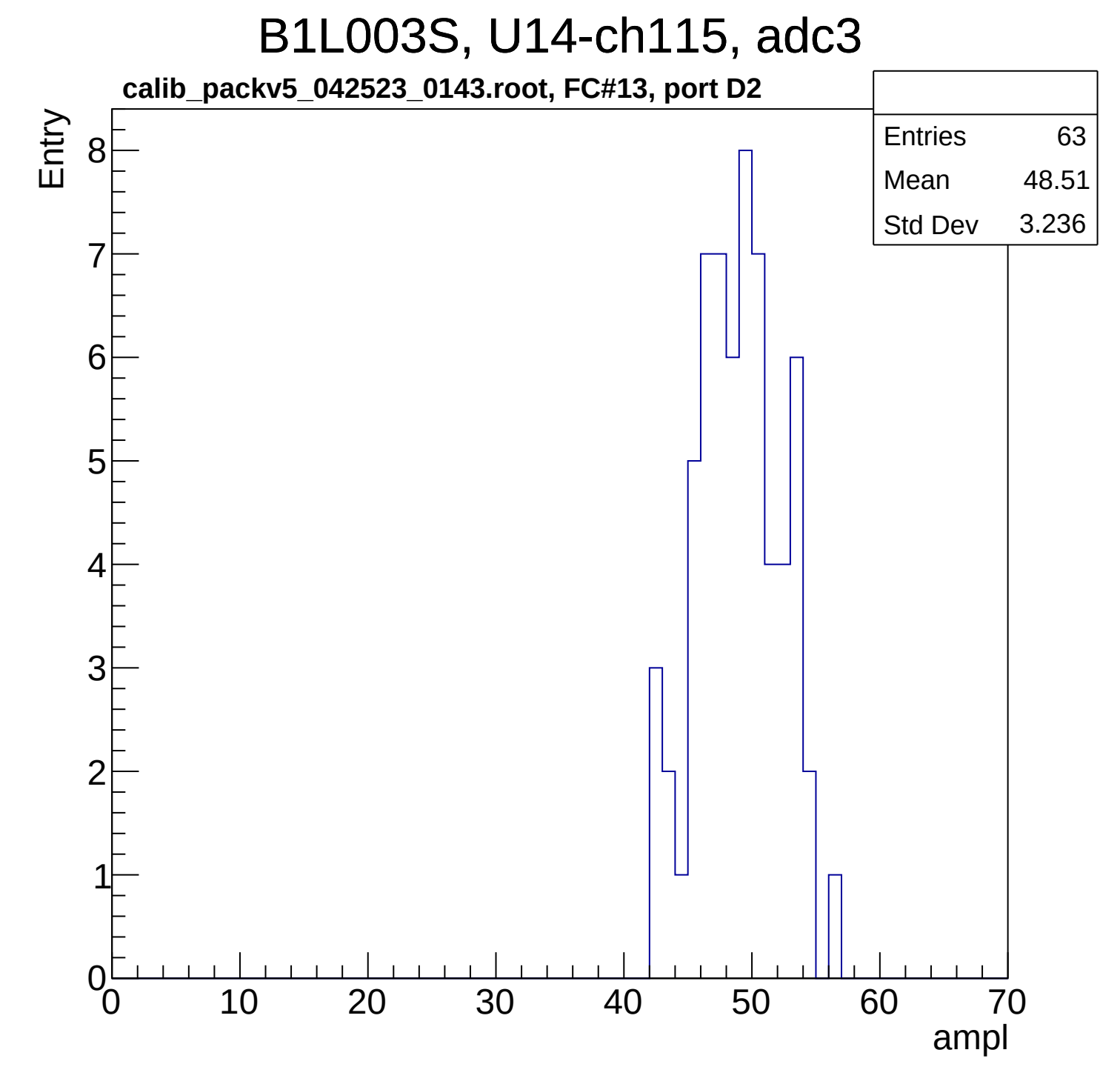

B1L003S, U14-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.78
Std Dev 19.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

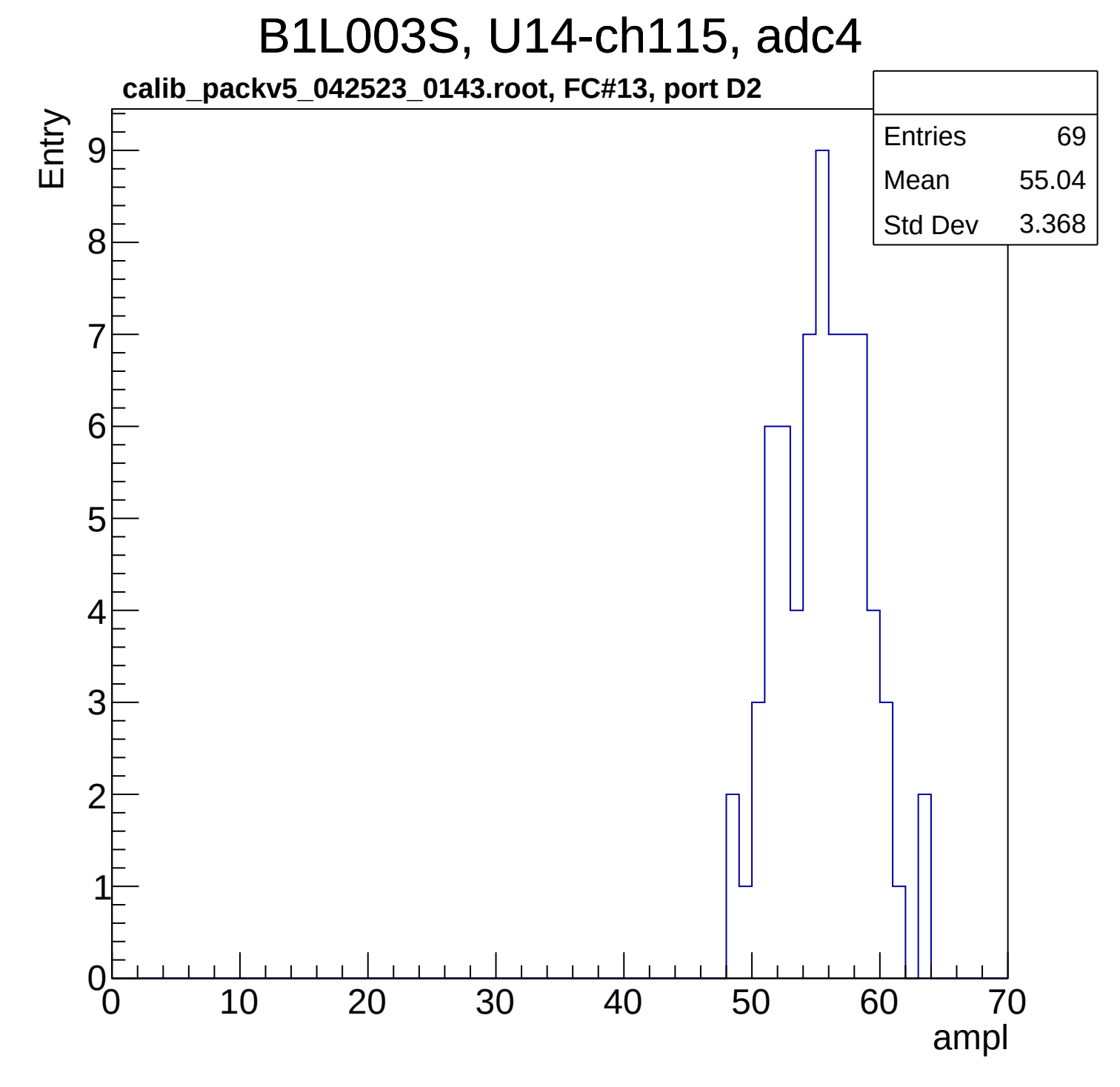

B1L003S, U14-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

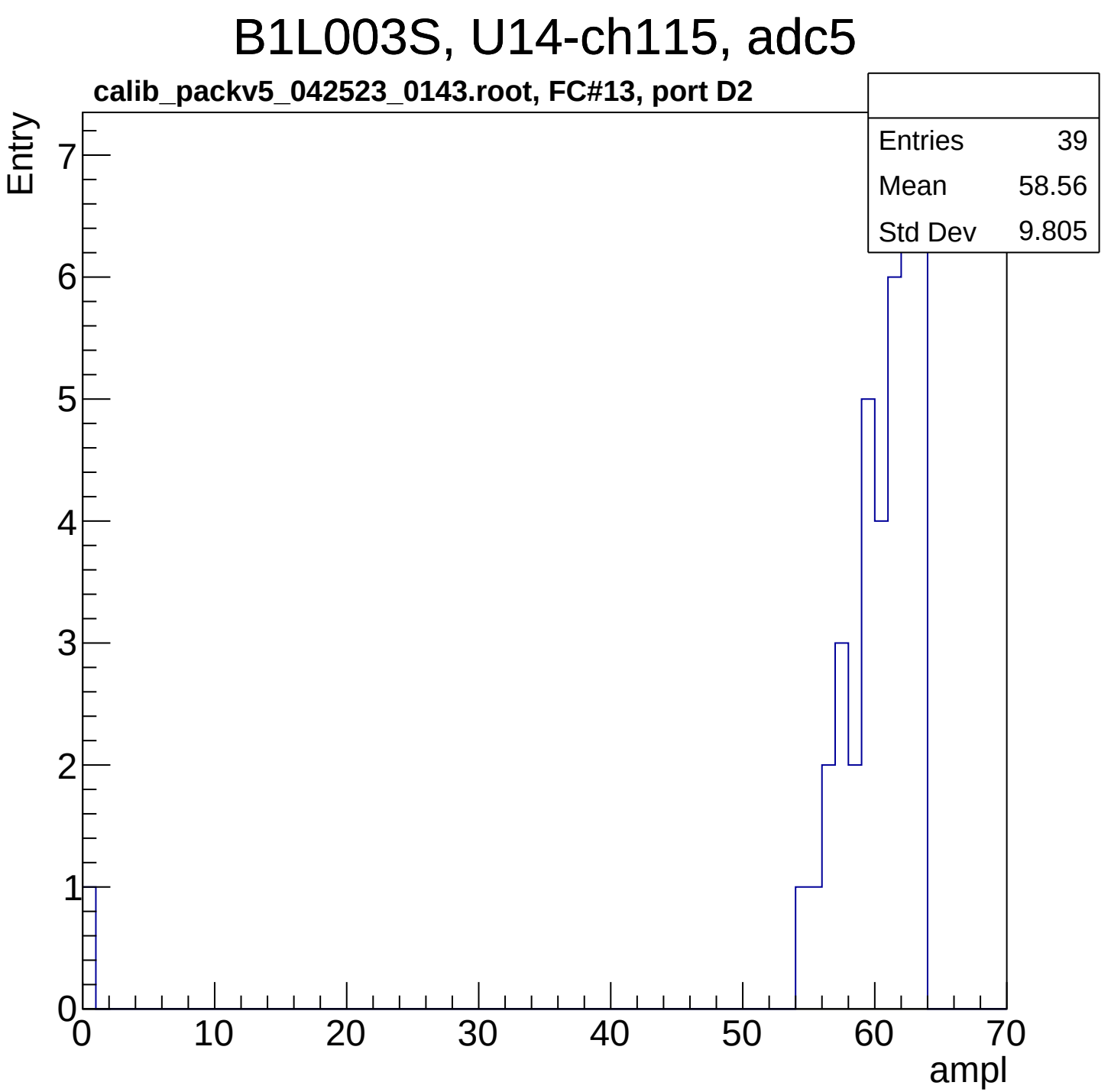

B1L003S, U14-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 29.38
Std Dev 4.117
Gaus mean : 30.0613
Gaus Width: 4.7034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

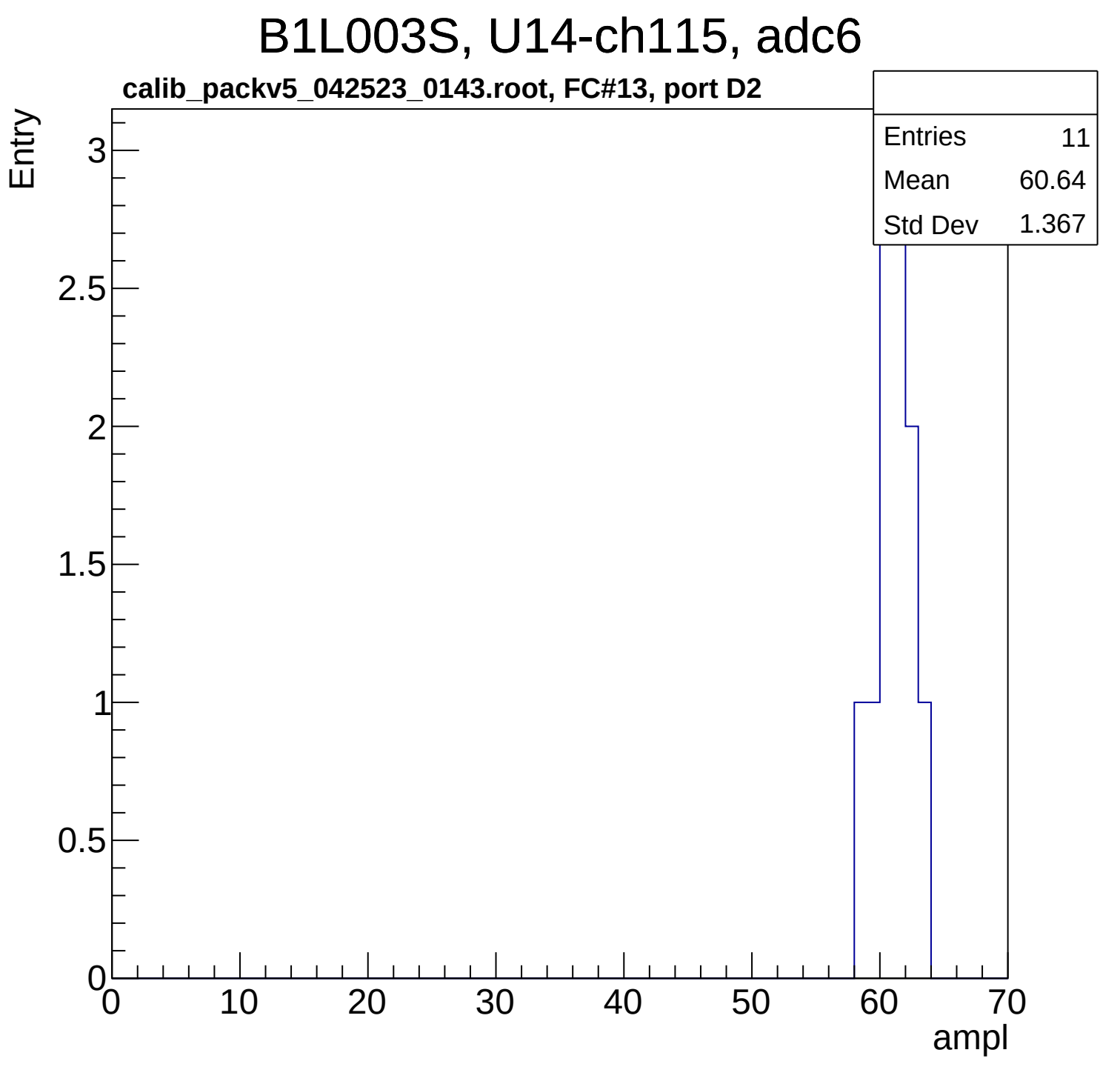

B1L003S, U14-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 35.45
Std Dev 3.108
Gaus mean : 36.0939
Gaus Width: 3.1548
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

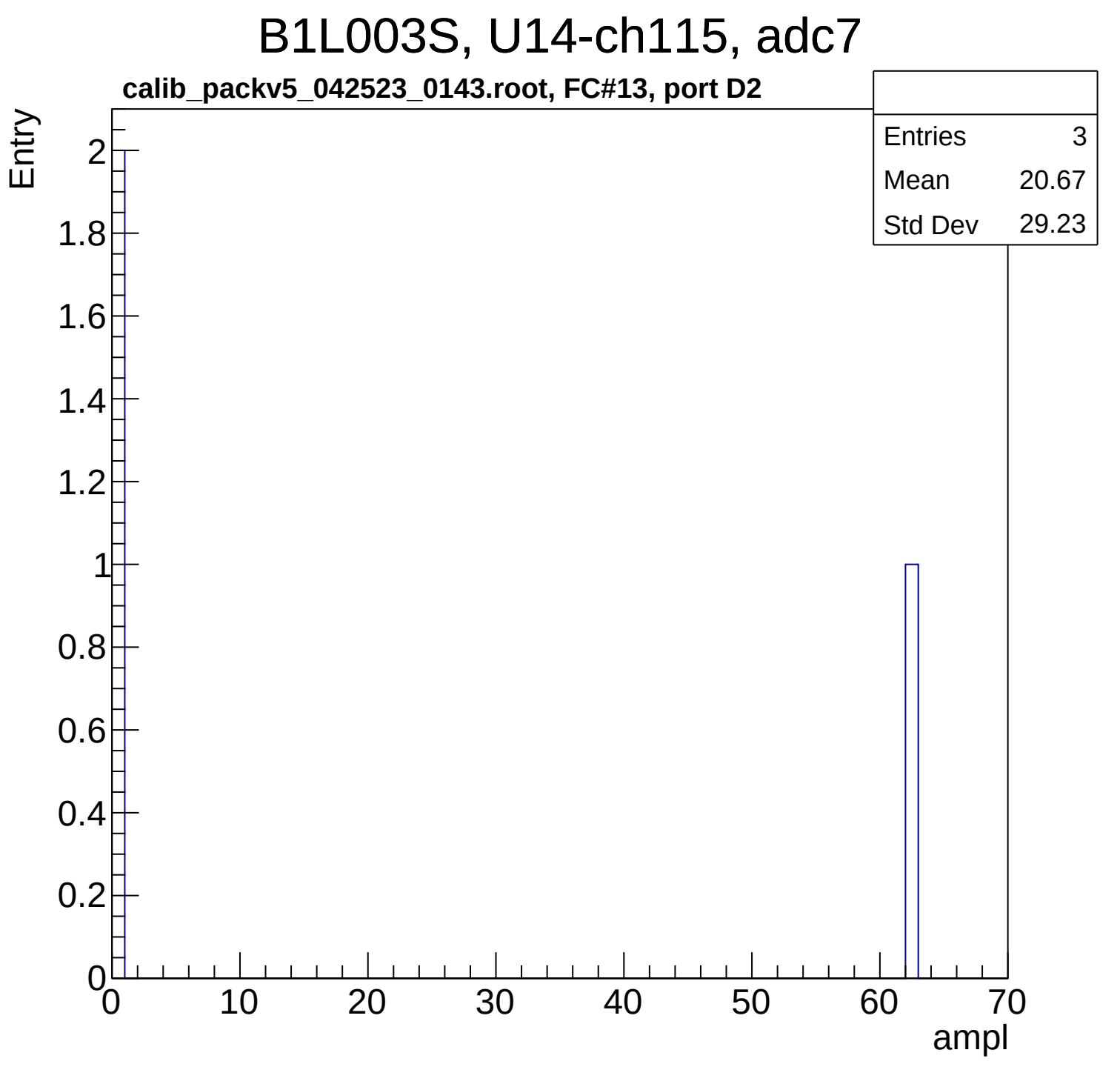

B1L003S, U14-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.77
Std Dev 3.343
Gaus mean : 42.3498
Gaus Width: 3.7438
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

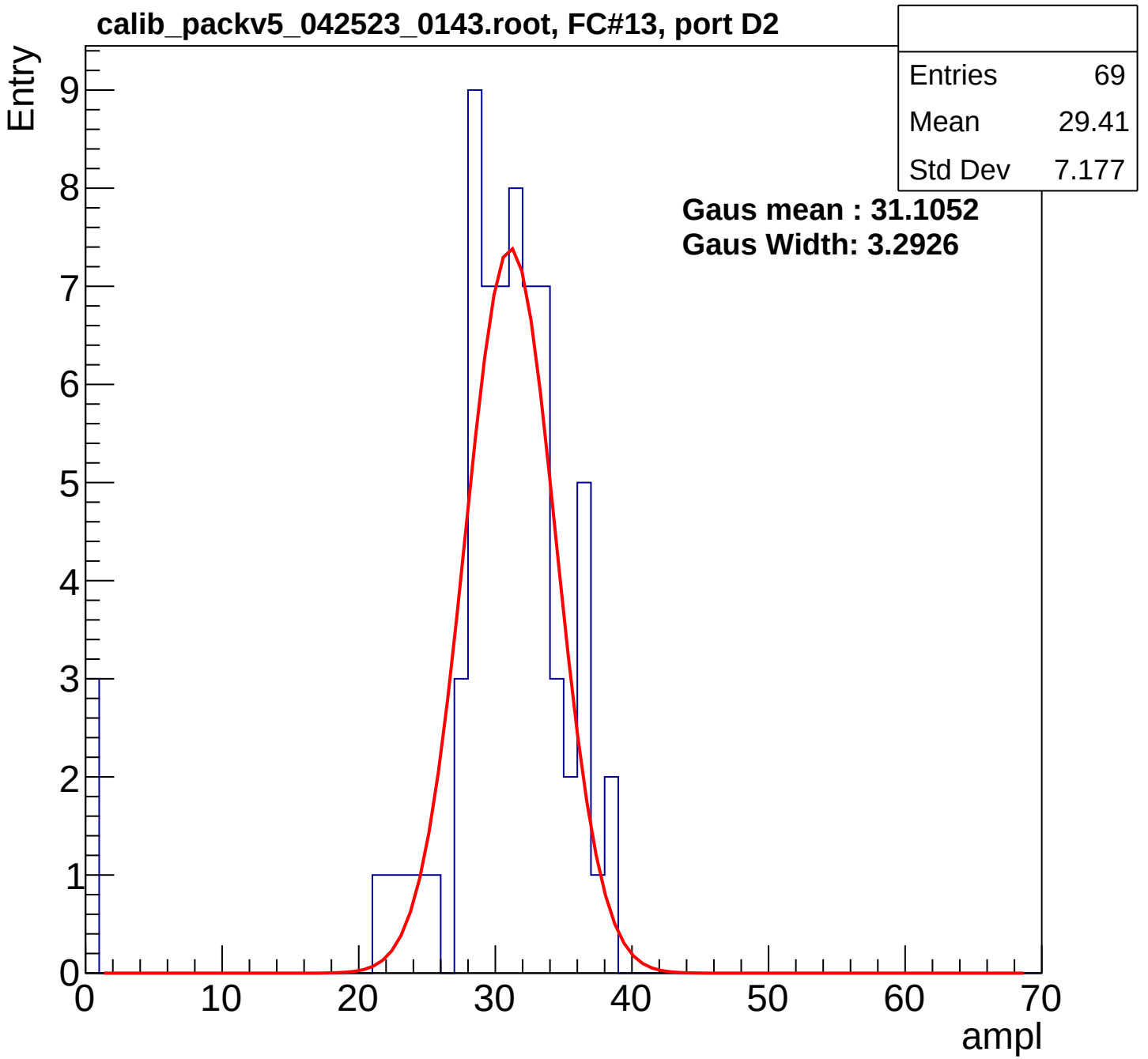

B1L003S, U14-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.51
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

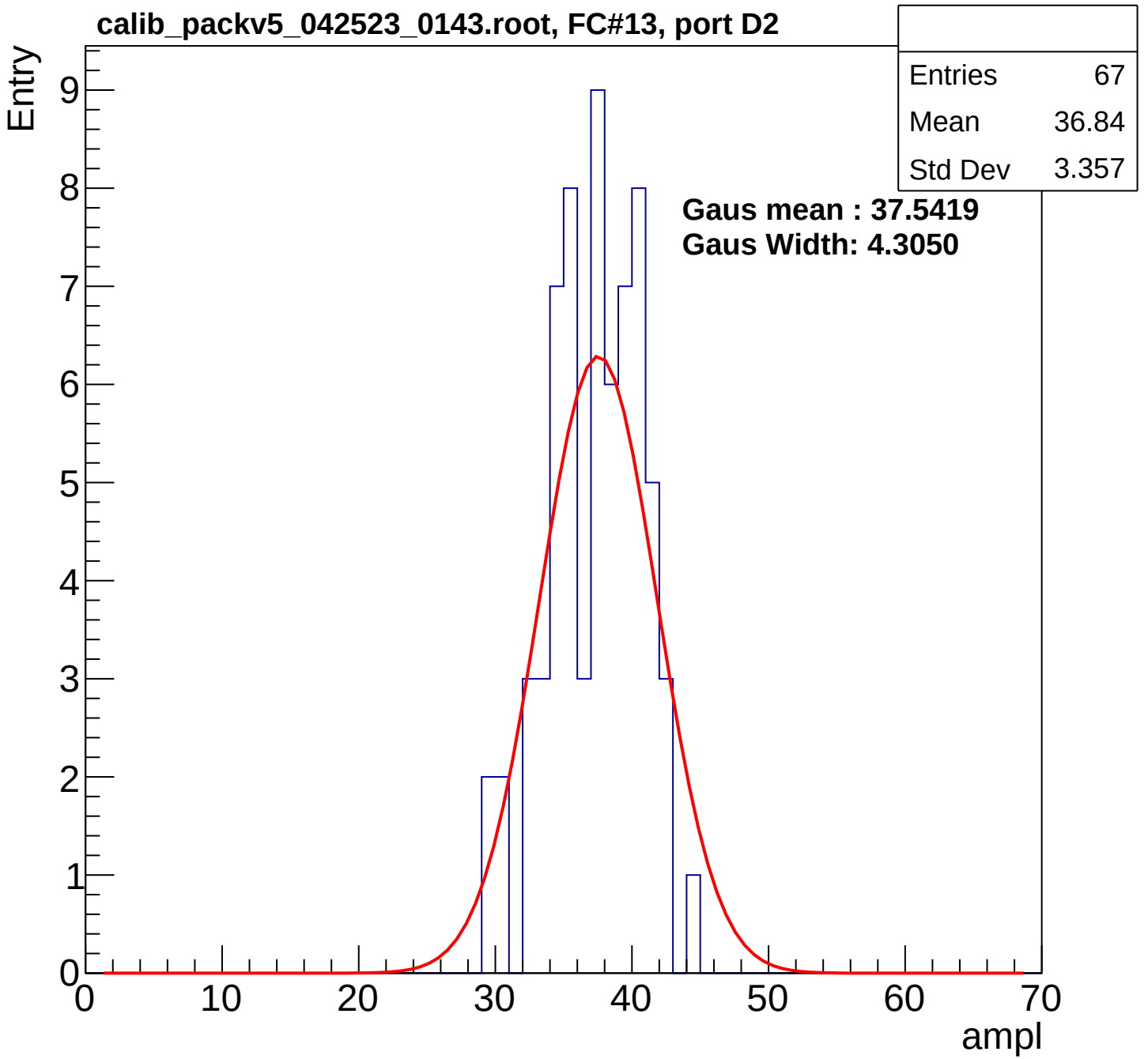

B1L003S, U14-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 55.04
Std Dev 3.368
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

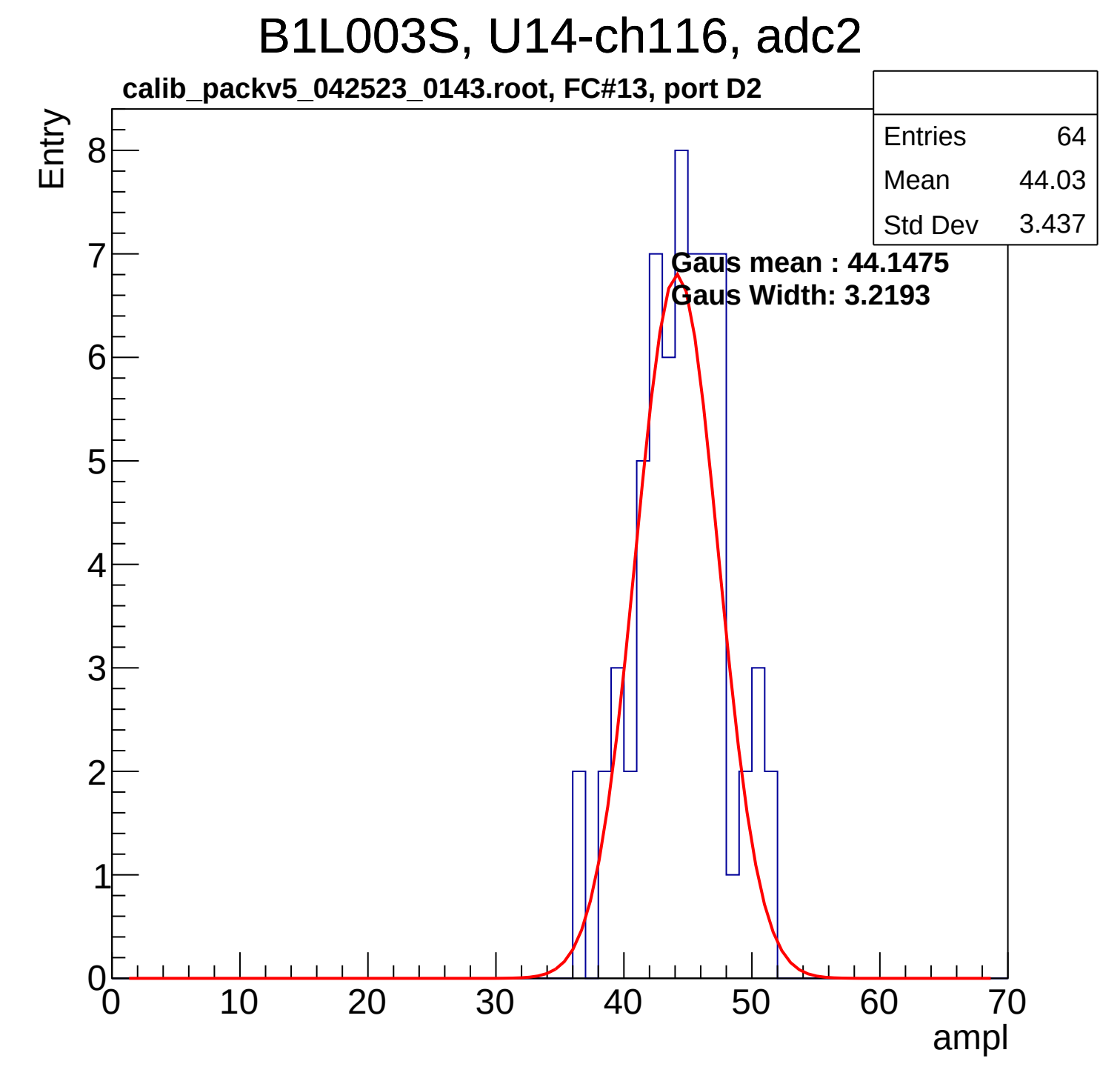

B1L003S, U14-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.56
Std Dev 9.805
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

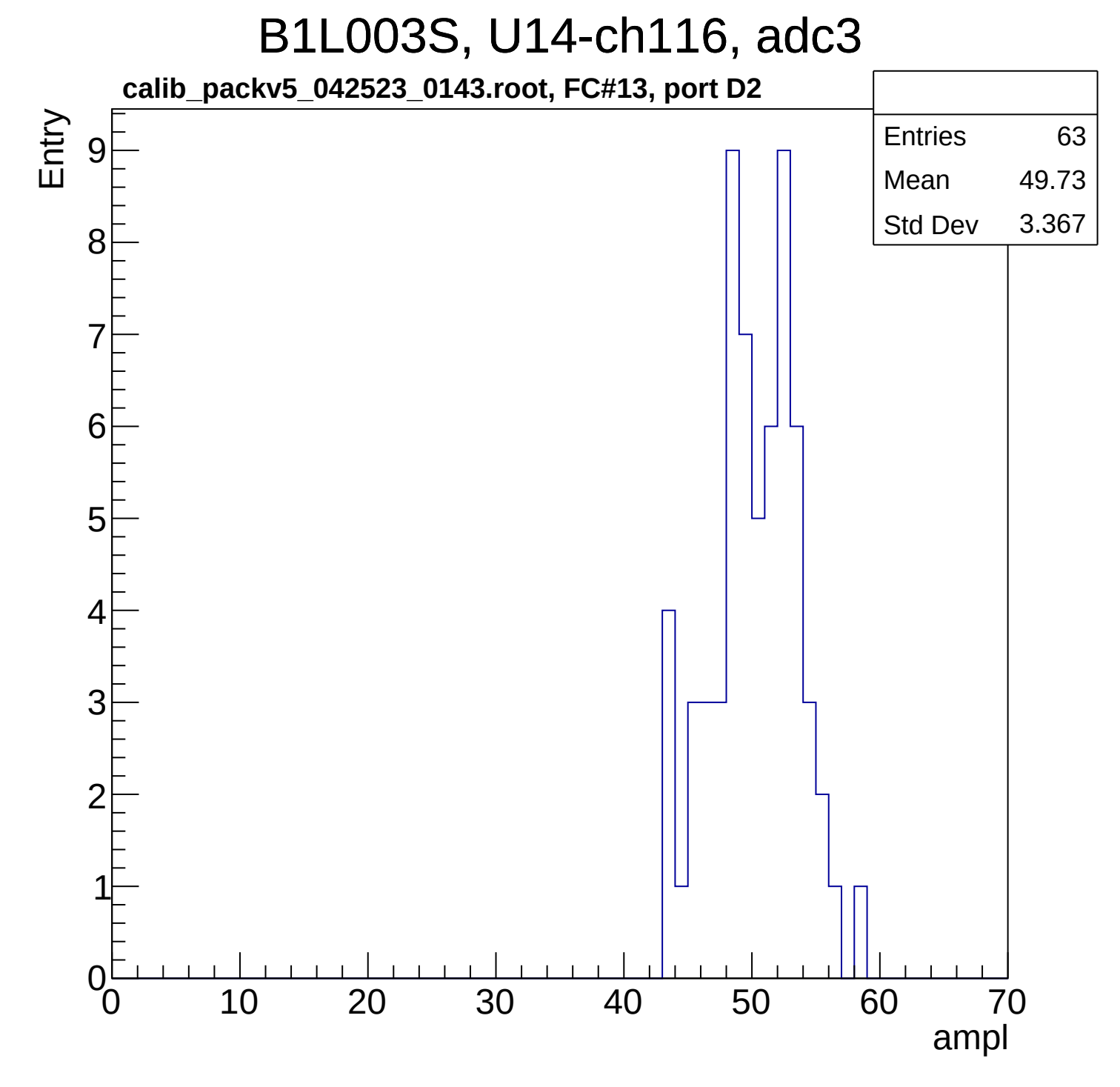

B1L003S, U14-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 60.64
Std Dev 1.367
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

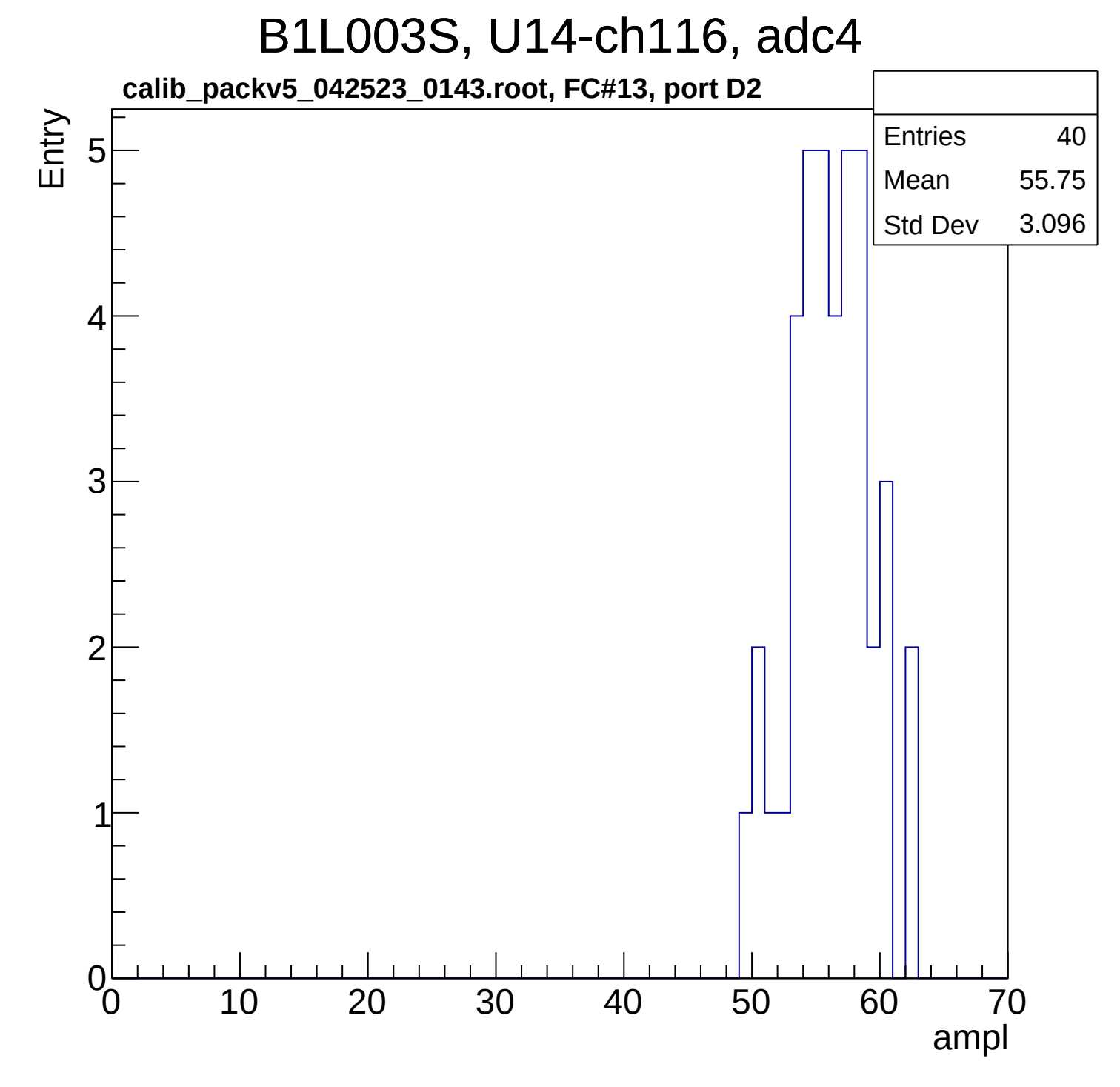

B1L003S, U14-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

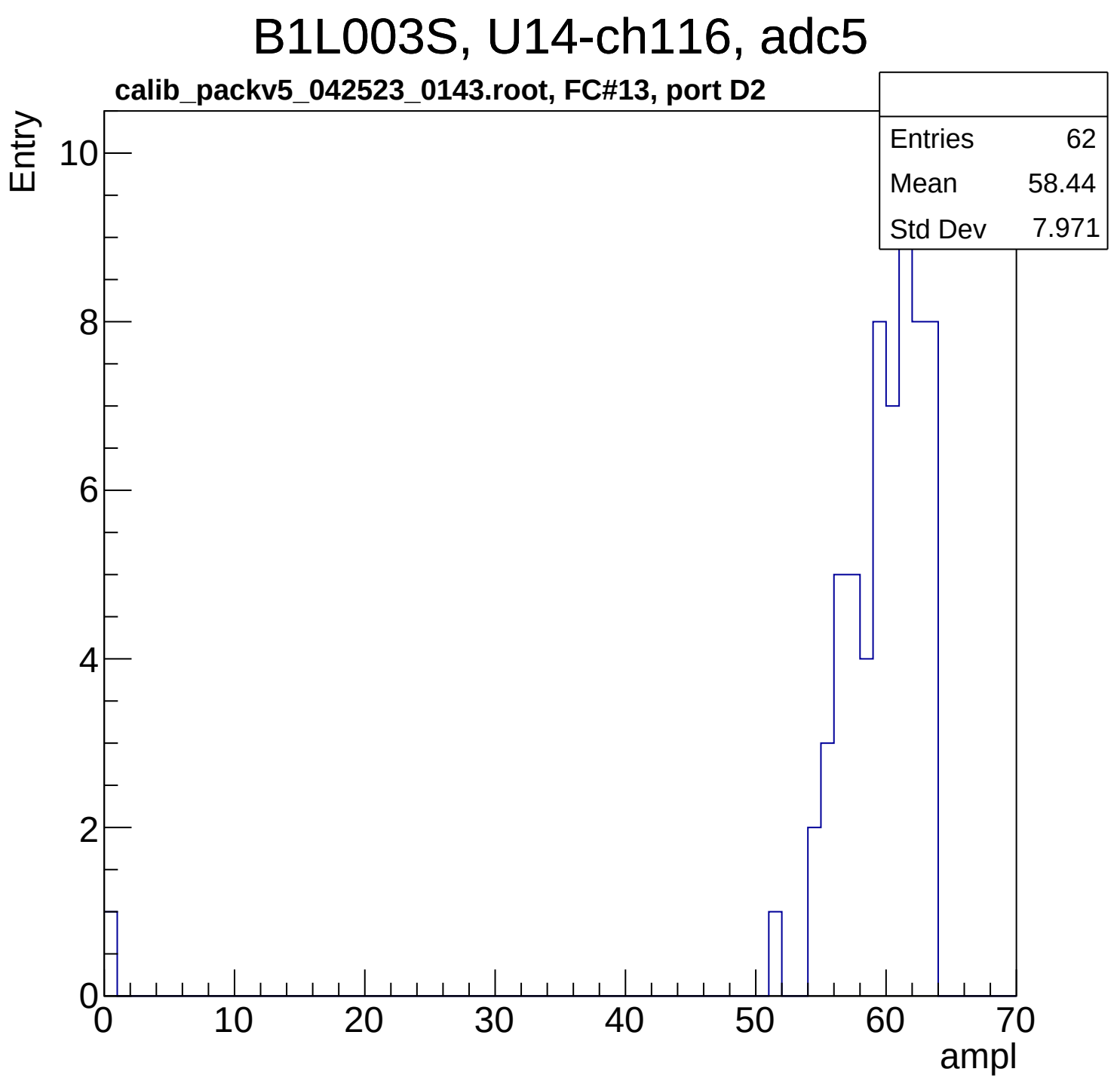

B1L003S, U14-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 29.41
Std Dev 7.177
Gaus mean : 31.1052
Gaus Width: 3.2926
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

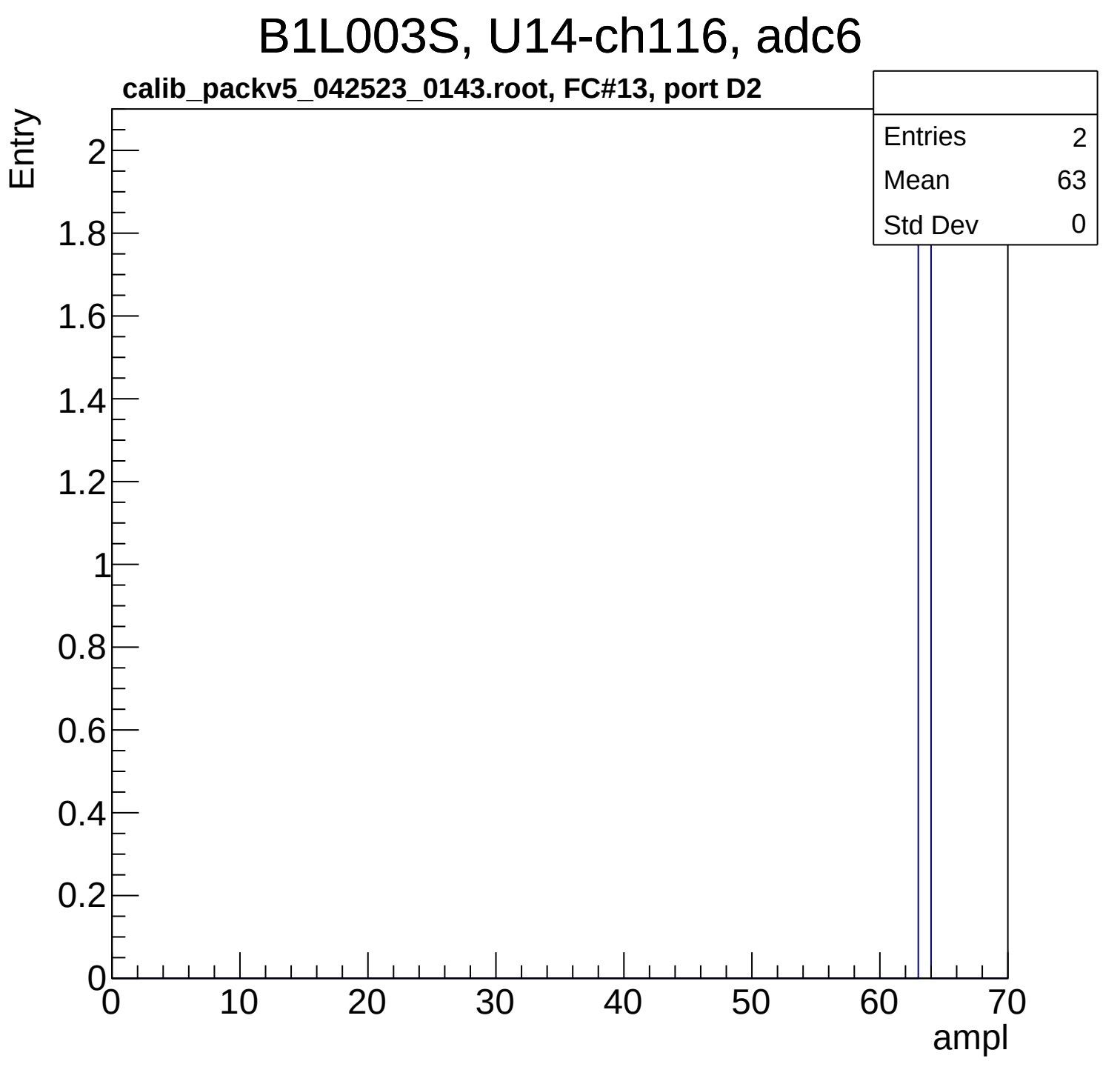

B1L003S, U14-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.84
Std Dev 3.357
Gaus mean : 37.5419
Gaus Width: 4.3050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

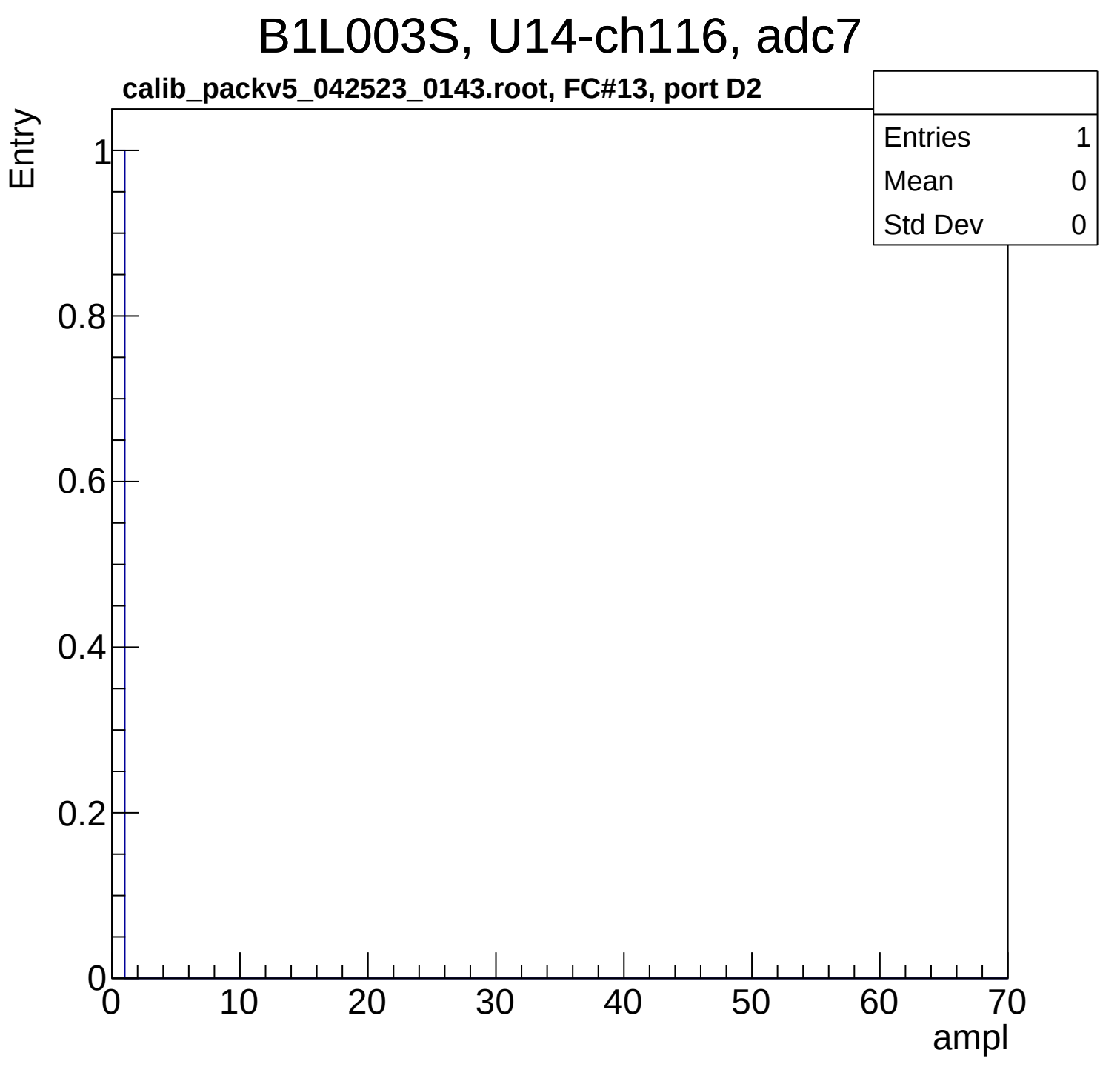

B1L003S, U14-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.03
Std Dev 3.437
Gaus mean : 44.1475
Gaus Width: 3.2193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

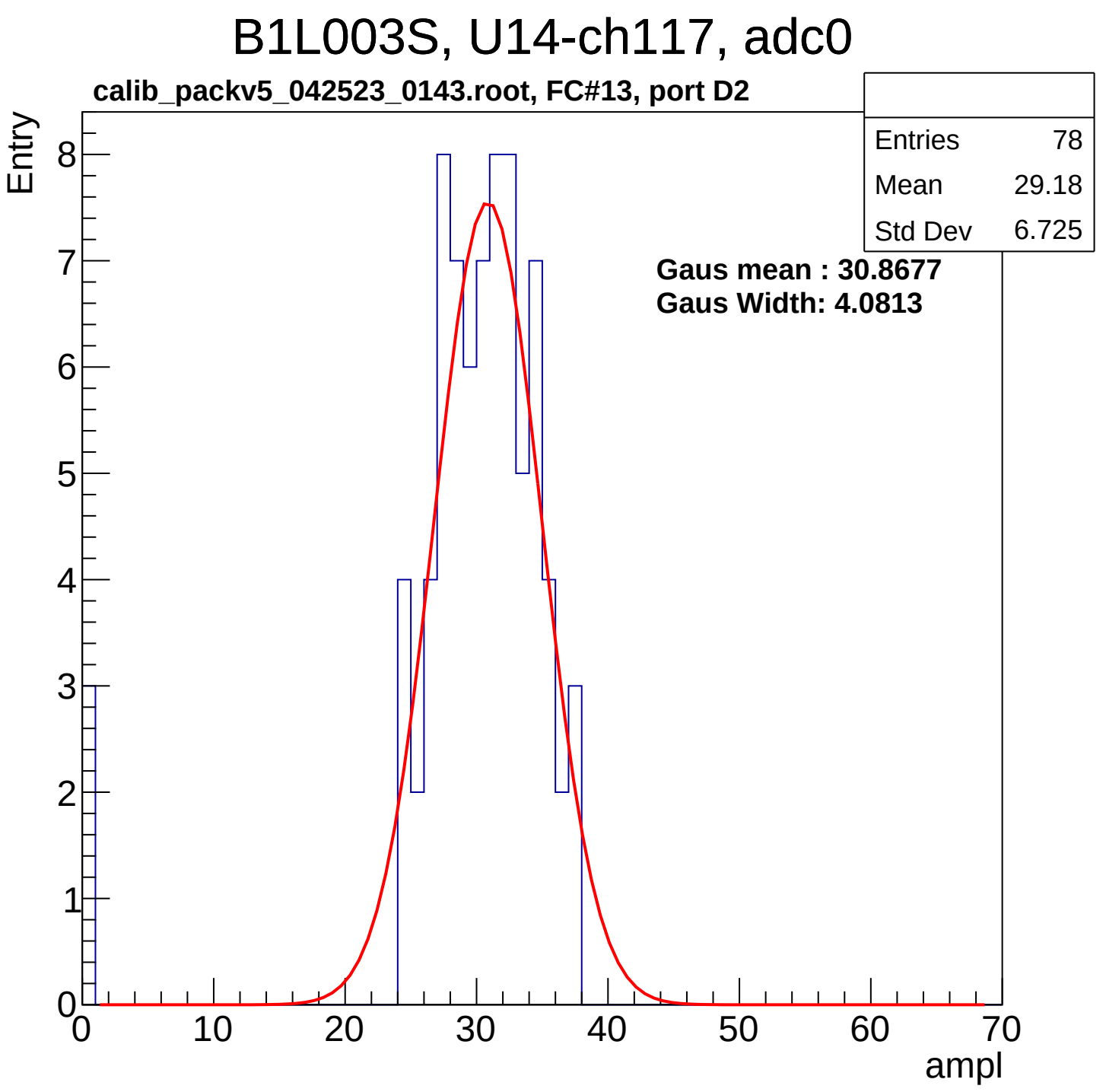

B1L003S, U14-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.73
Std Dev 3.367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

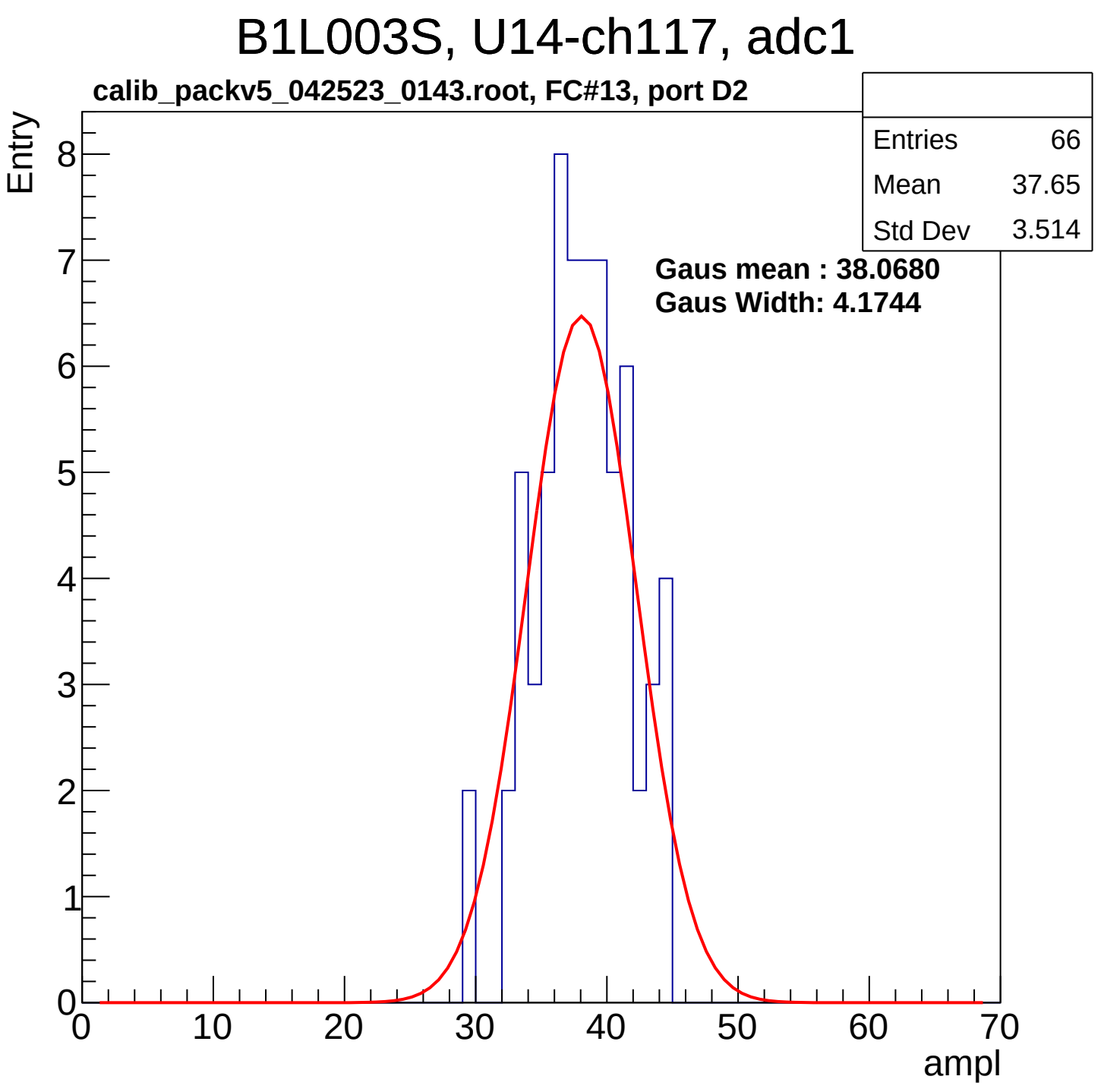

B1L003S, U14-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 55.75
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

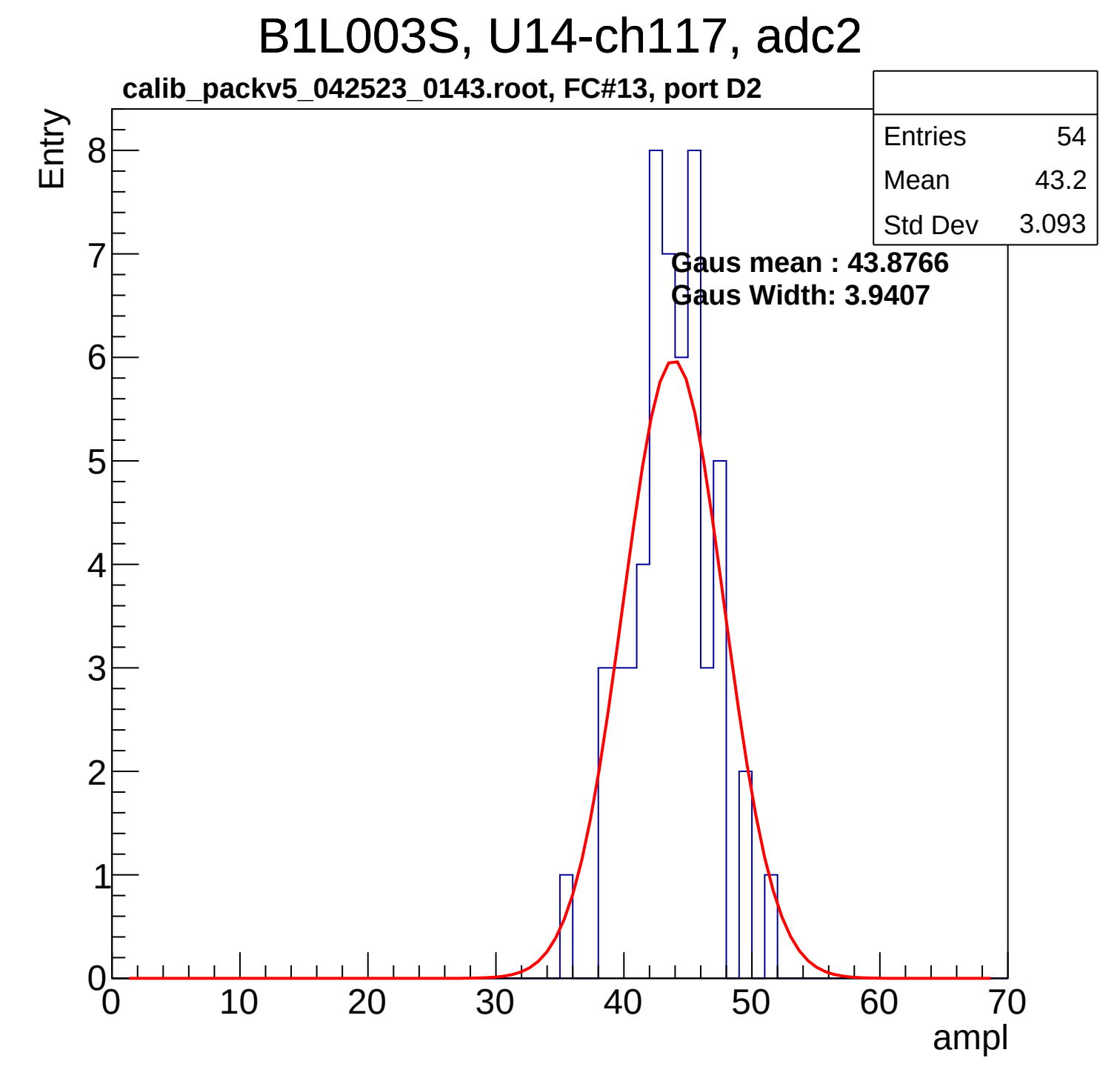

B1L003S, U14-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.44
Std Dev 7.971
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

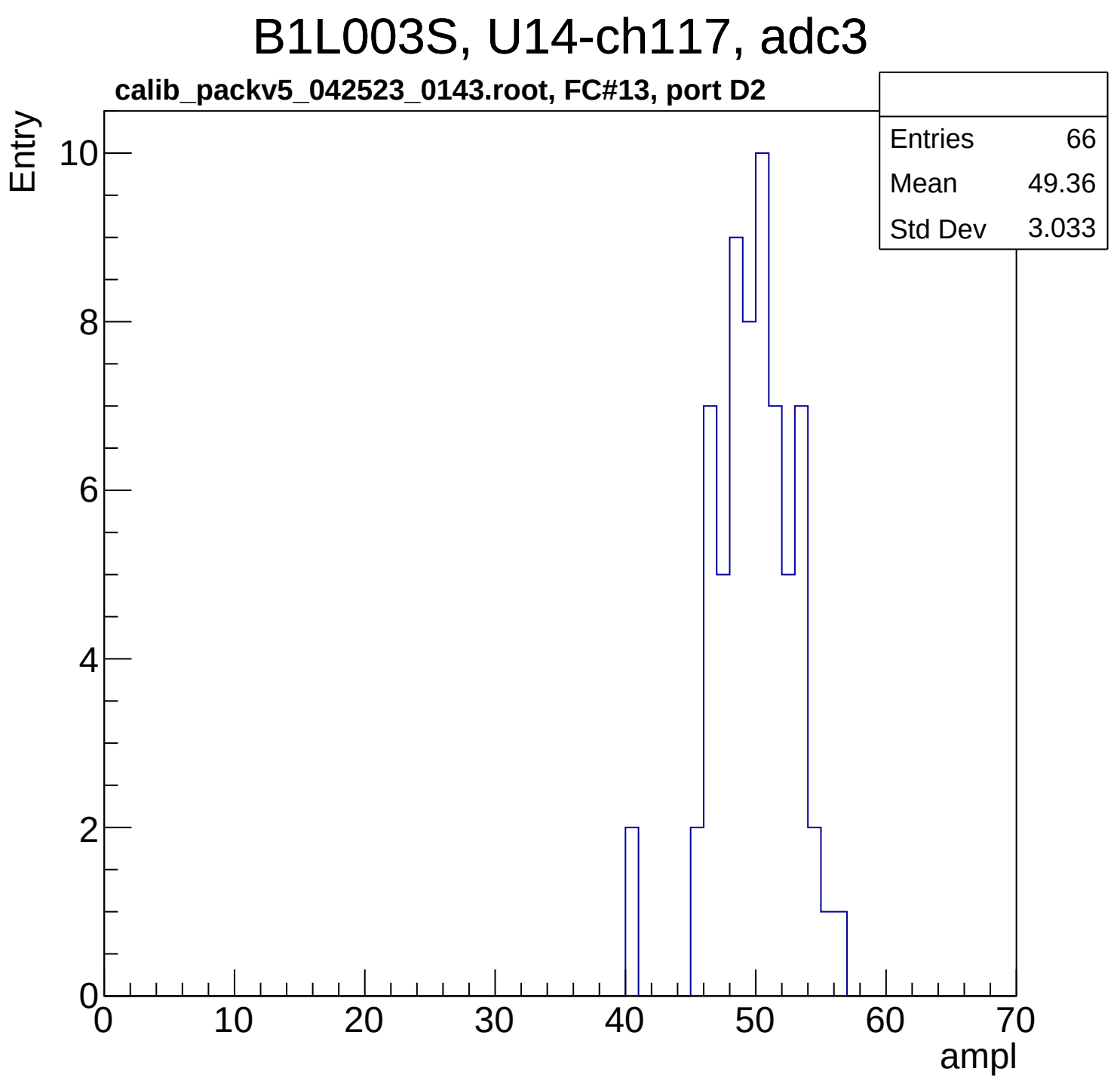

B1L003S, U14-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

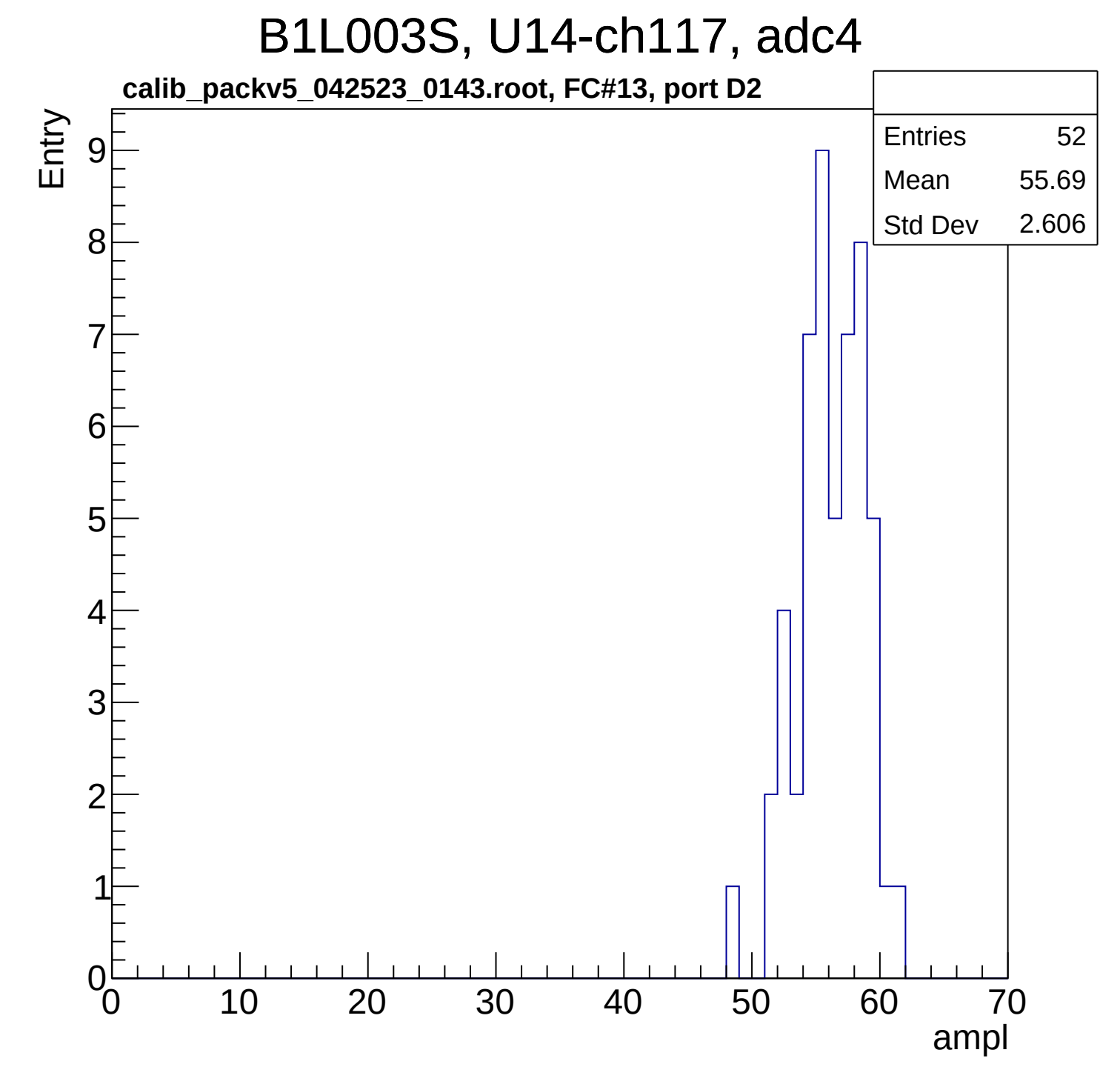

B1L003S, U14-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

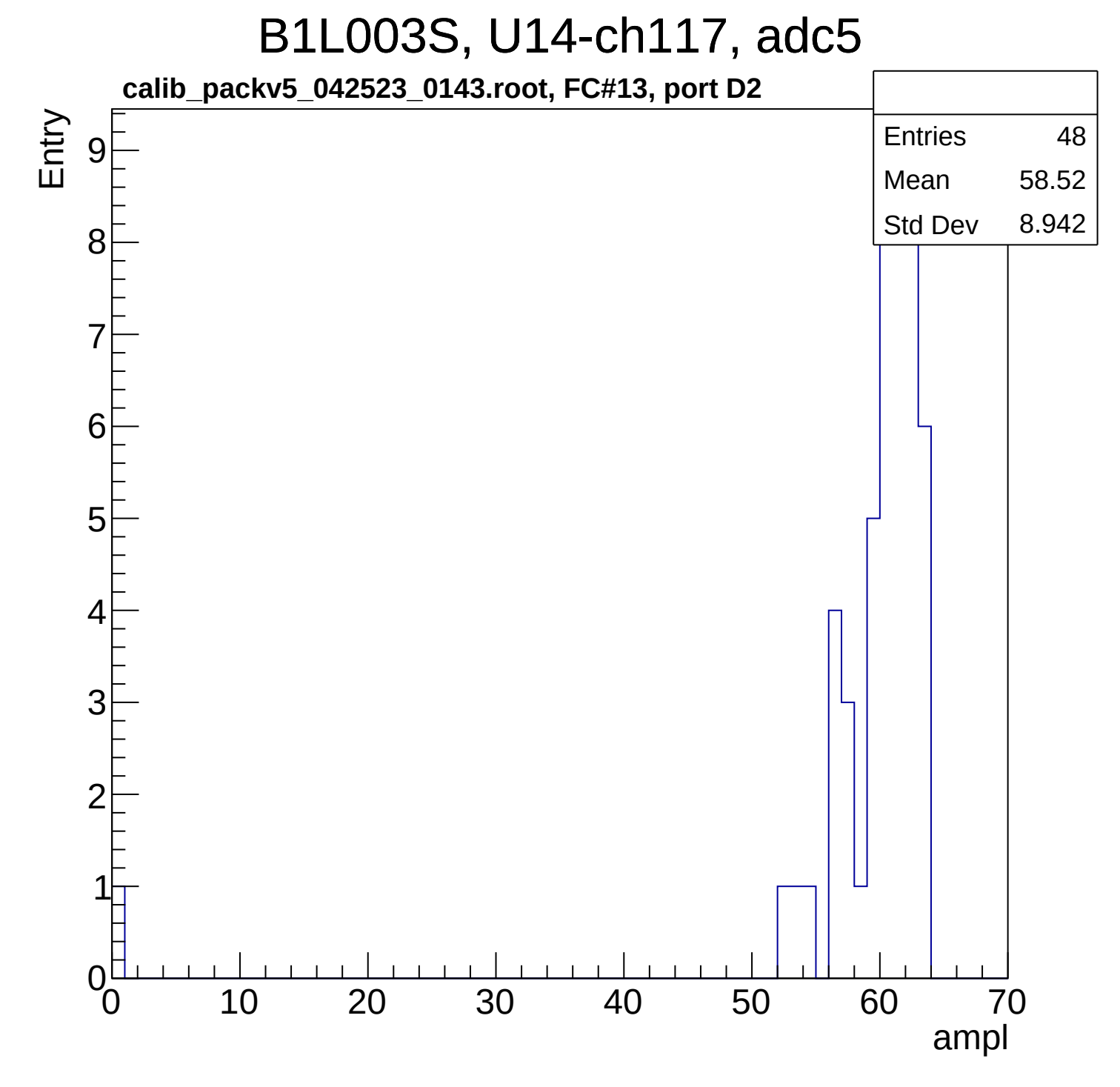

B1L003S, U14-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.18
Std Dev 6.725
Gaus mean : 30.8677
Gaus Width: 4.0813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

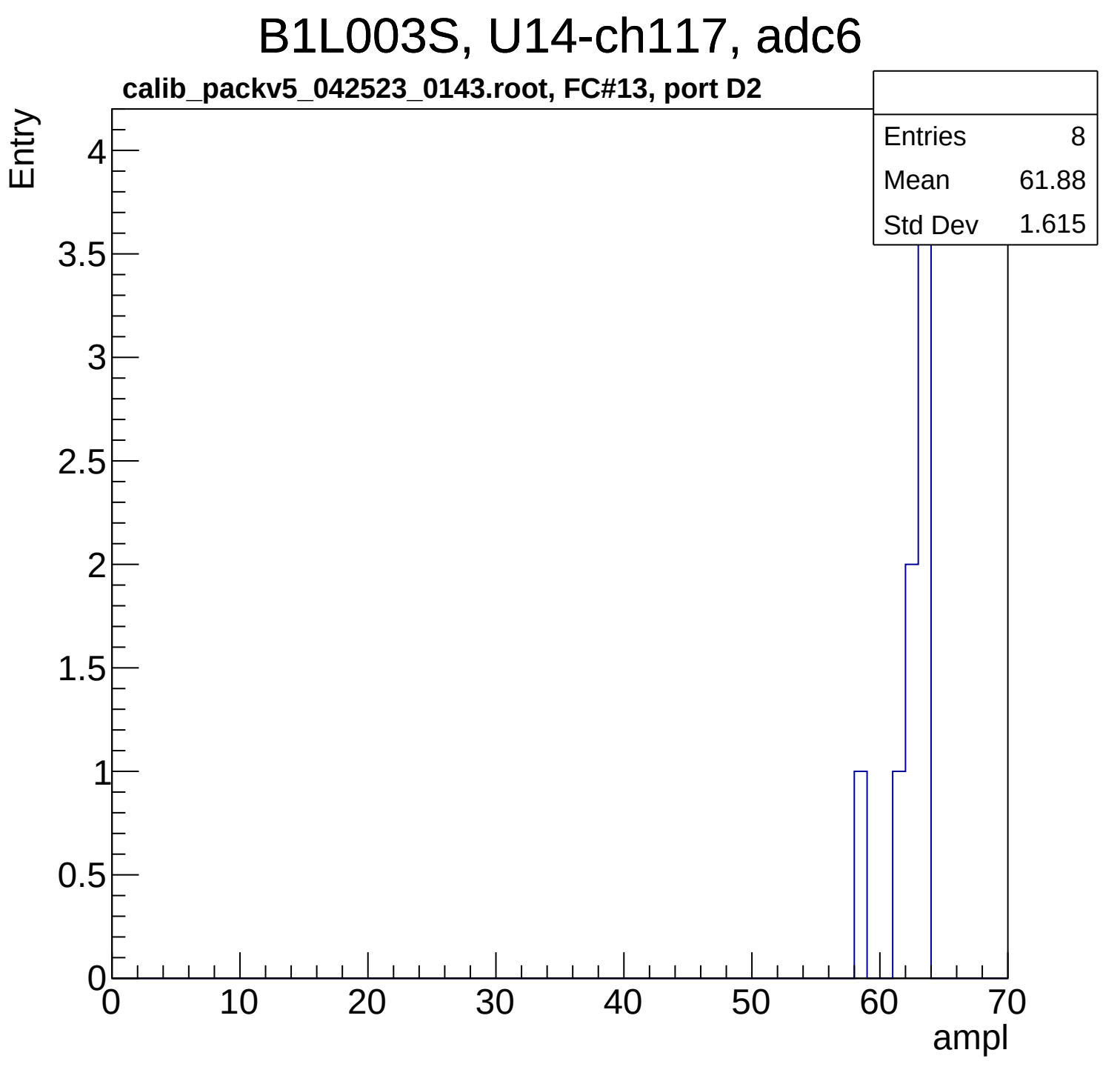

B1L003S, U14-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.65
Std Dev 3.514
Gaus mean : 38.0680
Gaus Width: 4.1744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

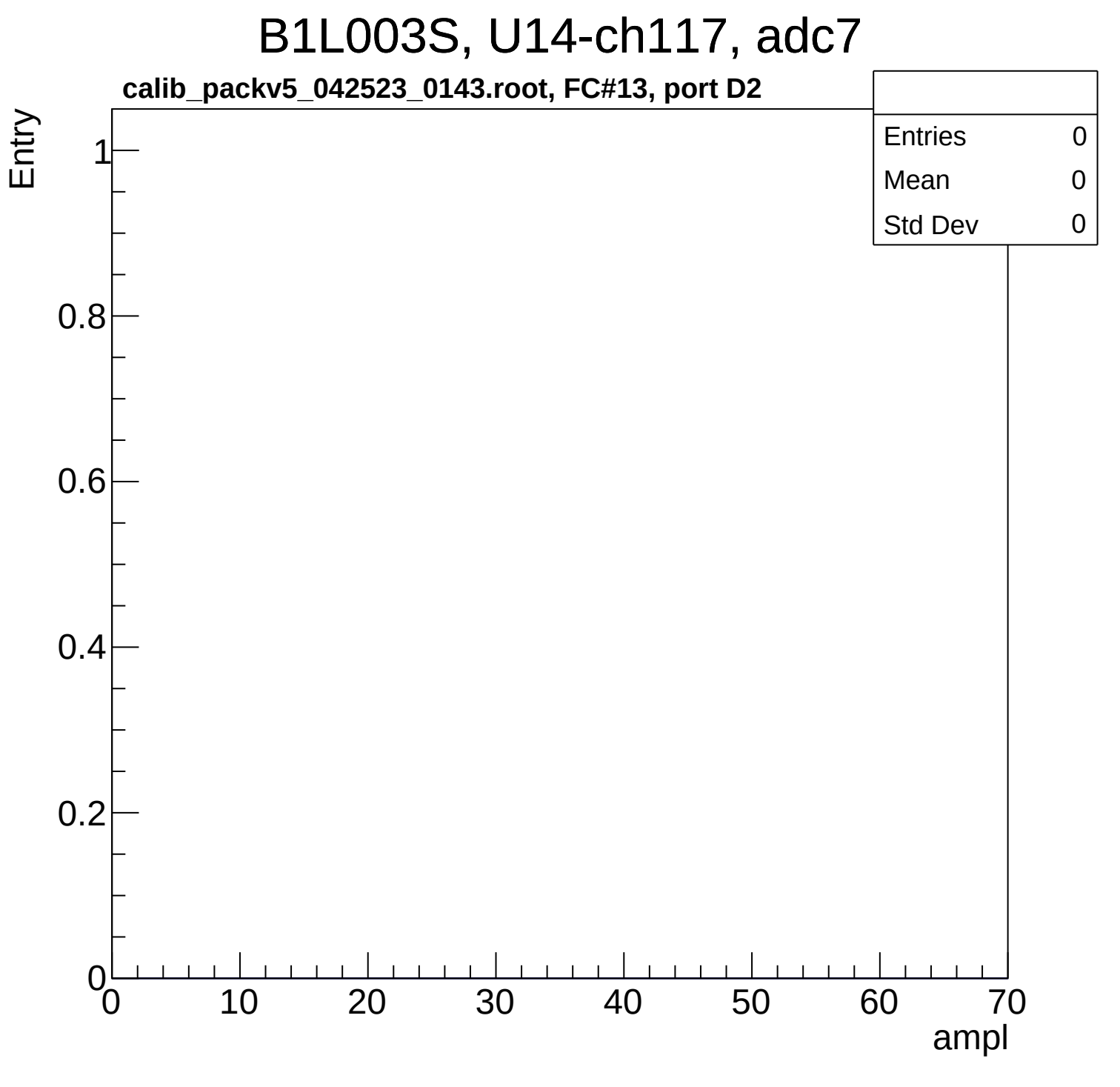

B1L003S, U14-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.2
Std Dev 3.093
Gaus mean : 43.8766
Gaus Width: 3.9407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

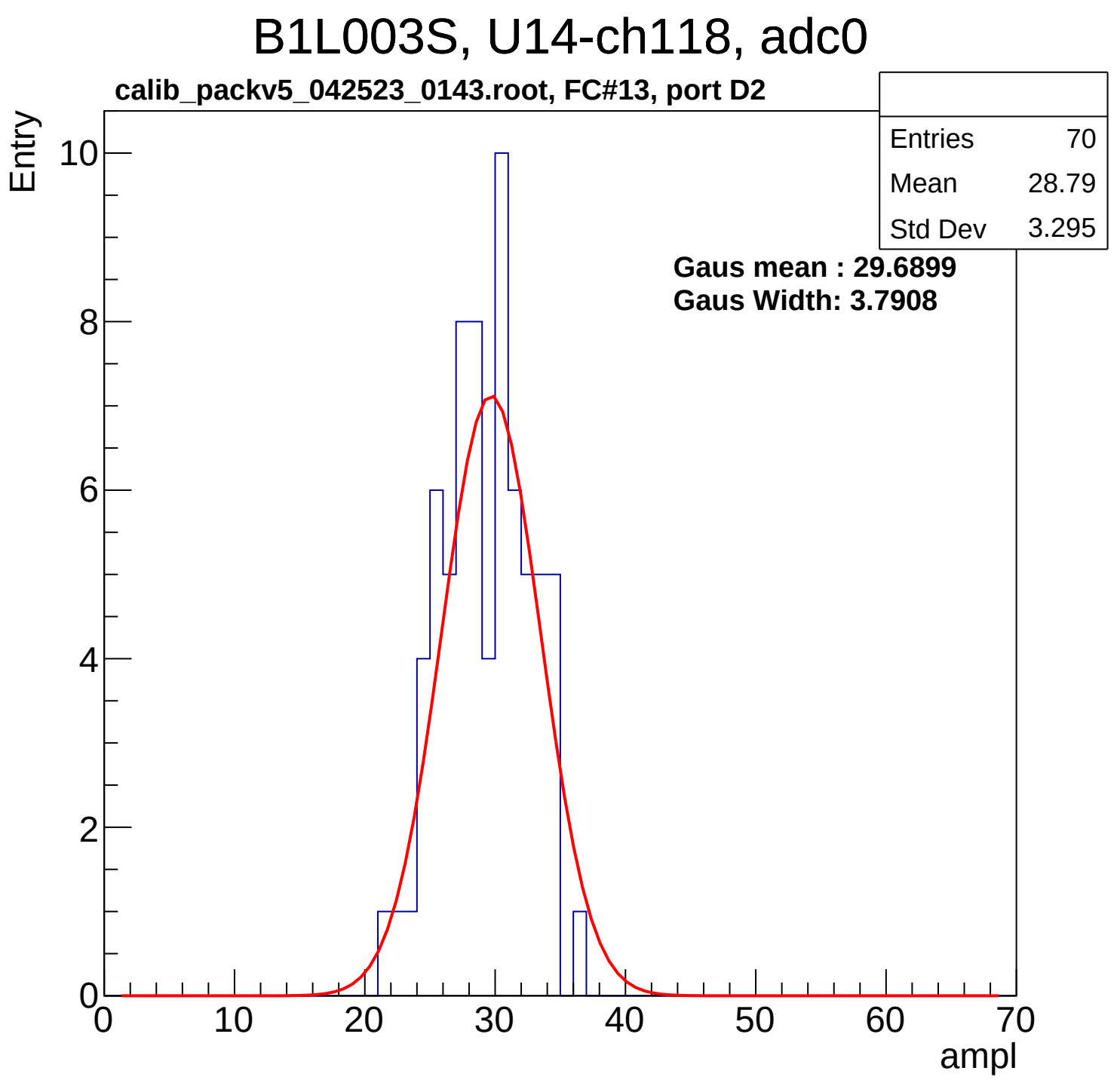

B1L003S, U14-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.36
Std Dev 3.033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

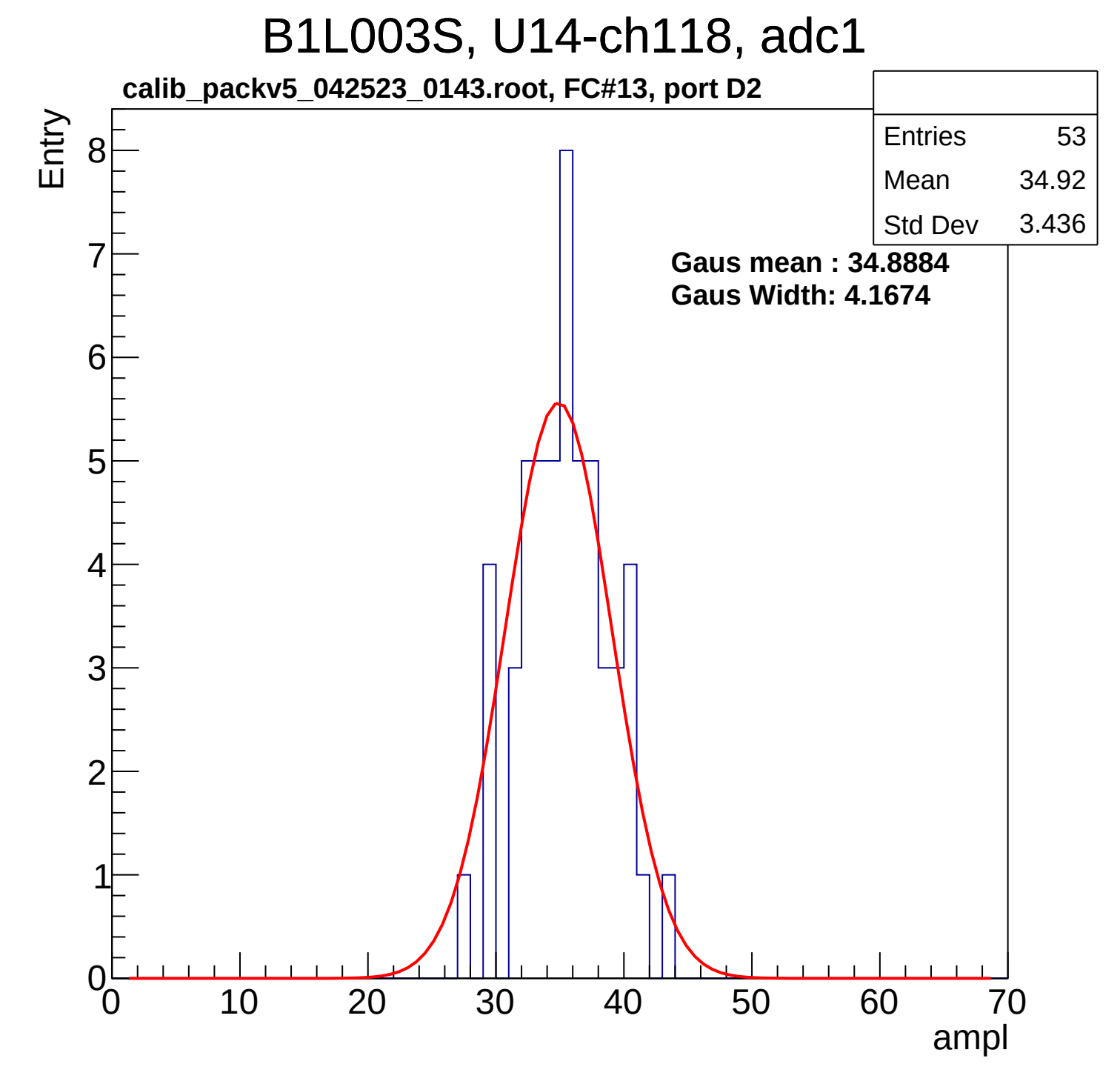

B1L003S, U14-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.69
Std Dev 2.606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

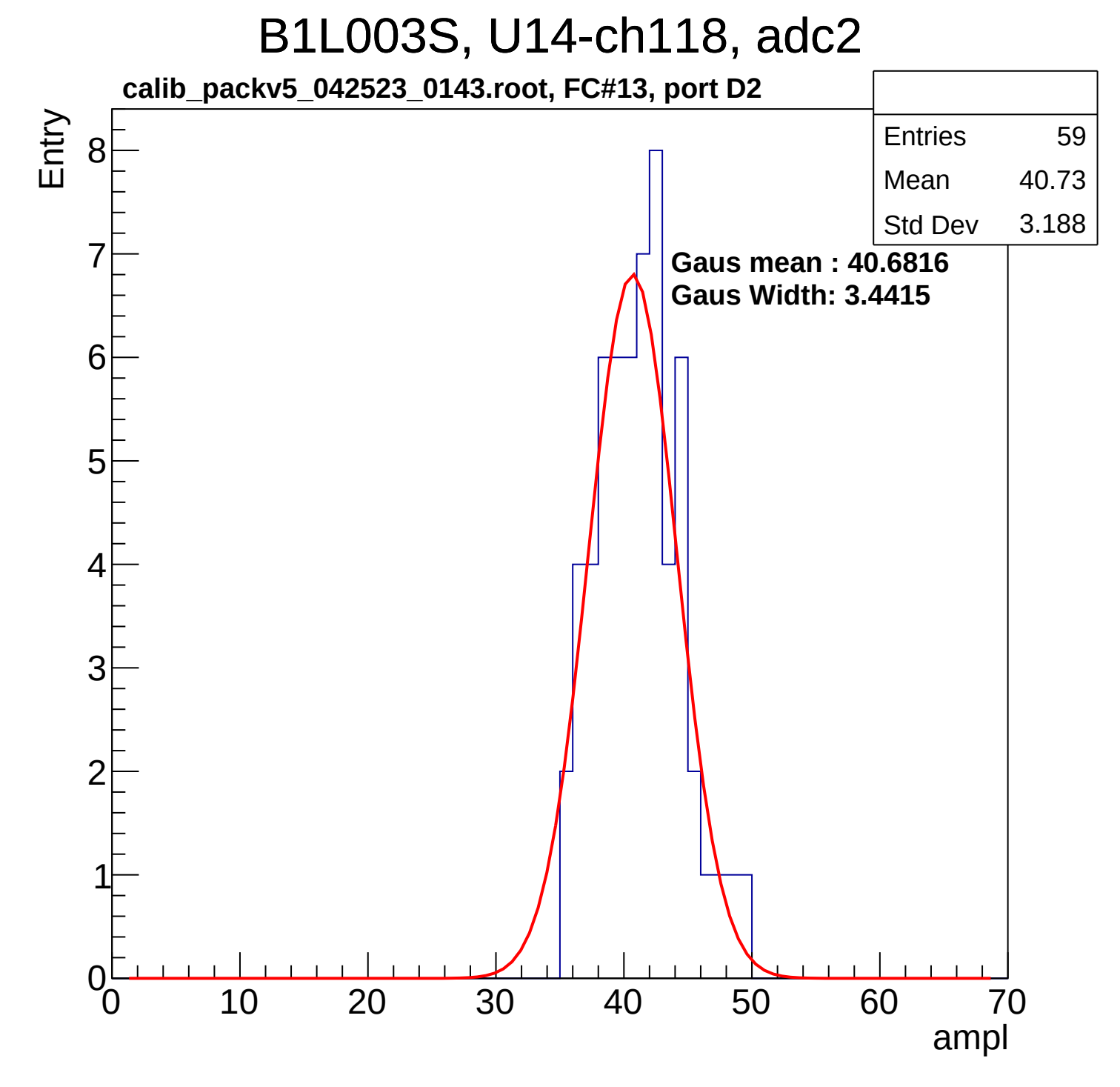

B1L003S, U14-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.52
Std Dev 8.942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

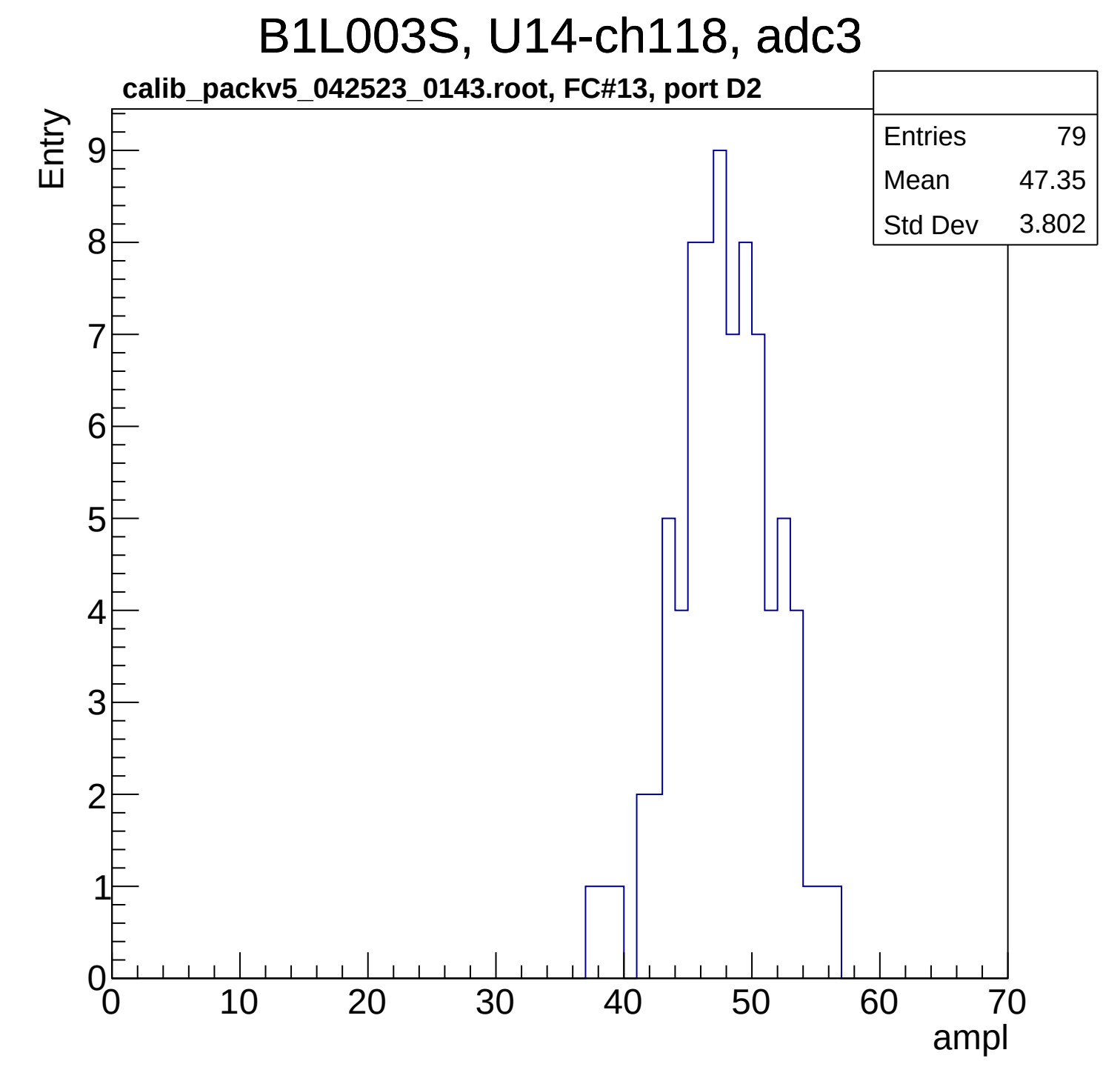

B1L003S, U14-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.88
Std Dev 1.615
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

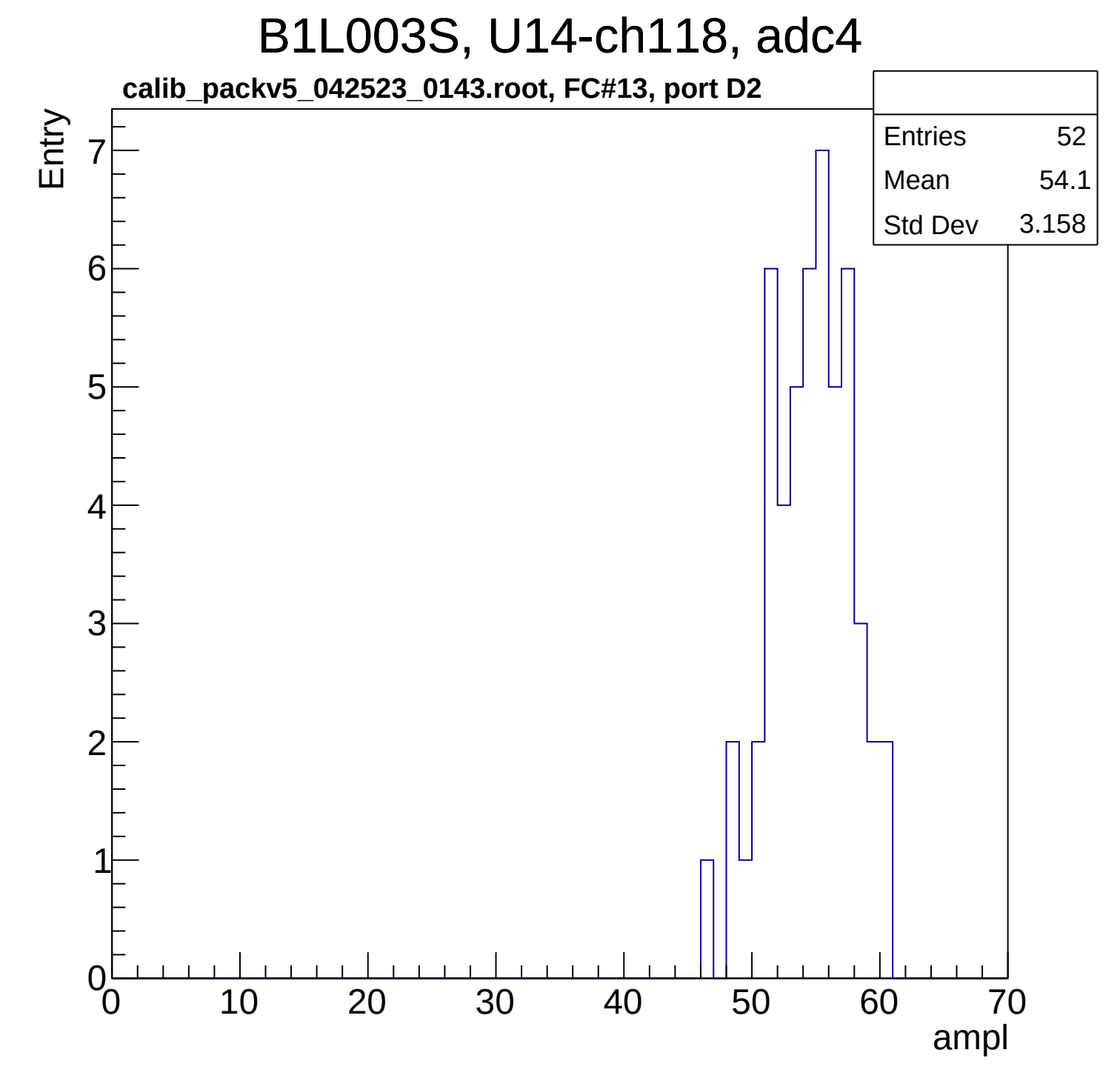

B1L003S, U14-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

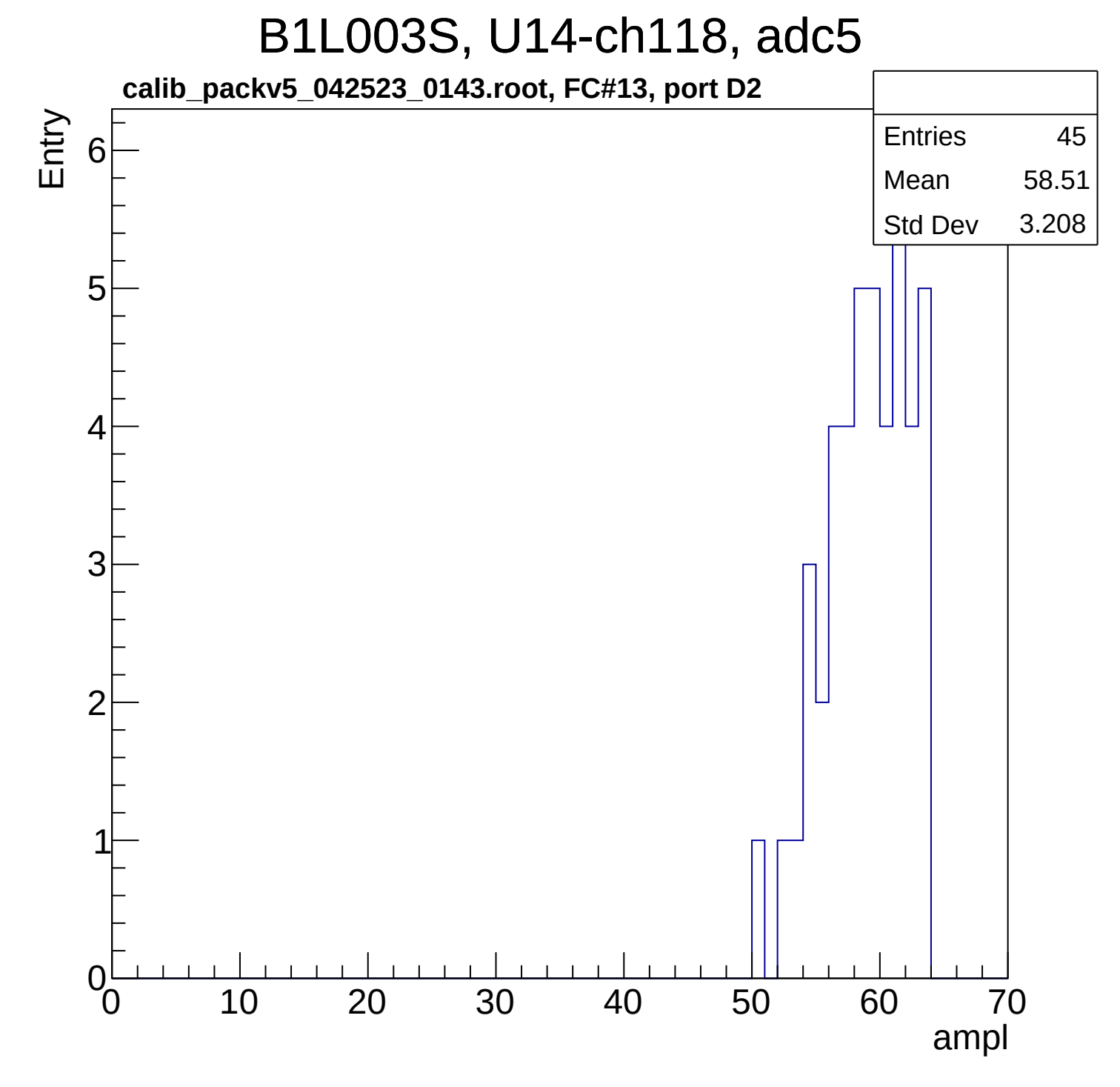

B1L003S, U14-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.79
Std Dev 3.295
Gaus mean : 29.6899
Gaus Width: 3.7908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

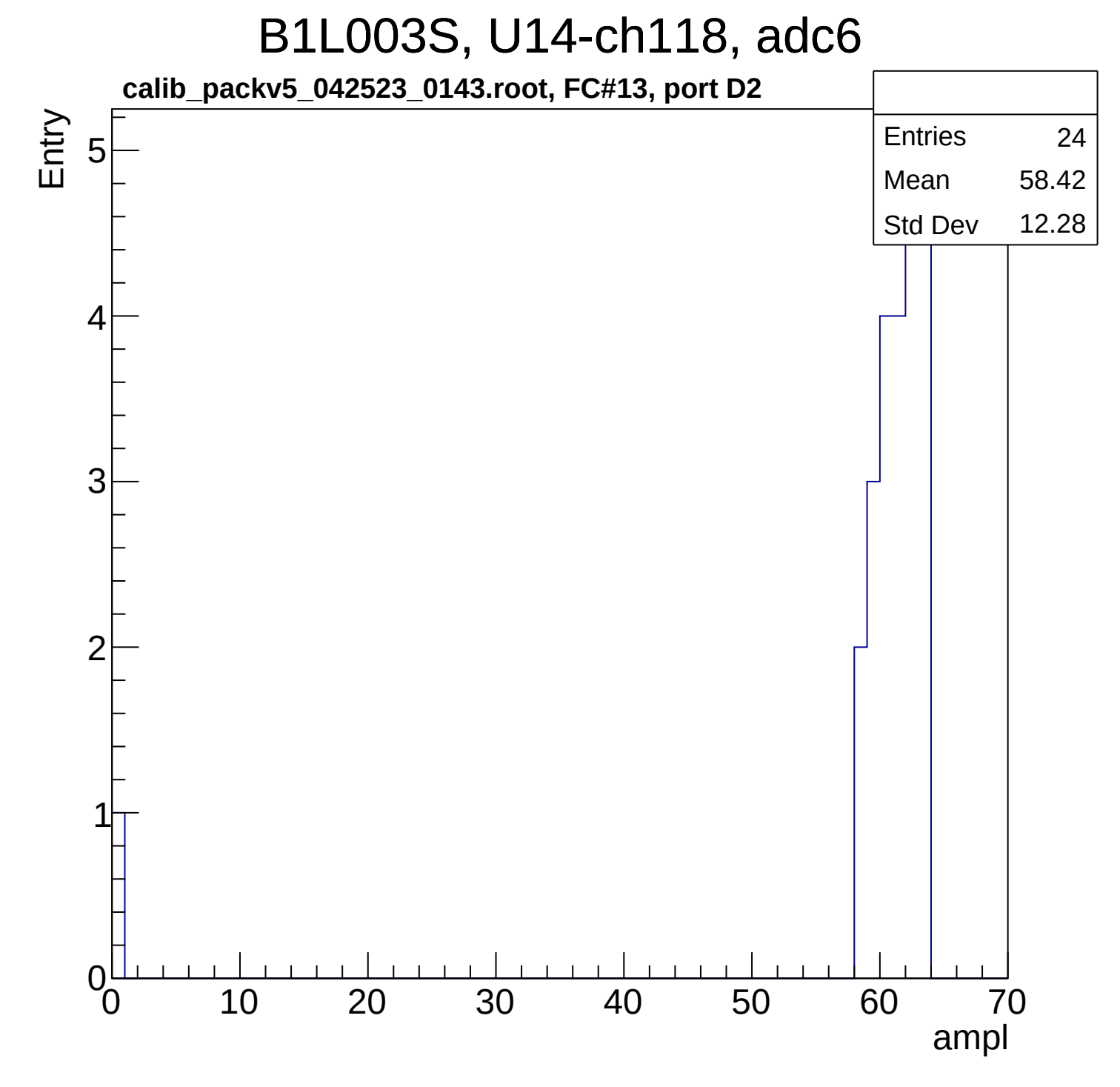

B1L003S, U14-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 34.92
Std Dev 3.436
Gaus mean : 34.8884
Gaus Width: 4.1674
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

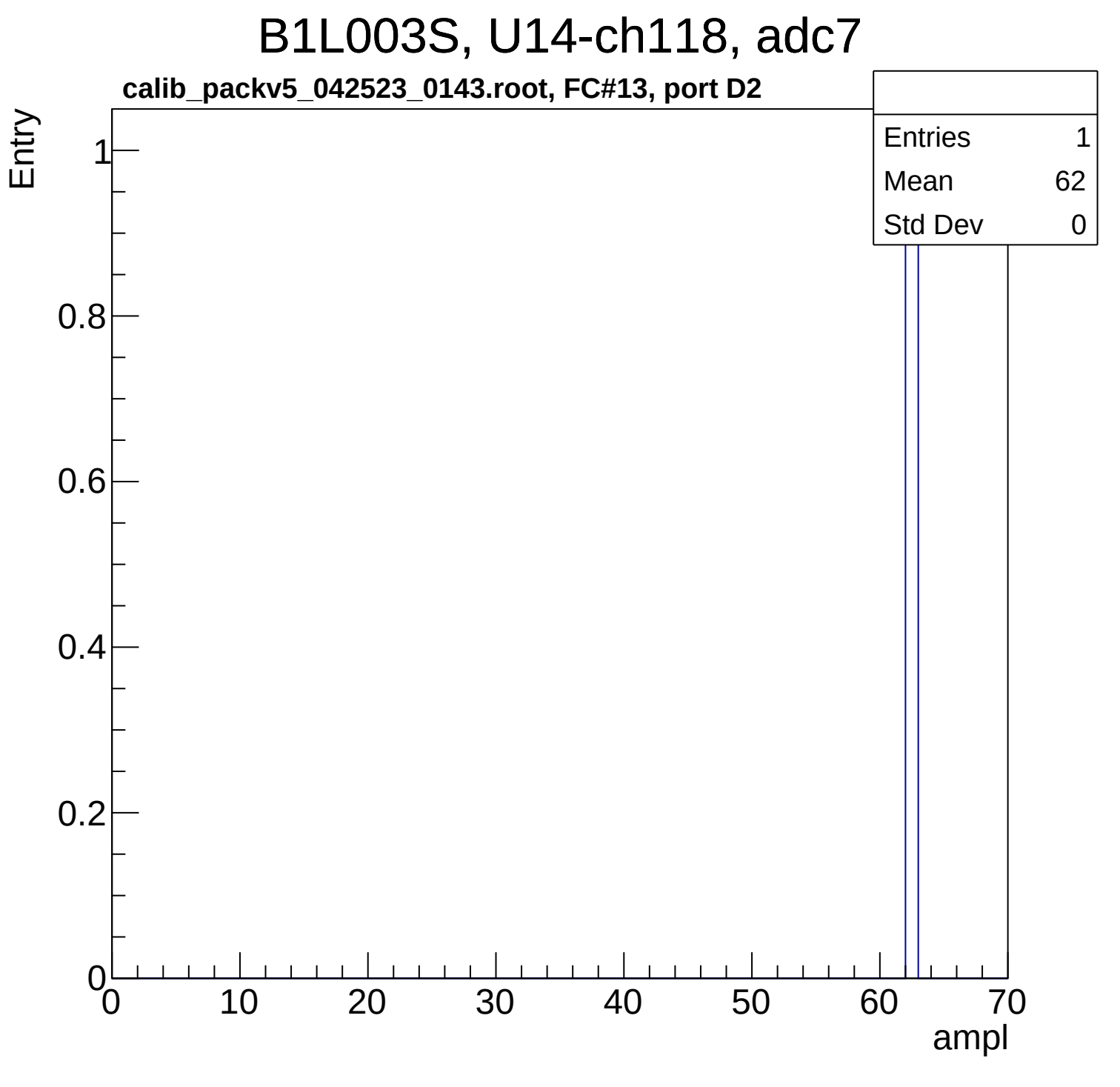

B1L003S, U14-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 40.73
Std Dev 3.188
Gaus mean : 40.6816
Gaus Width: 3.4415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

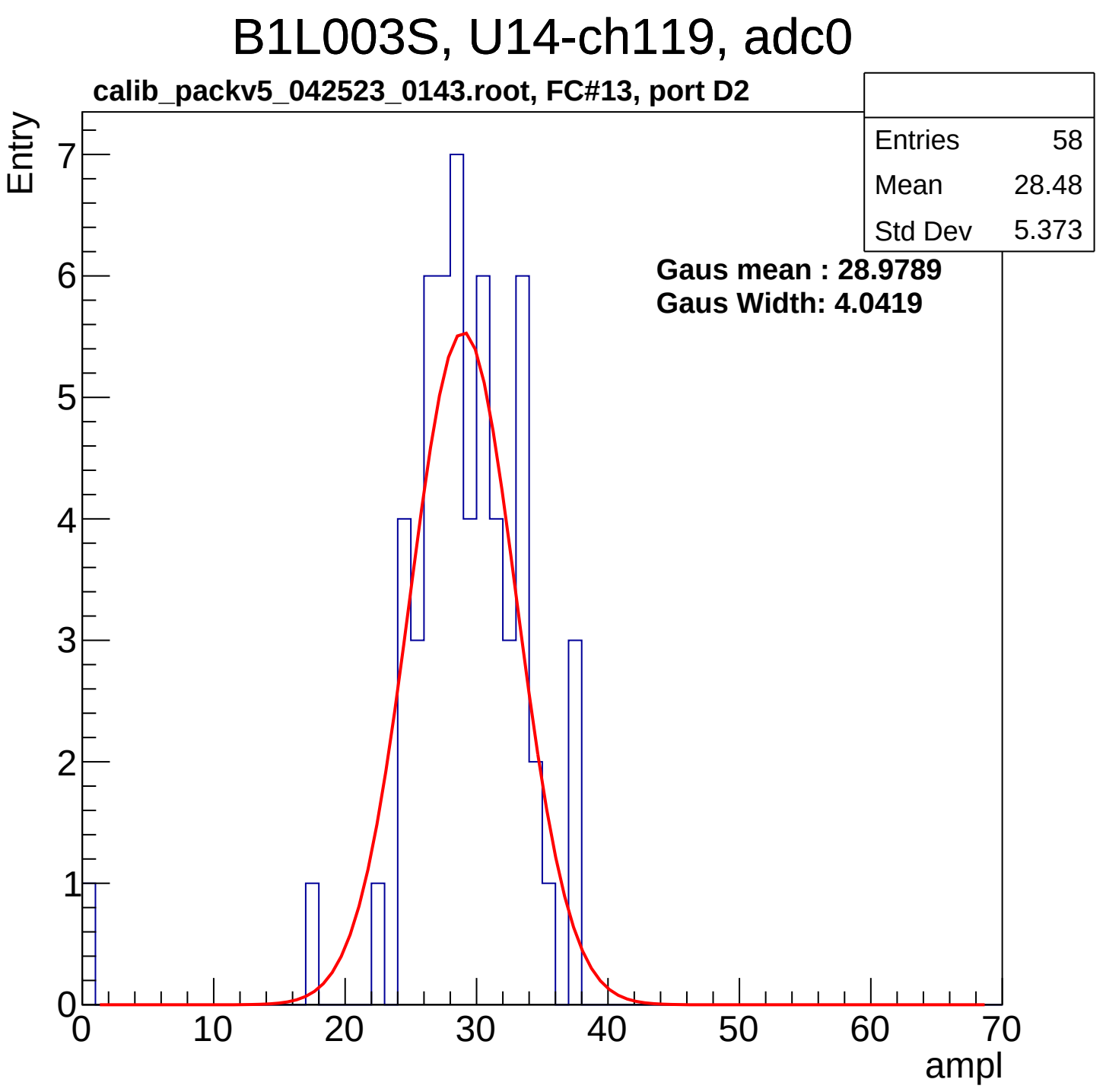

B1L003S, U14-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 47.35
Std Dev 3.802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

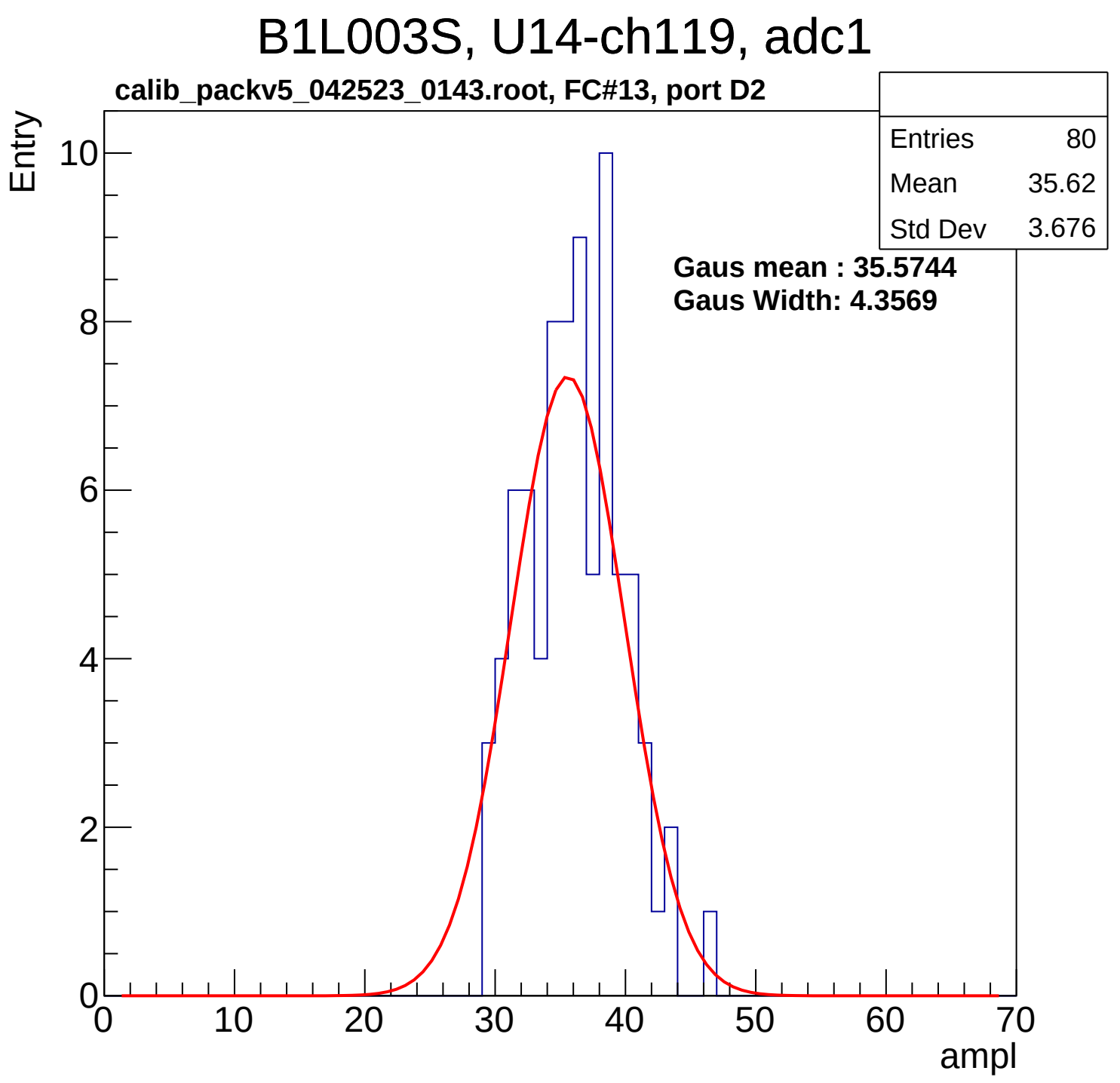

B1L003S, U14-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.1
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

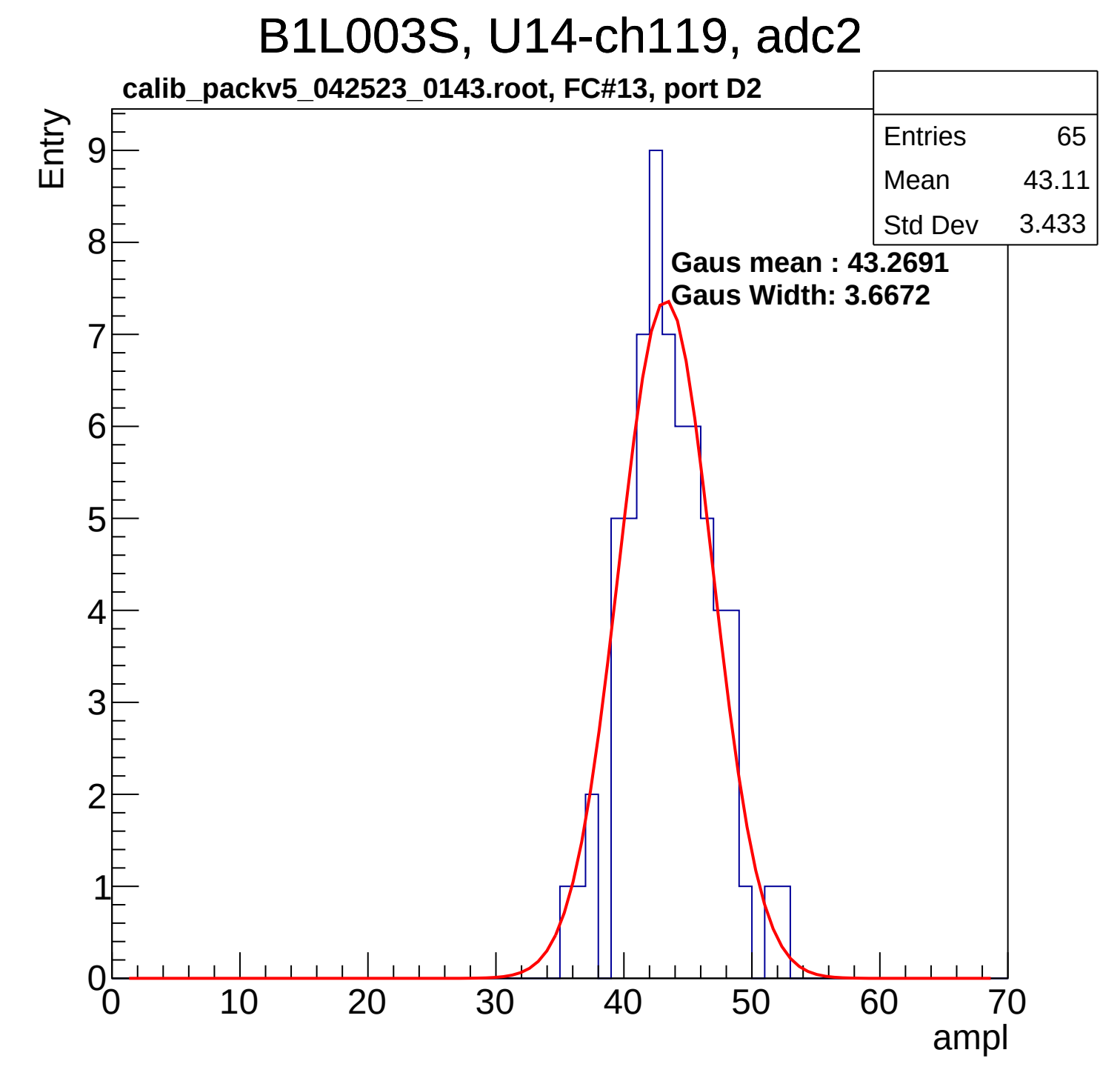

B1L003S, U14-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.51
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

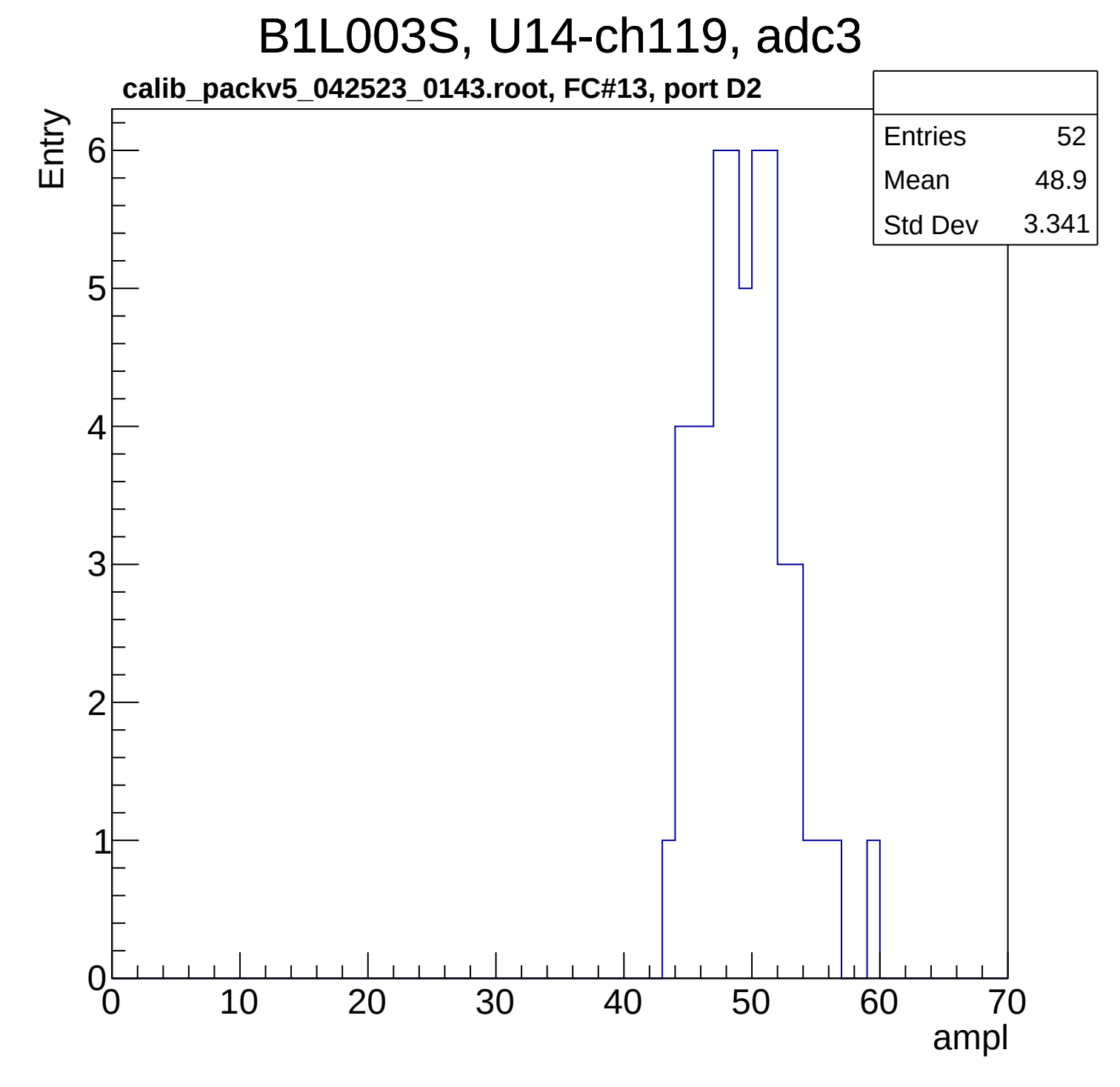

B1L003S, U14-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.42
Std Dev 12.28
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

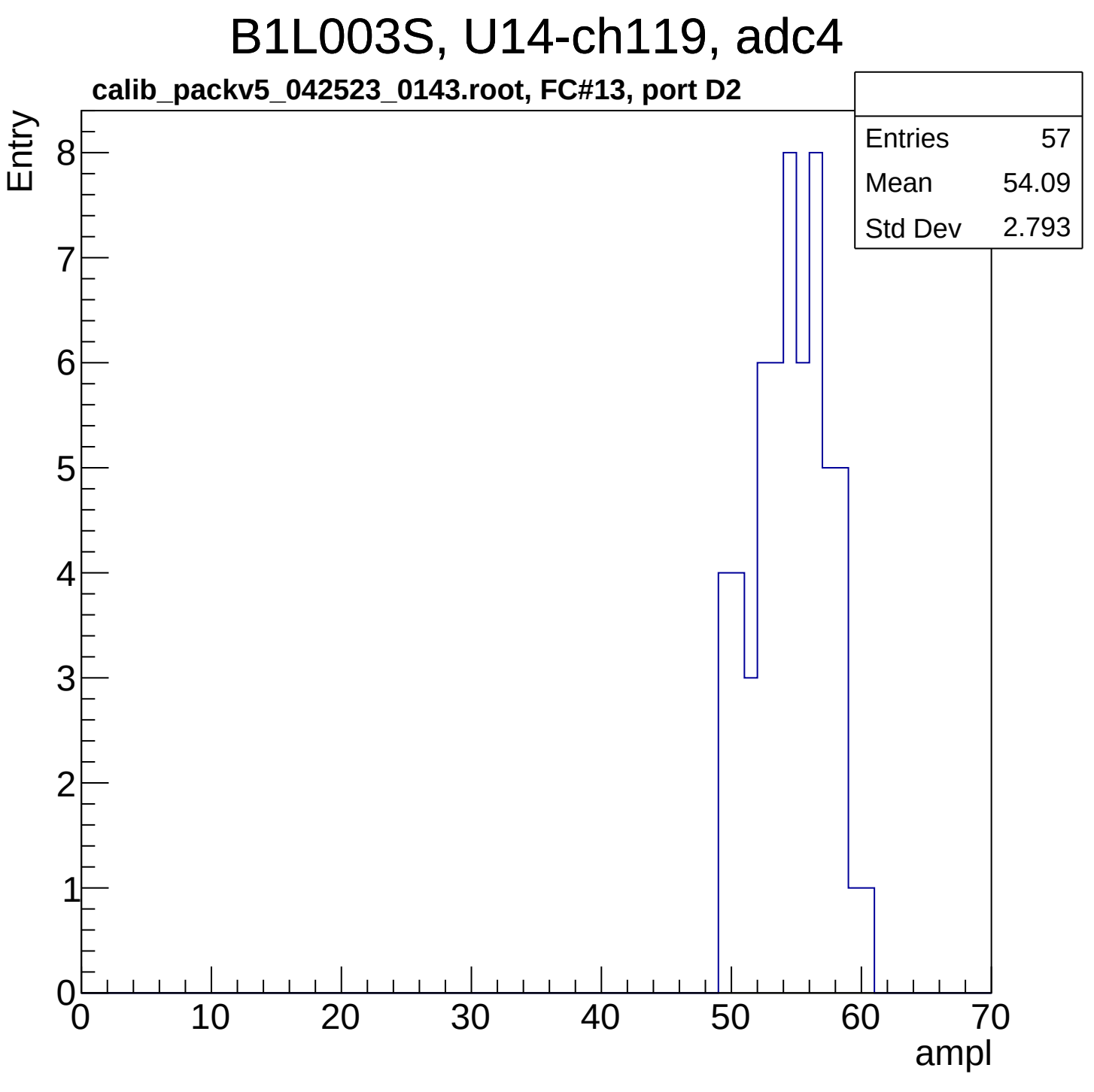

B1L003S, U14-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

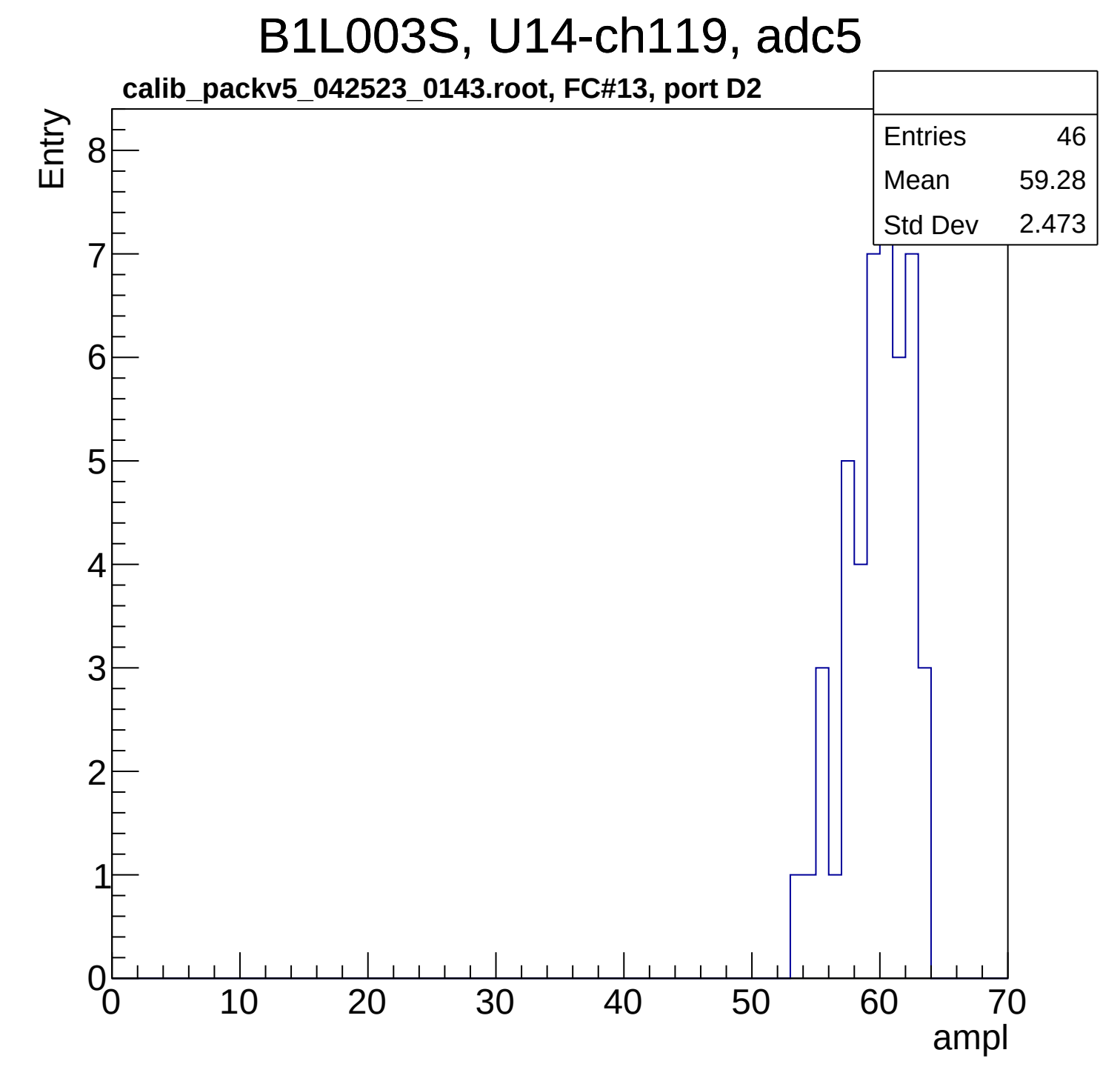

B1L003S, U14-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.48
Std Dev 5.373
Gaus mean : 28.9789
Gaus Width: 4.0419
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

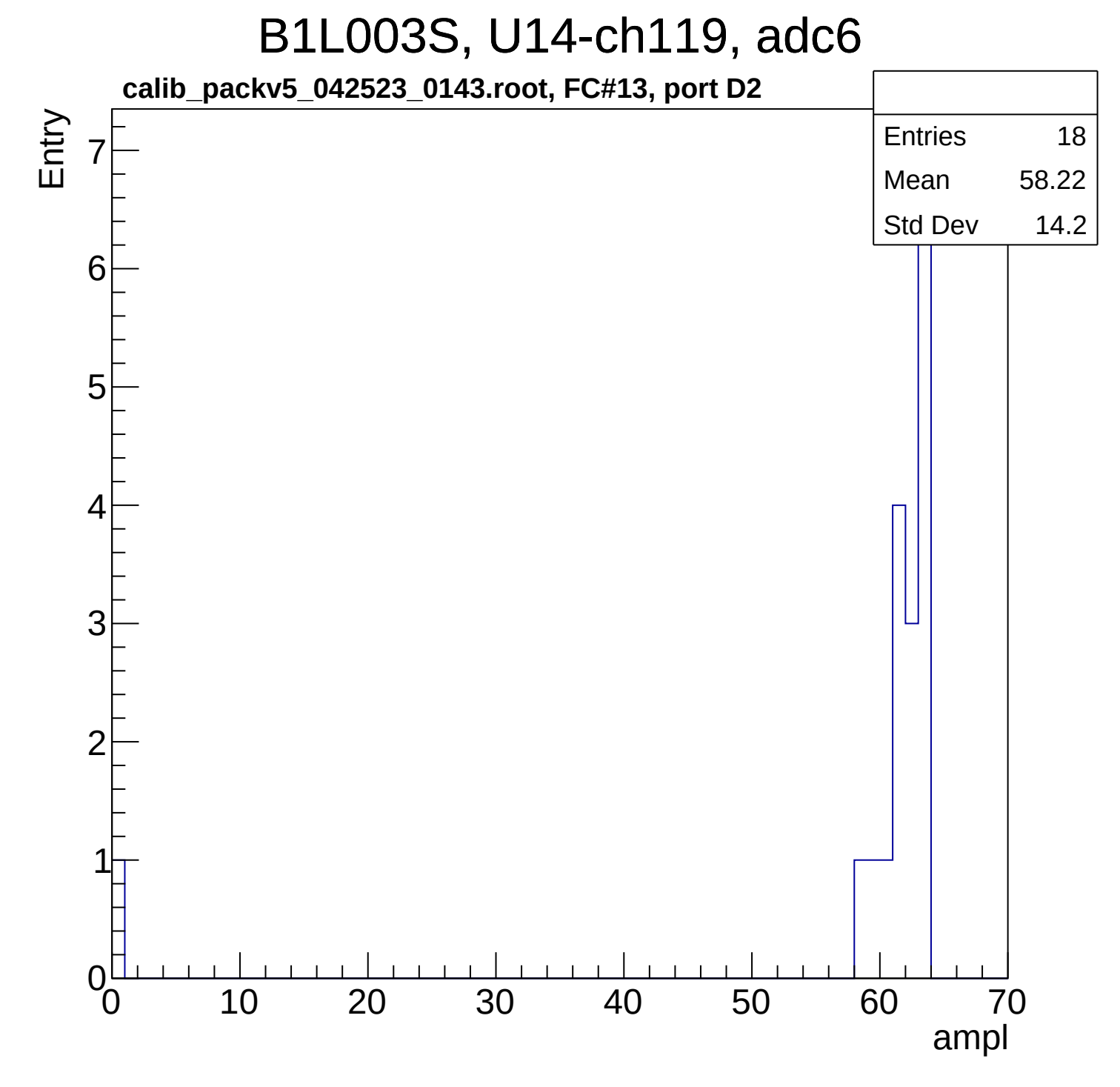

B1L003S, U14-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 35.62
Std Dev 3.676
Gaus mean : 35.5744
Gaus Width: 4.3569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

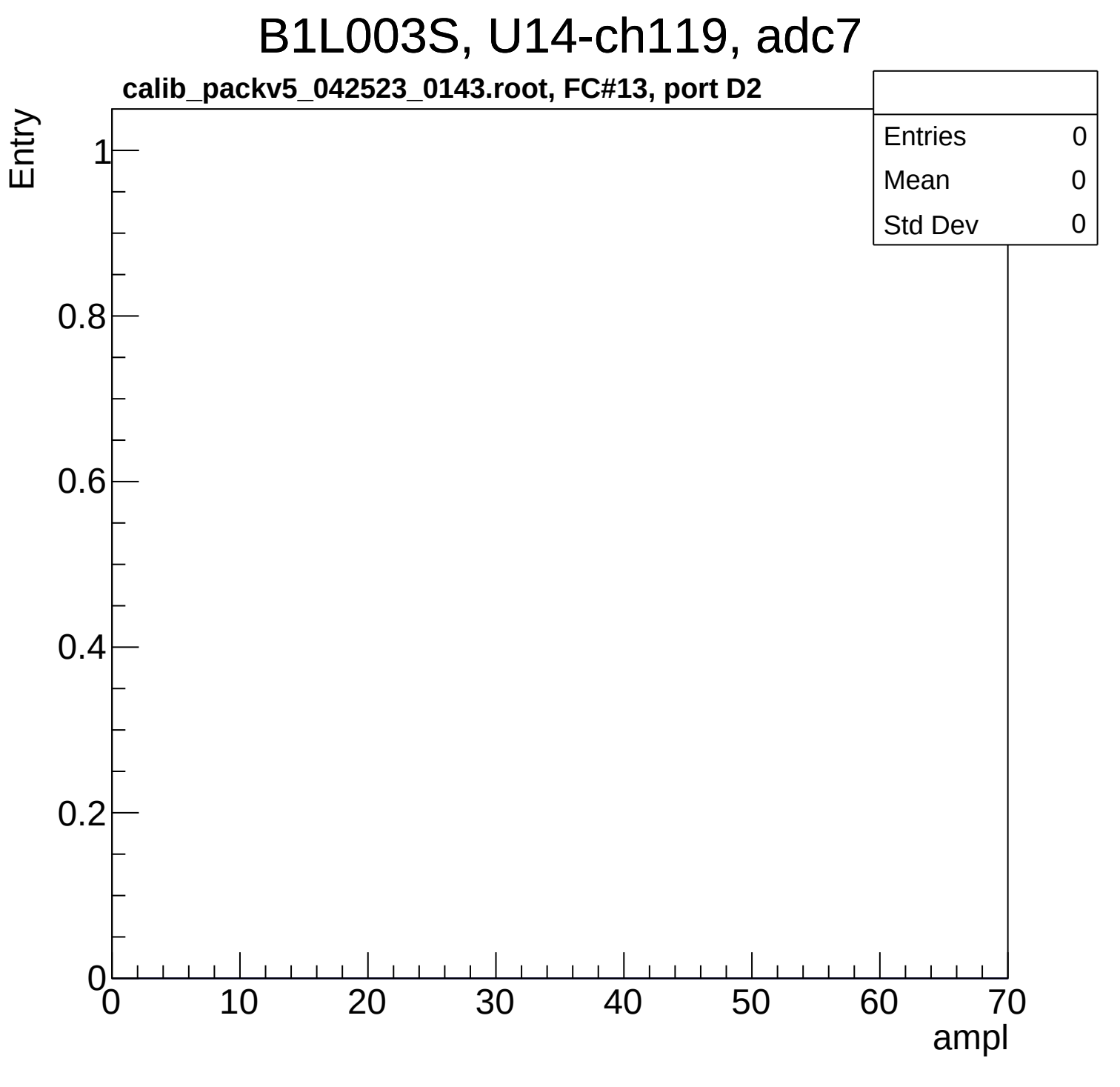

B1L003S, U14-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.11
Std Dev 3.433
Gaus mean : 43.2691
Gaus Width: 3.6672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

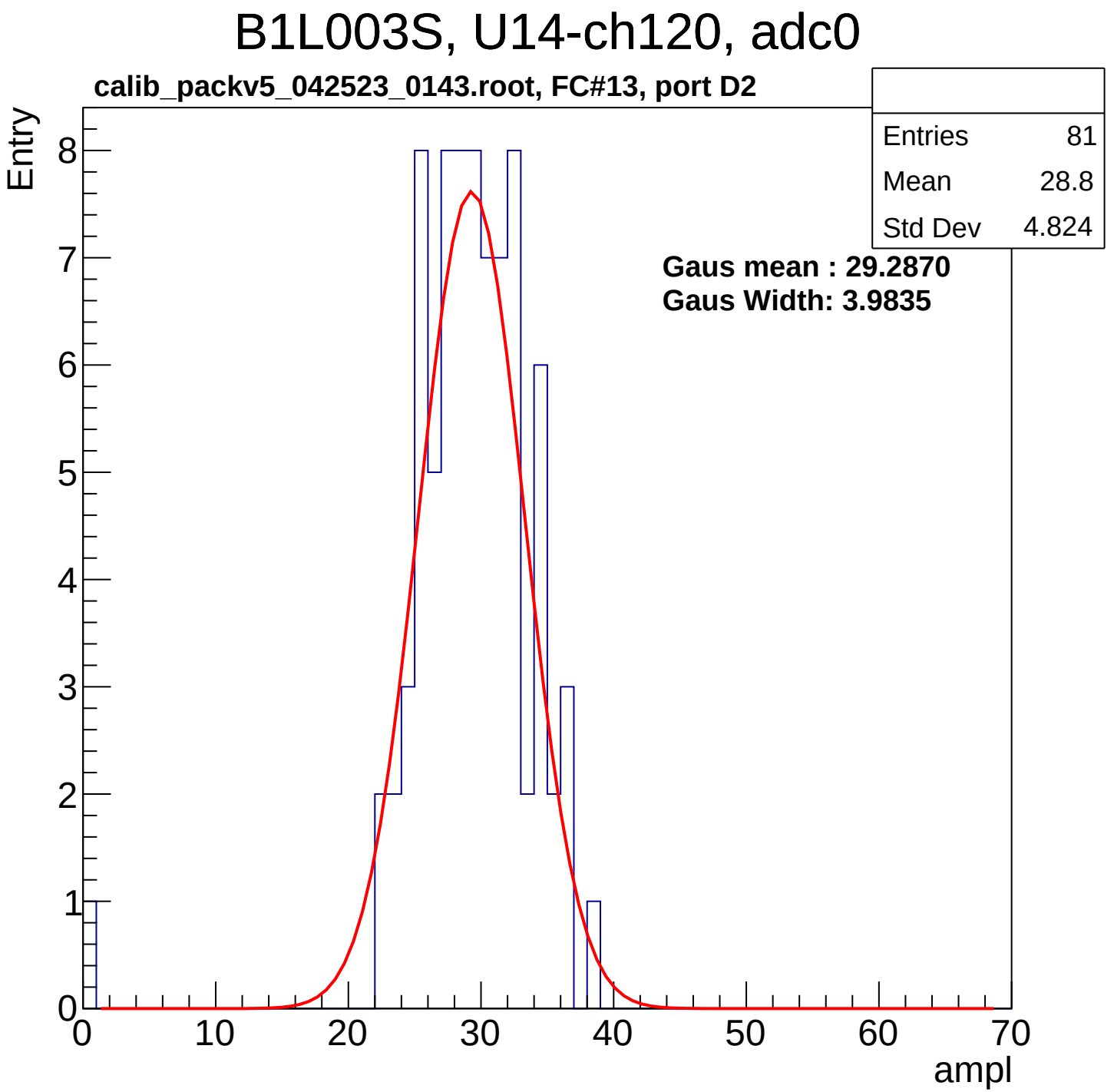

B1L003S, U14-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 48.9
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

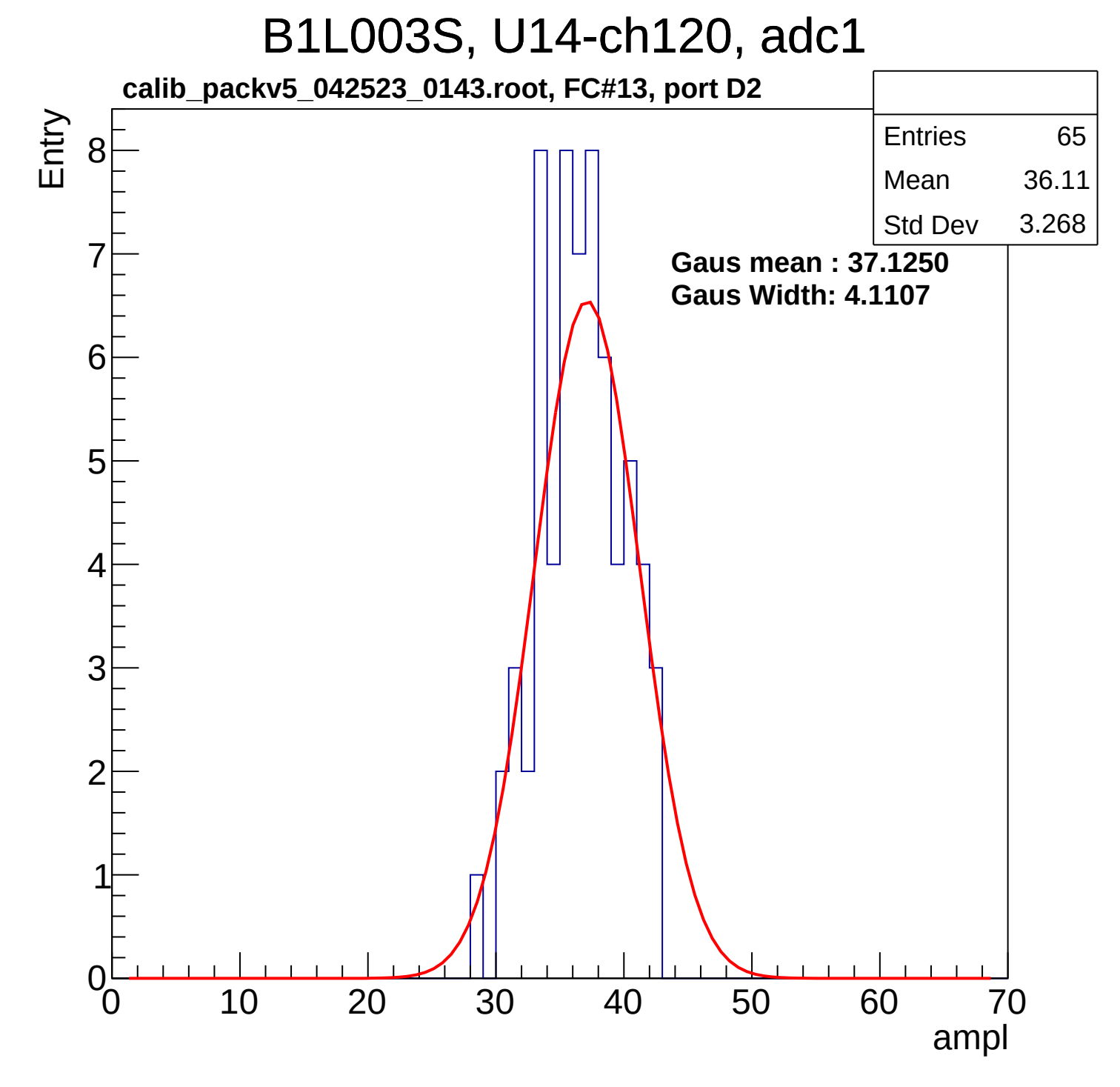

B1L003S, U14-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.09
Std Dev 2.793
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

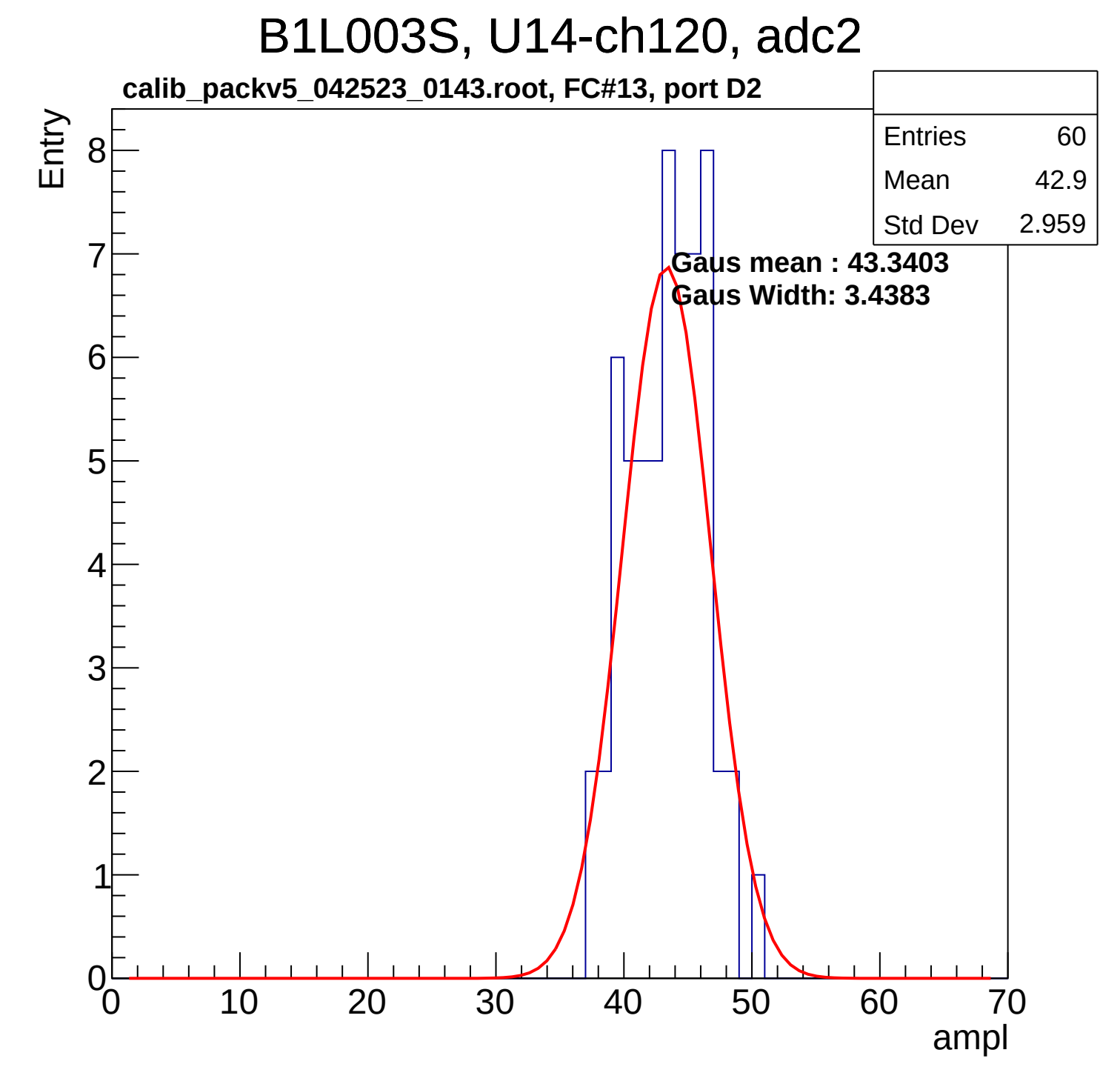

B1L003S, U14-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.28
Std Dev 2.473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

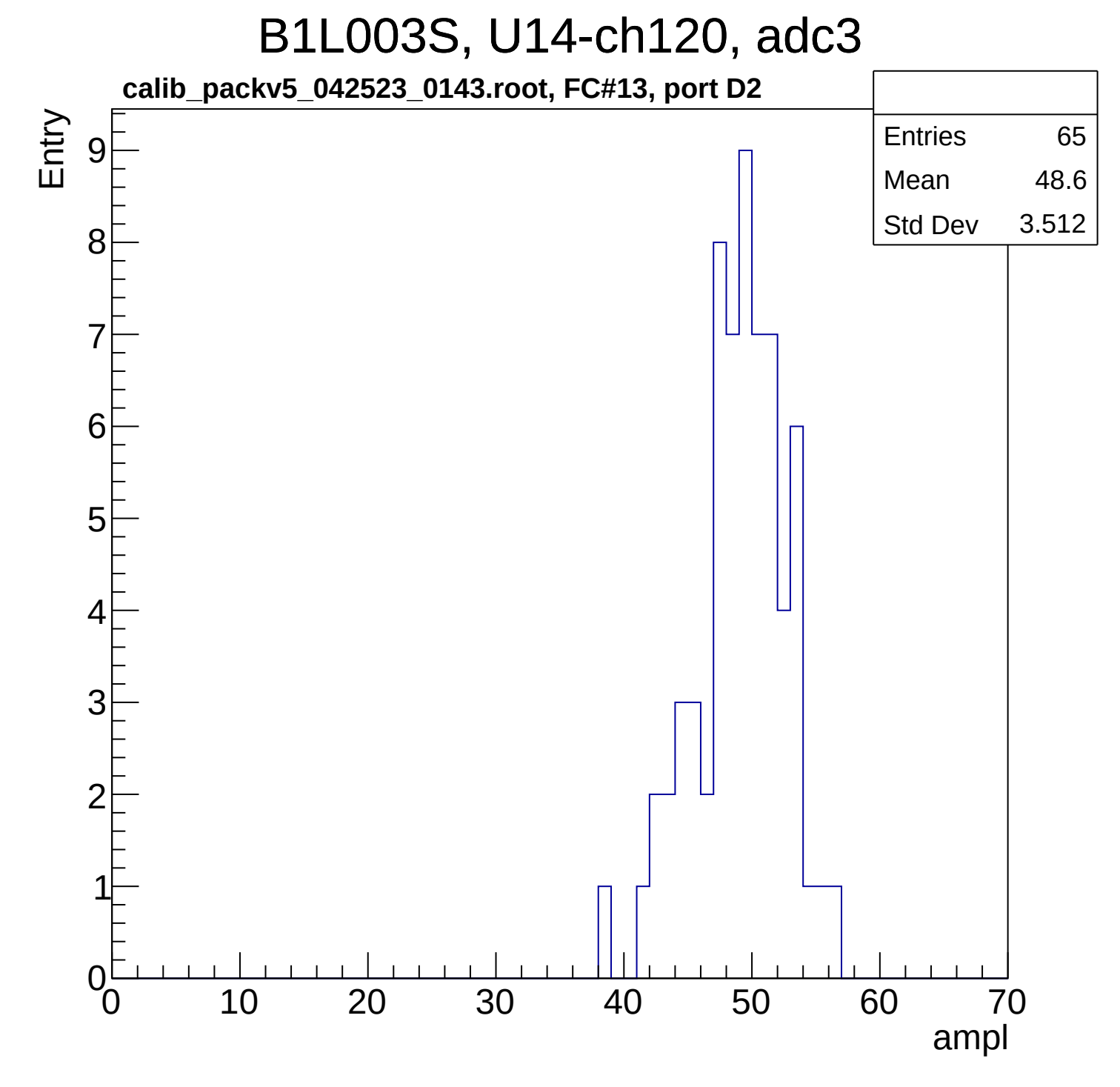

B1L003S, U14-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 58.22
Std Dev 14.2
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

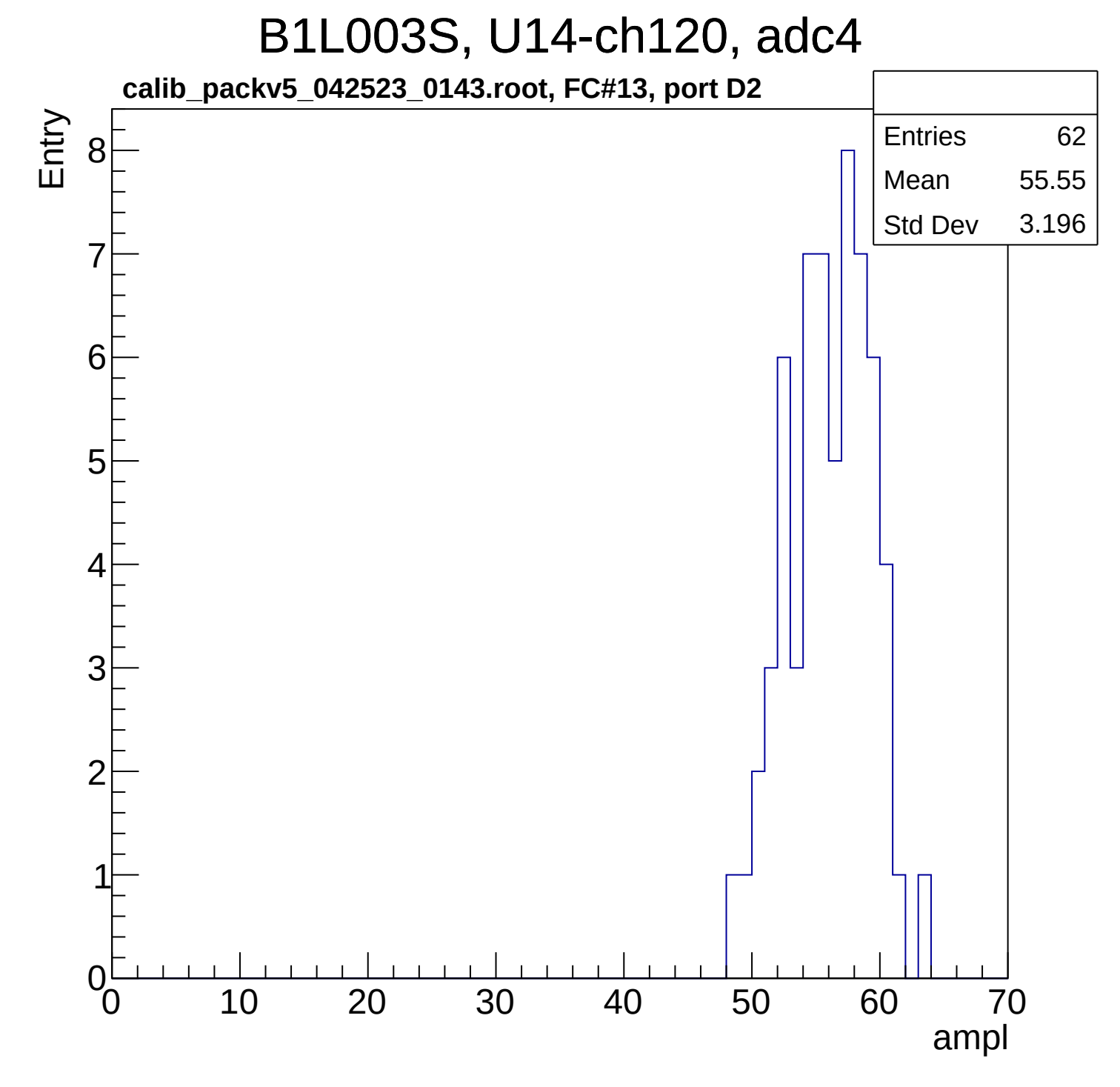

B1L003S, U14-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

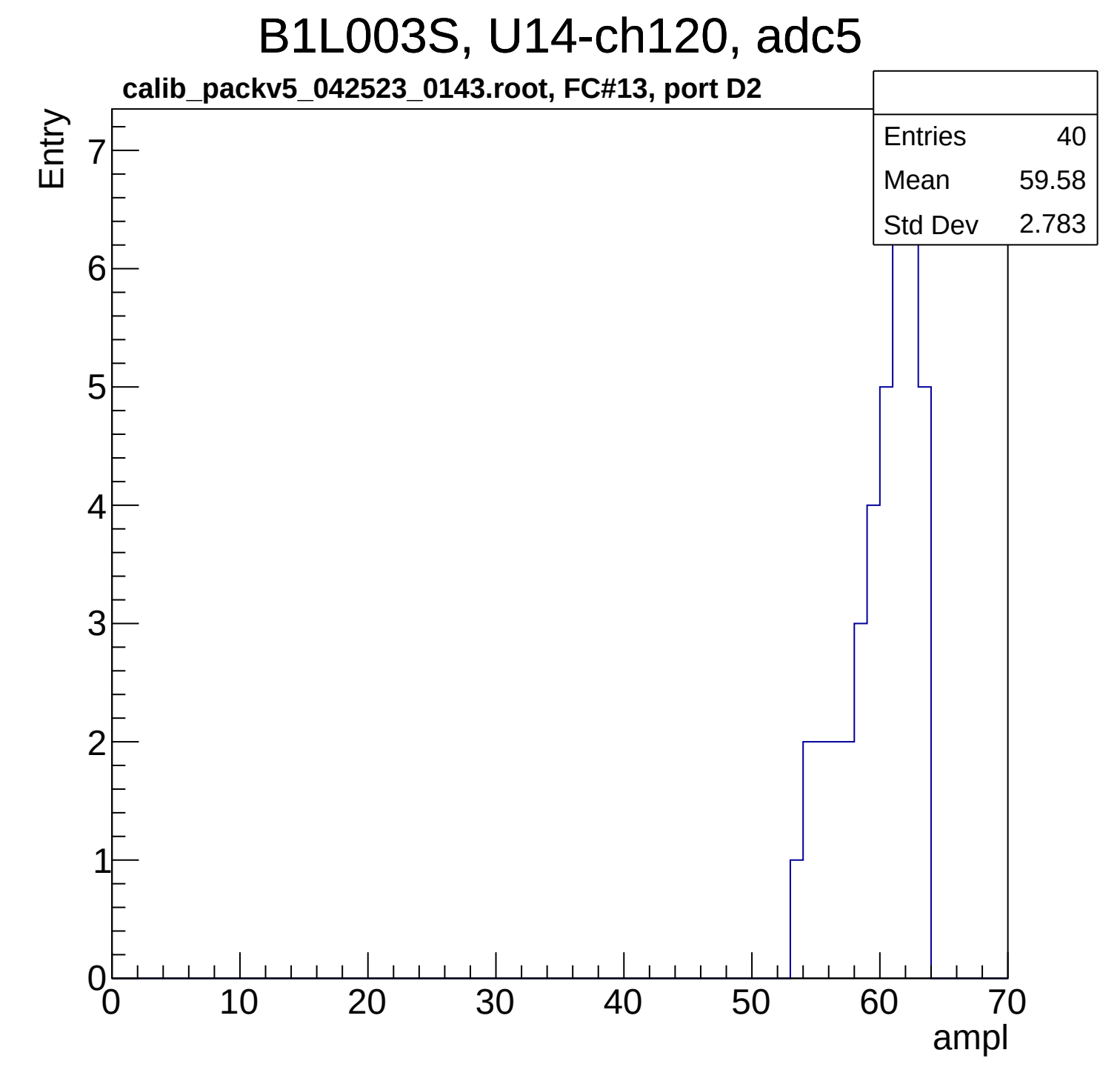

B1L003S, U14-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.8
Std Dev 4.824
Gaus mean : 29.2870
Gaus Width: 3.9835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

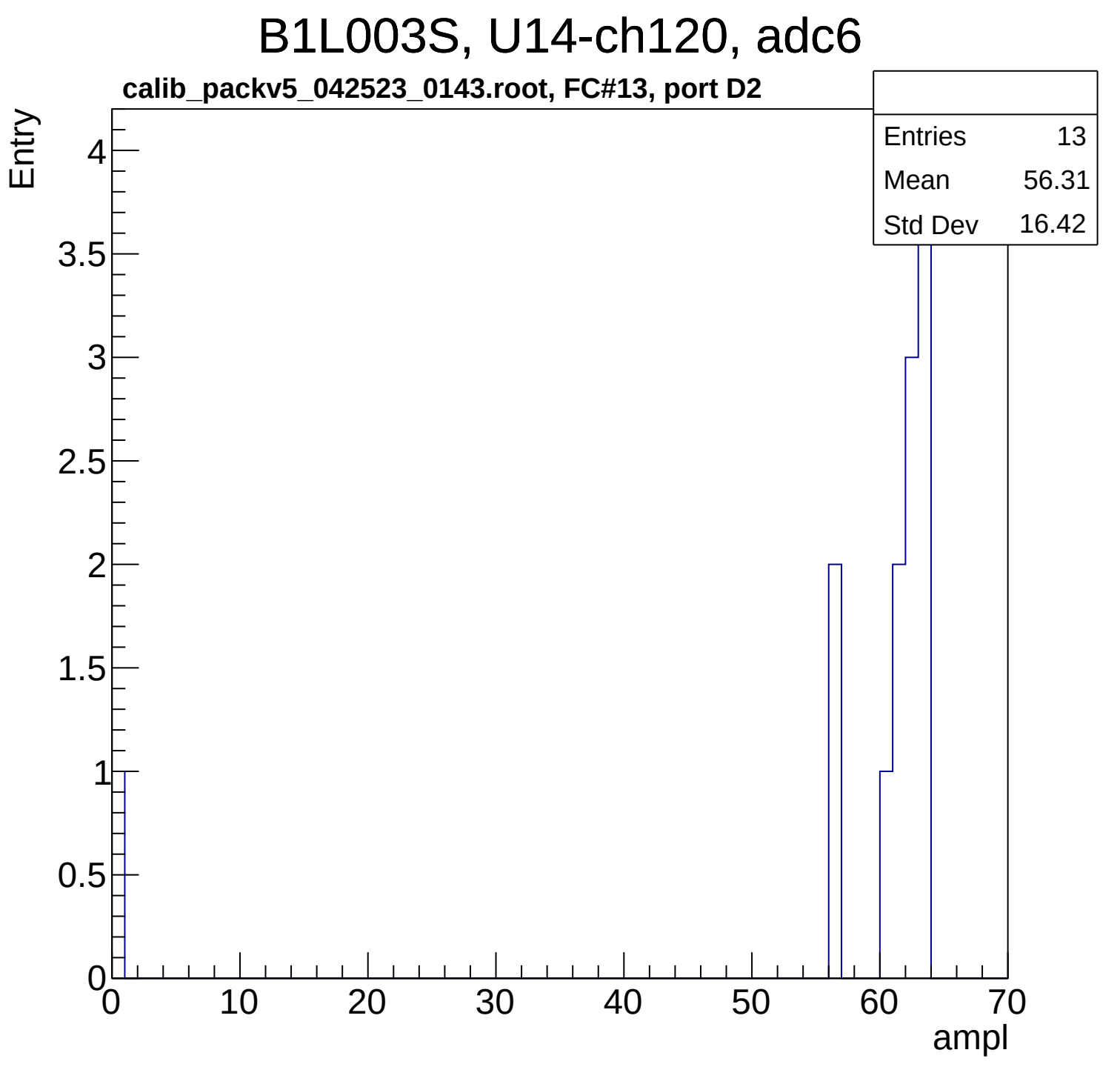

B1L003S, U14-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 36.11
Std Dev 3.268
Gaus mean : 37.1250
Gaus Width: 4.1107
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

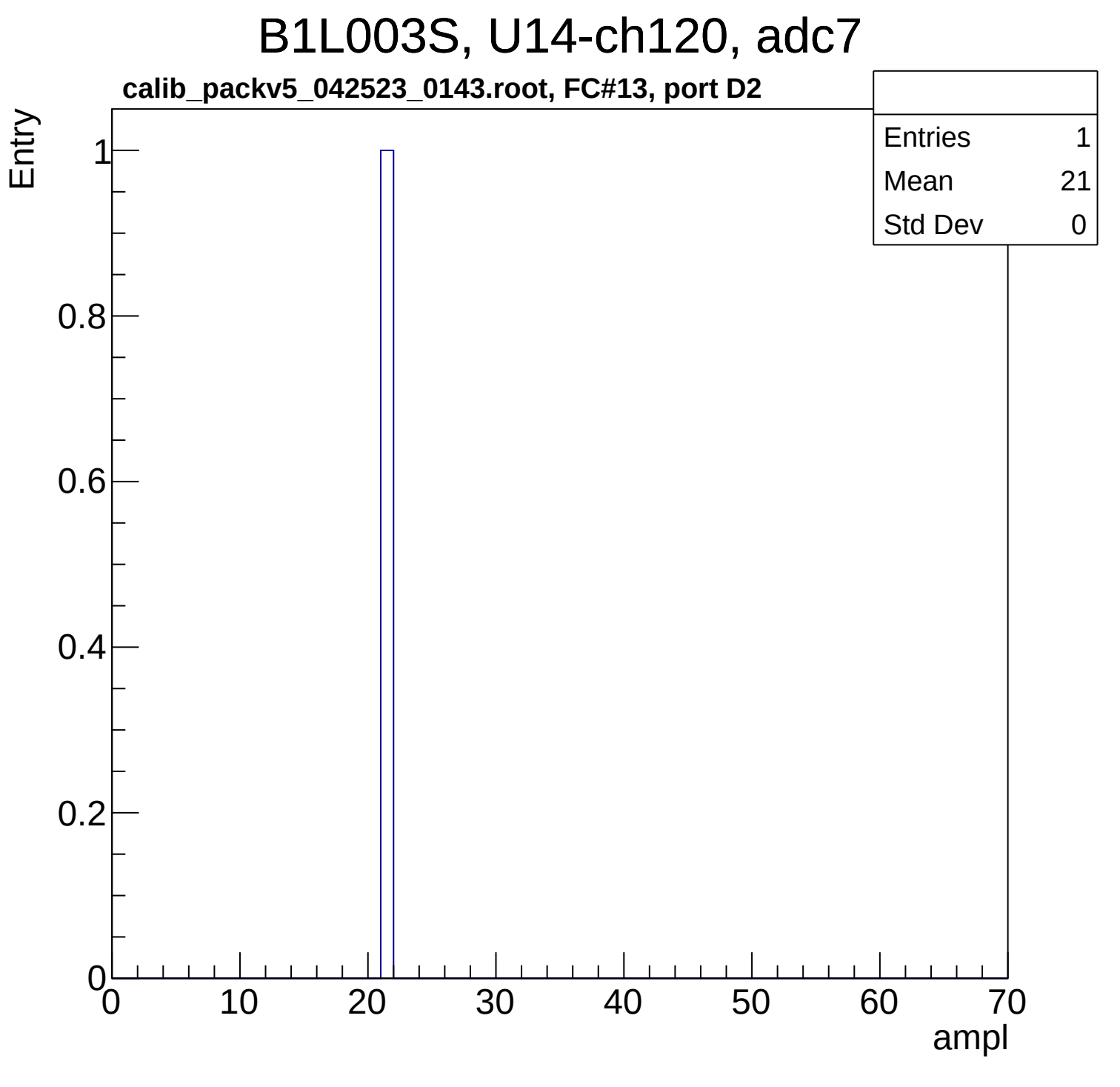

B1L003S, U14-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.9
Std Dev 2.959
Gaus mean : 43.3403
Gaus Width: 3.4383
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

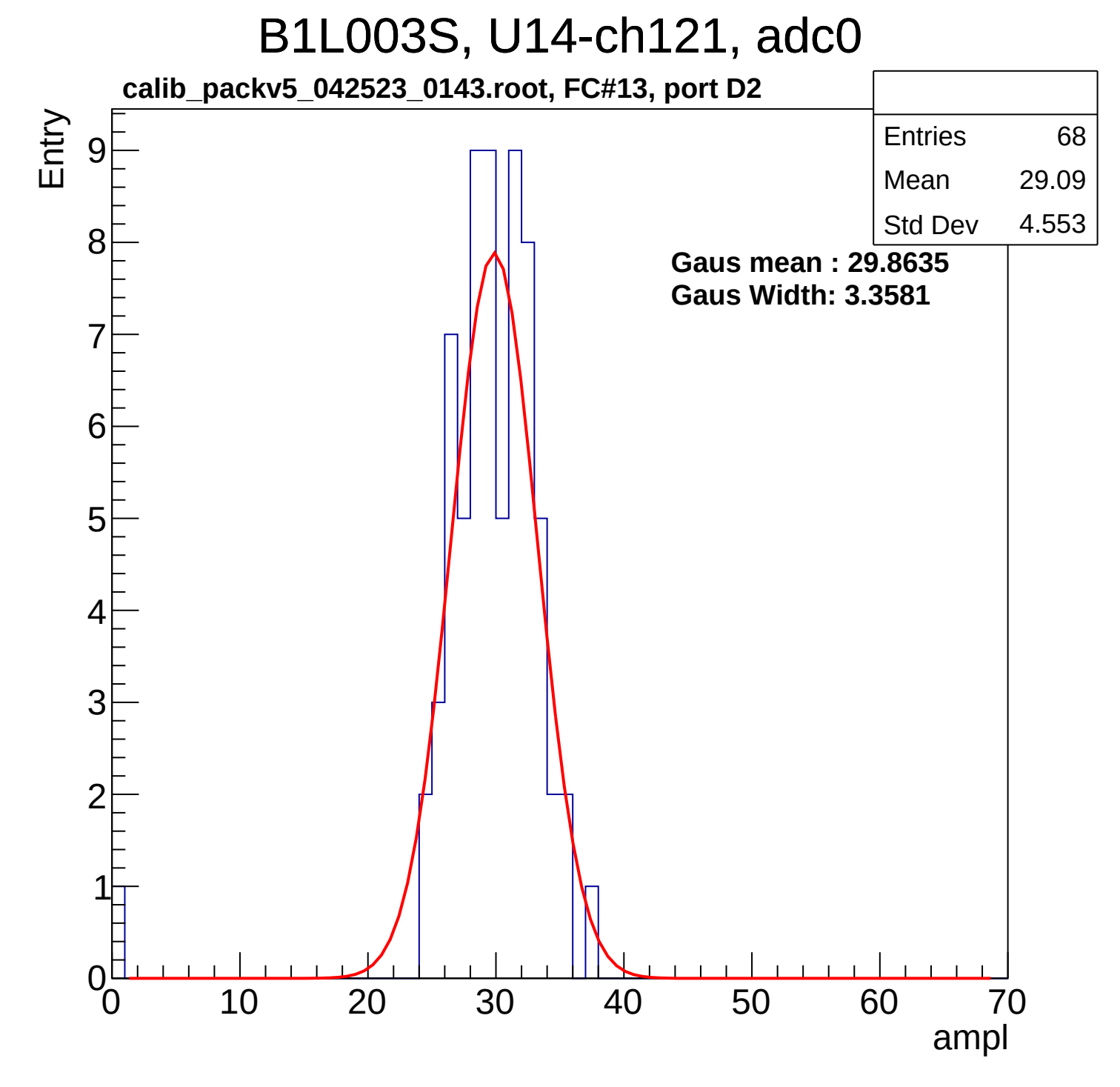

B1L003S, U14-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.6
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

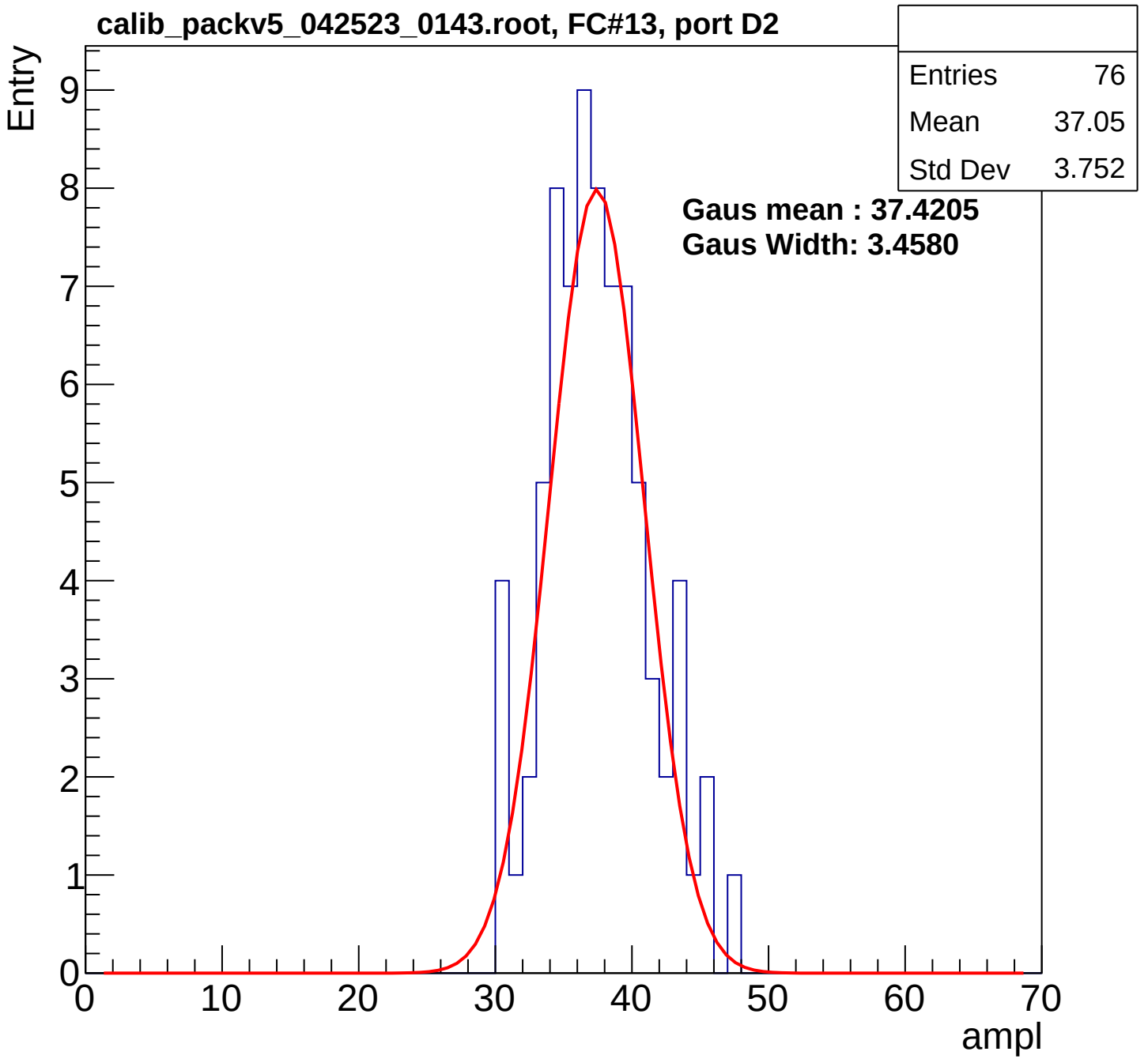

B1L003S, U14-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.55
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

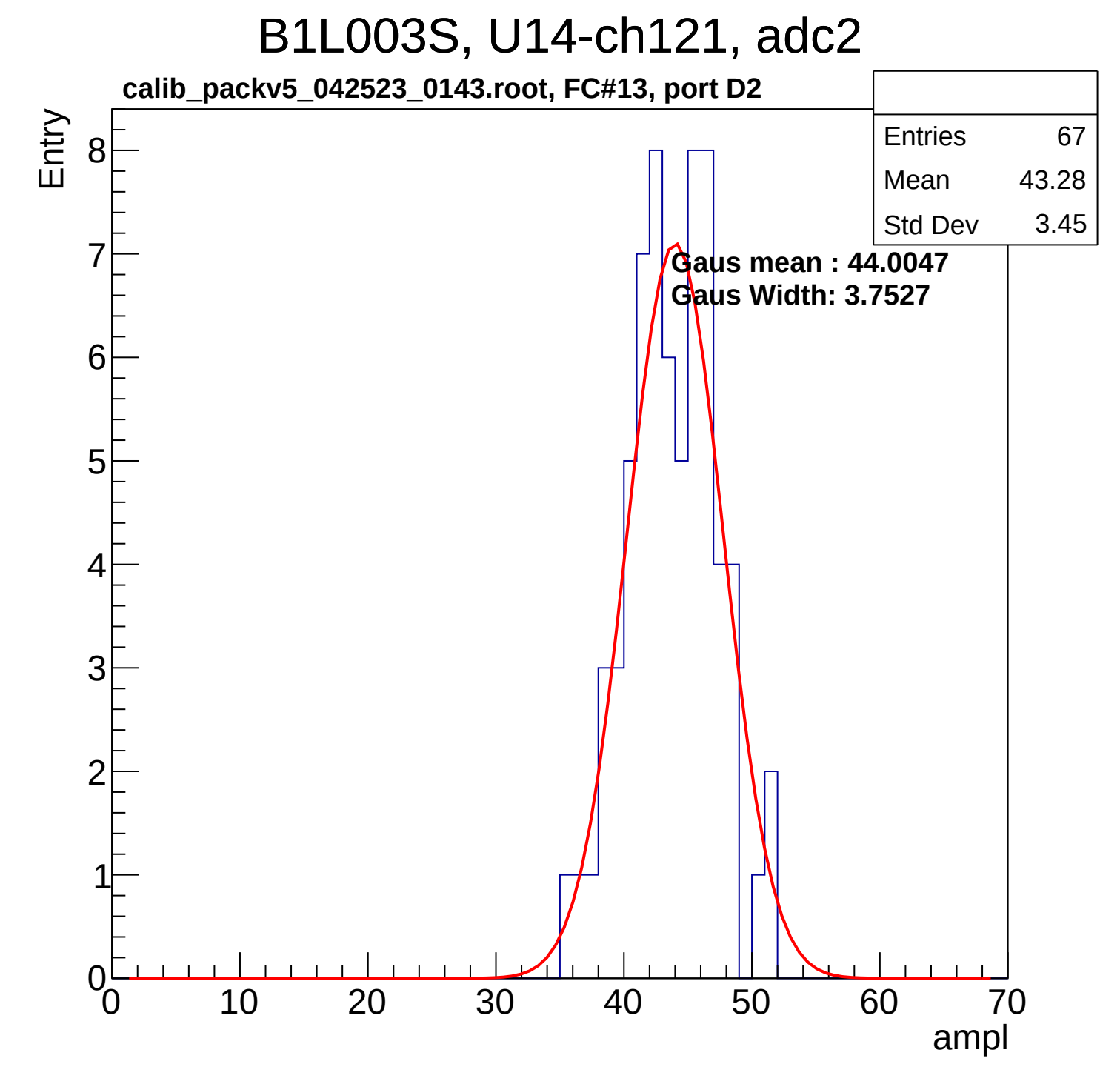

B1L003S, U14-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.58
Std Dev 2.783
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

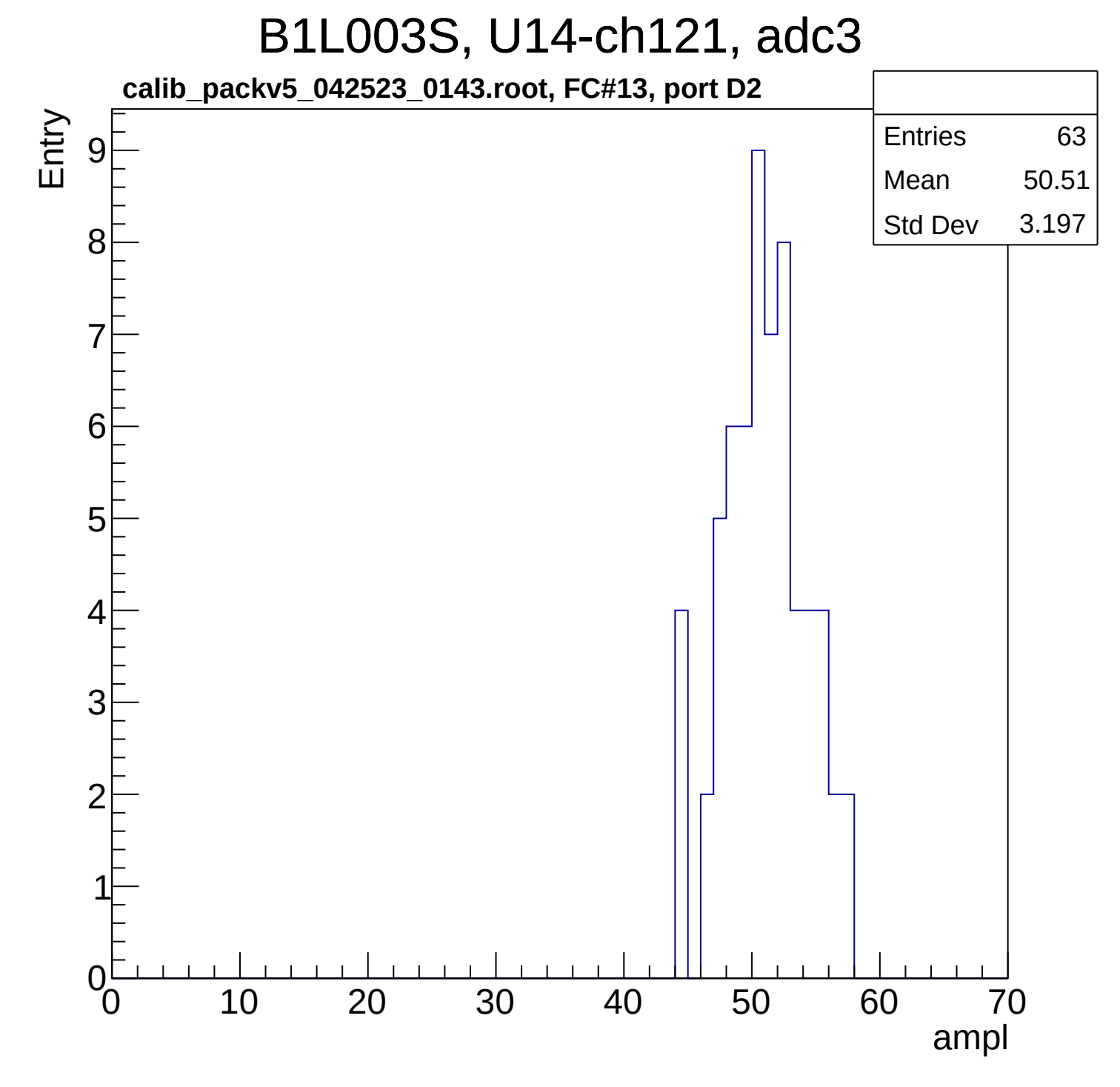

B1L003S, U14-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.31
Std Dev 16.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

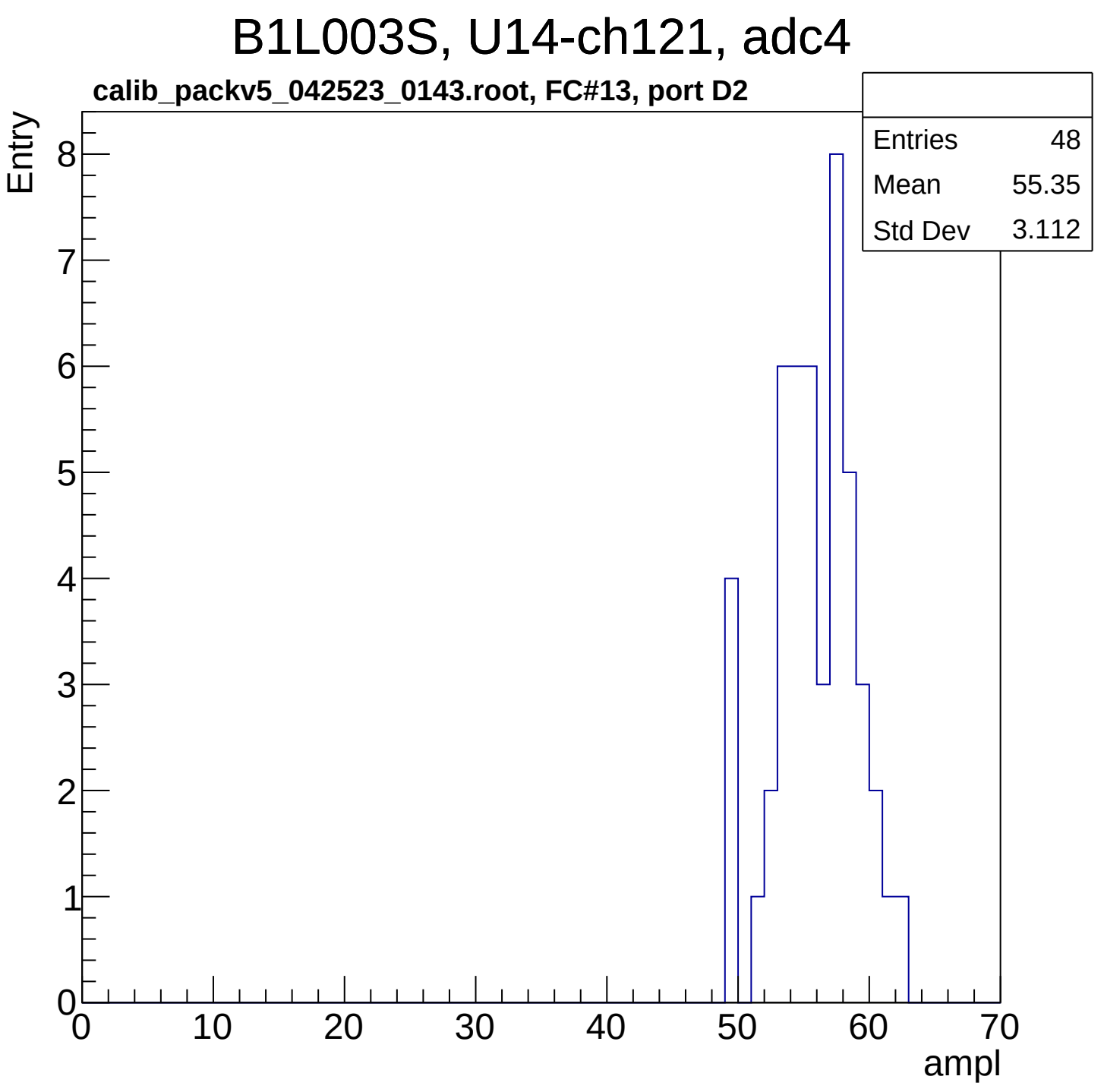

B1L003S, U14-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

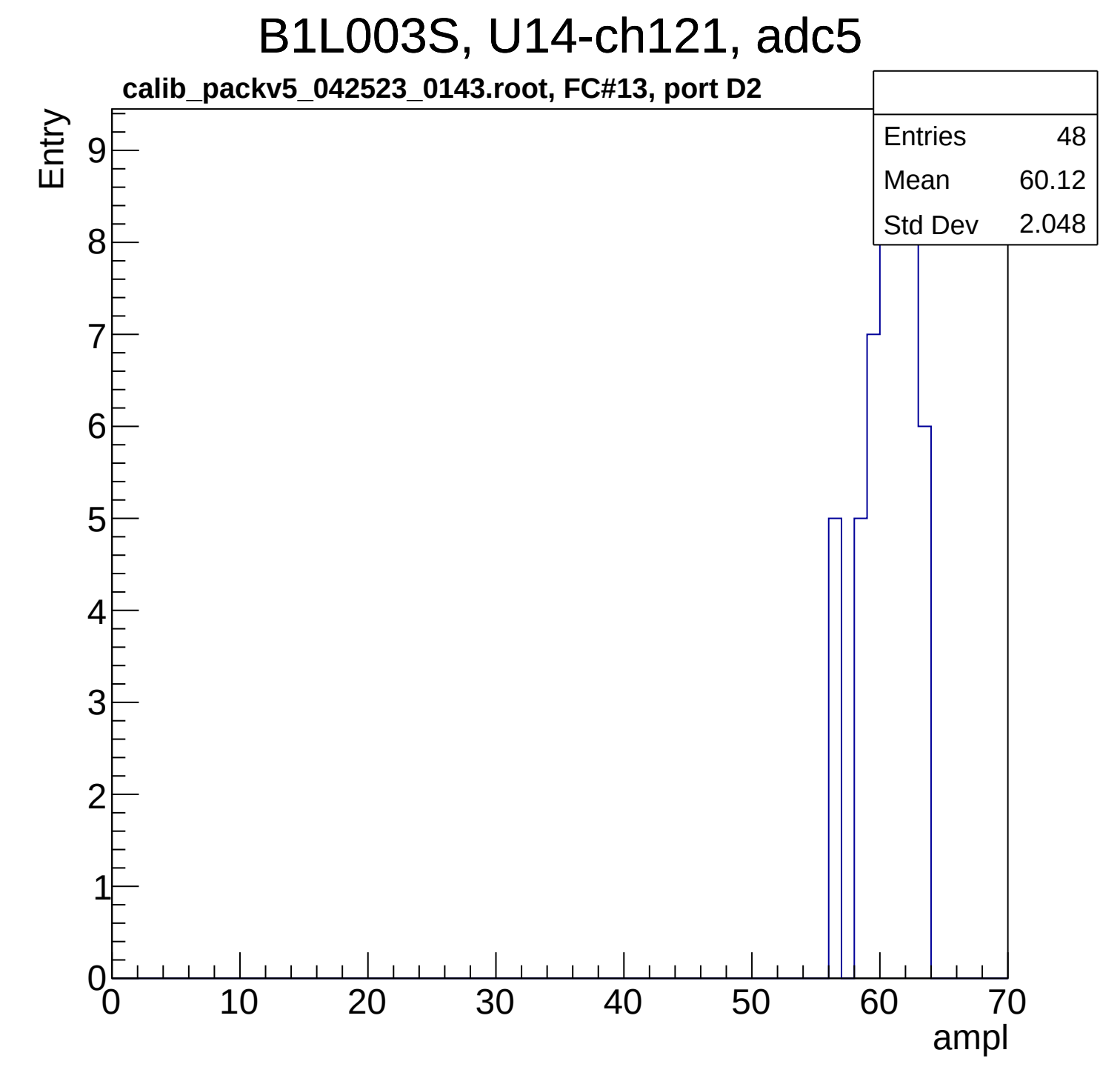

B1L003S, U14-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.09
Std Dev 4.553
Gaus mean : 29.8635
Gaus Width: 3.3581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

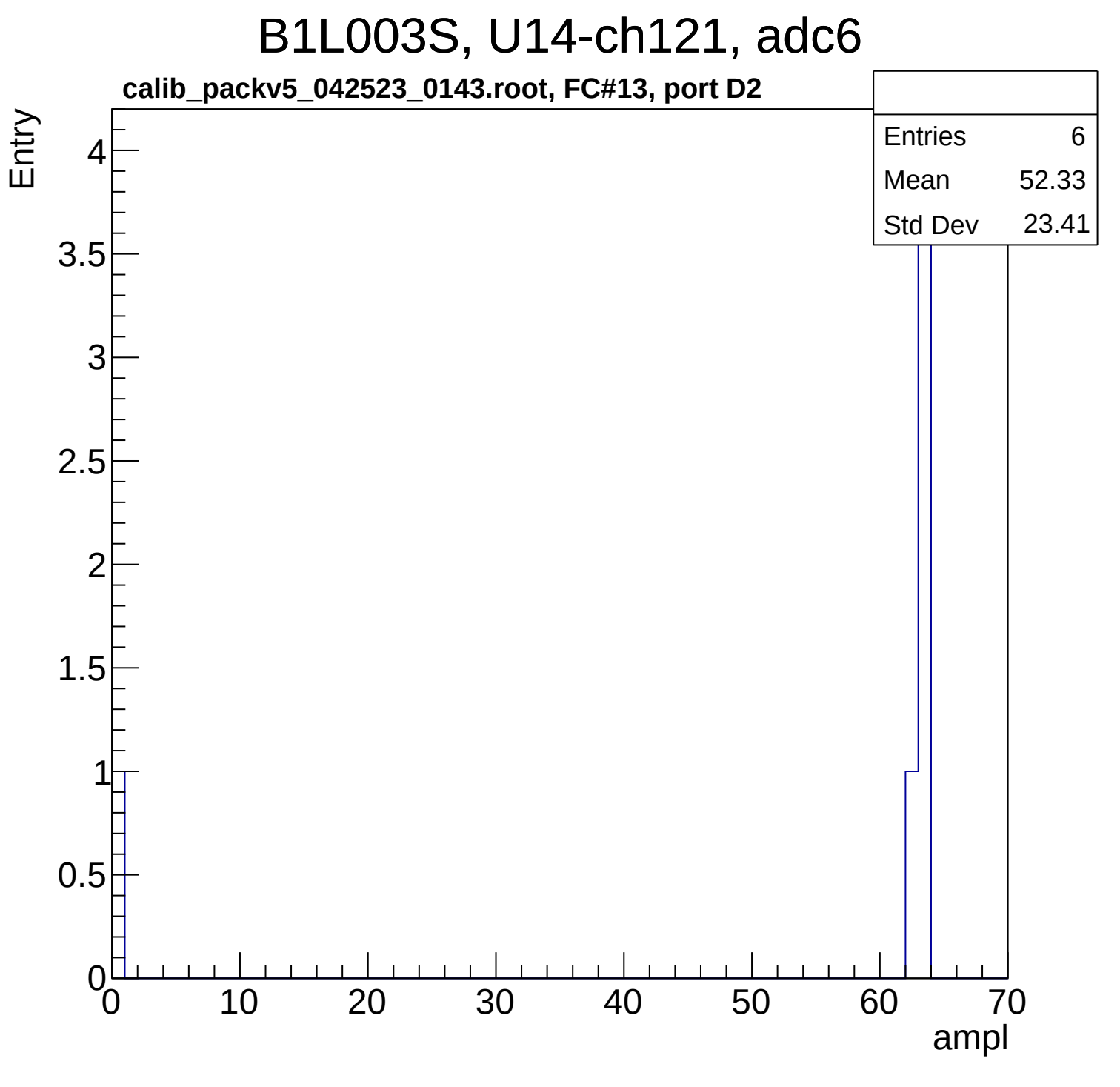

B1L003S, U14-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 37.05
Std Dev 3.752
Gaus mean : 37.4205
Gaus Width: 3.4580
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

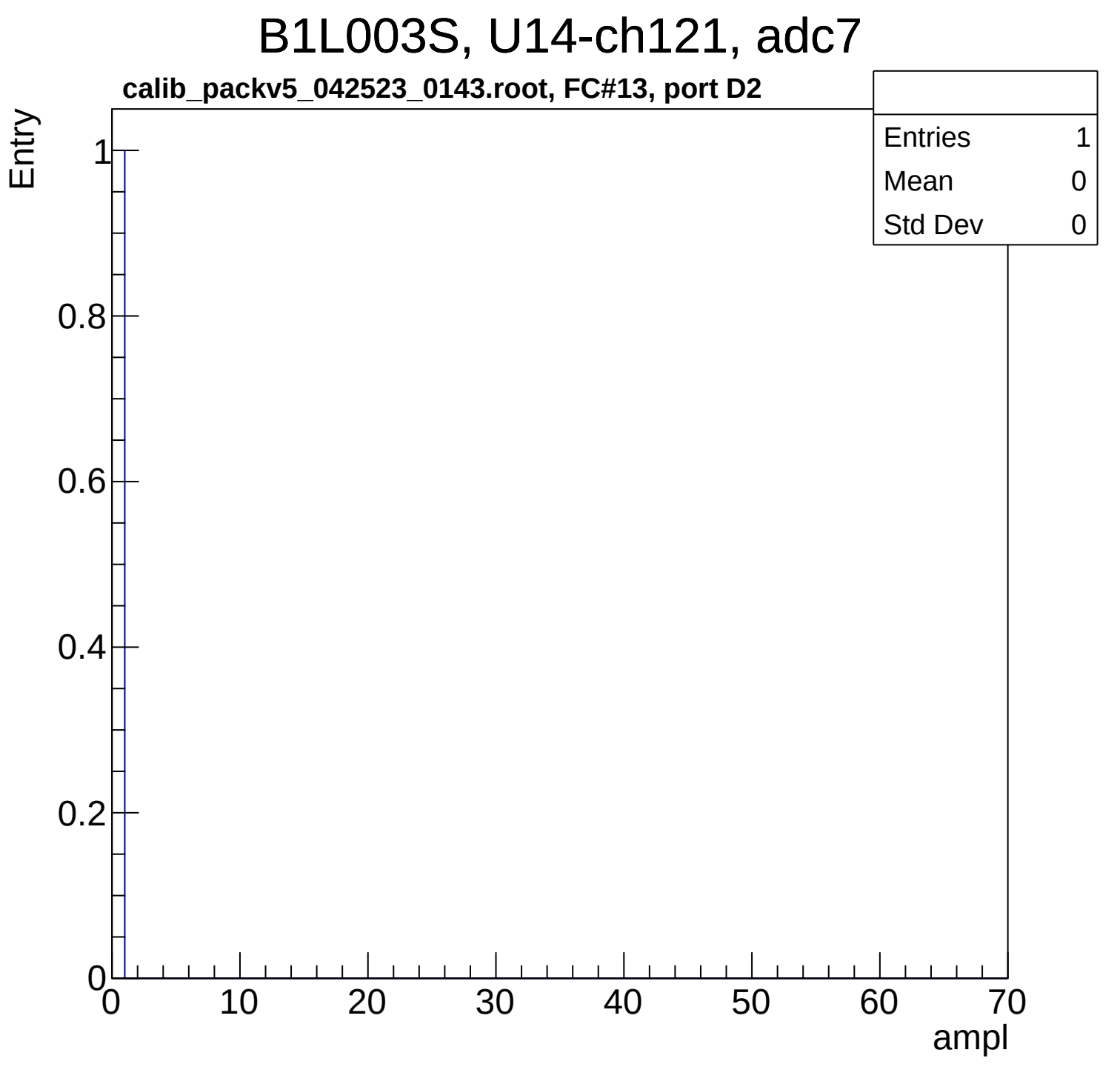

B1L003S, U14-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.28
Std Dev 3.45
Gaus mean : 44.0047
Gaus Width: 3.7527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

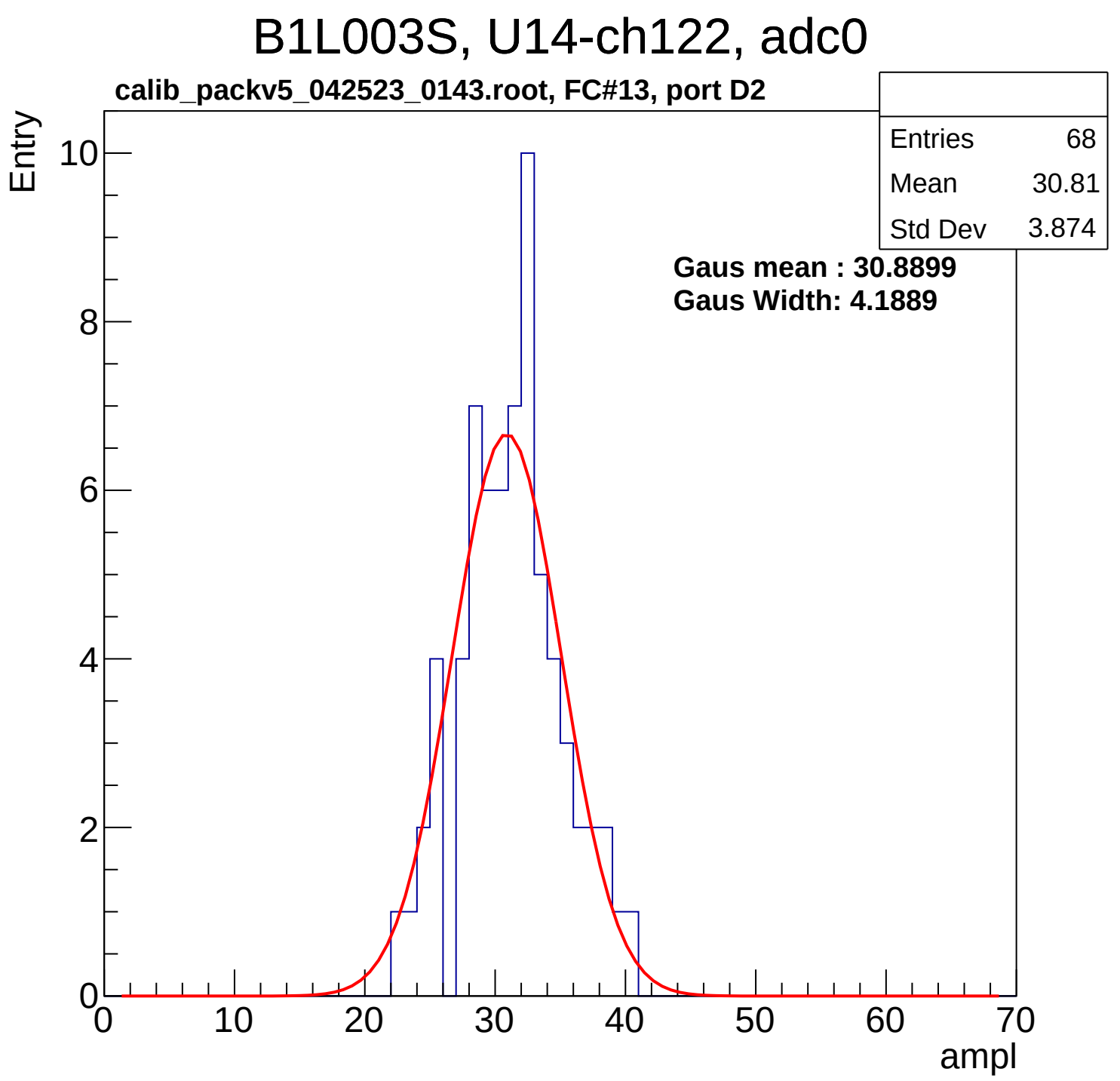

B1L003S, U14-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 50.51
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

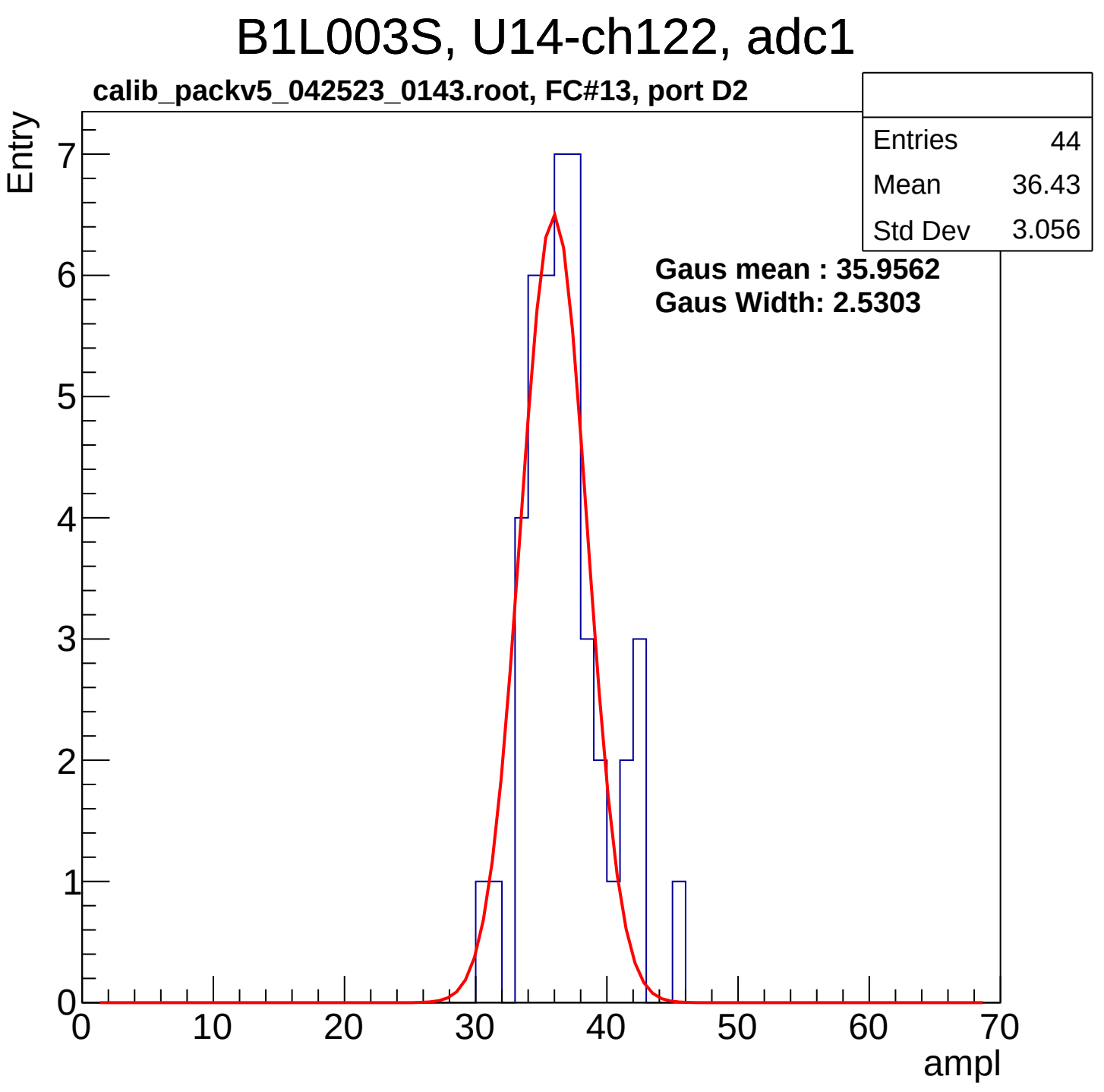

B1L003S, U14-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.35
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

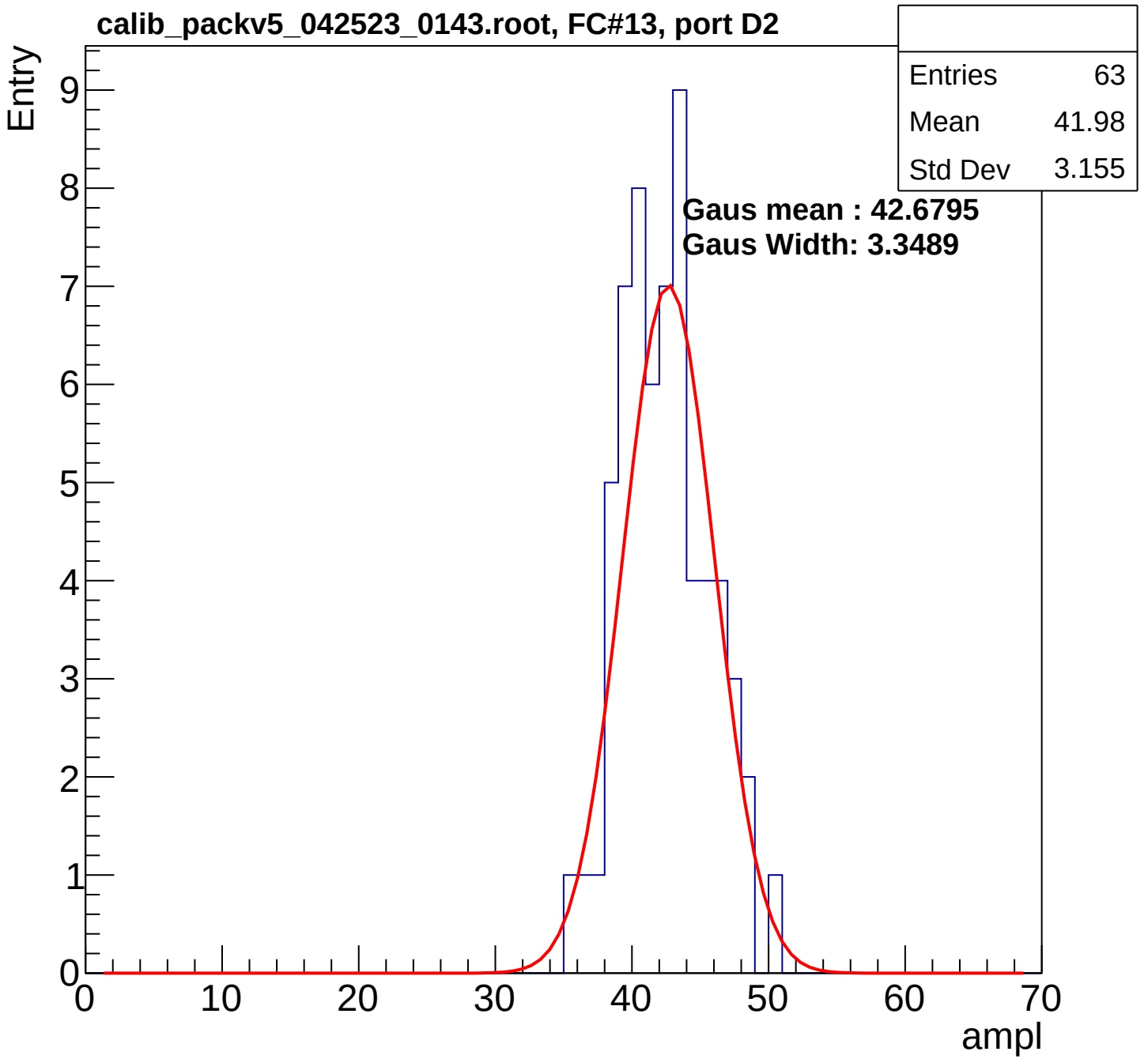

B1L003S, U14-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 60.12
Std Dev 2.048
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

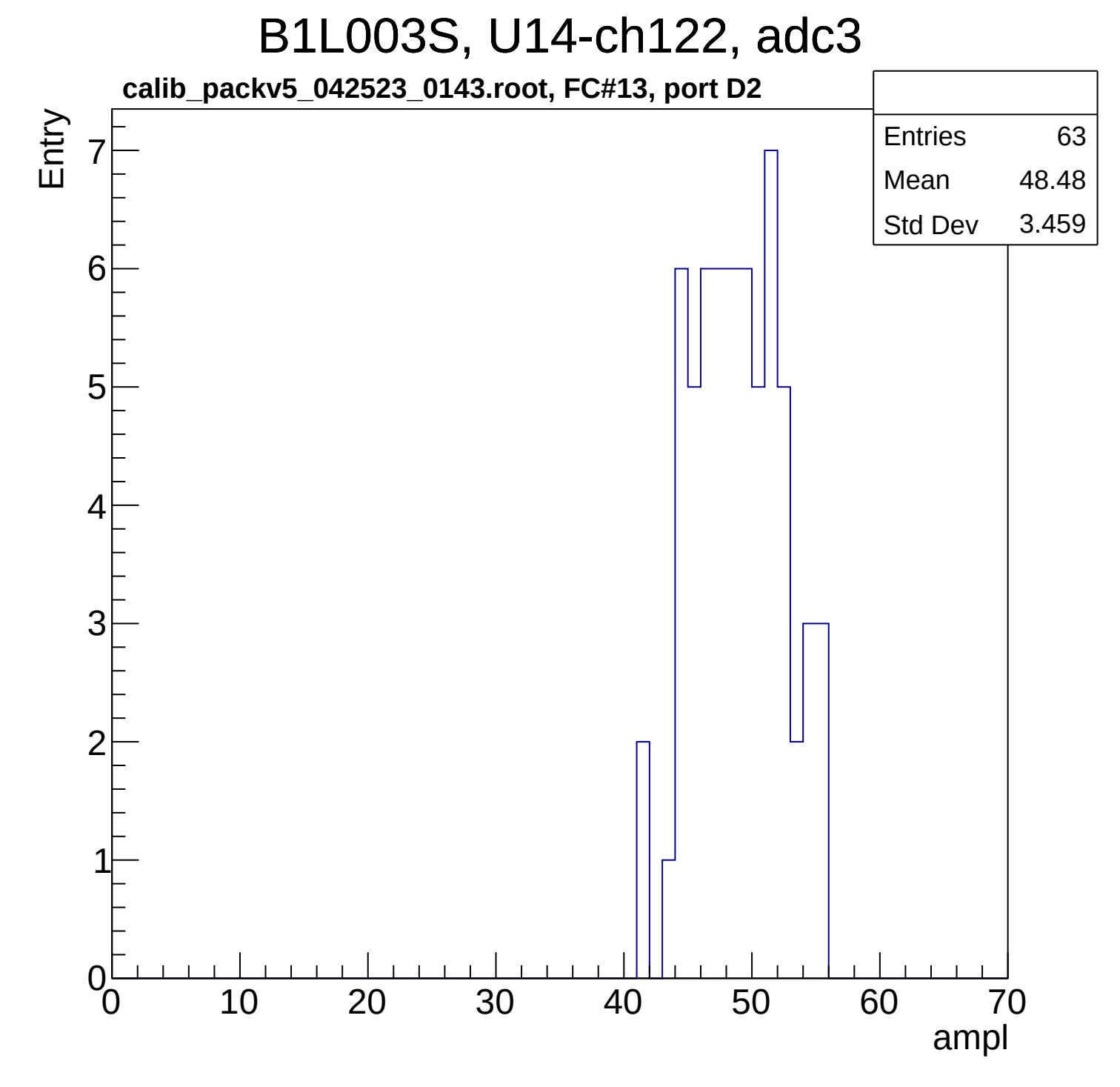

B1L003S, U14-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52.33
Std Dev 23.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

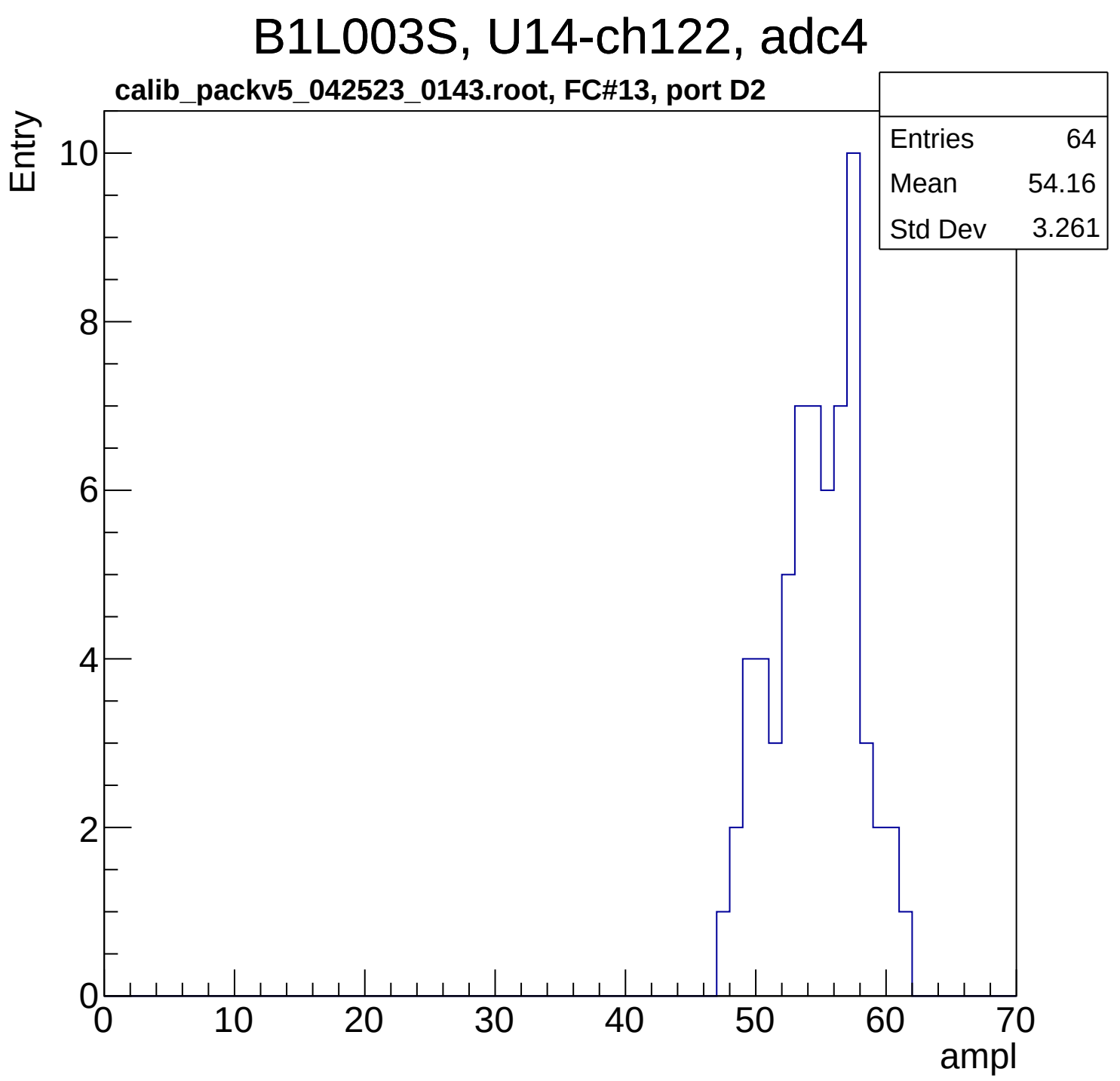

B1L003S, U14-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

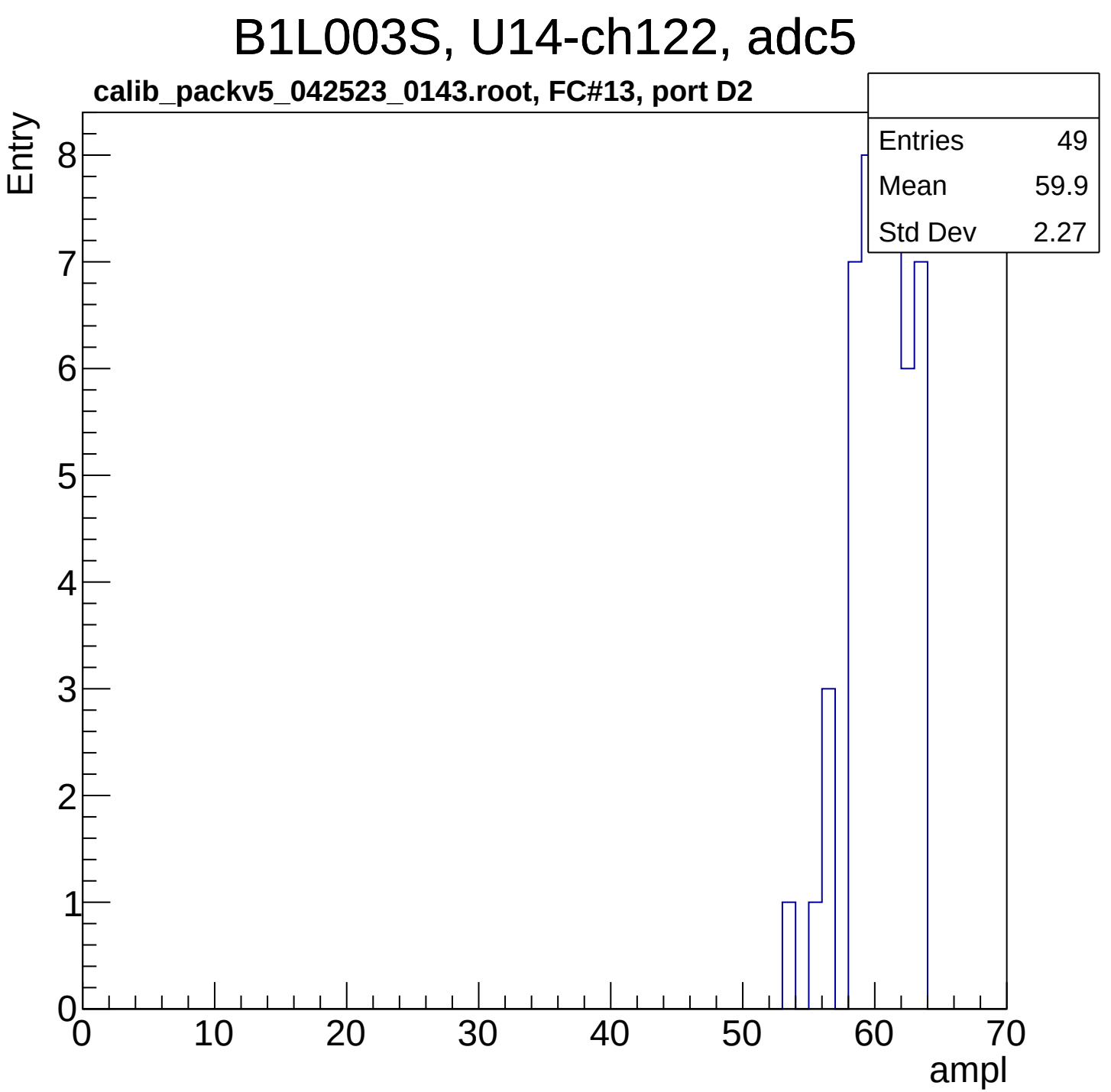

B1L003S, U14-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.81
Std Dev 3.874
Gaus mean : 30.8899
Gaus Width: 4.1889
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

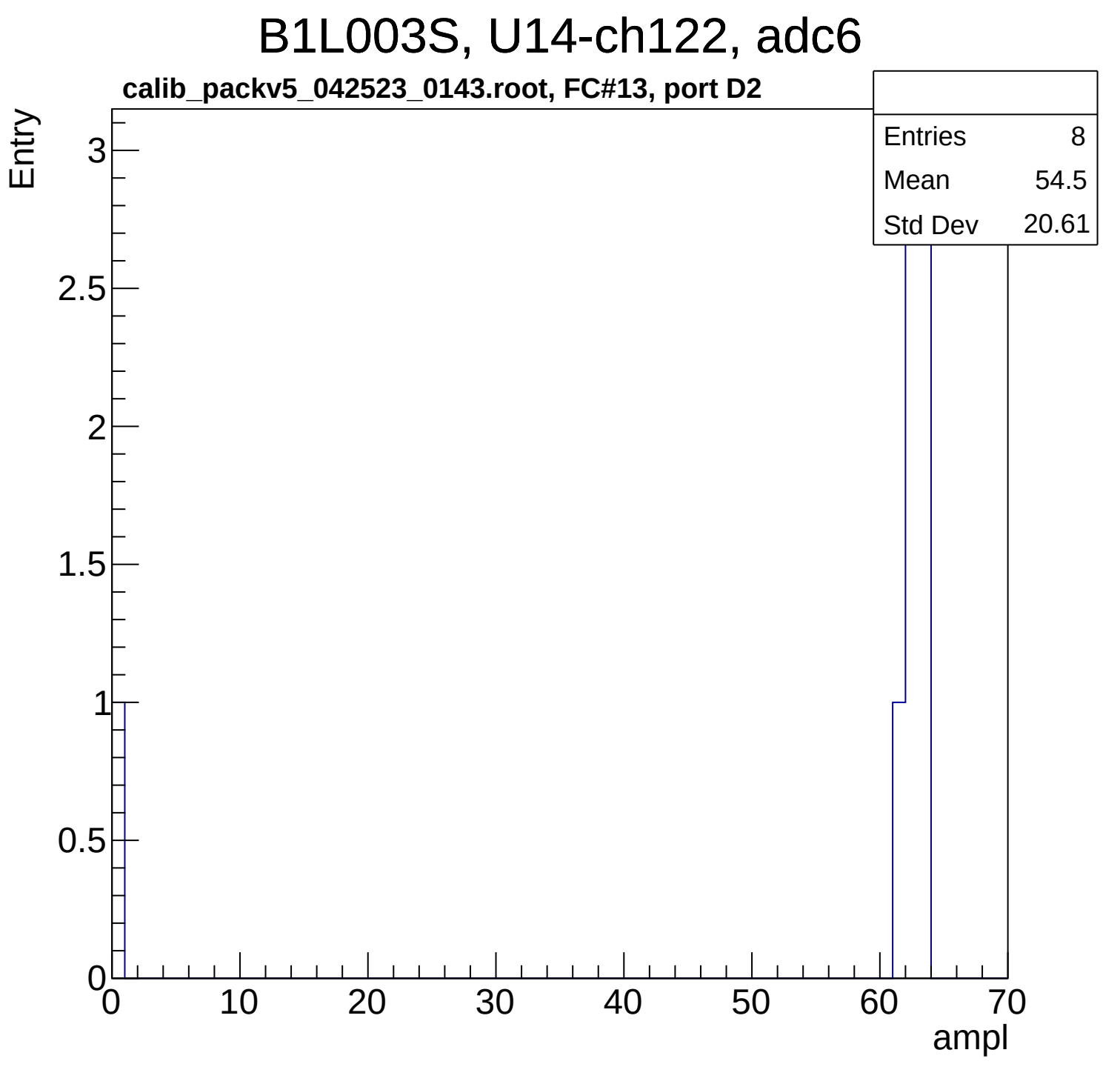

B1L003S, U14-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 36.43
Std Dev 3.056
Gaus mean : 35.9562
Gaus Width: 2.5303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

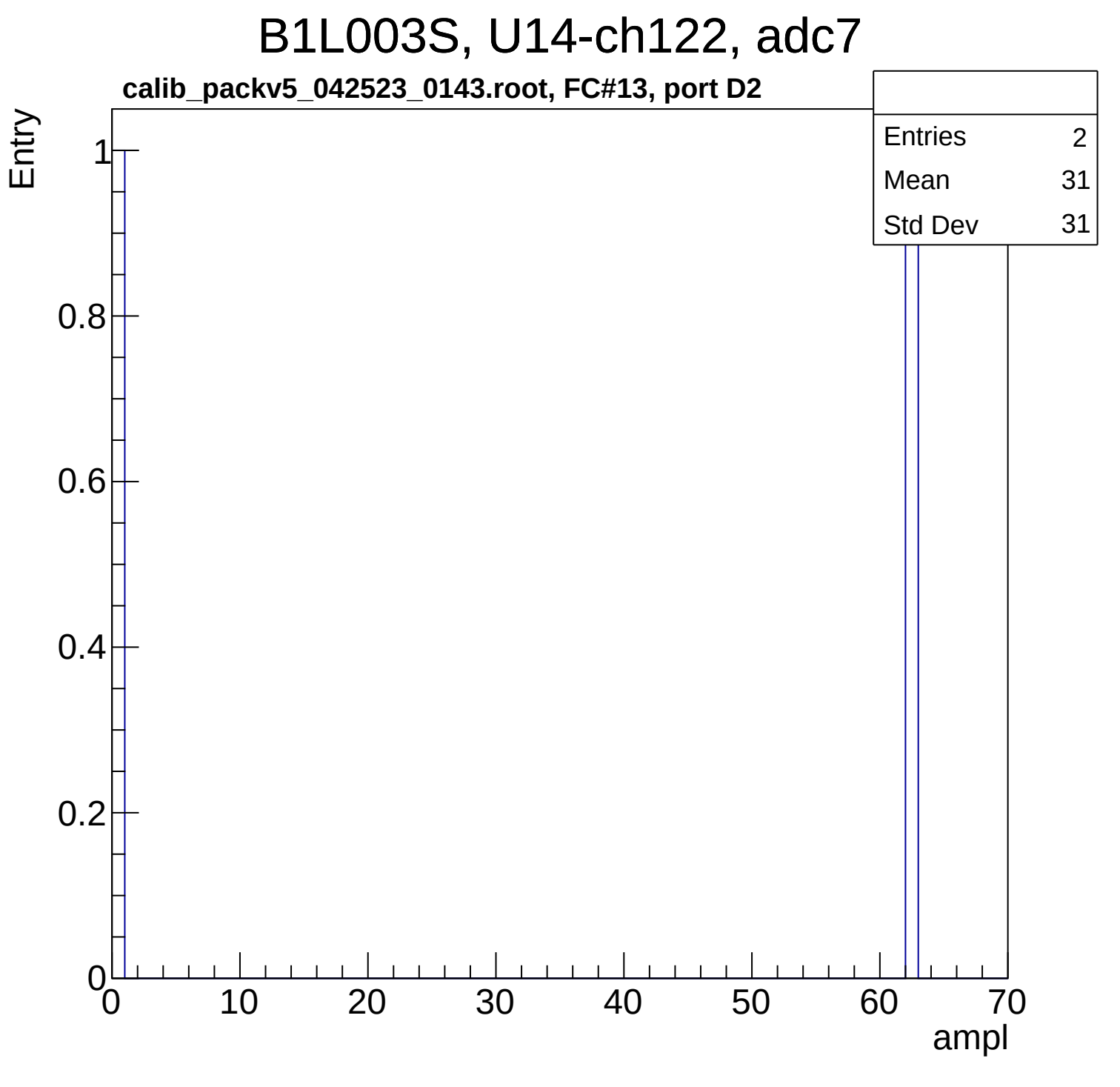

B1L003S, U14-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.98
Std Dev 3.155
Gaus mean : 42.6795
Gaus Width: 3.3489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

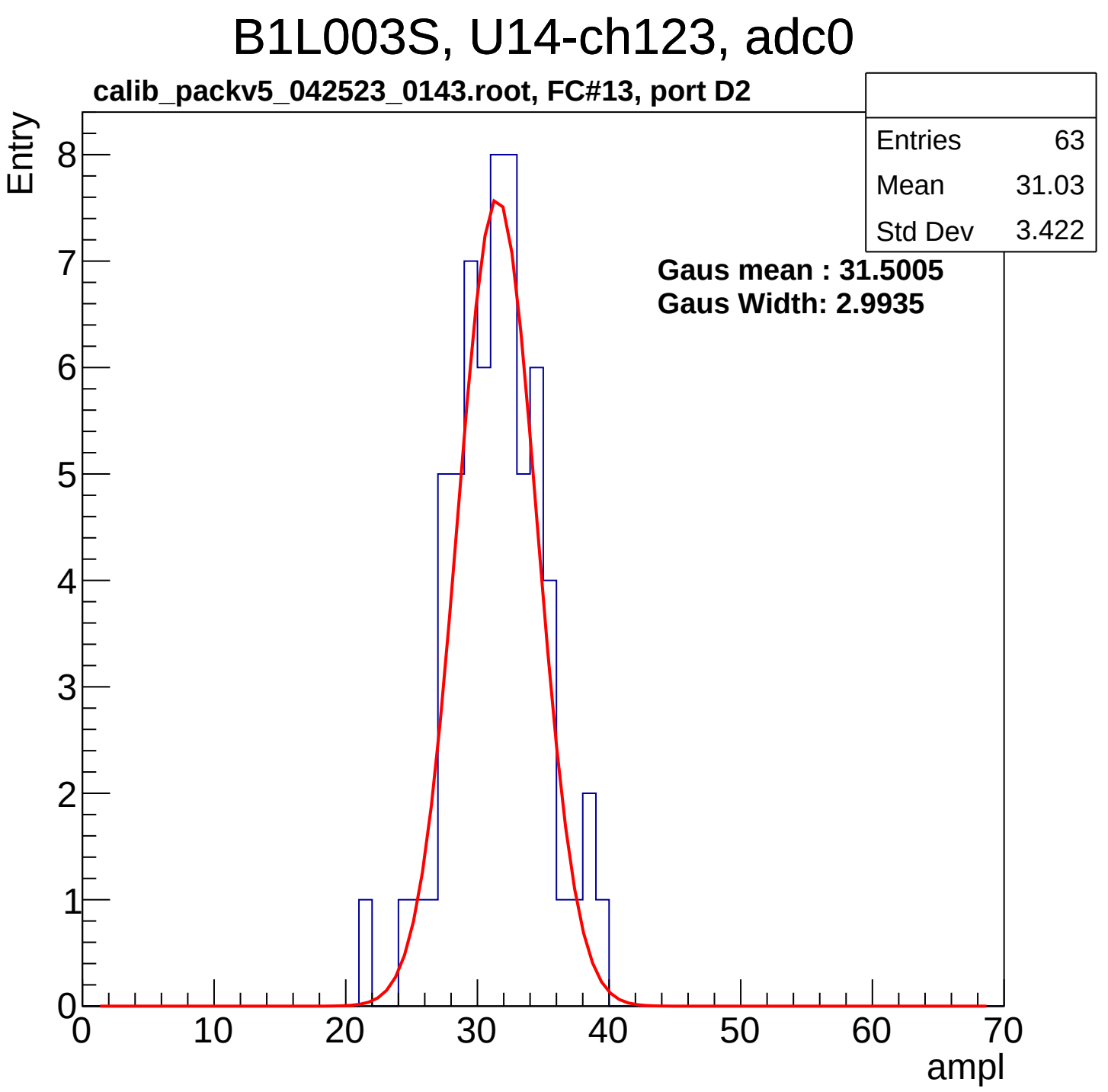

B1L003S, U14-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.48
Std Dev 3.459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

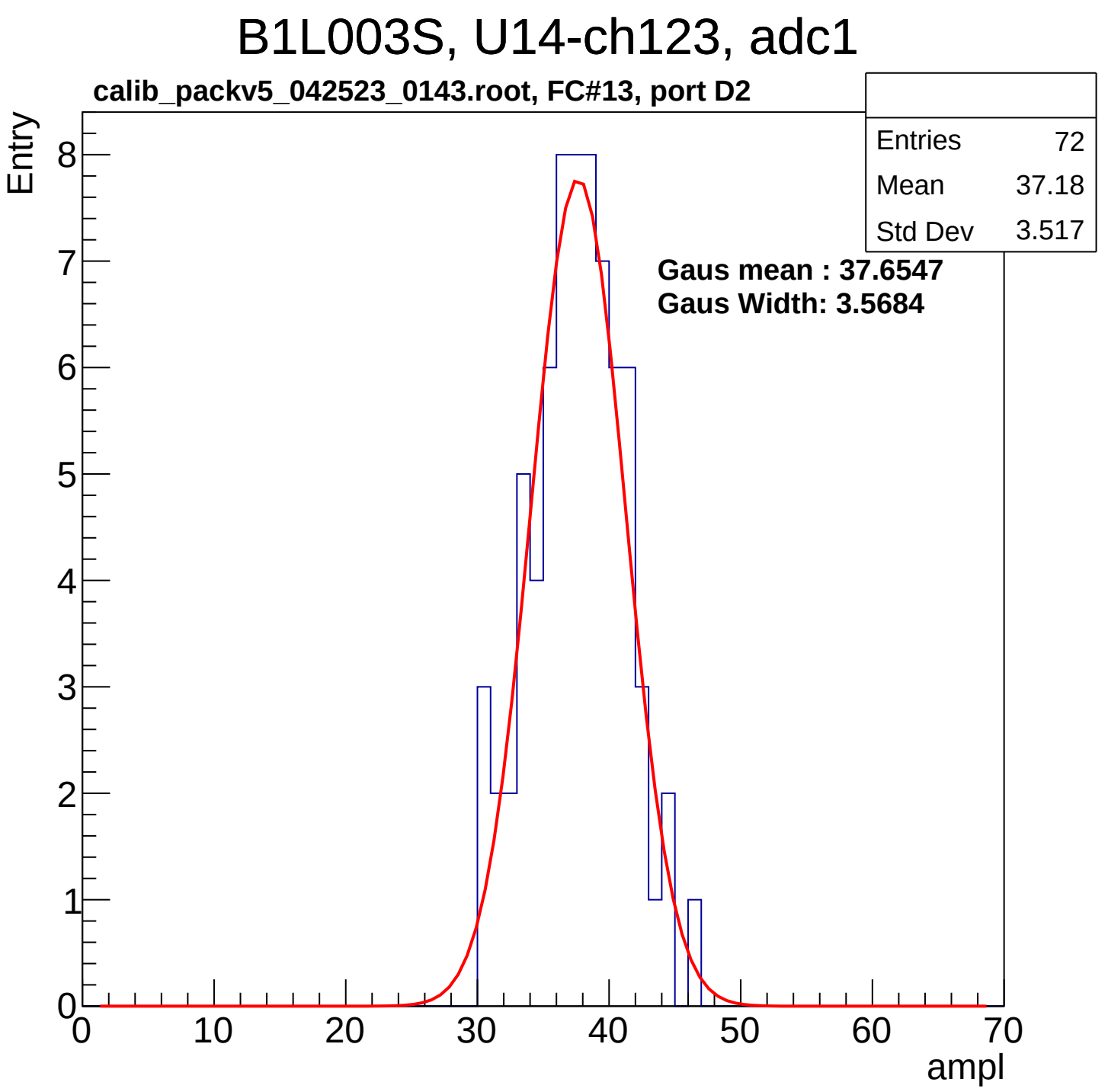

B1L003S, U14-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.16
Std Dev 3.261
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

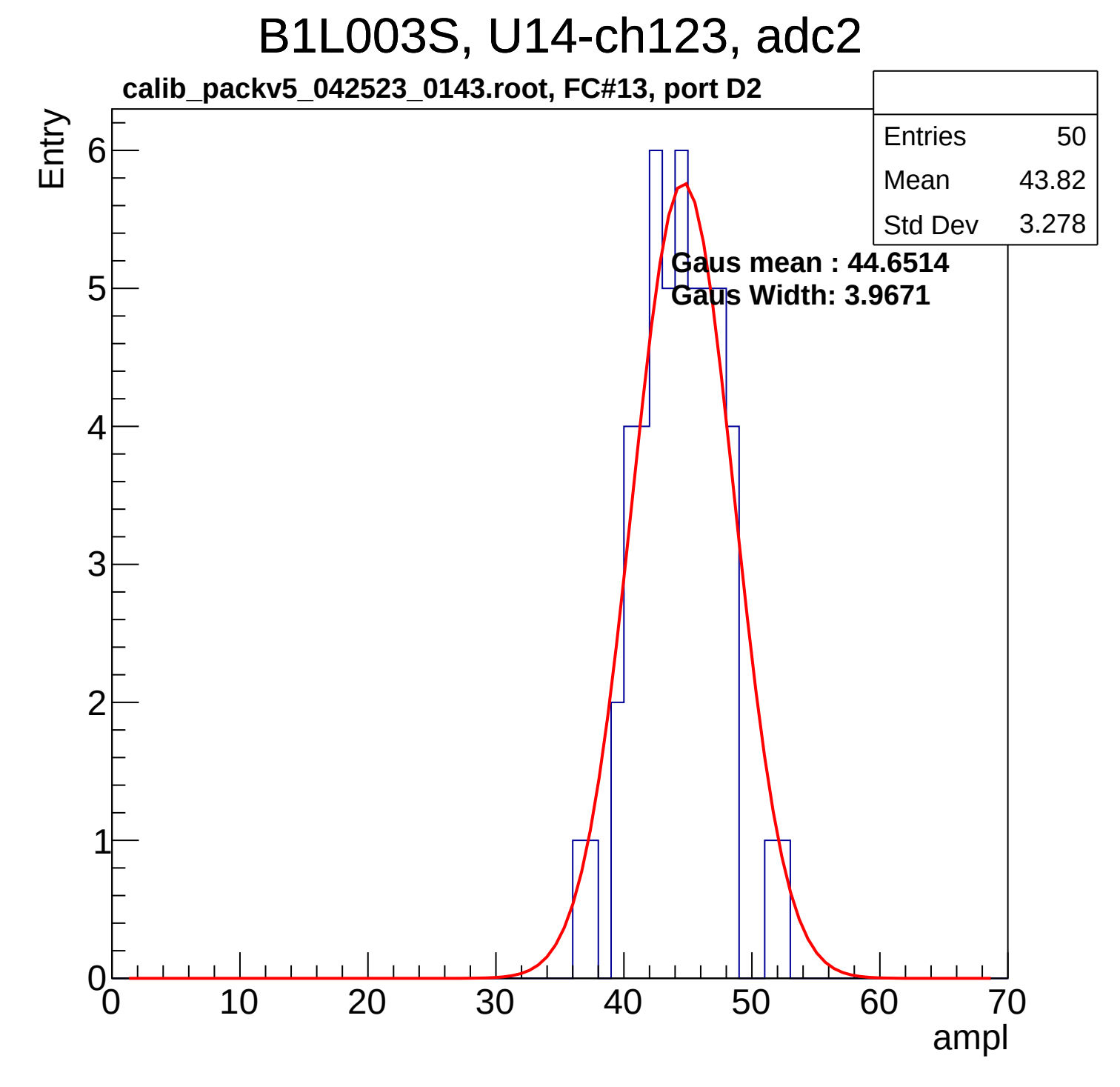

B1L003S, U14-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.9
Std Dev 2.27
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

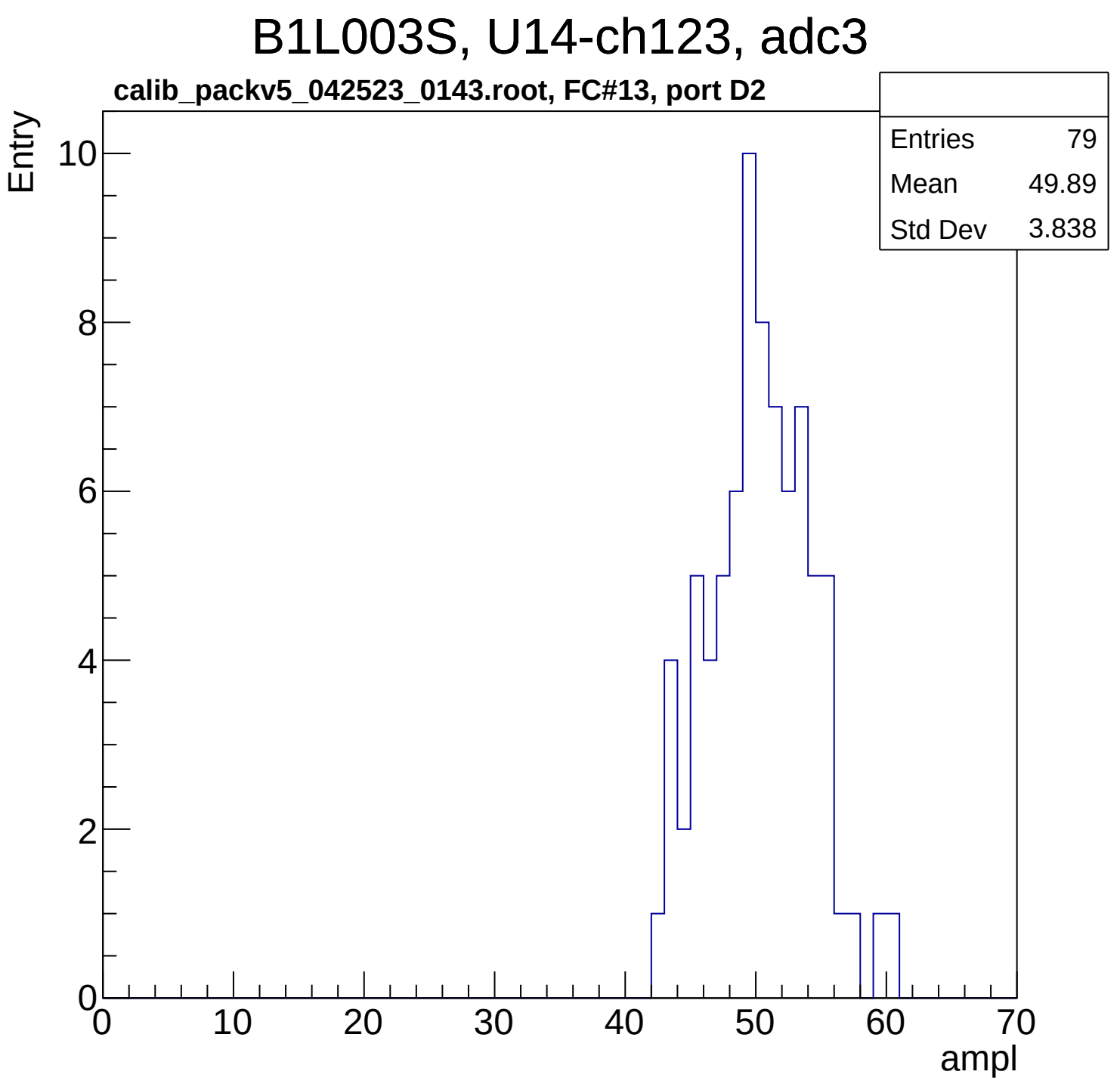

B1L003S, U14-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.5
Std Dev 20.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

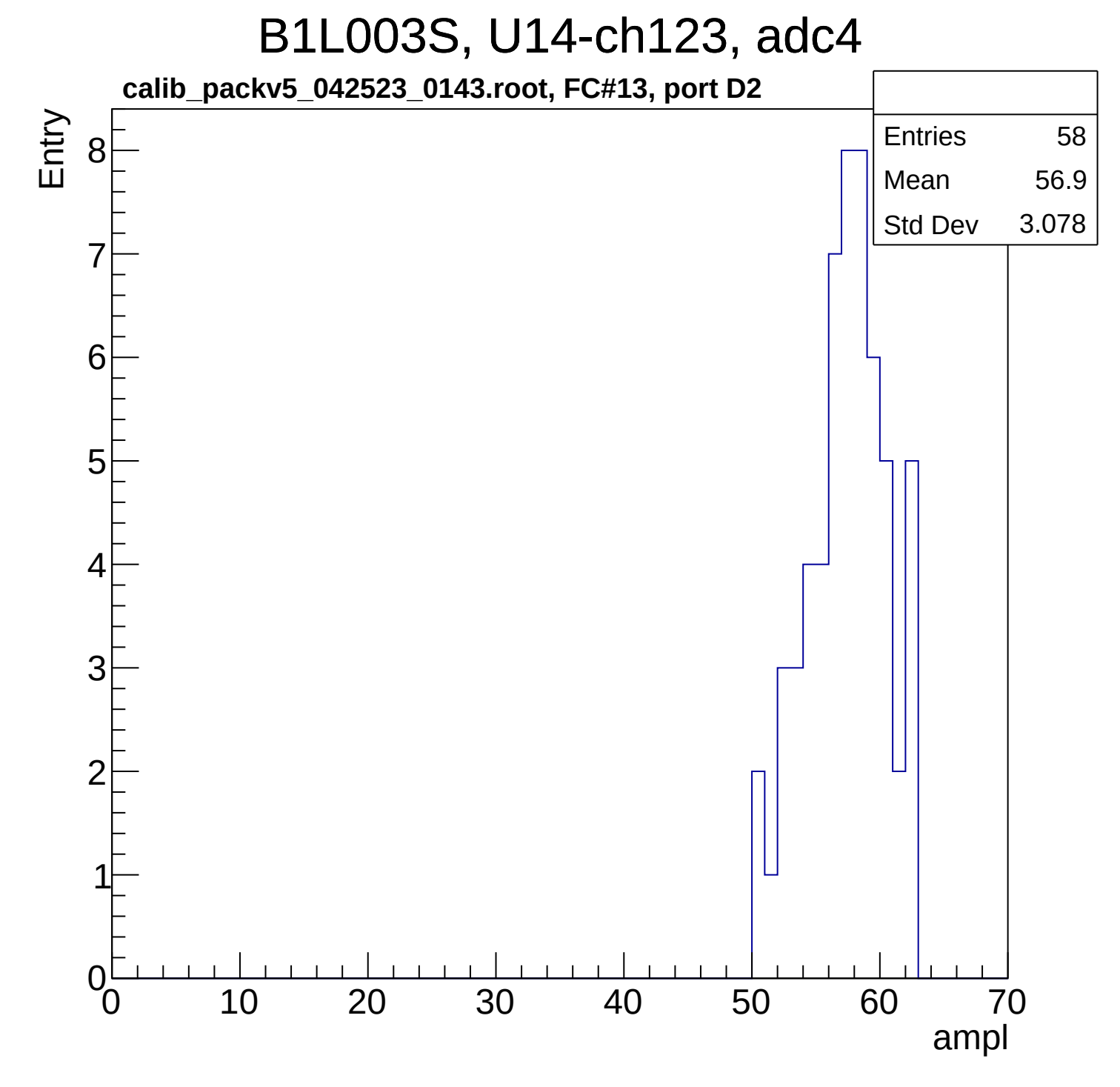

B1L003S, U14-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

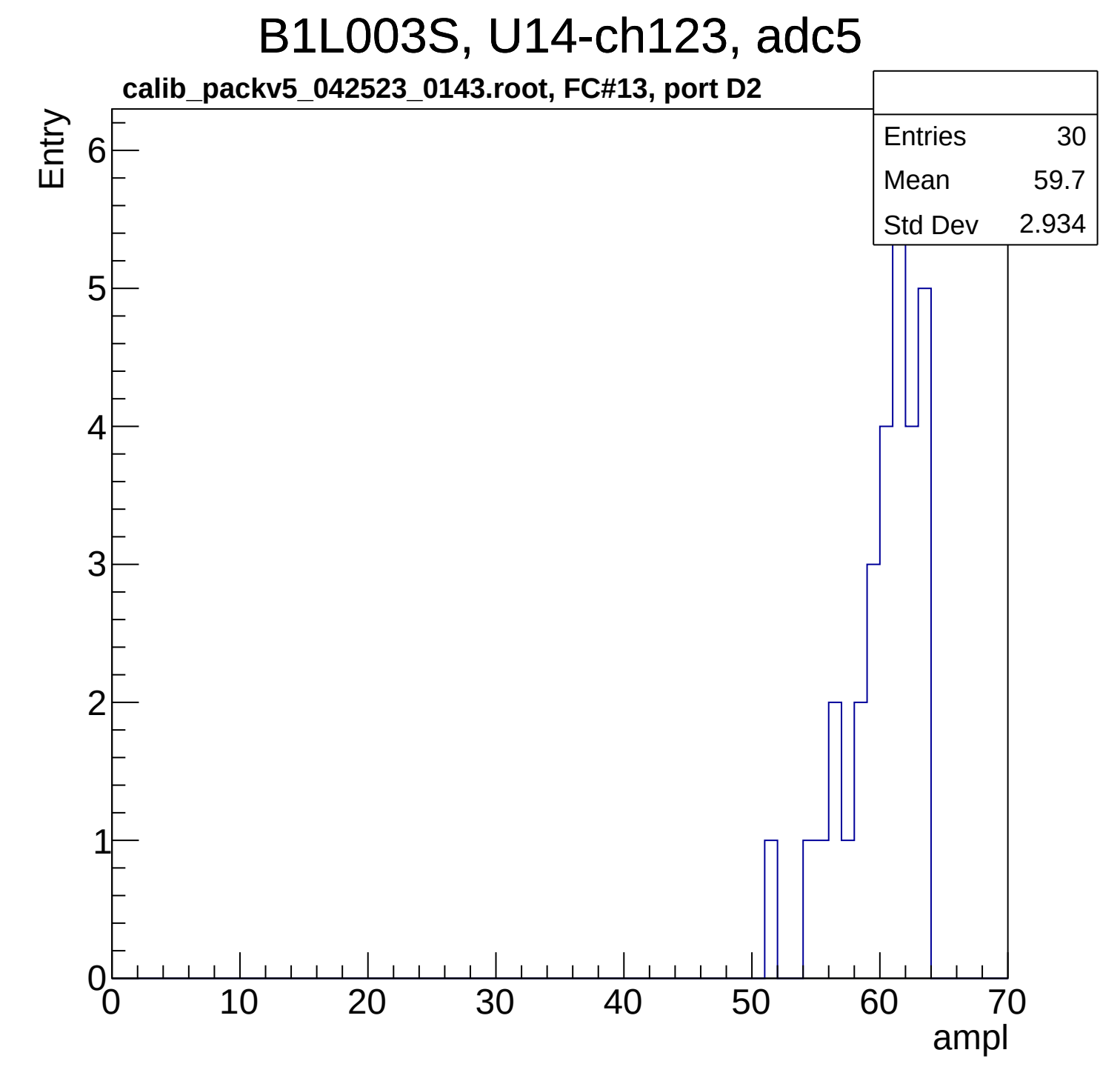

B1L003S, U14-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 31.03
Std Dev 3.422
Gaus mean : 31.5005
Gaus Width: 2.9935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

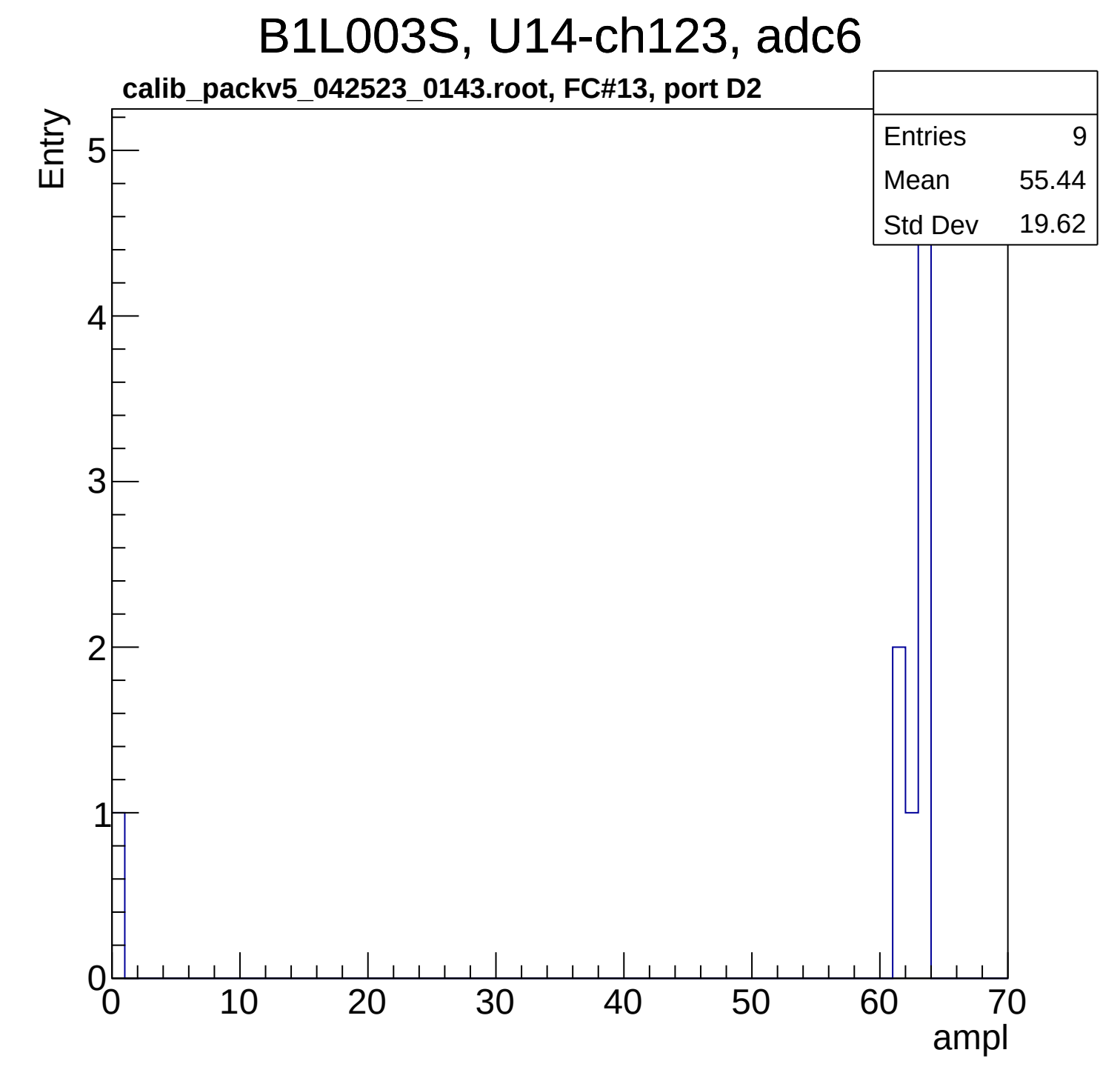

B1L003S, U14-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.18
Std Dev 3.517
Gaus mean : 37.6547
Gaus Width: 3.5684
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

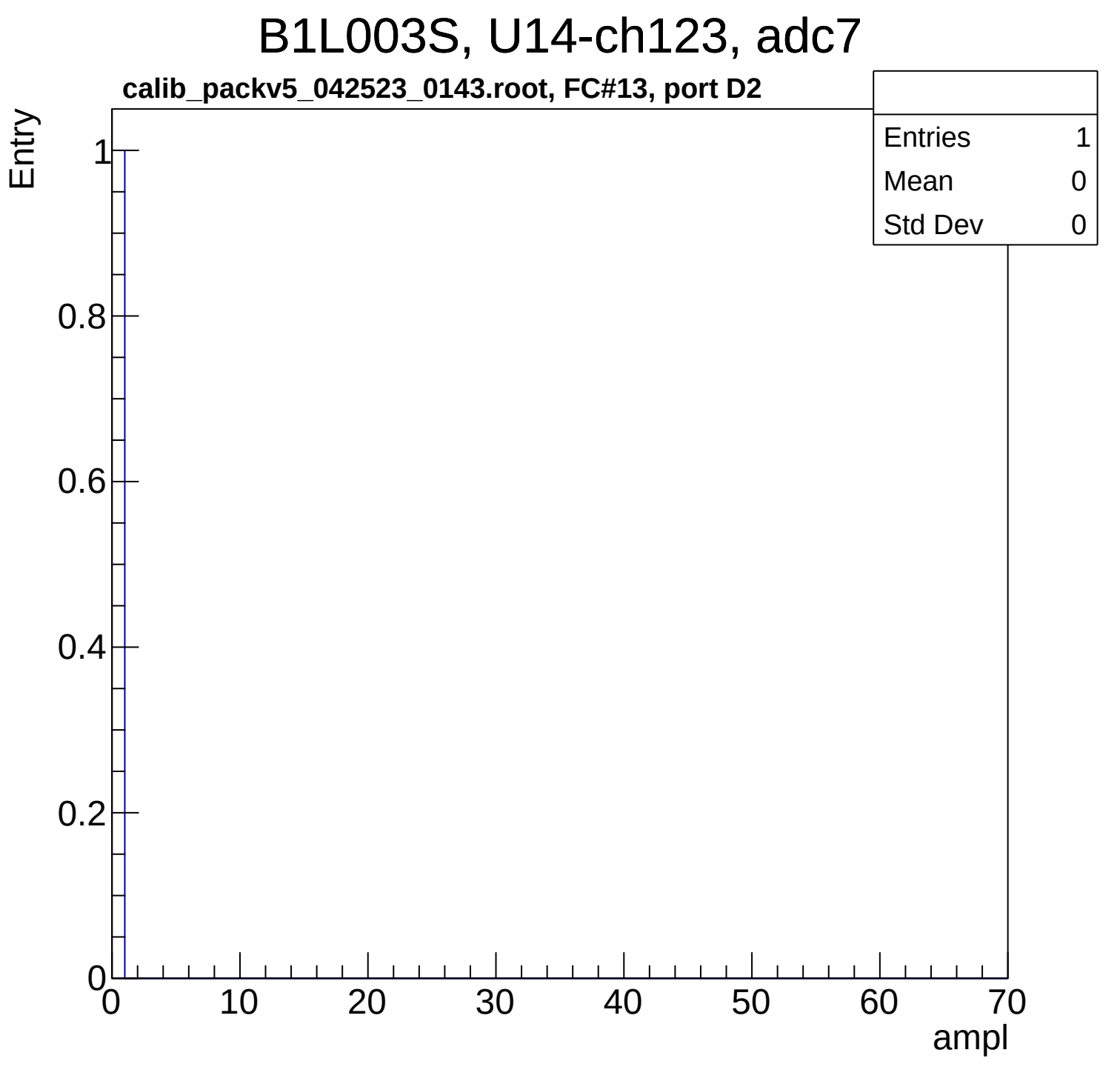

B1L003S, U14-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 43.82
Std Dev 3.278
Gaus mean : 44.6514
Gaus Width: 3.9671
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

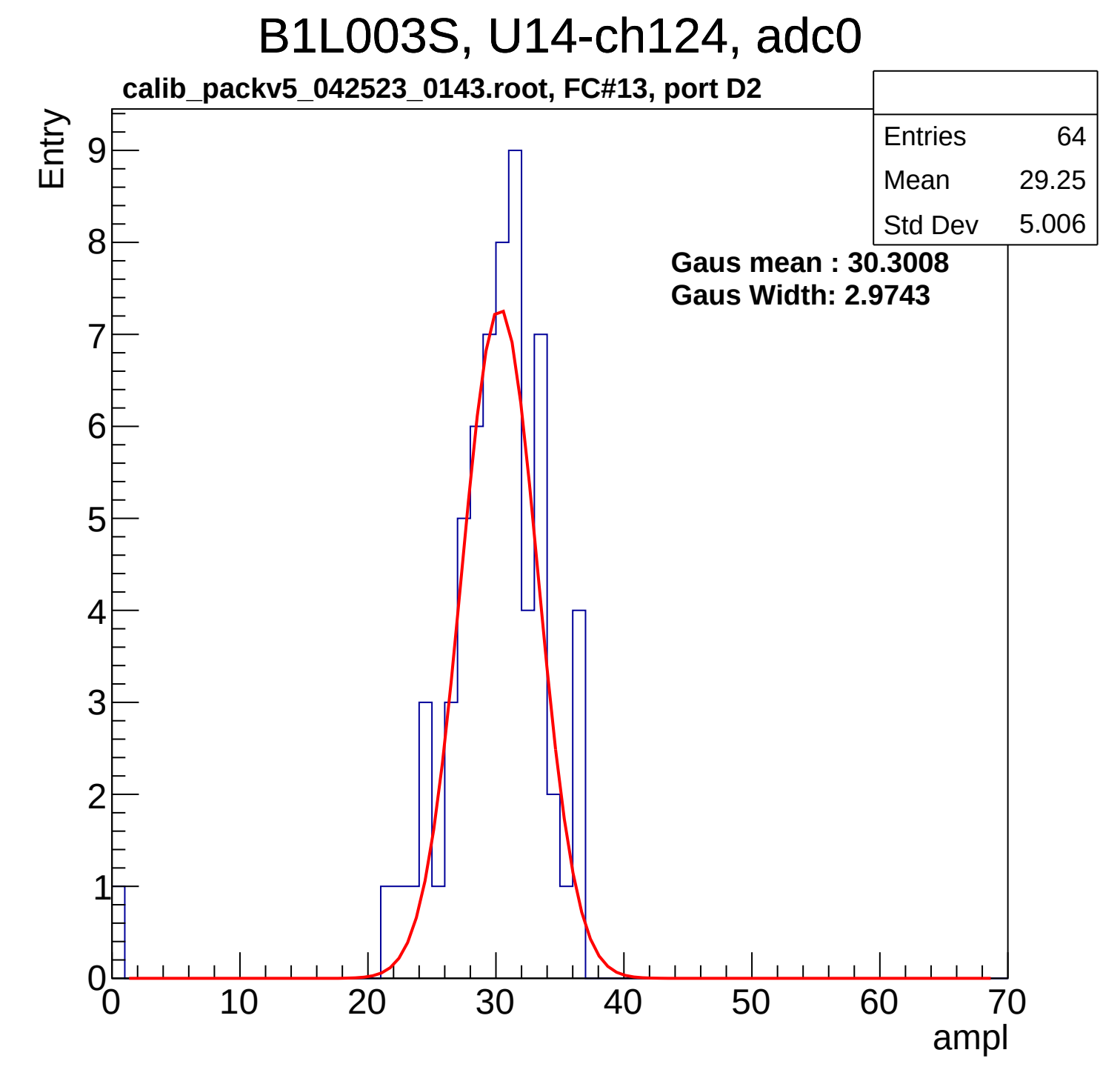

B1L003S, U14-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.89
Std Dev 3.838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

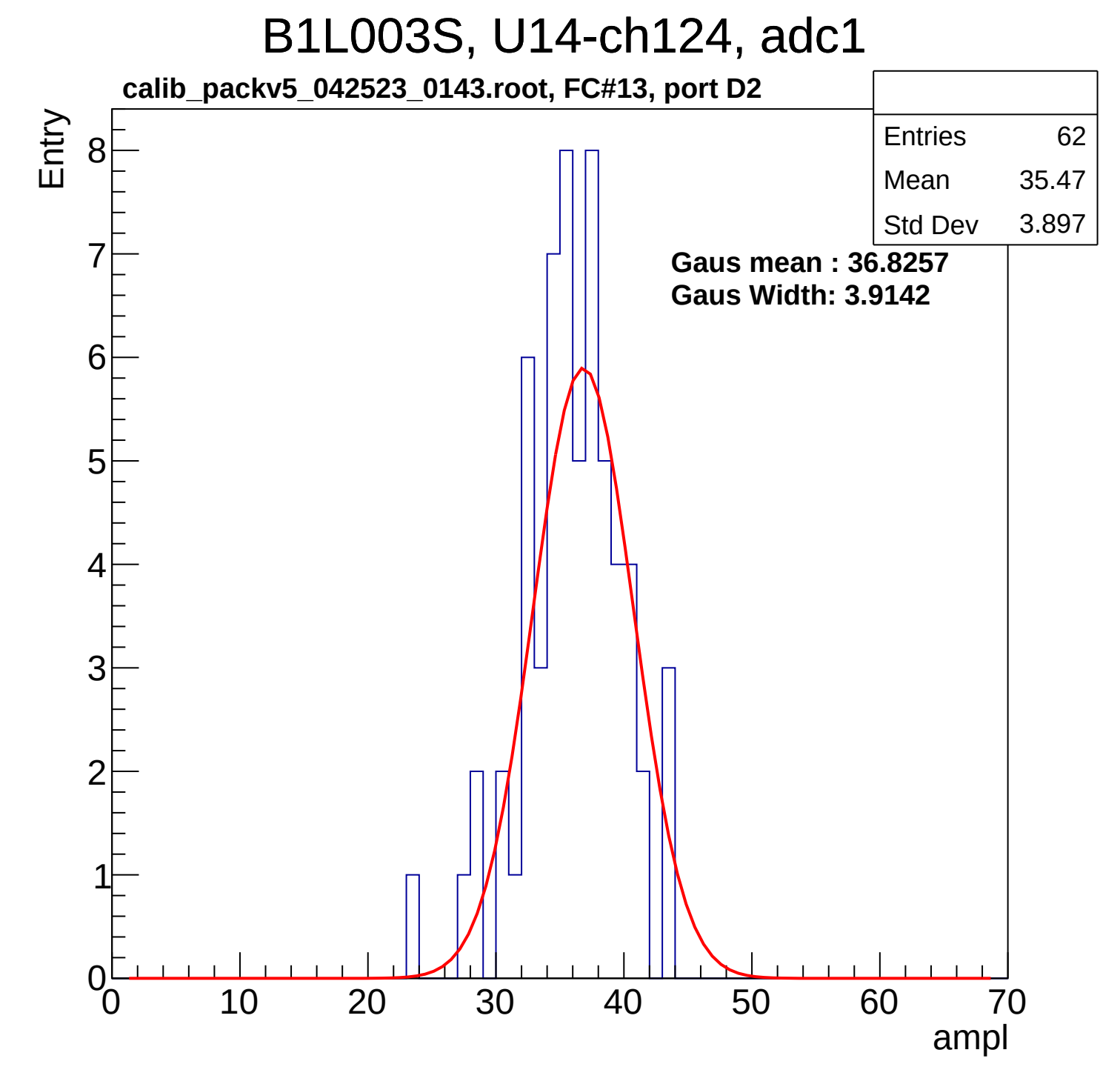

B1L003S, U14-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 56.9
Std Dev 3.078
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

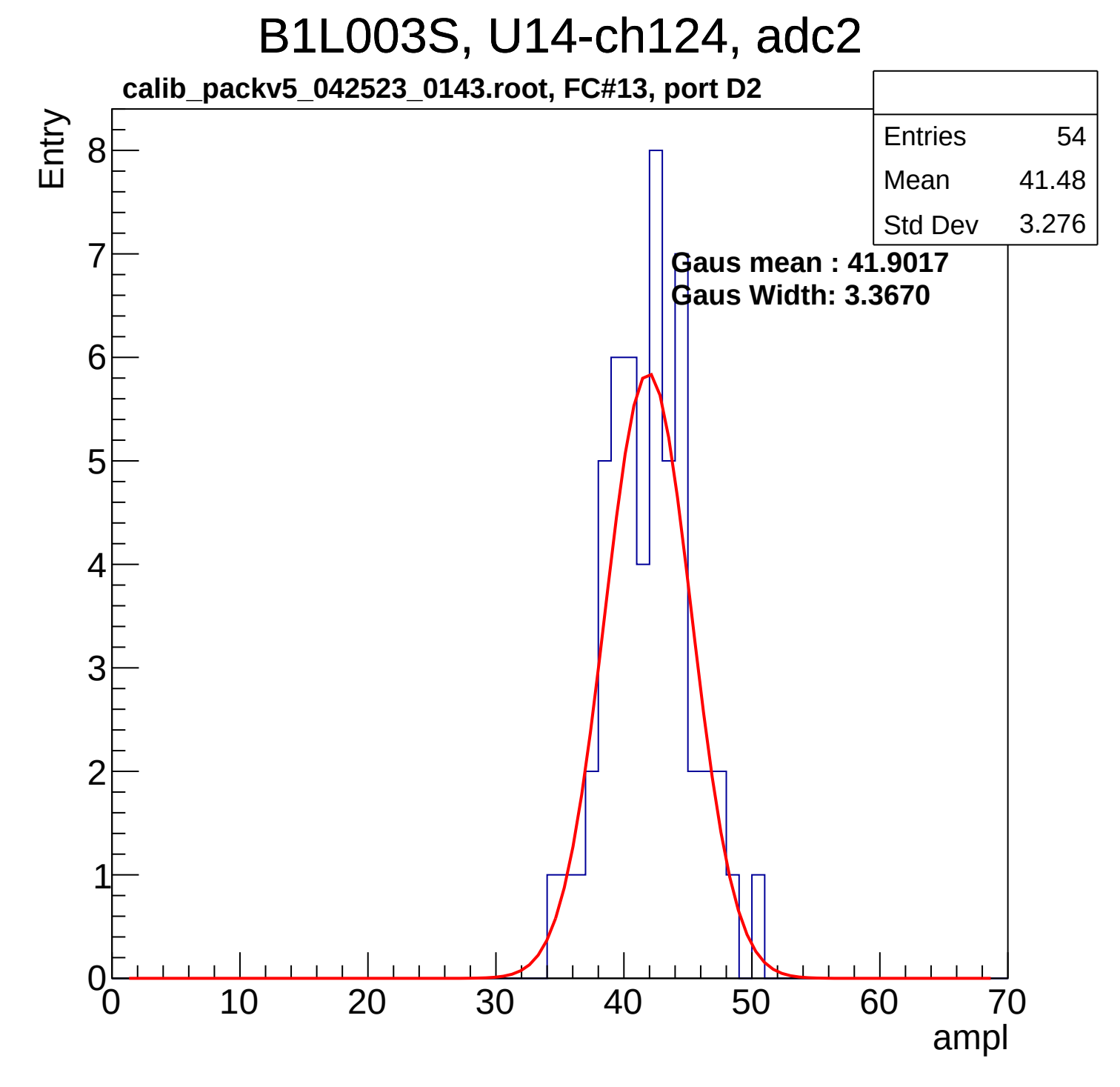

B1L003S, U14-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 59.7
Std Dev 2.934
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

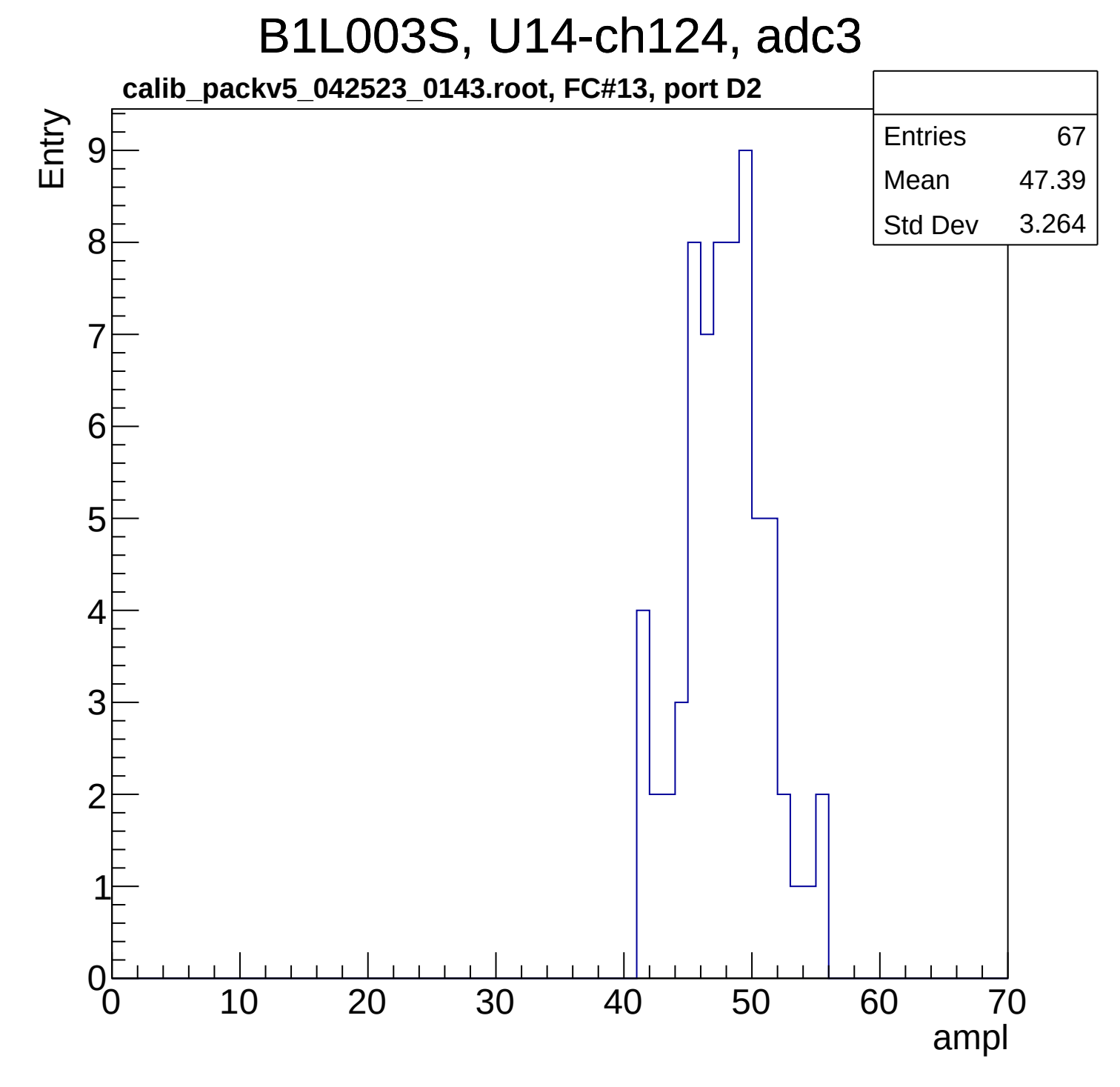

B1L003S, U14-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.44
Std Dev 19.62
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

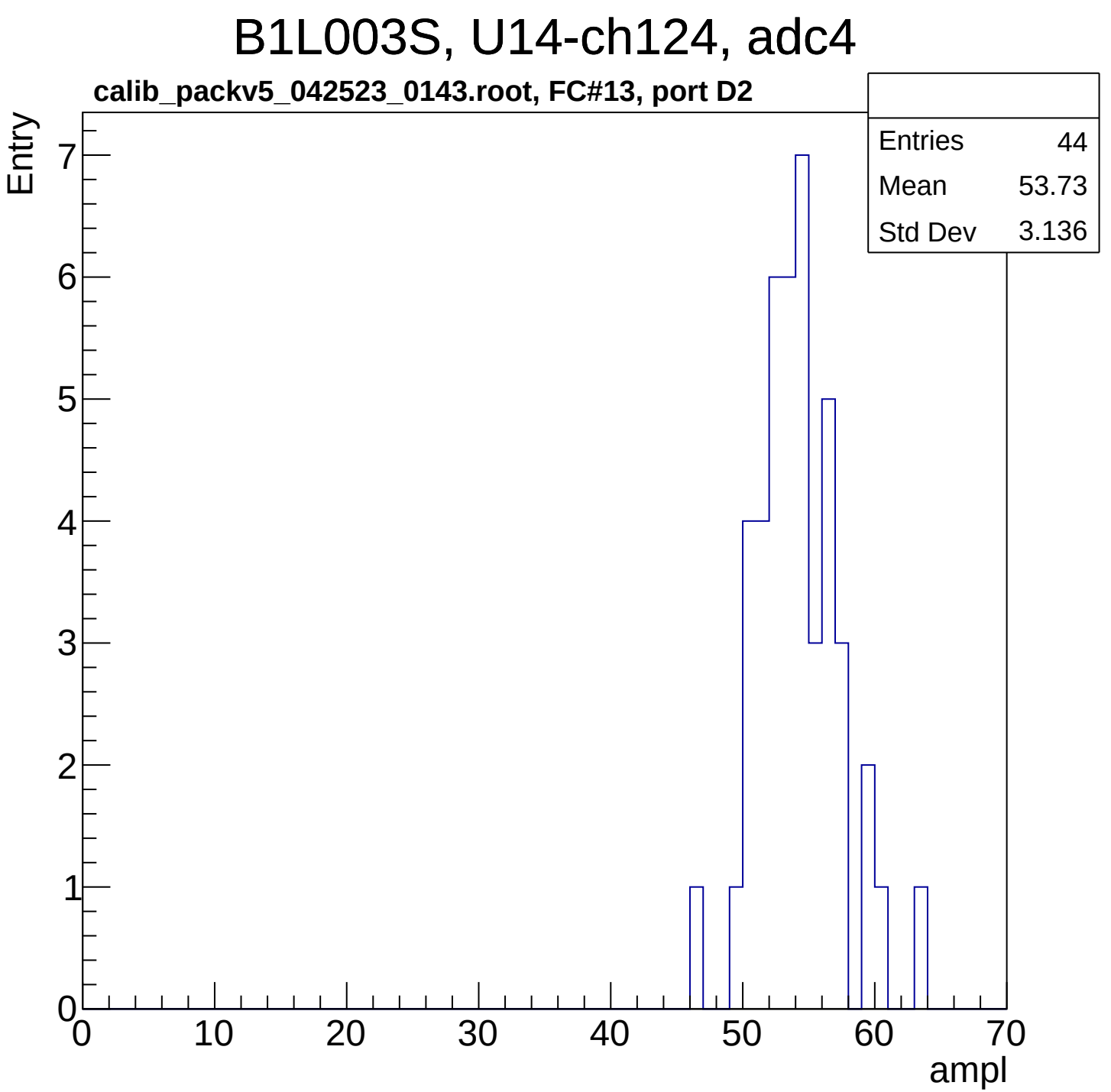

B1L003S, U14-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

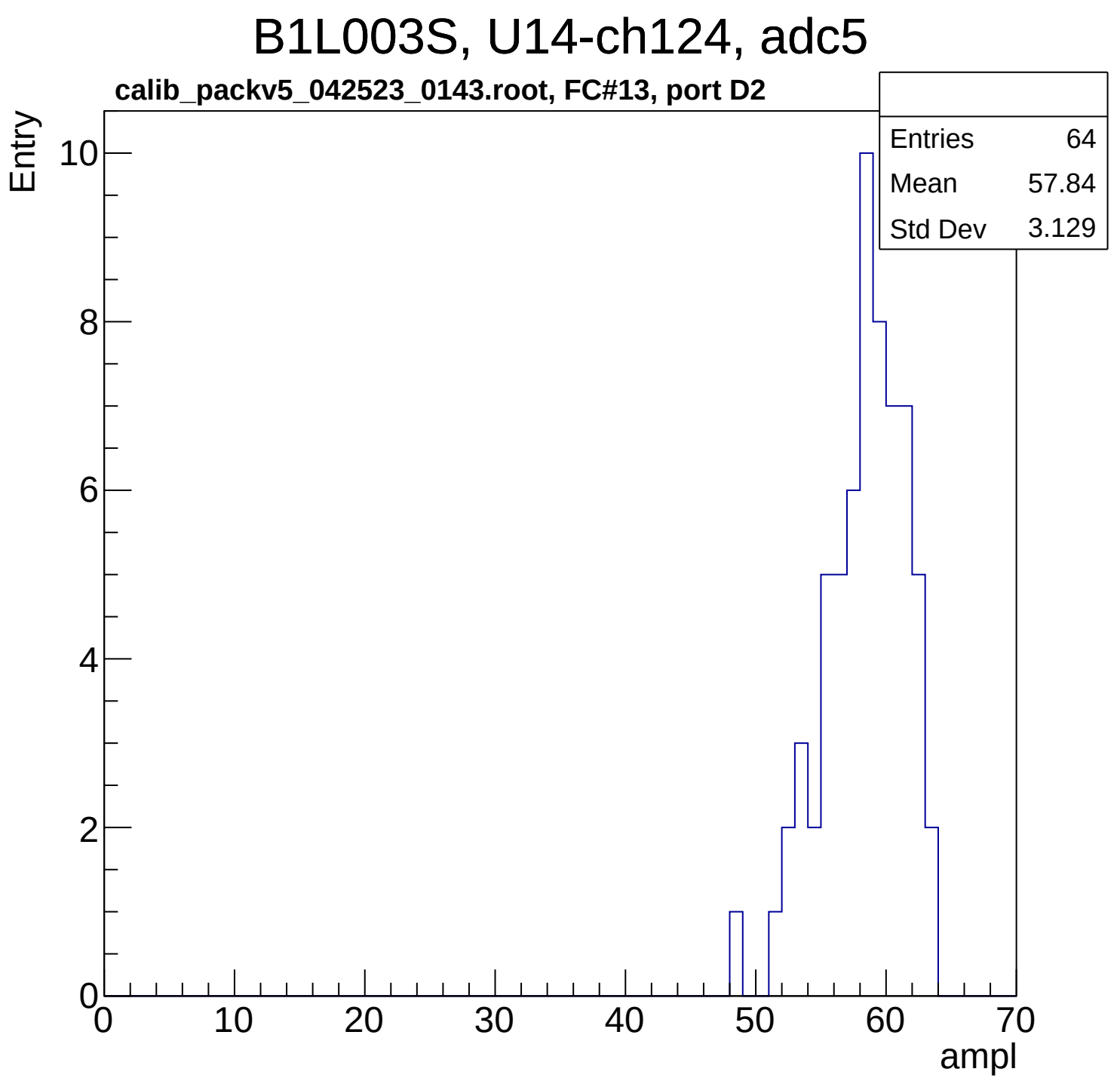

B1L003S, U14-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.25
Std Dev 5.006
Gaus mean : 30.3008
Gaus Width: 2.9743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

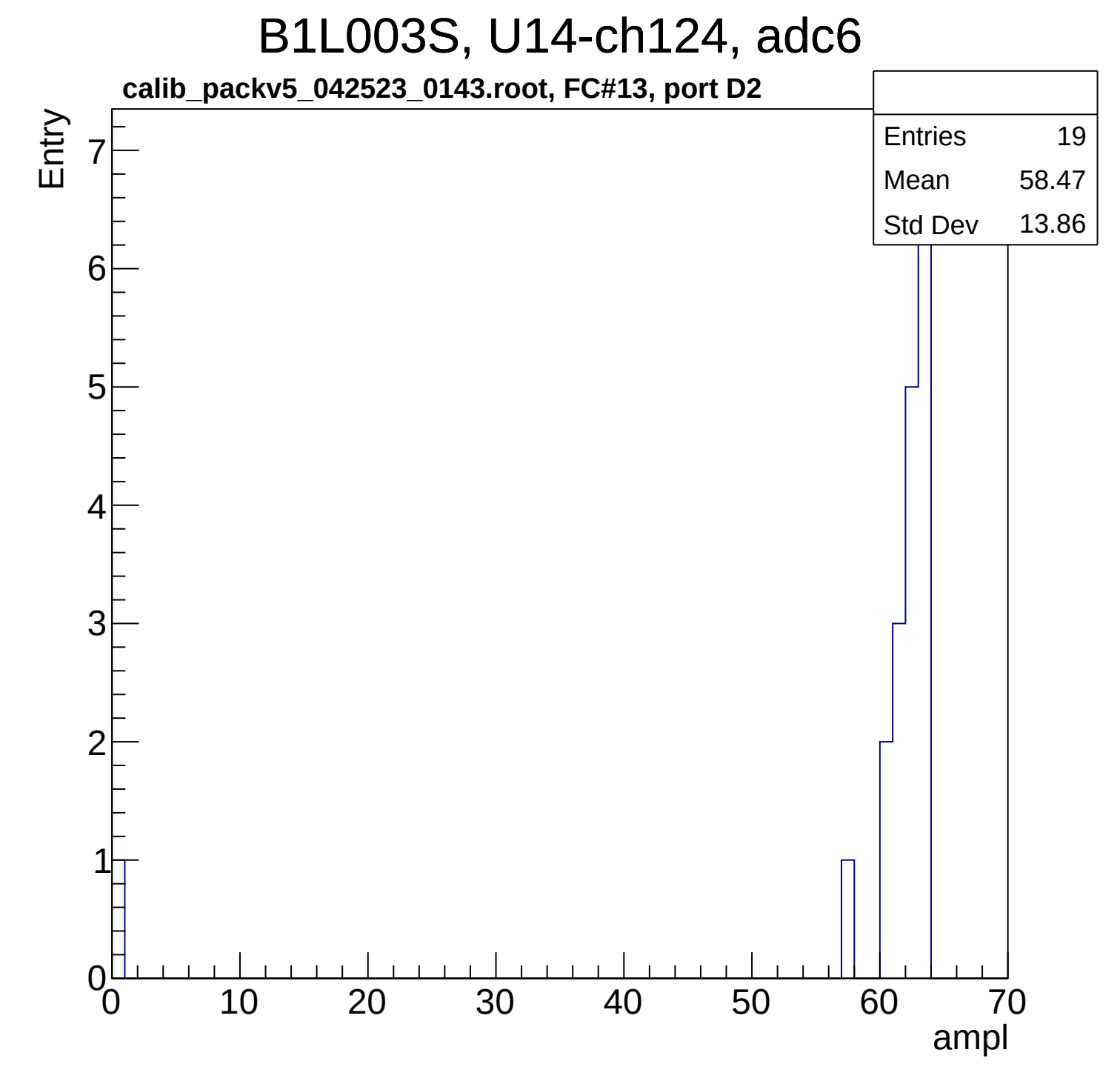

B1L003S, U14-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.47
Std Dev 3.897
Gaus mean : 36.8257
Gaus Width: 3.9142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

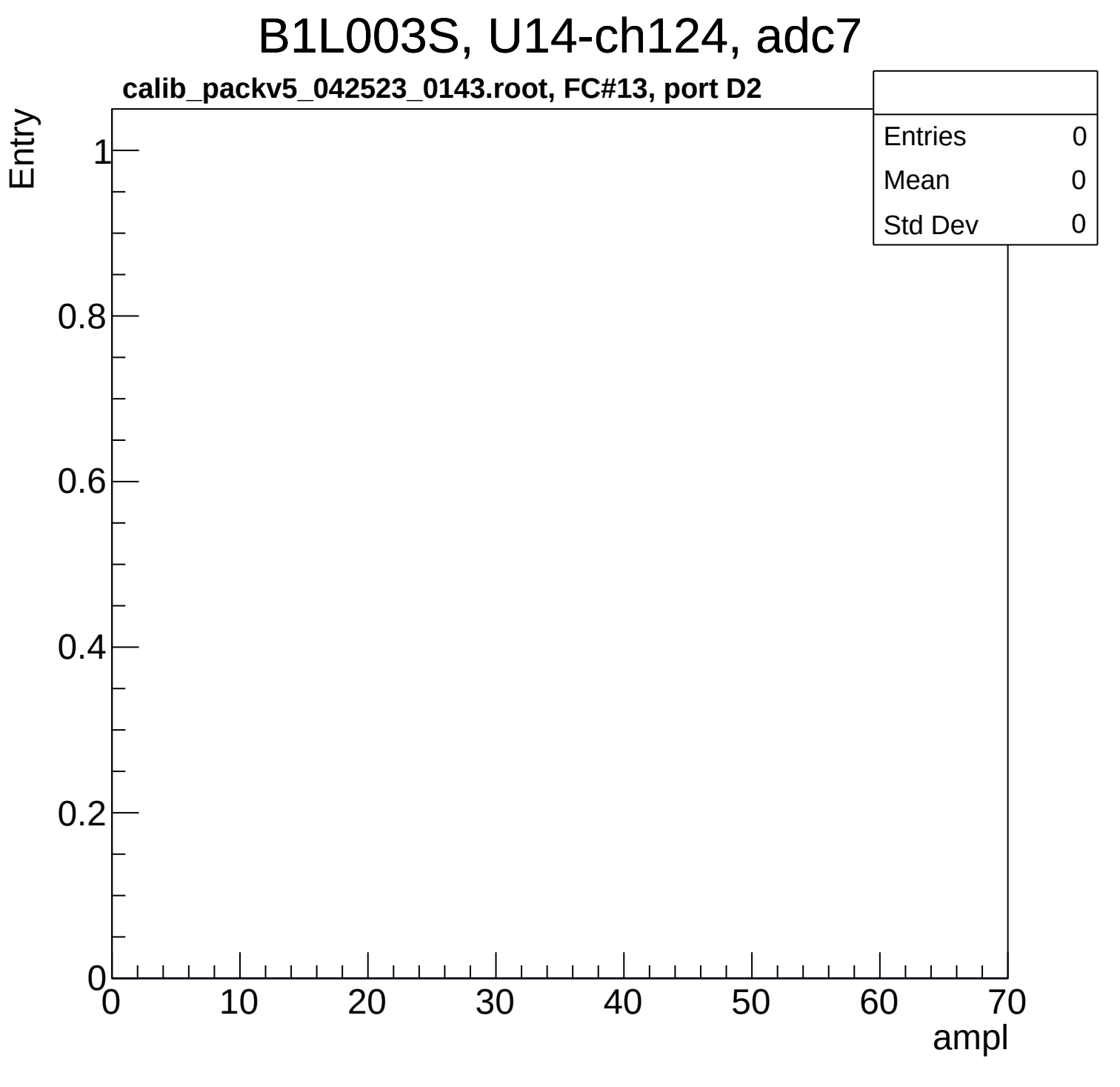

B1L003S, U14-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.48
Std Dev 3.276
Gaus mean : 41.9017
Gaus Width: 3.3670
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

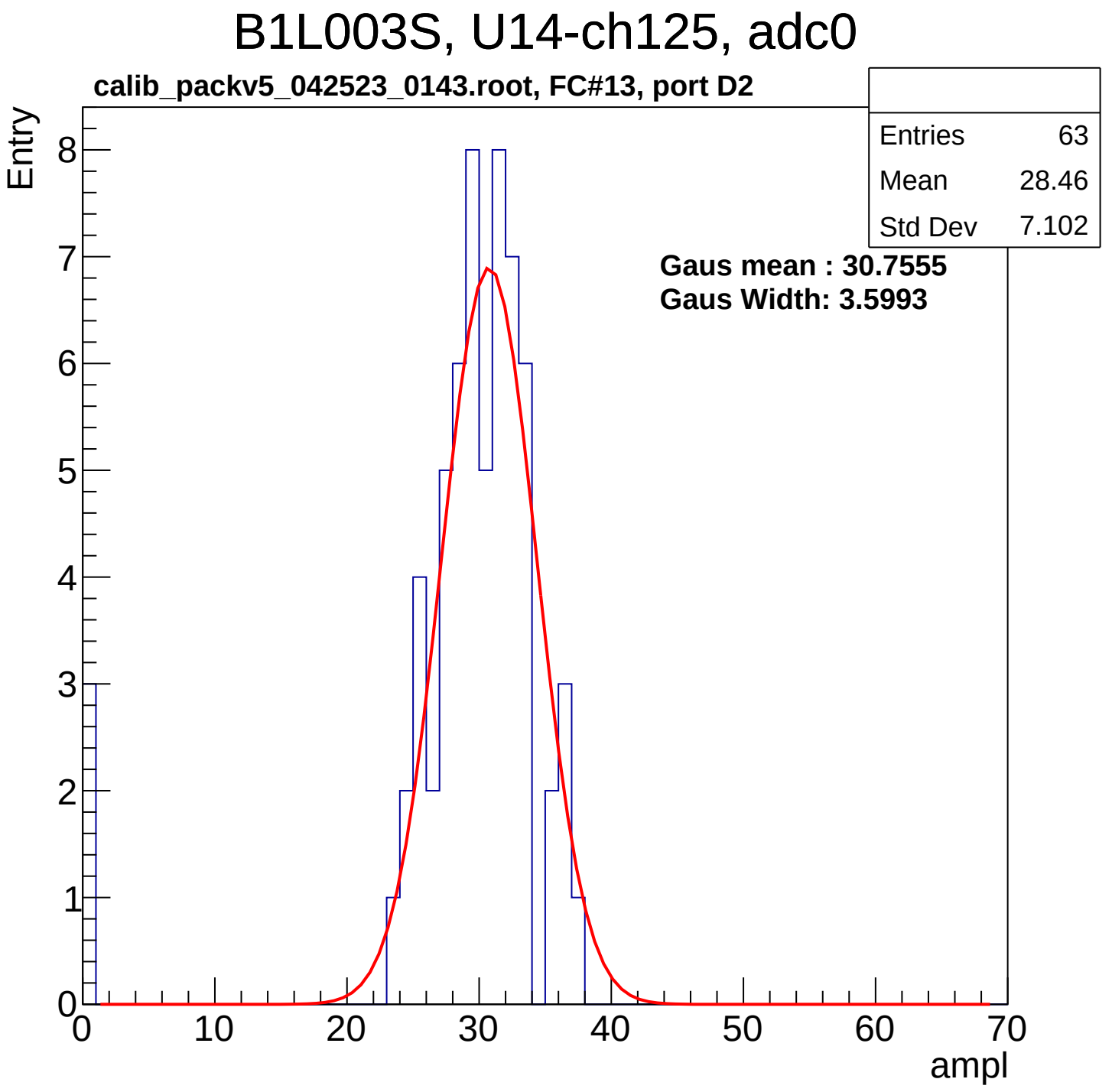

B1L003S, U14-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.39
Std Dev 3.264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

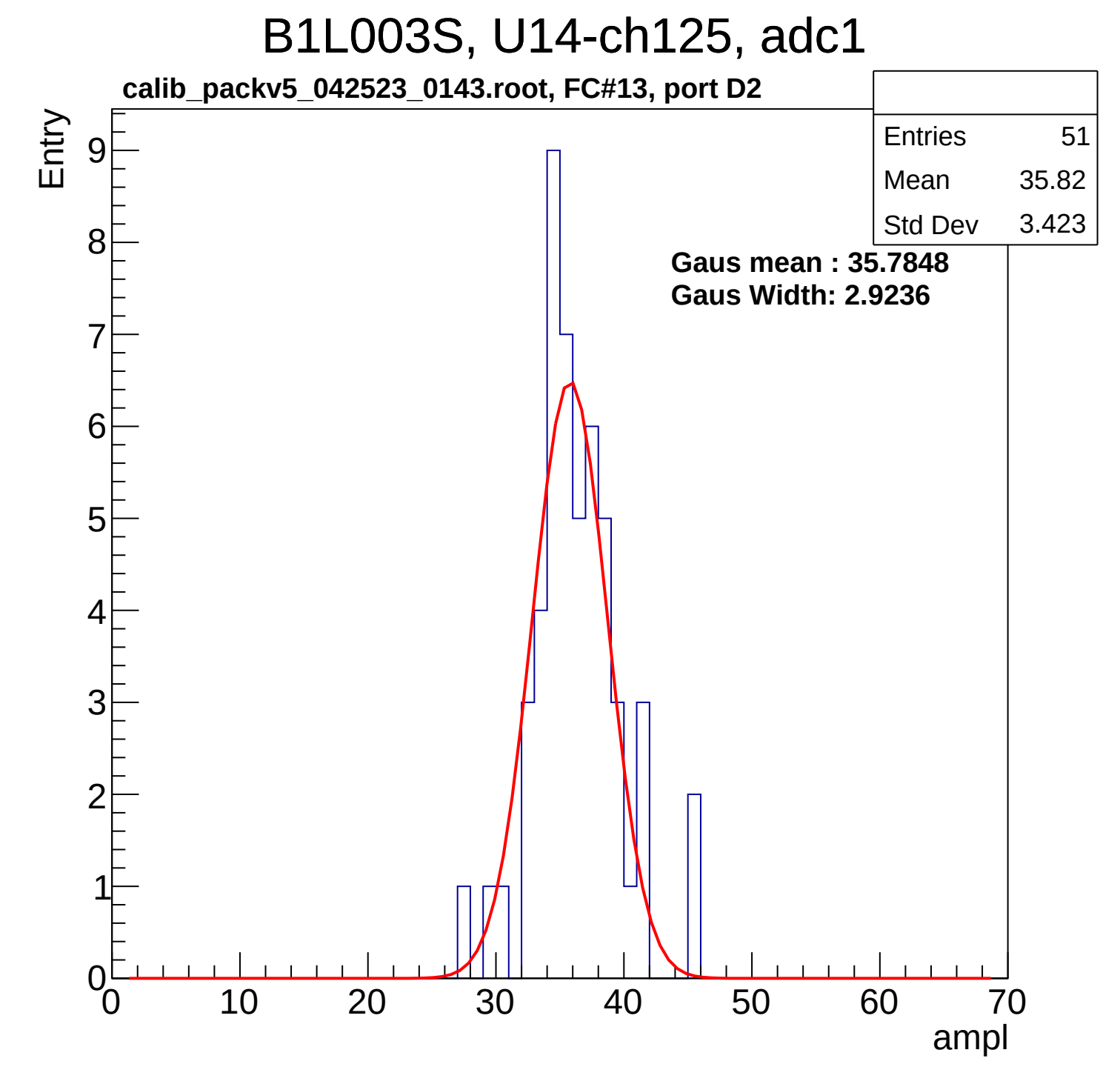

B1L003S, U14-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 53.73
Std Dev 3.136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

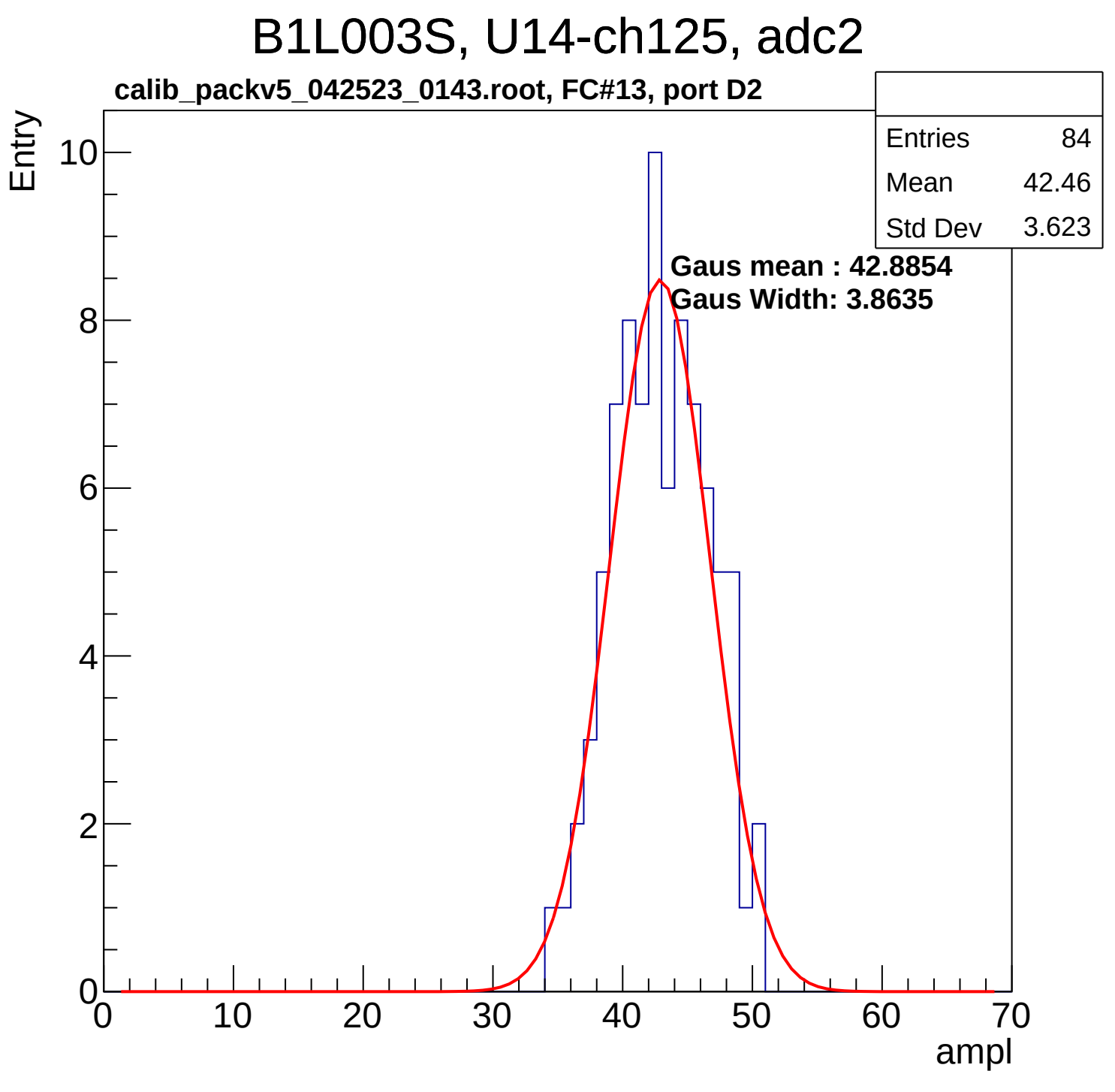

B1L003S, U14-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 57.84
Std Dev 3.129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

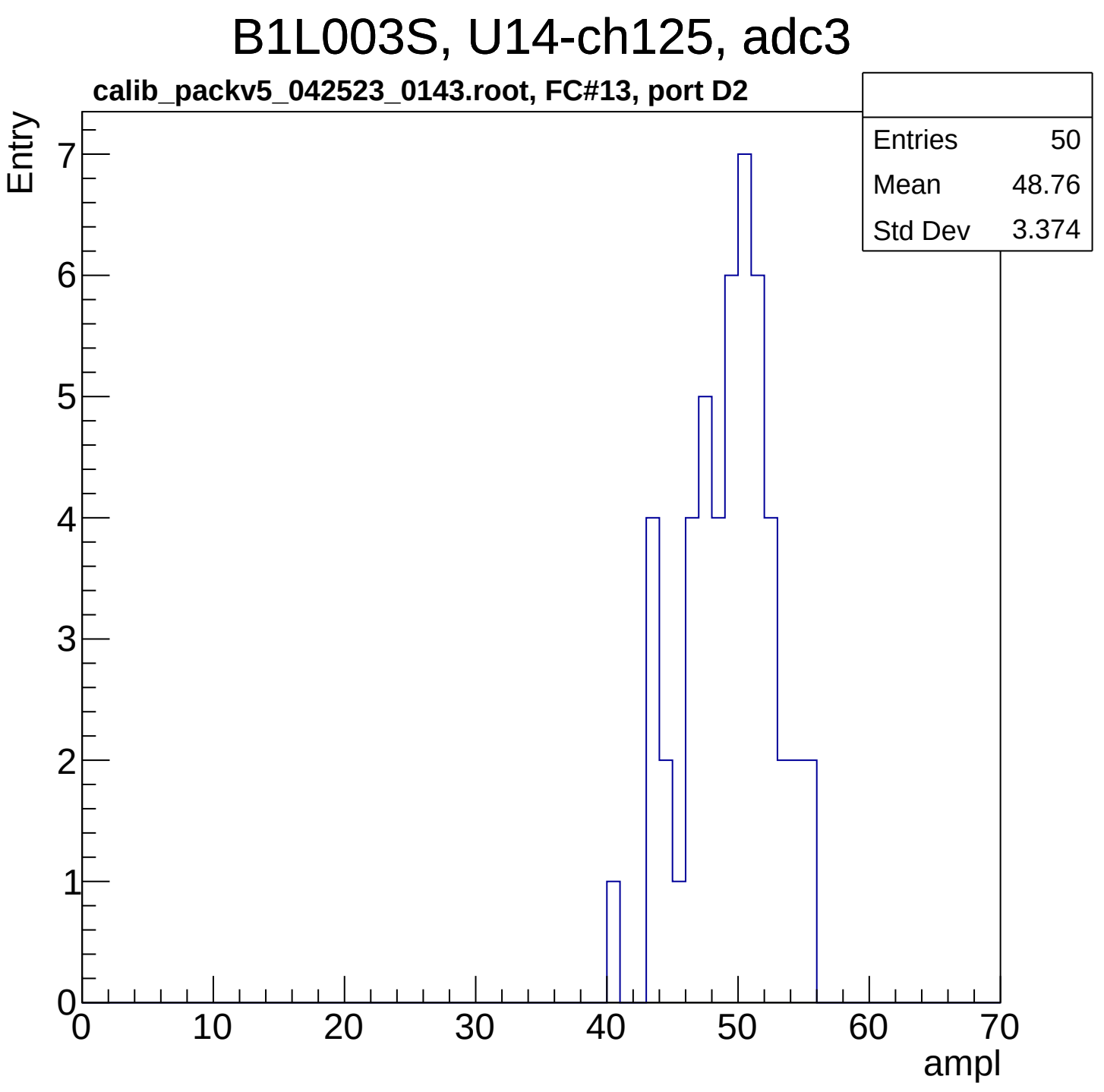

B1L003S, U14-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.47
Std Dev 13.86
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

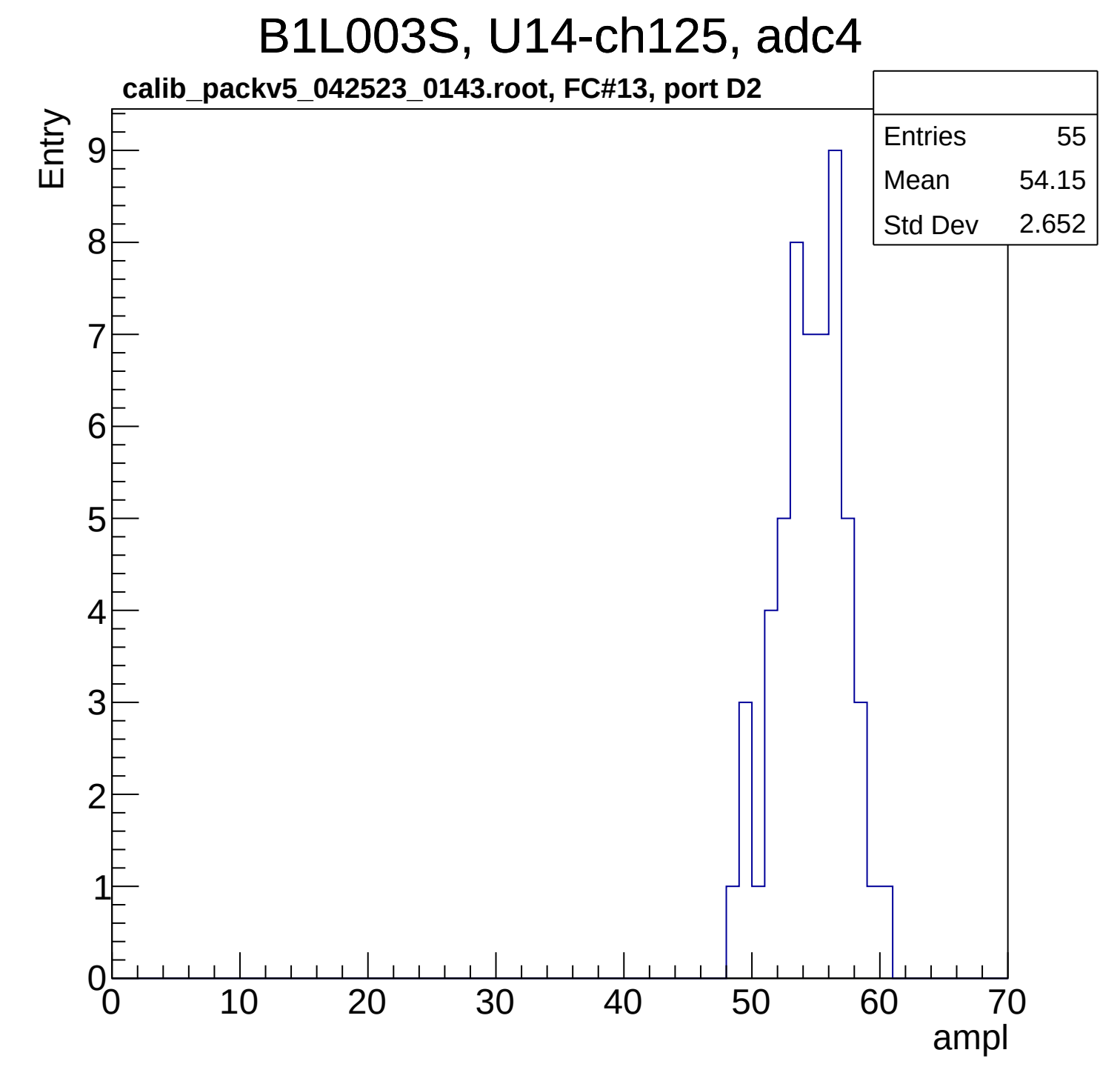

B1L003S, U14-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

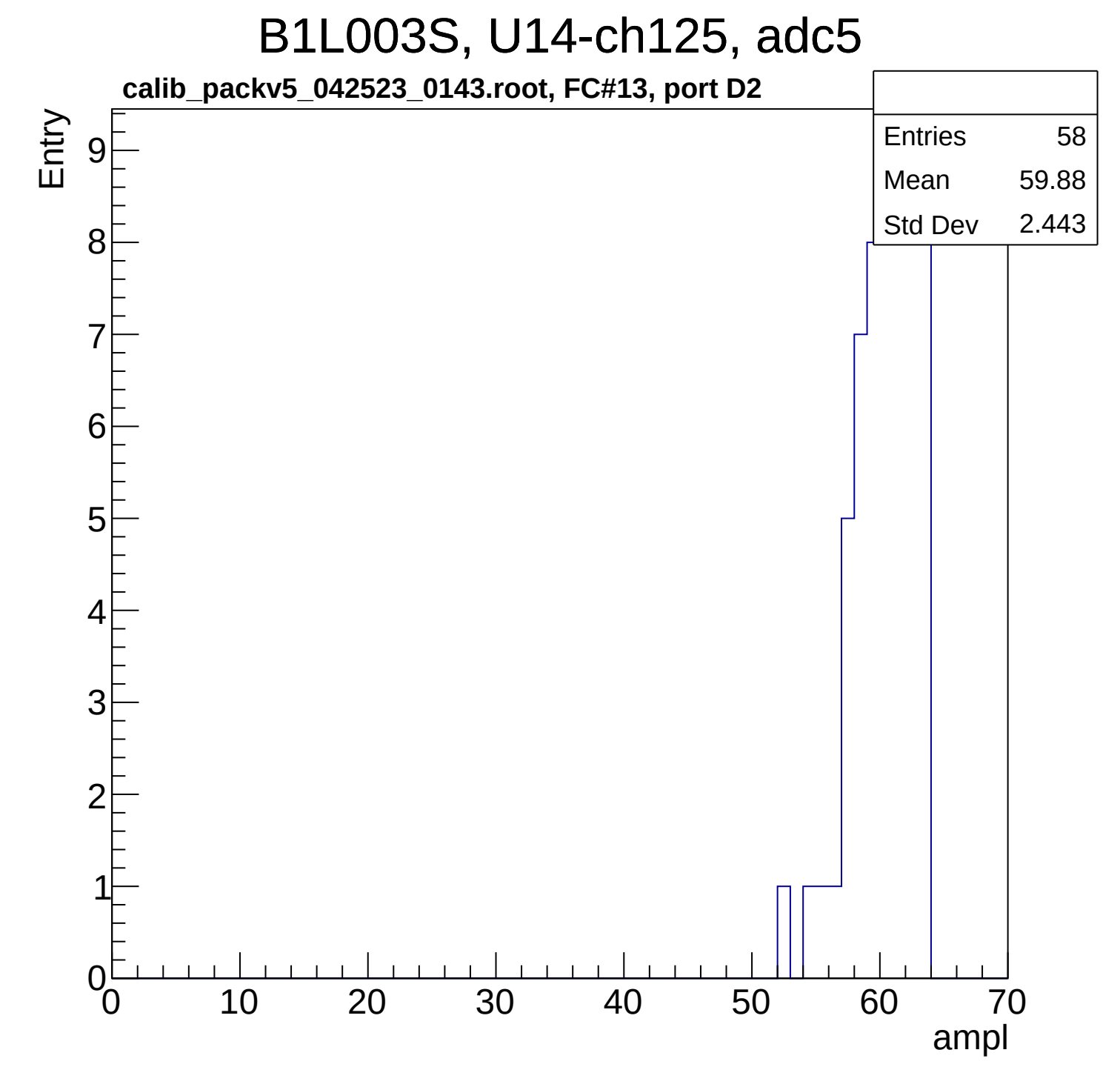

B1L003S, U14-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.46
Std Dev 7.102
Gaus mean : 30.7555
Gaus Width: 3.5993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

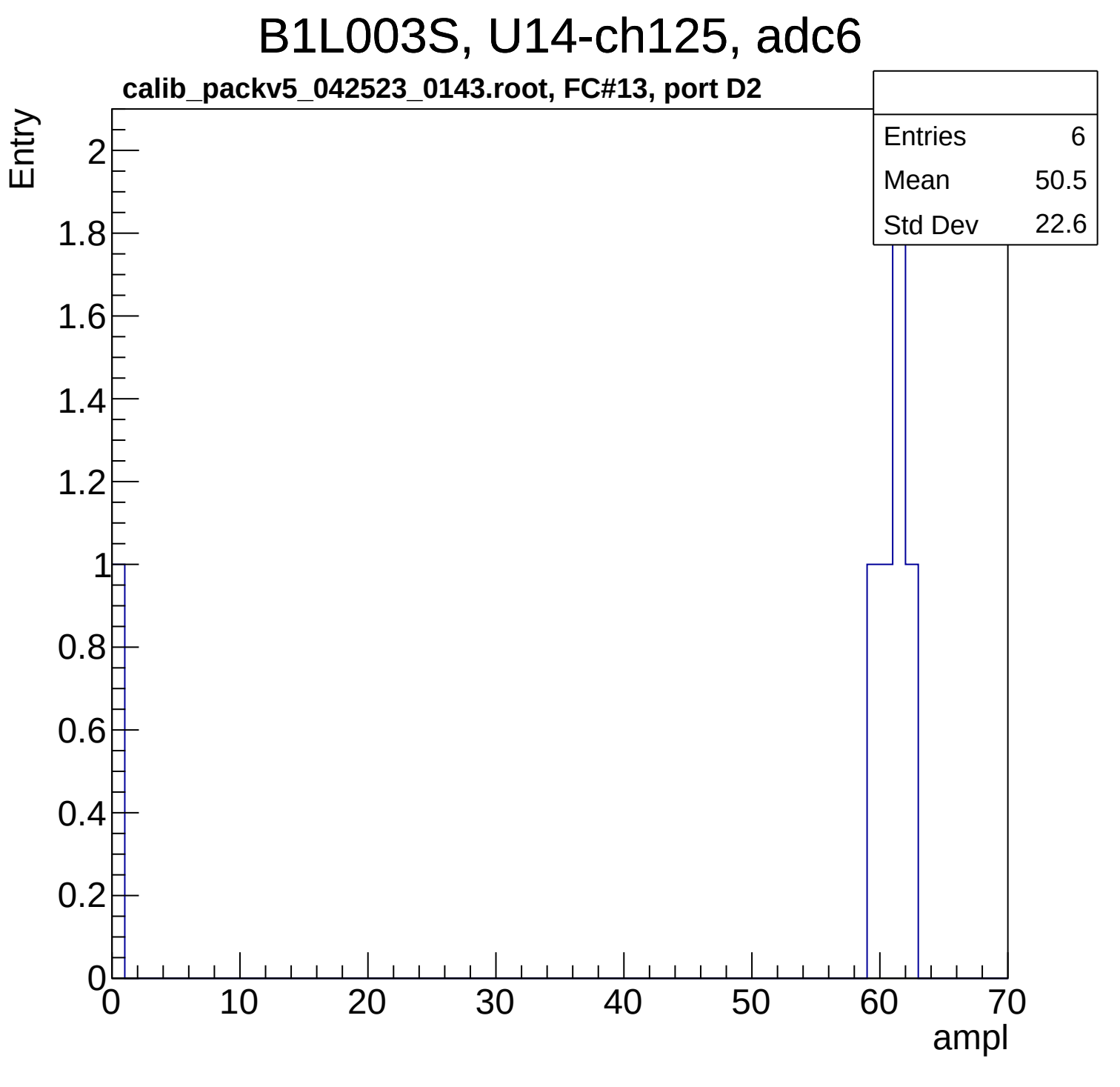

B1L003S, U14-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 35.82
Std Dev 3.423
Gaus mean : 35.7848
Gaus Width: 2.9236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

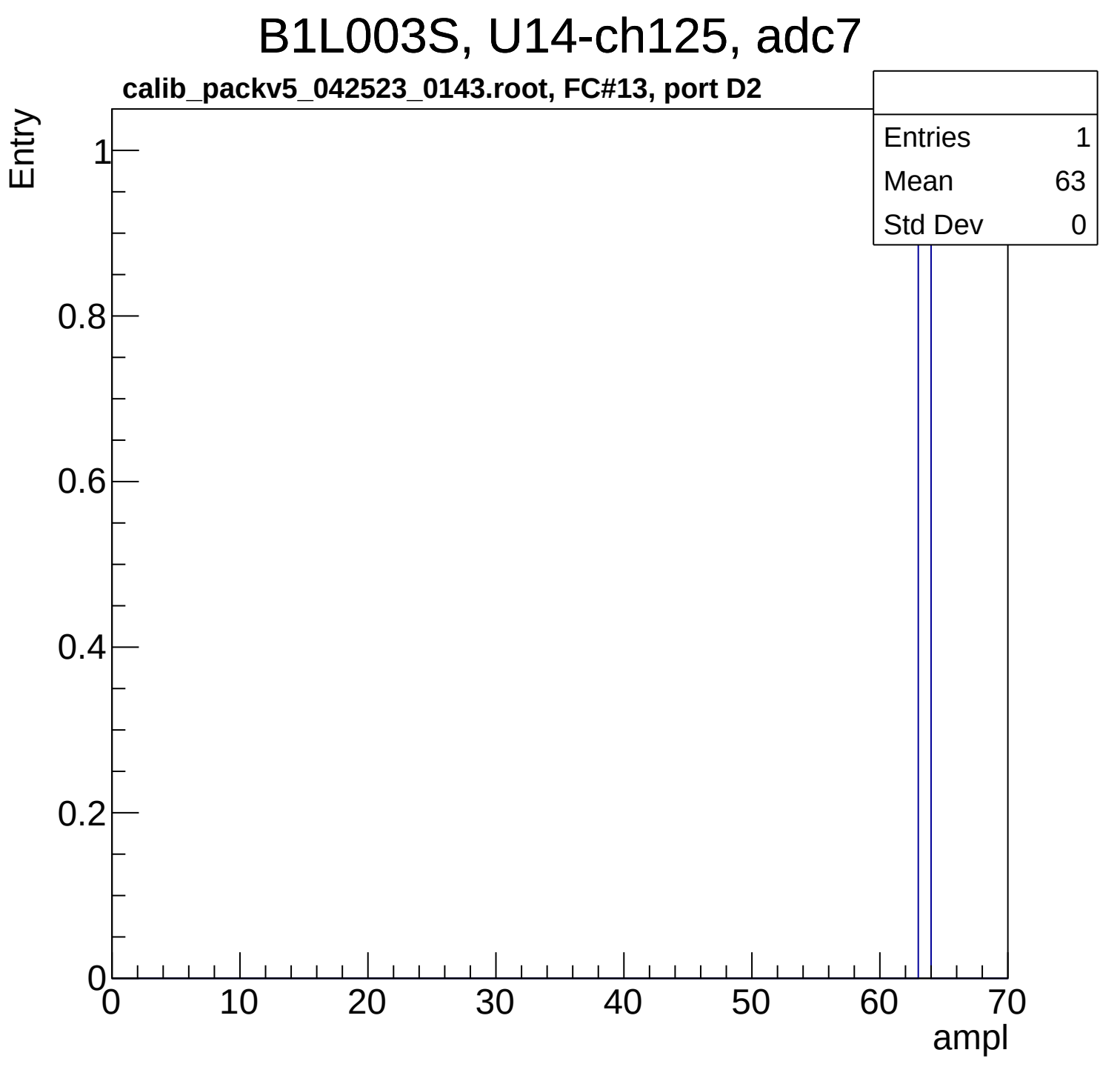

B1L003S, U14-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 42.46
Std Dev 3.623
Gaus mean : 42.8854
Gaus Width: 3.8635
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

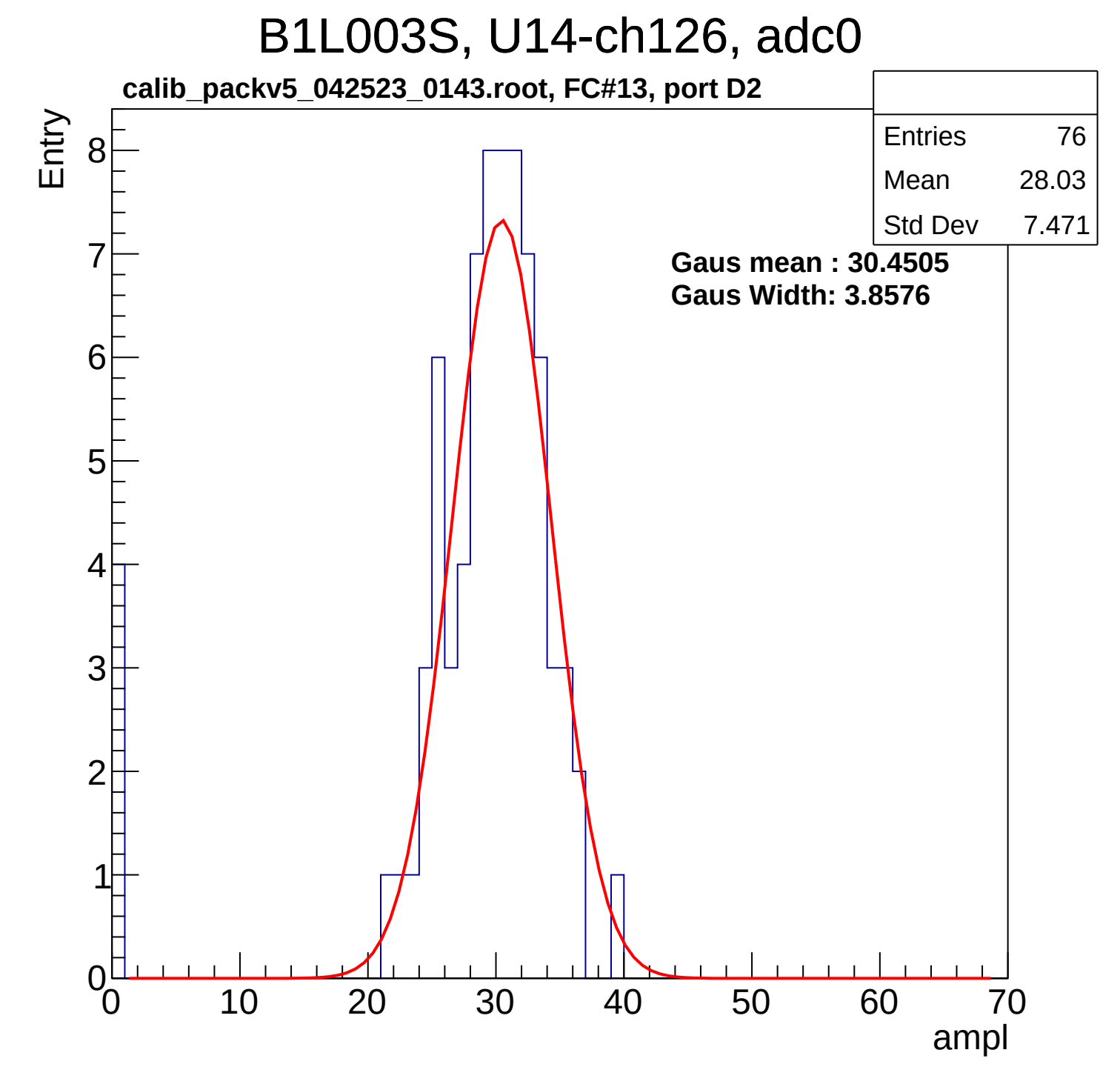

B1L003S, U14-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 48.76
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

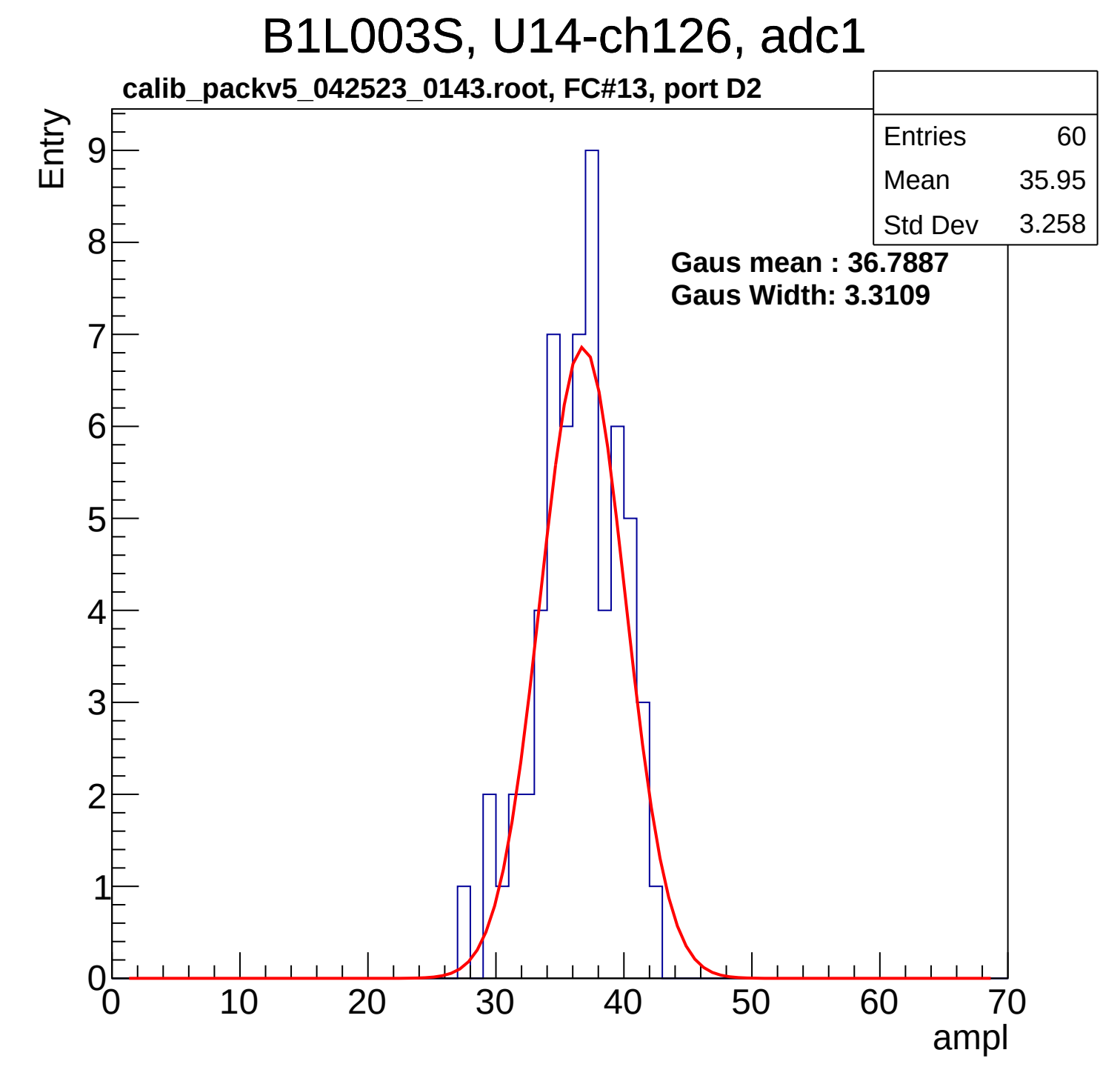

B1L003S, U14-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.15
Std Dev 2.652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

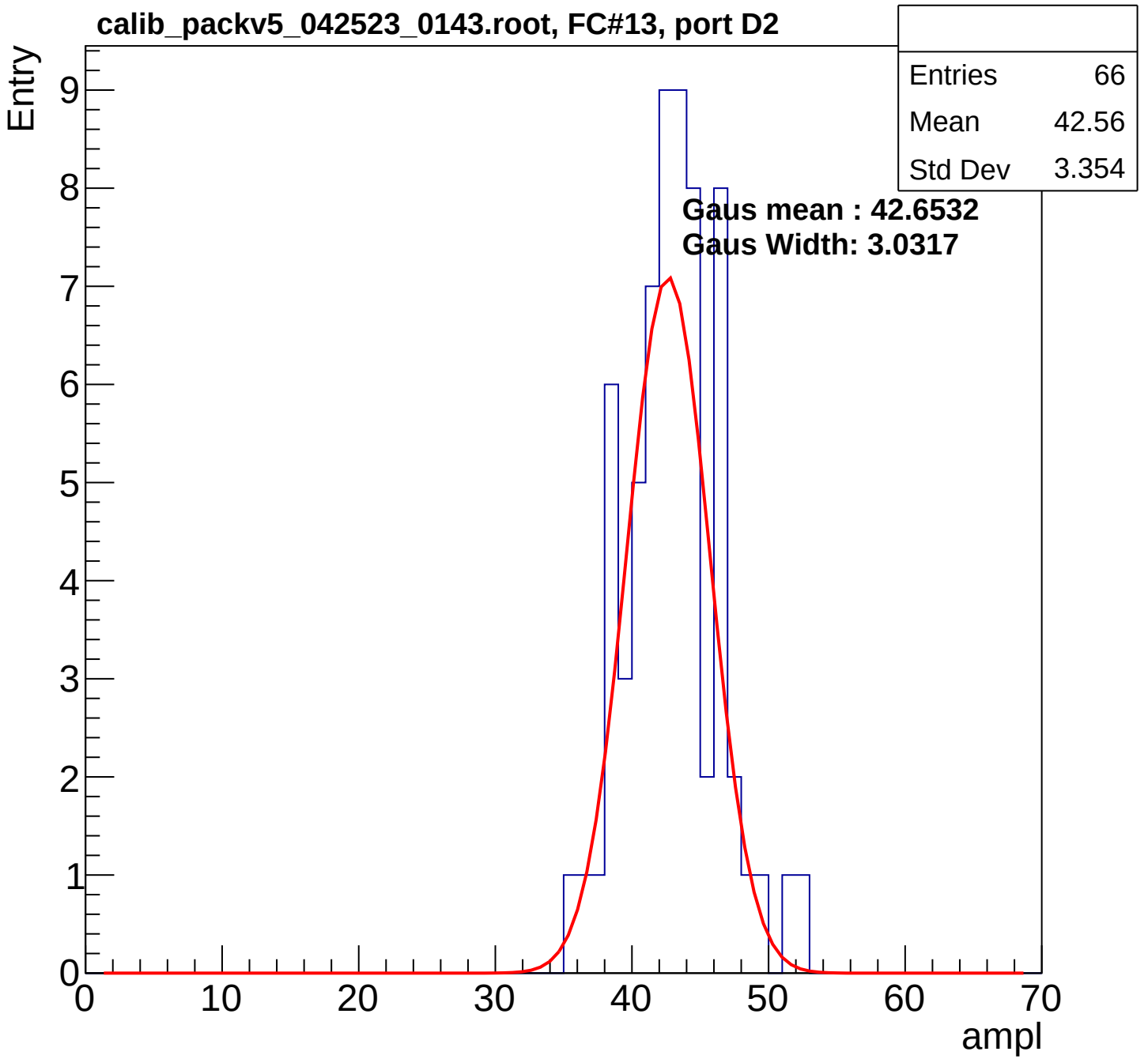

B1L003S, U14-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59.88
Std Dev 2.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

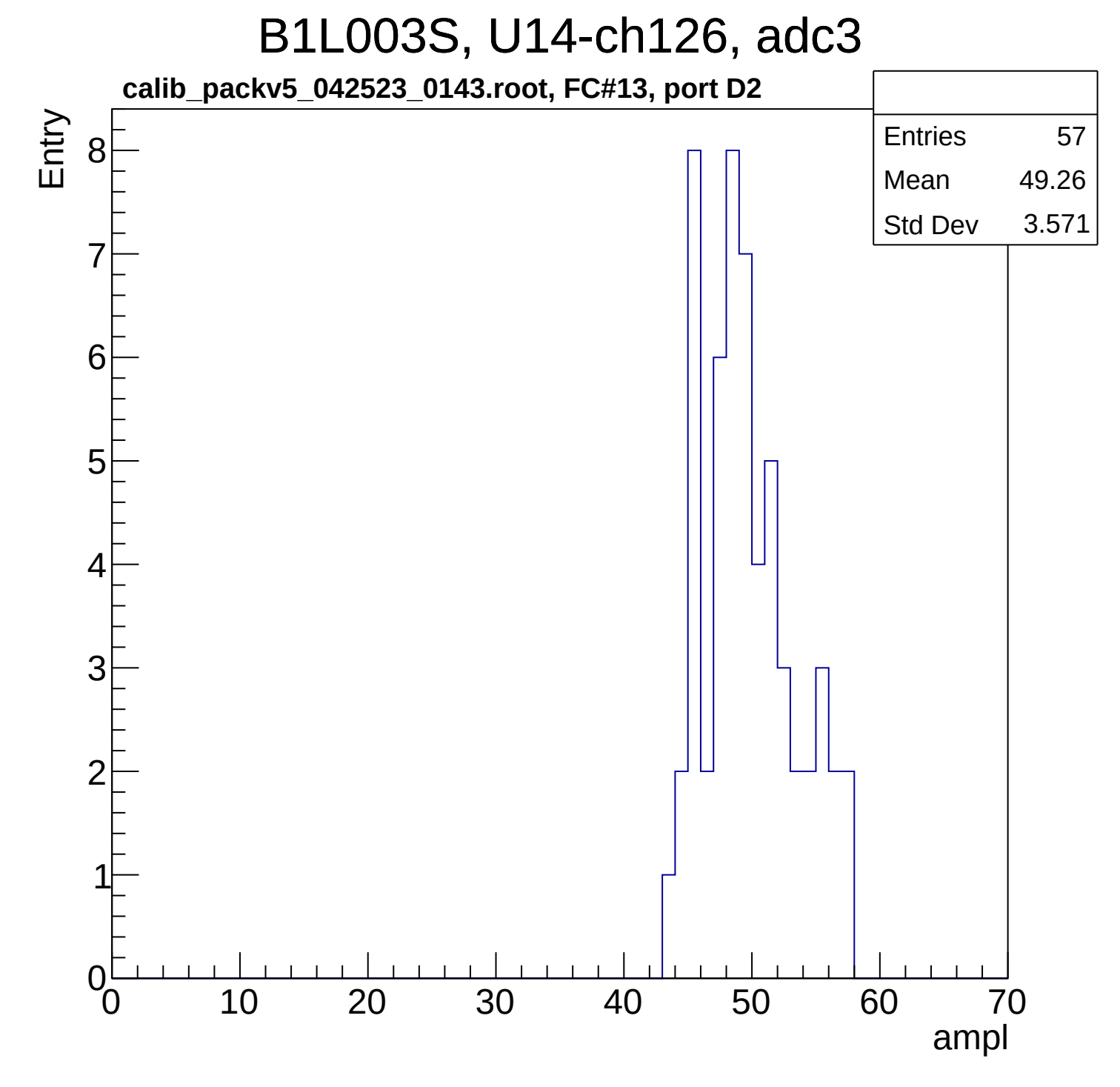

B1L003S, U14-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 50.5
Std Dev 22.6
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

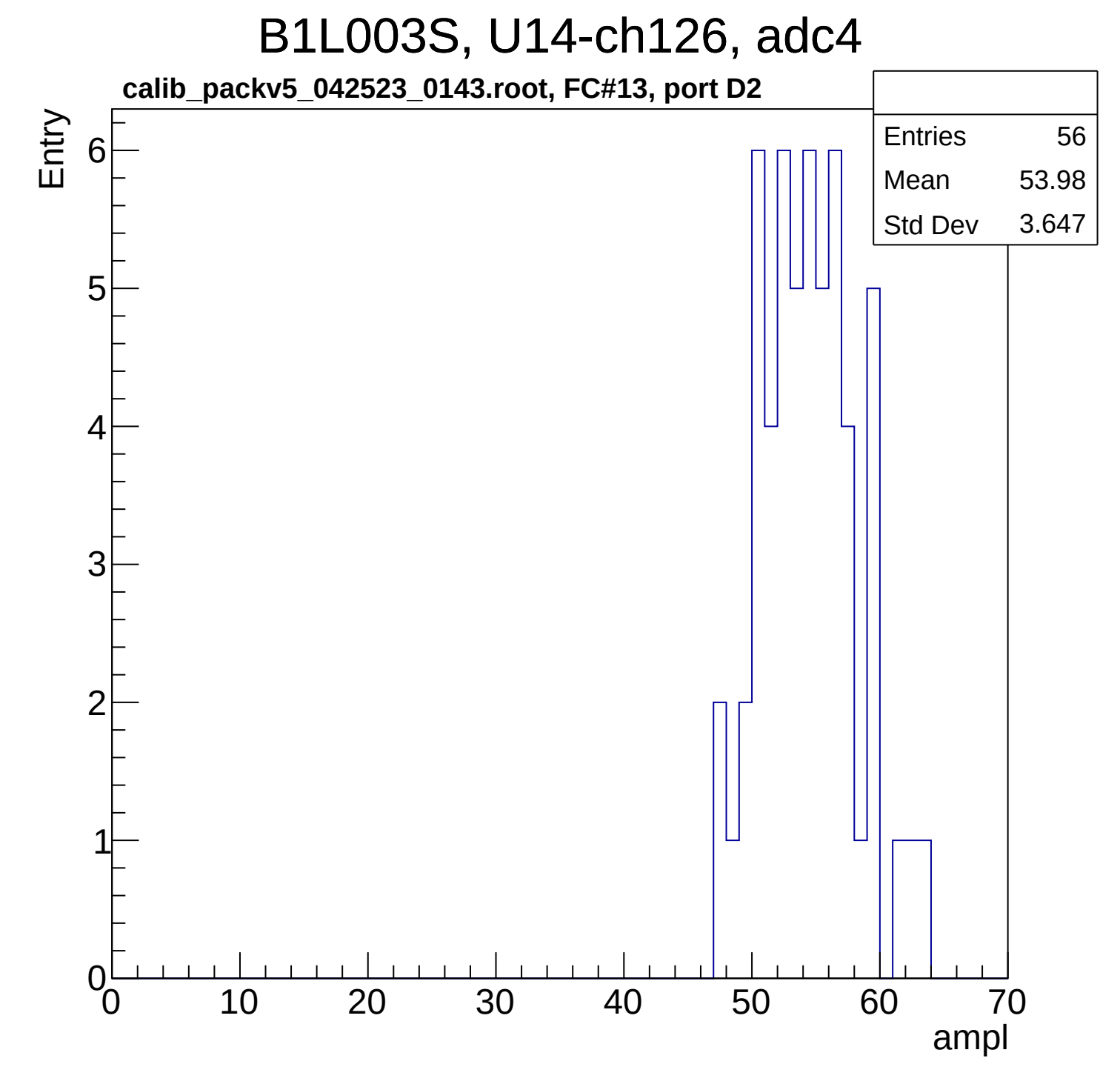

B1L003S, U14-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

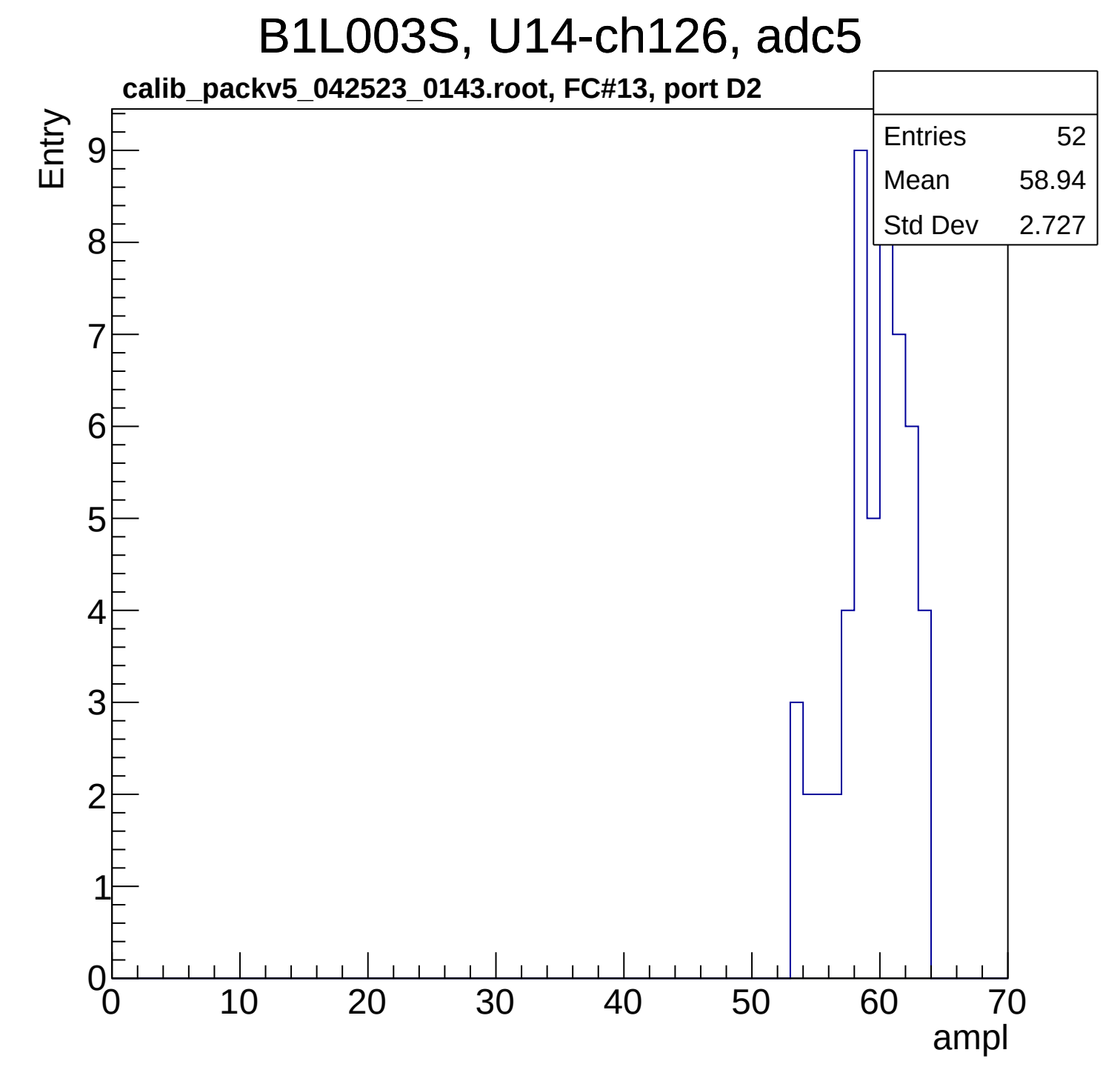

B1L003S, U14-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.03
Std Dev 7.471
Gaus mean : 30.4505
Gaus Width: 3.8576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

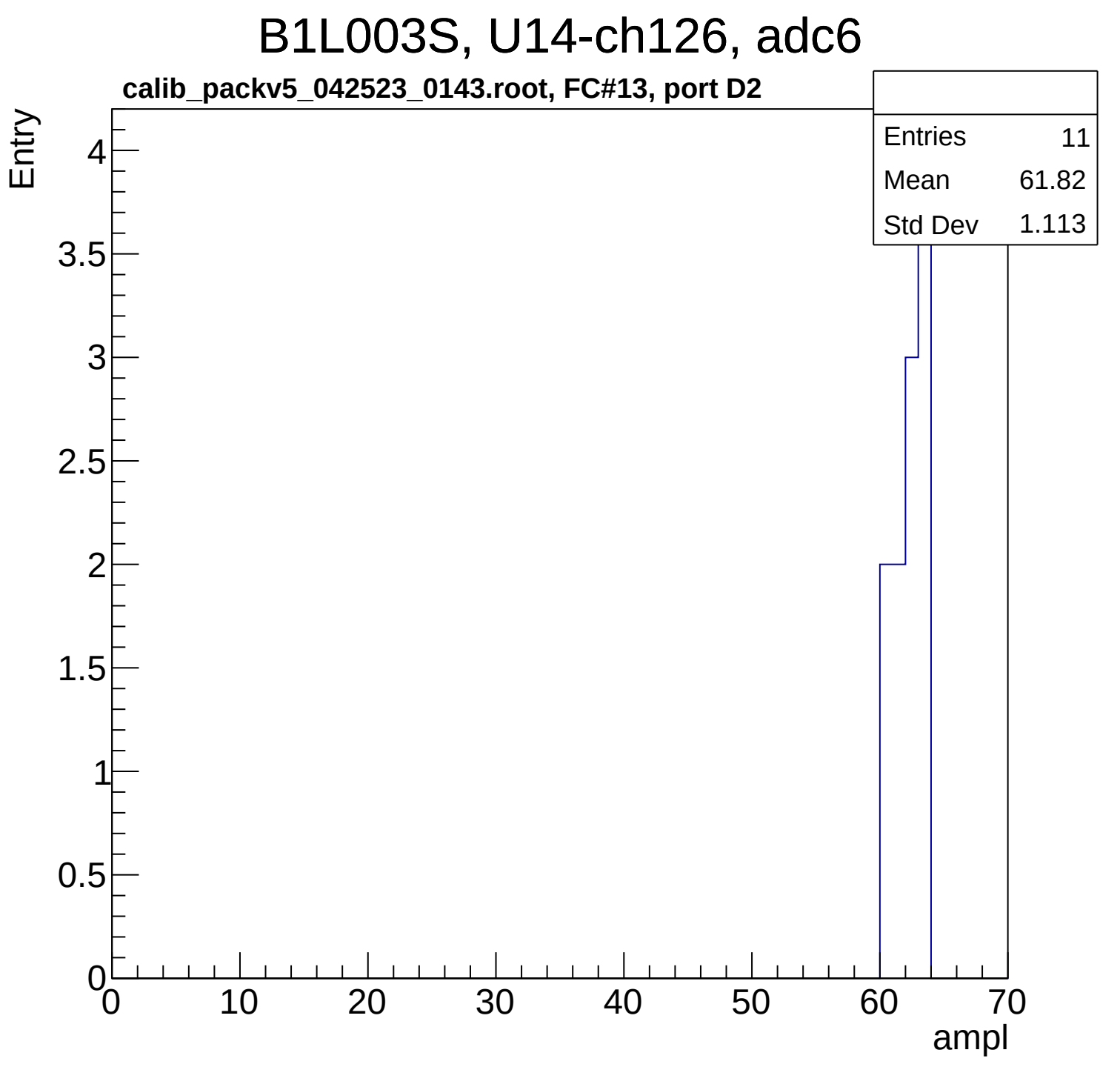

B1L003S, U14-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.95
Std Dev 3.258
Gaus mean : 36.7887
Gaus Width: 3.3109
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

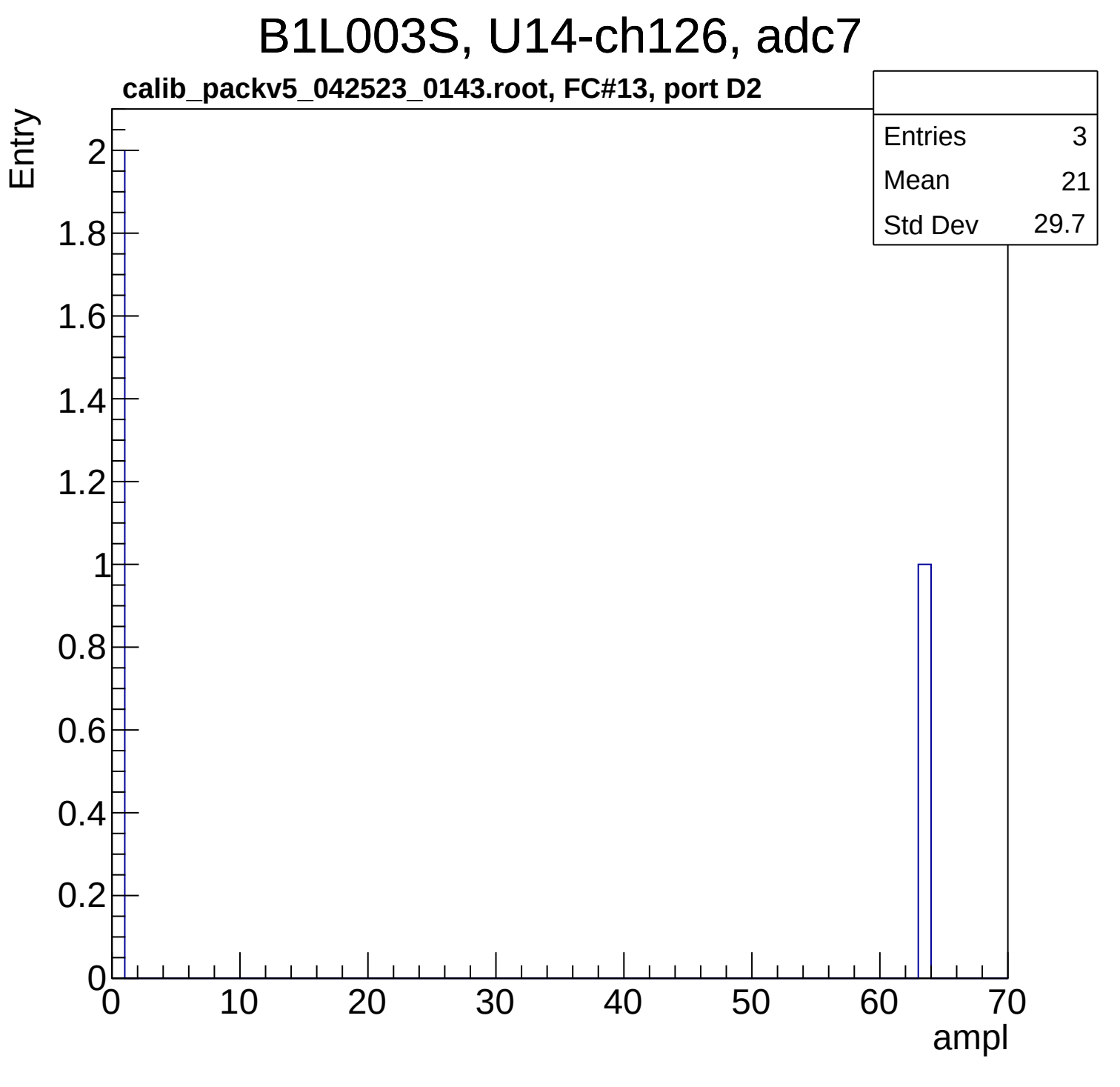

B1L003S, U14-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.56
Std Dev 3.354
Gaus mean : 42.6532
Gaus Width: 3.0317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

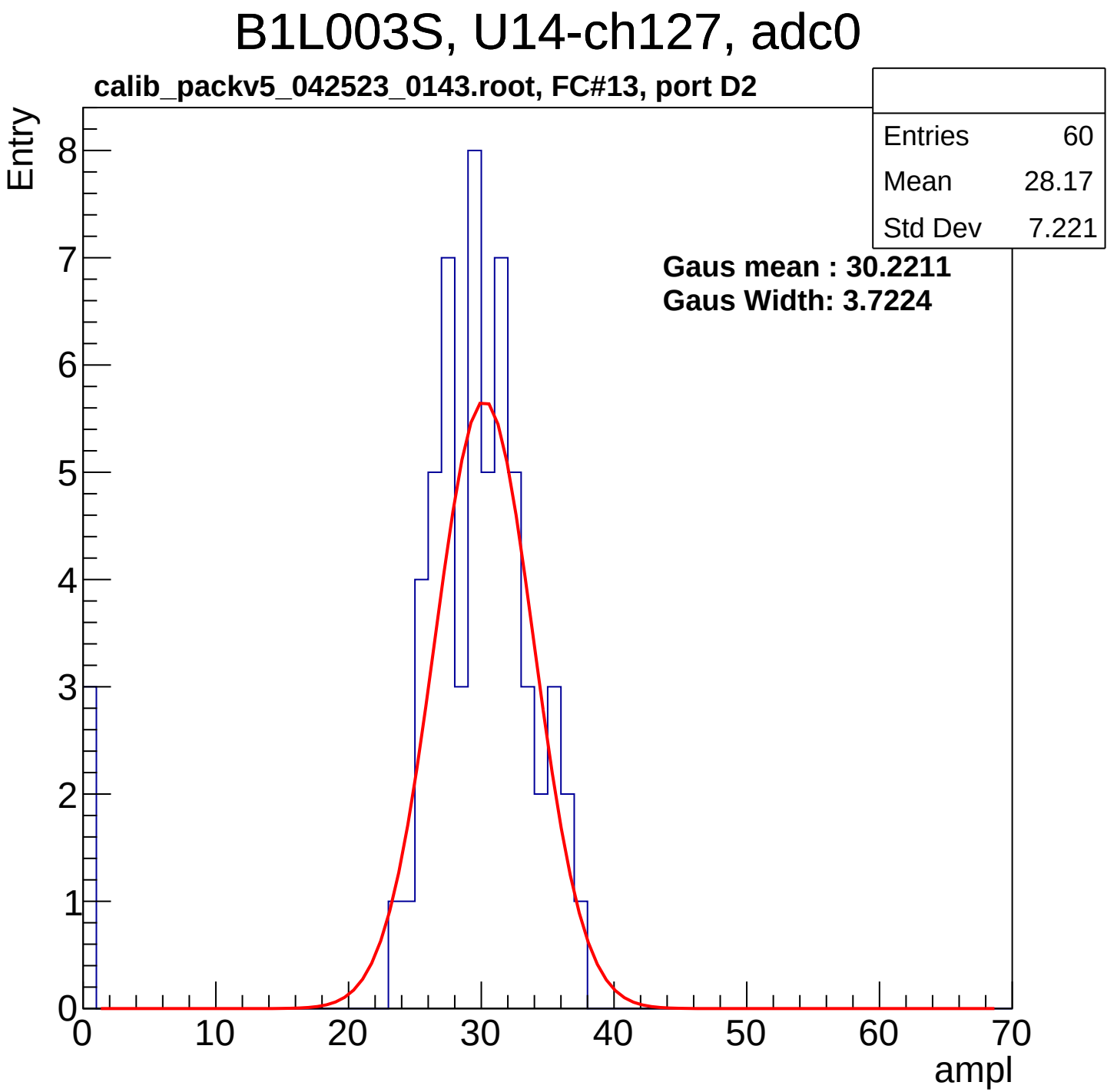

B1L003S, U14-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.26
Std Dev 3.571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

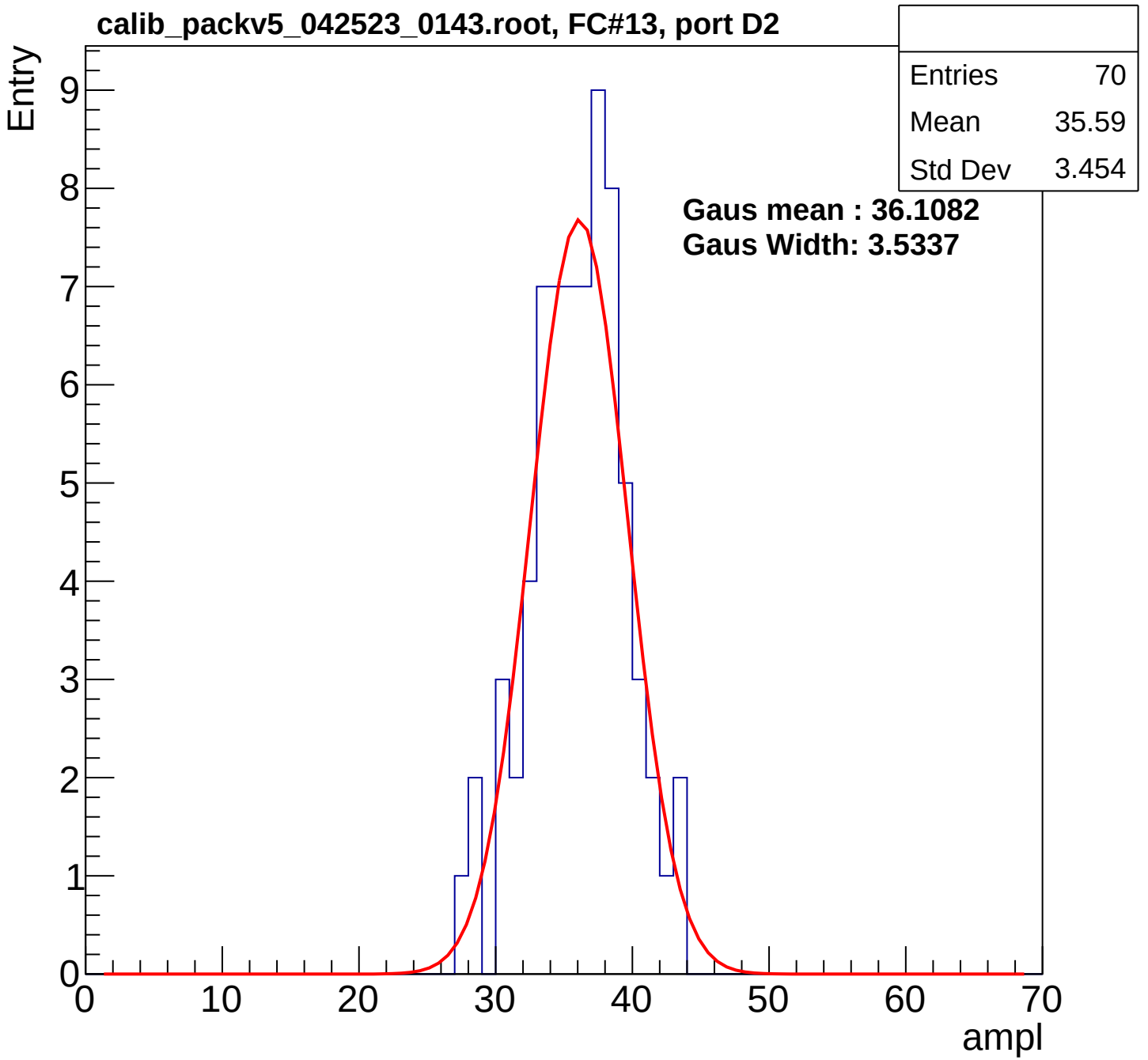

B1L003S, U14-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.98
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

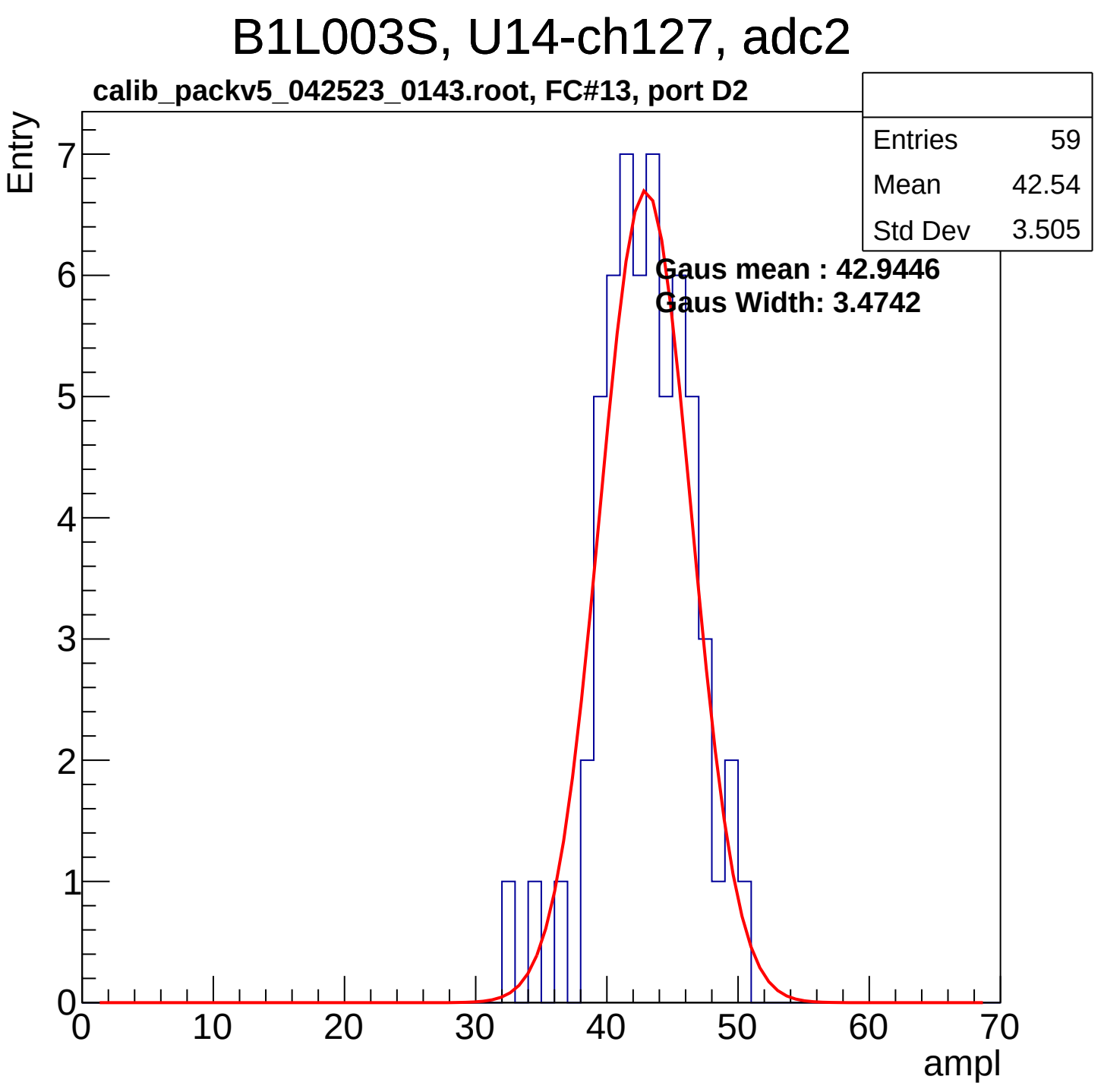

B1L003S, U14-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.94
Std Dev 2.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

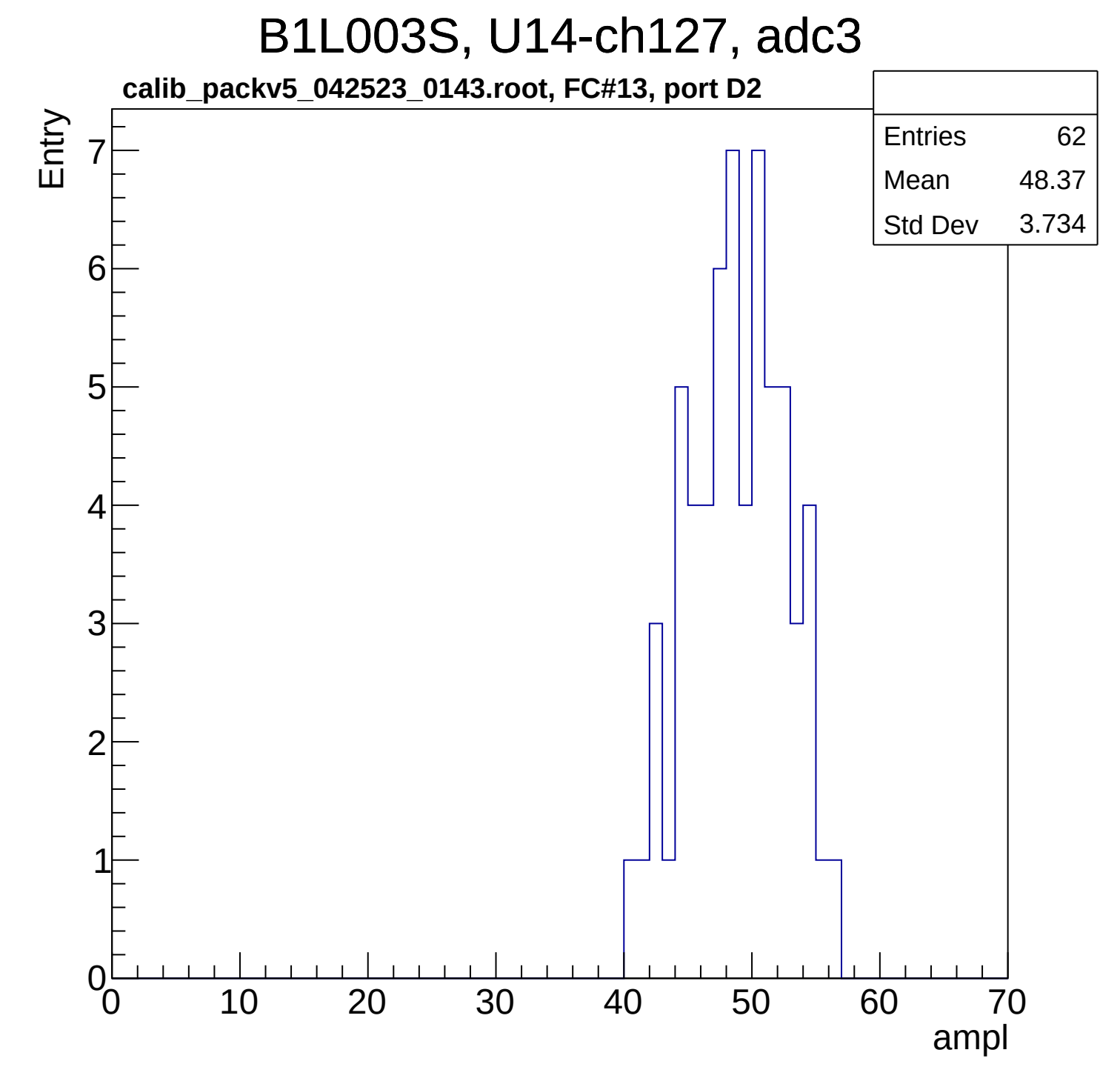

B1L003S, U14-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.82
Std Dev 1.113
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

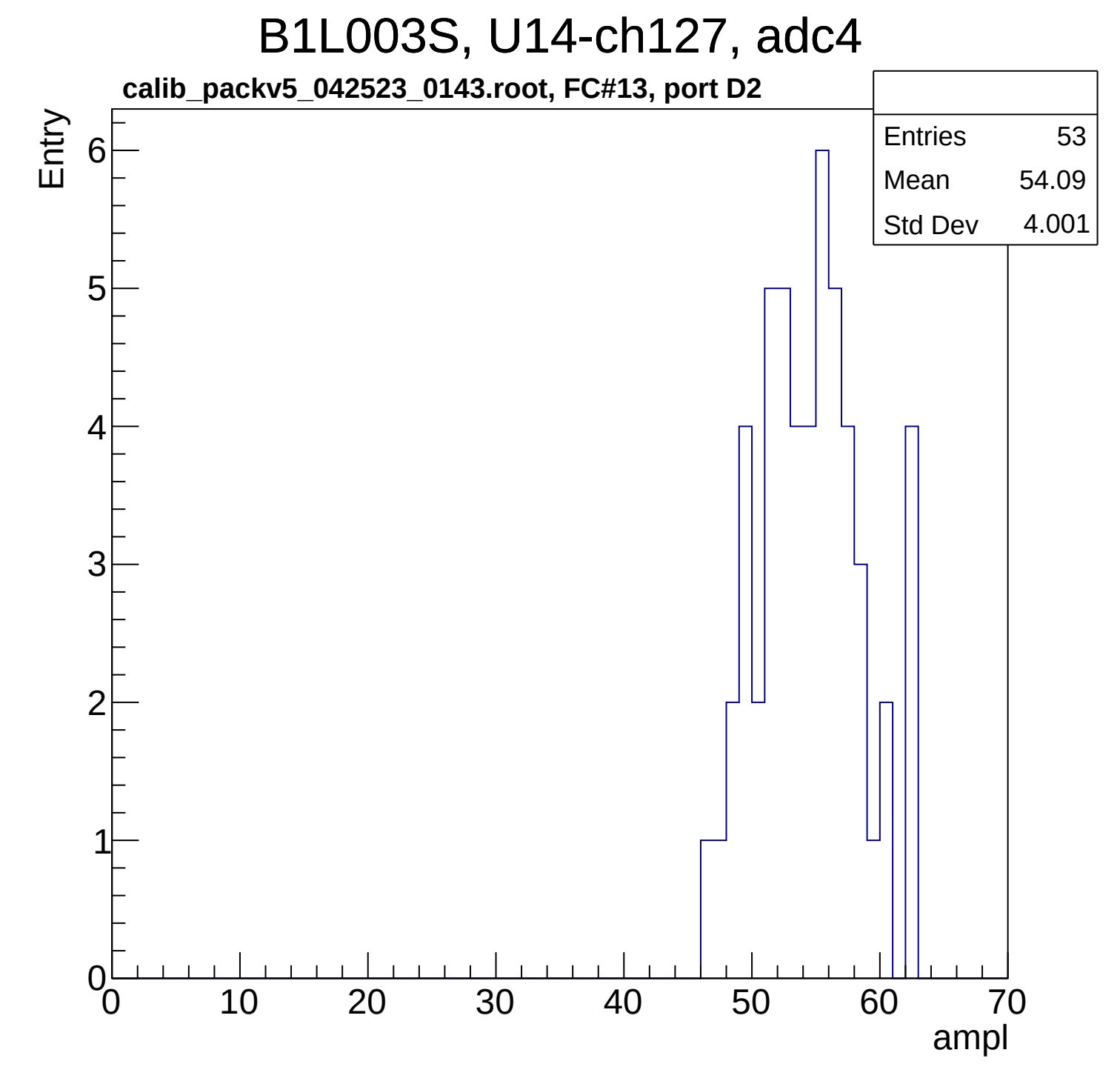

B1L003S, U14-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

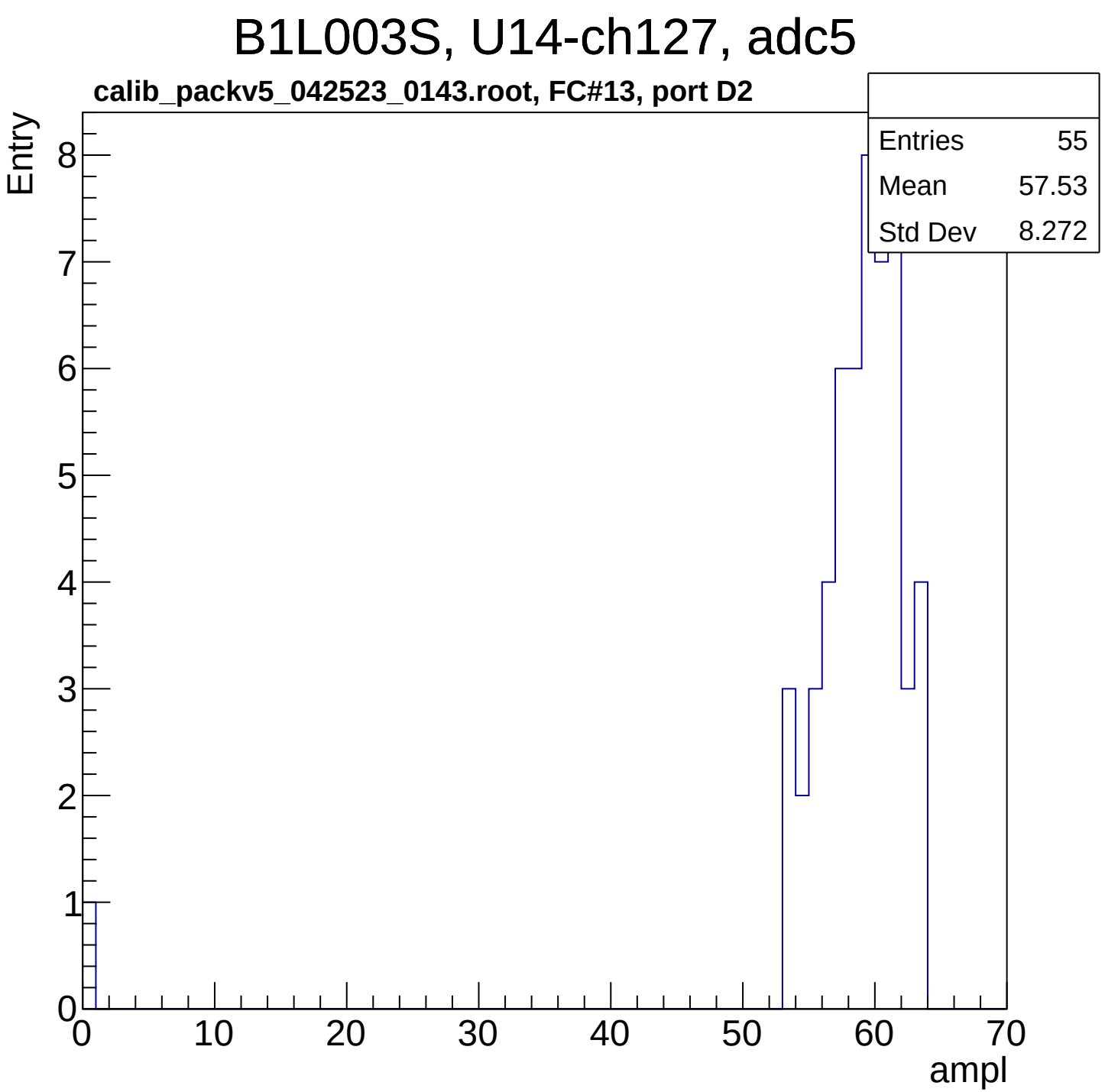

B1L003S, U14-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.17
Std Dev 7.221
Gaus mean : 30.2211
Gaus Width: 3.7224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

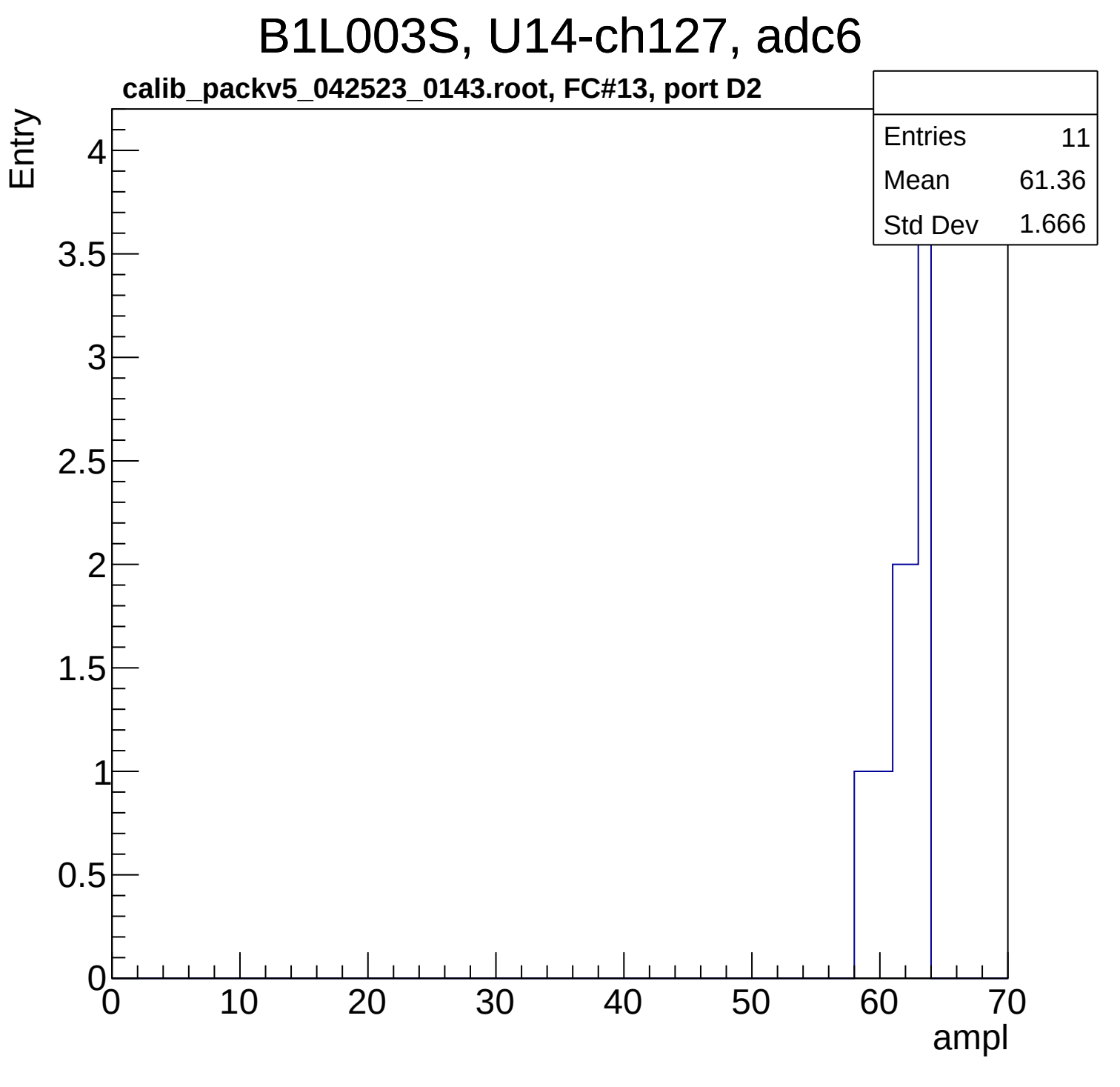

B1L003S, U14-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.59
Std Dev 3.454
Gaus mean : 36.1082
Gaus Width: 3.5337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

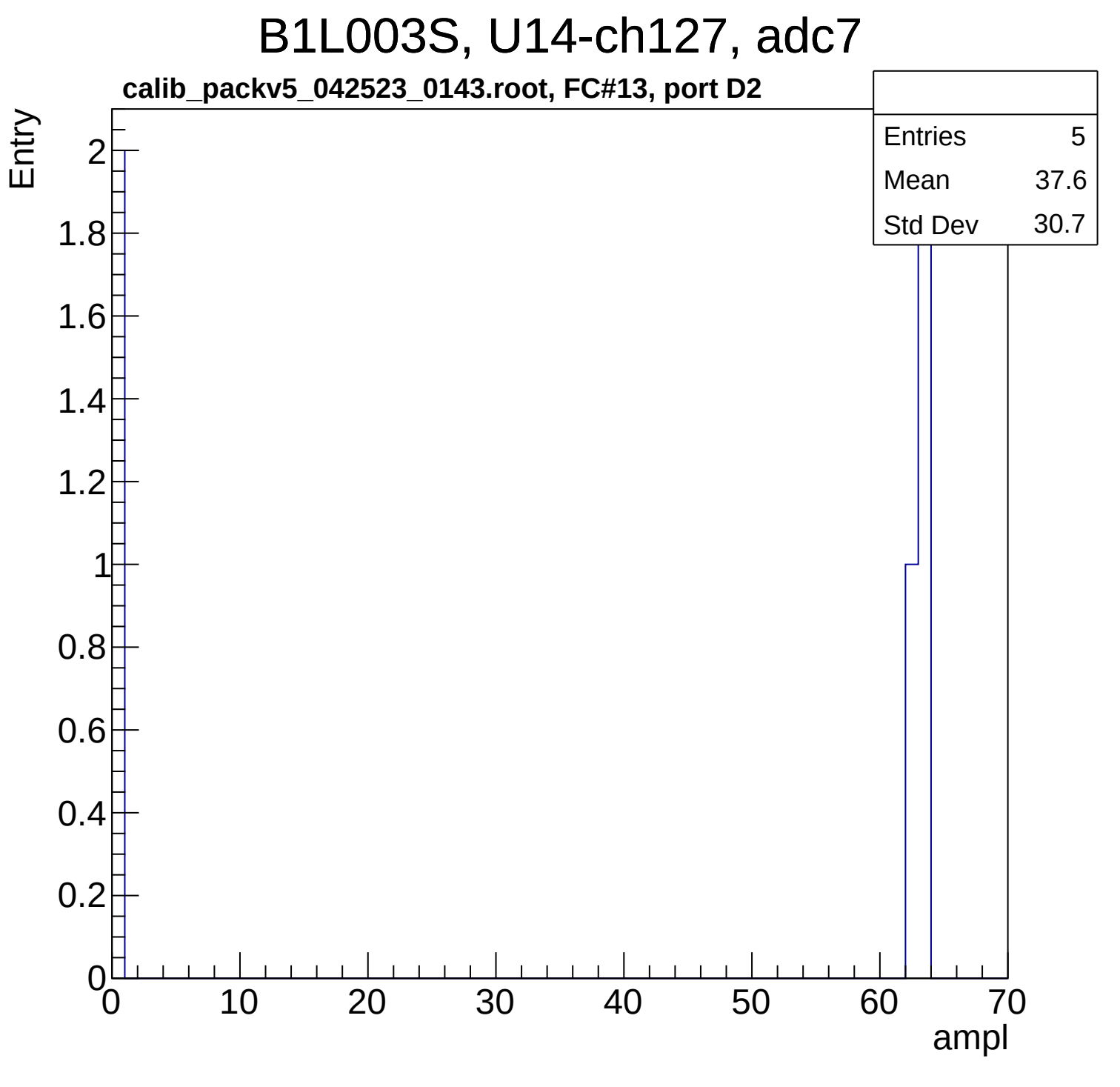

B1L003S, U14-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.54
Std Dev 3.505
Gaus mean : 42.9446
Gaus Width: 3.4742
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

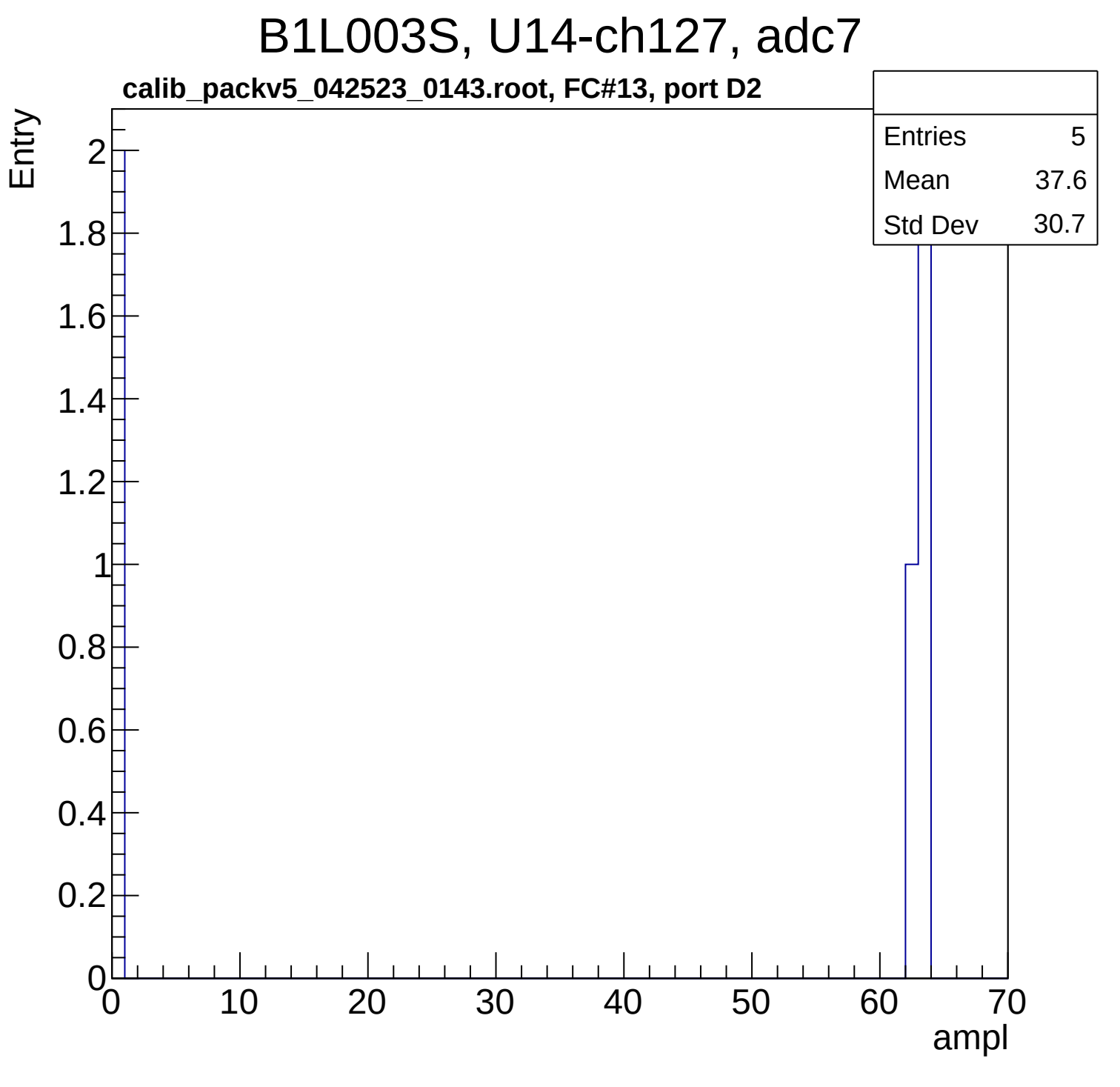

B1L003S, U14-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.37
Std Dev 3.734
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U14-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.09
Std Dev 4.001
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U14-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.53
Std Dev 8.272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U14-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.36
Std Dev 1.666
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U14-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U14-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70