



# B0L103S, U26-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.78
Std Dev	8.161

Turn on : 51.5054

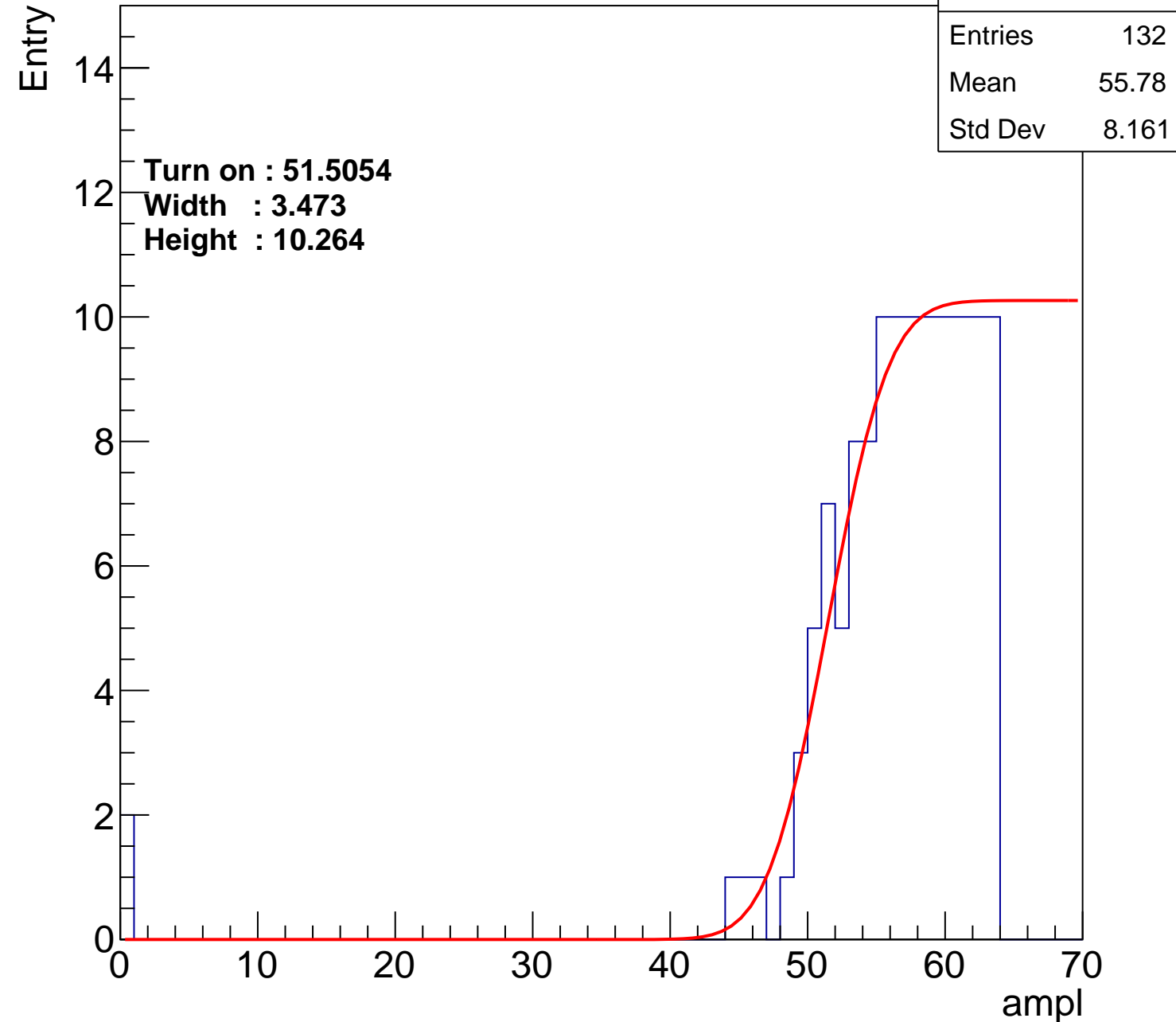
Width : 3.473

Height : 10.264

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch1

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	56.32
Std Dev	10.12

Turn on : 53.0601

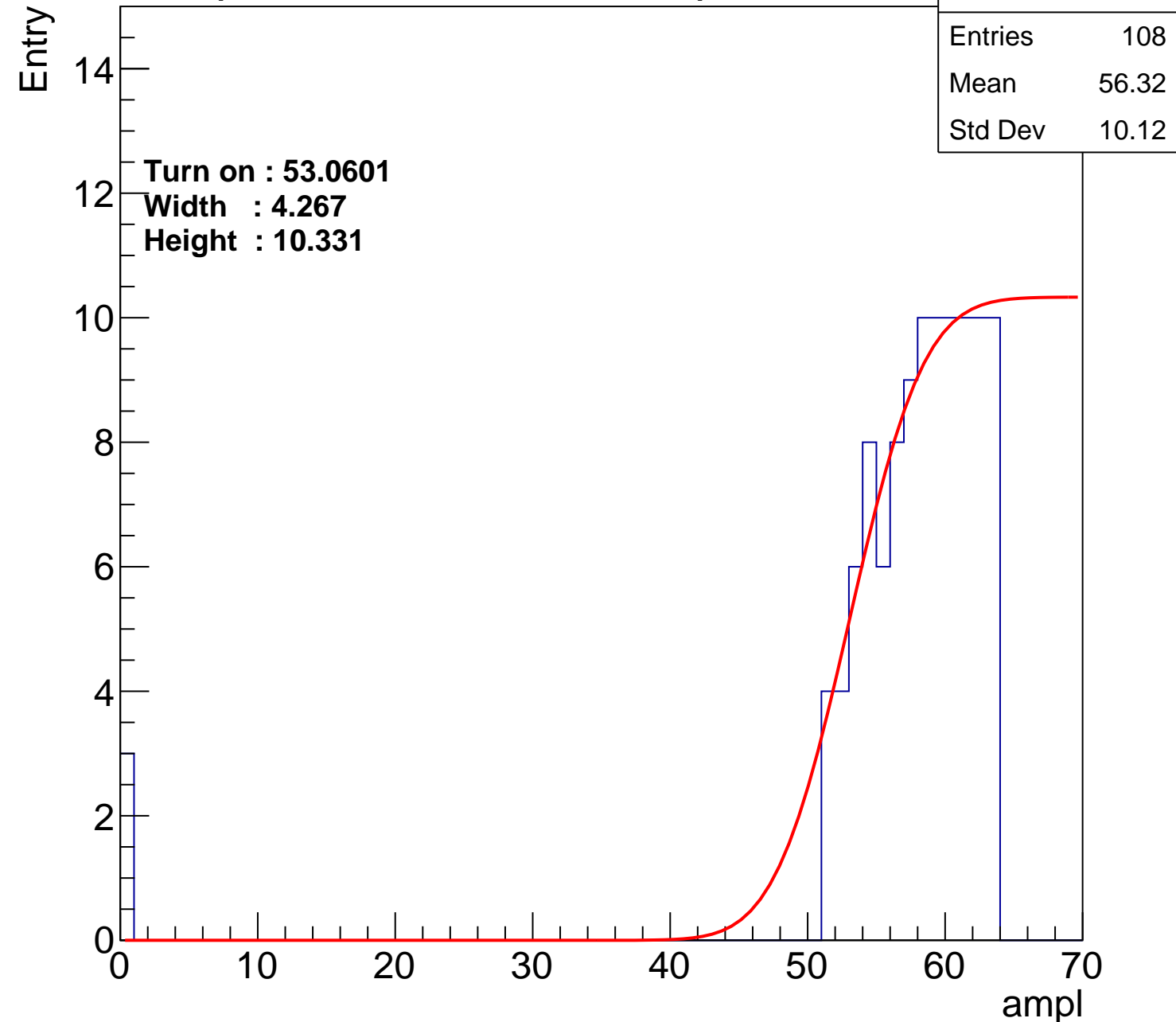
Width : 4.267

Height : 10.331

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.16
Std Dev	8.273

**Turn on : 52.2027**

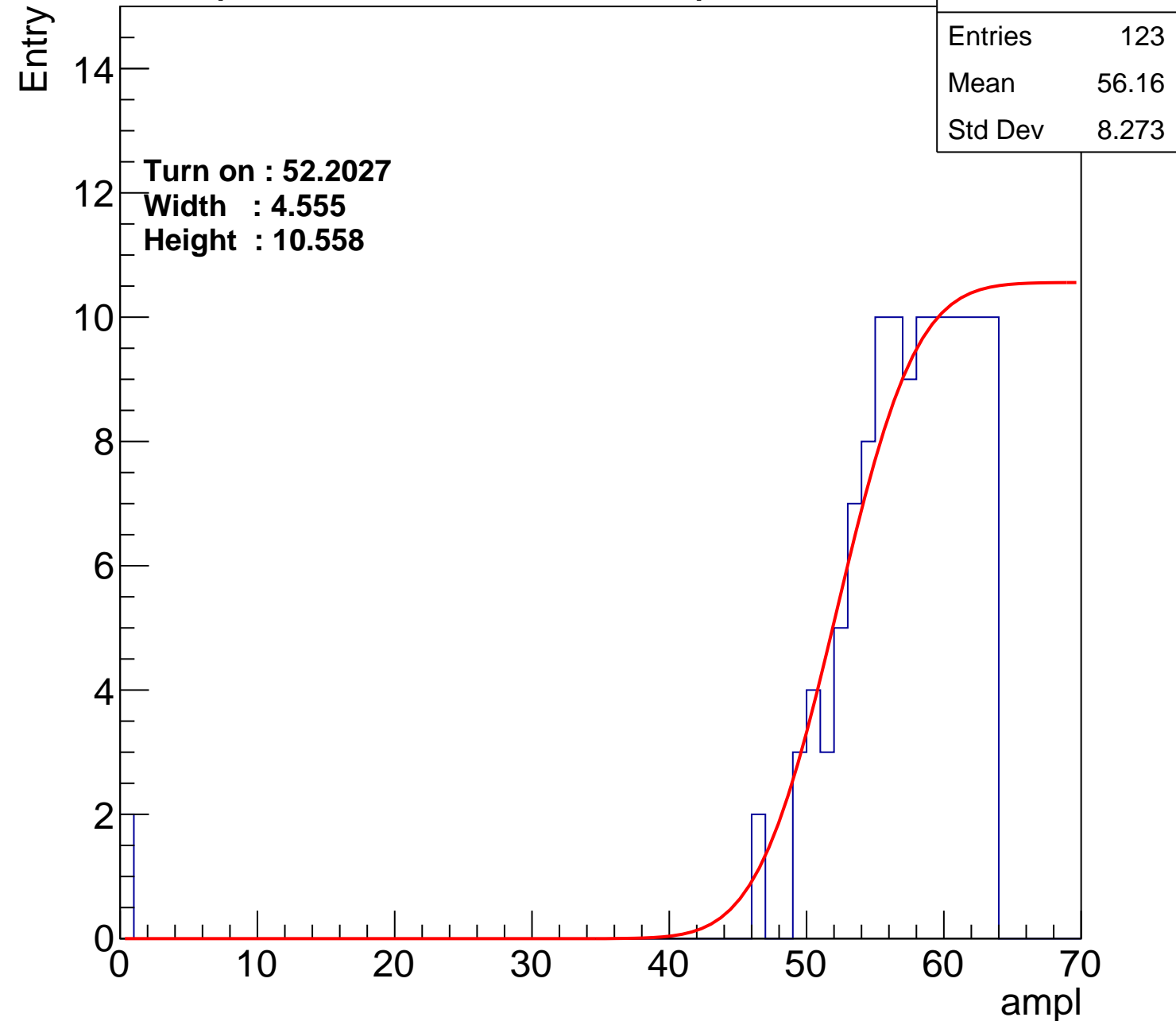
**Width : 4.555**

**Height : 10.558**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.87
Std Dev	8.004

Turn on : 50.9842

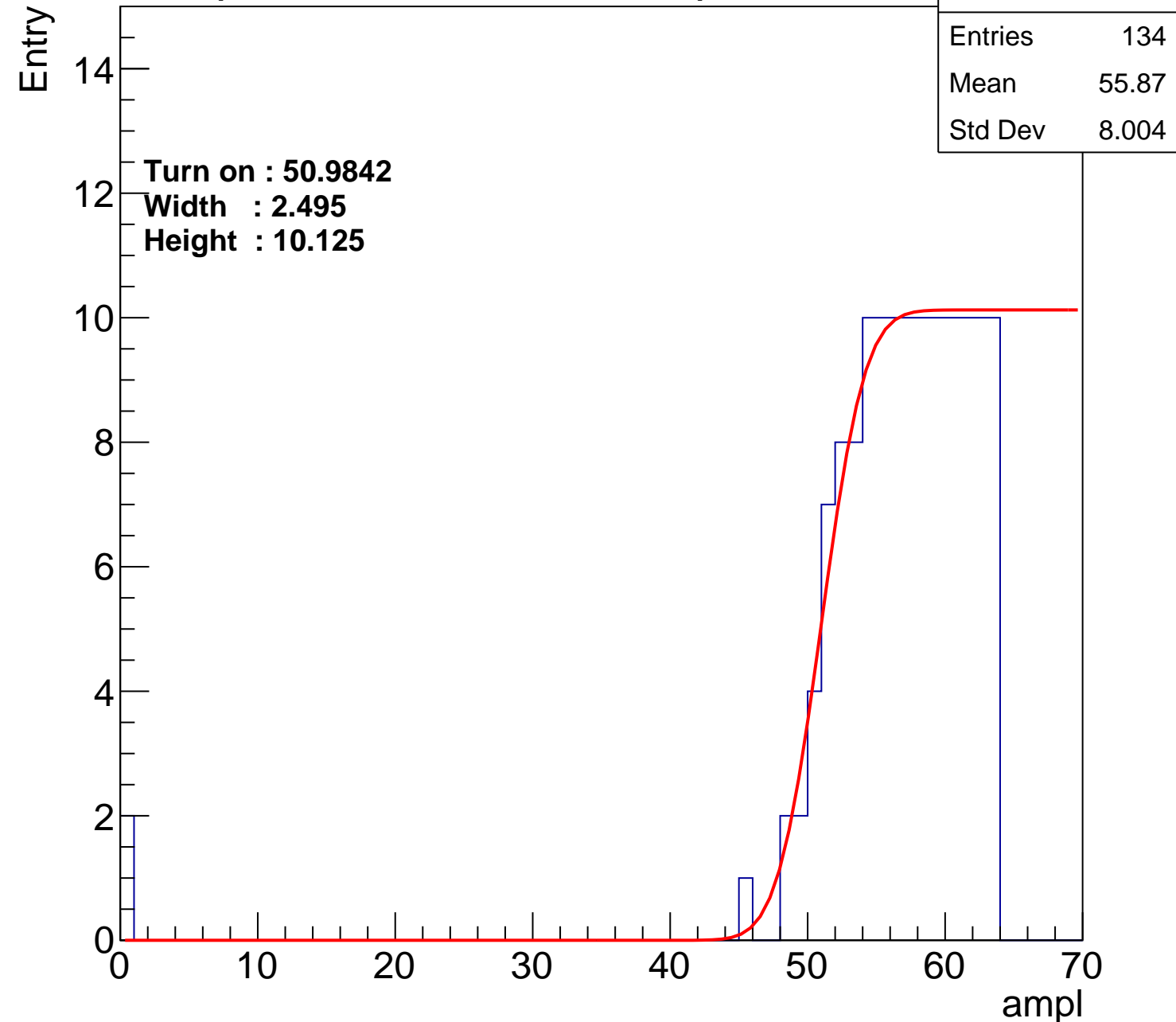
Width : 2.495

Height : 10.125

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch4

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	103
Mean	56.97
Std Dev	8.718

Turn on : 54.1687

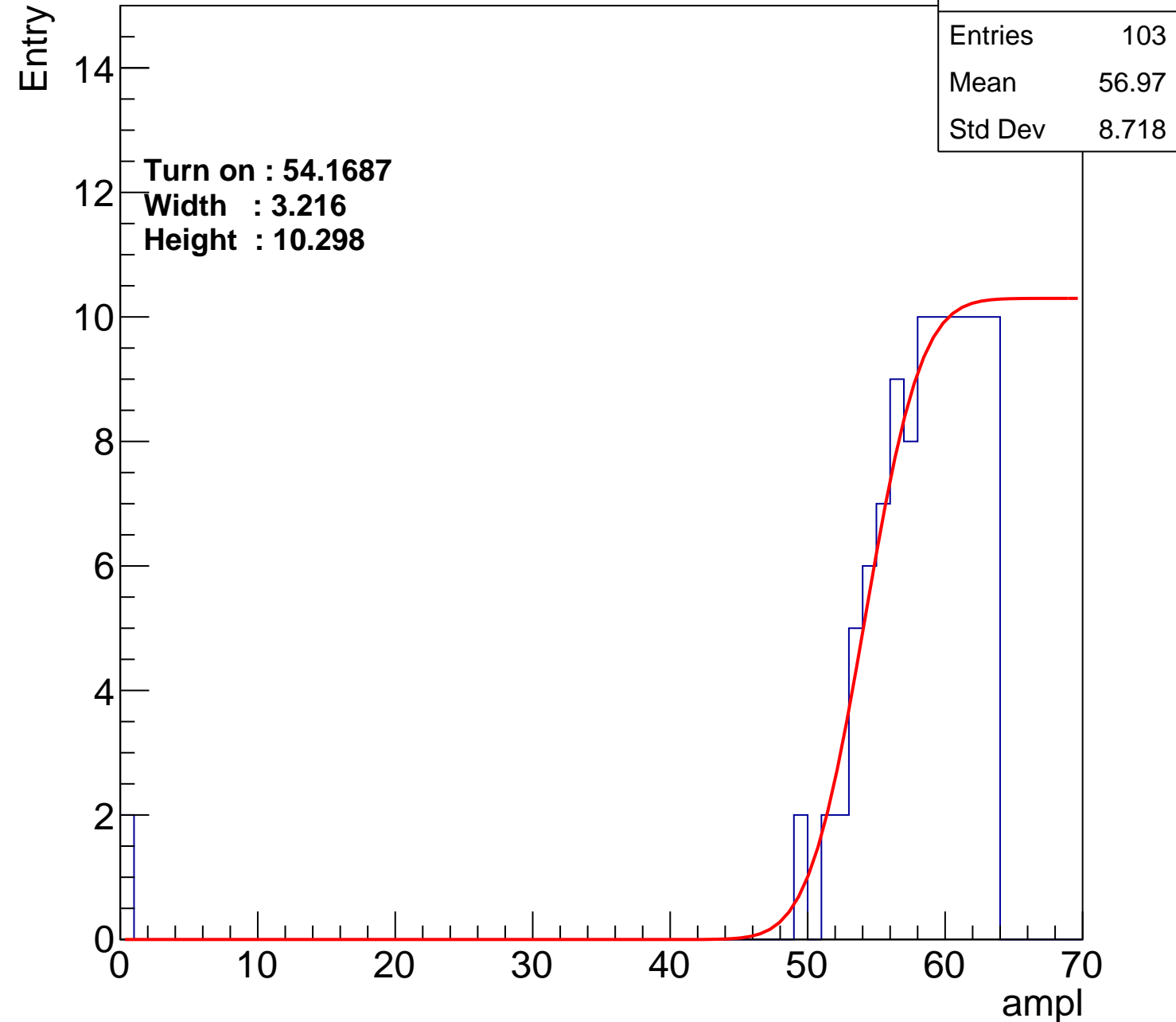
Width : 3.216

Height : 10.298

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch5

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	57.28
Std Dev	6.506

Turn on : 53.3092

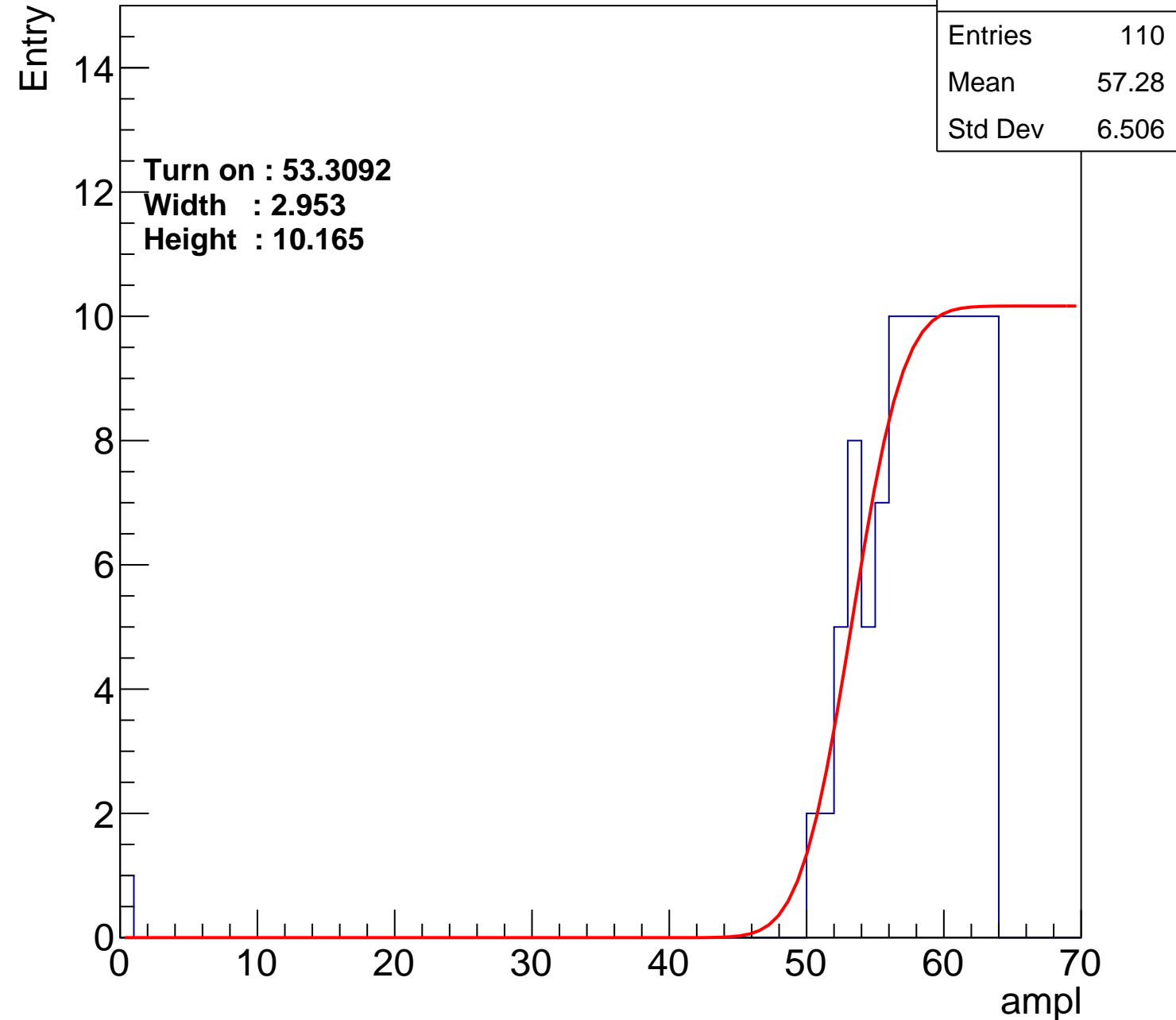
Width : 2.953

Height : 10.165

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	56.39
Std Dev	8.25

Turn on : 52.3238

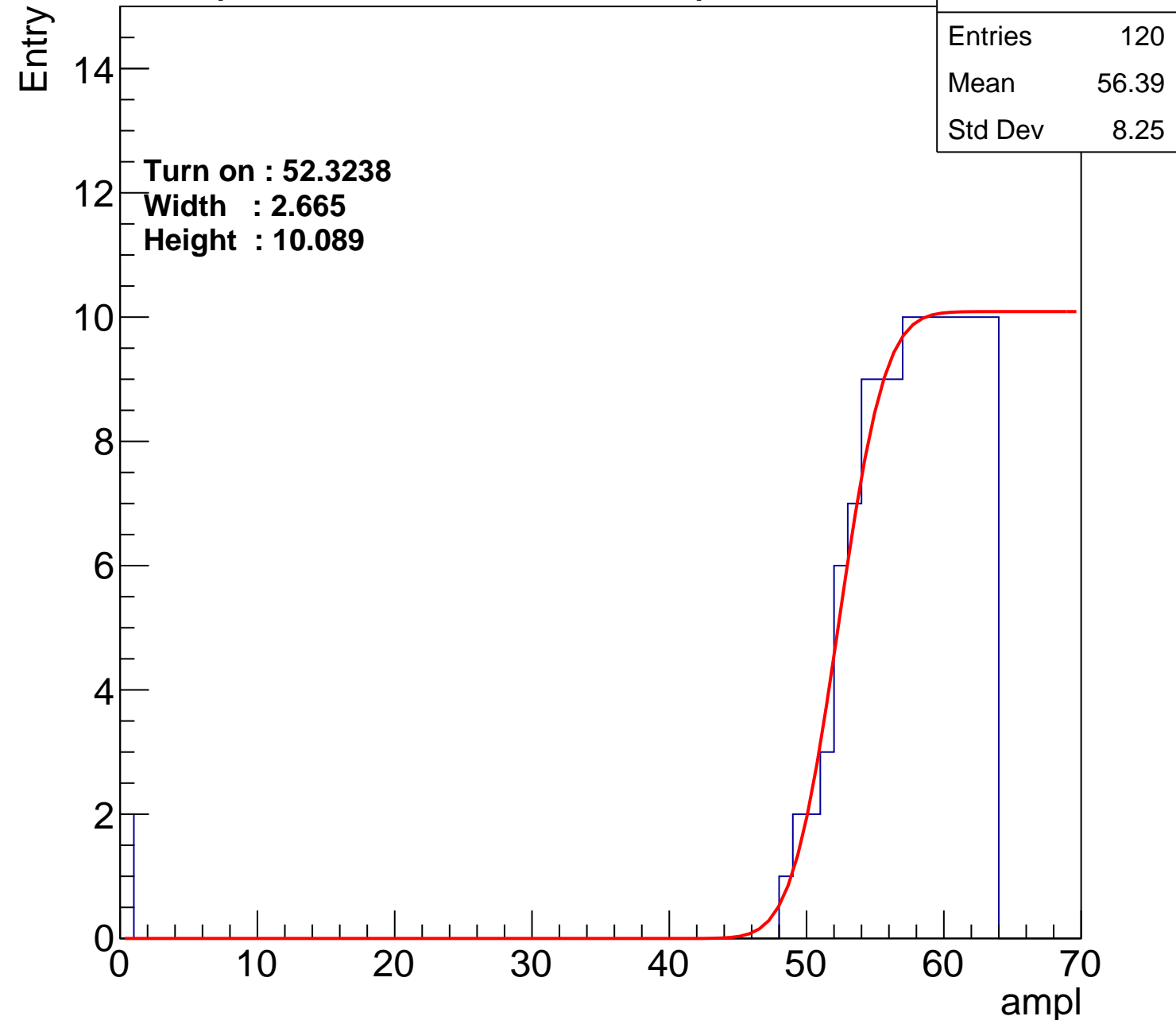
Width : 2.665

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.17
Std Dev	8.226

Turn on : 51.3181

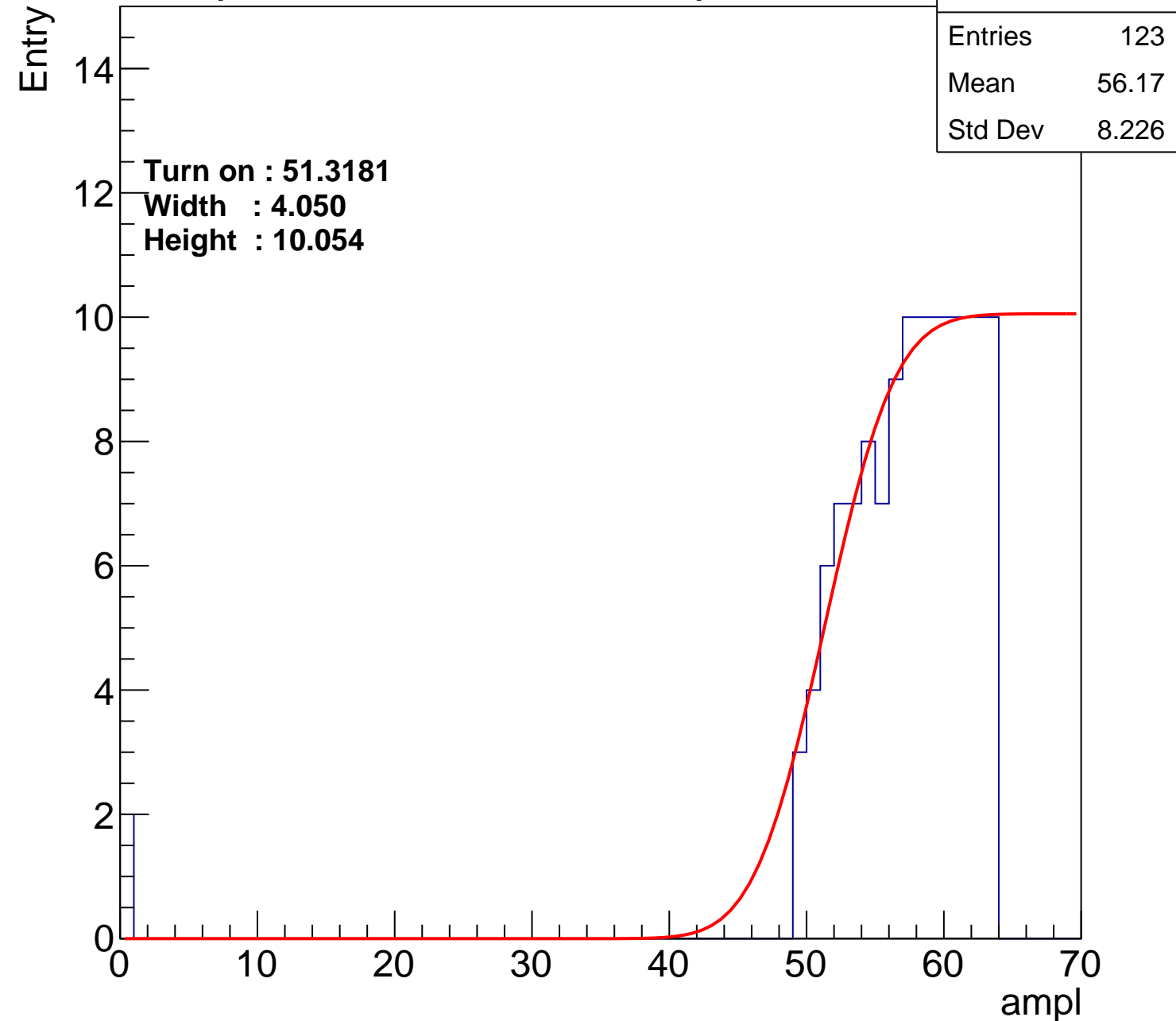
Width : 4.050

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch8

calib\_packv5\_040323\_1717.root, FC#2, port C3

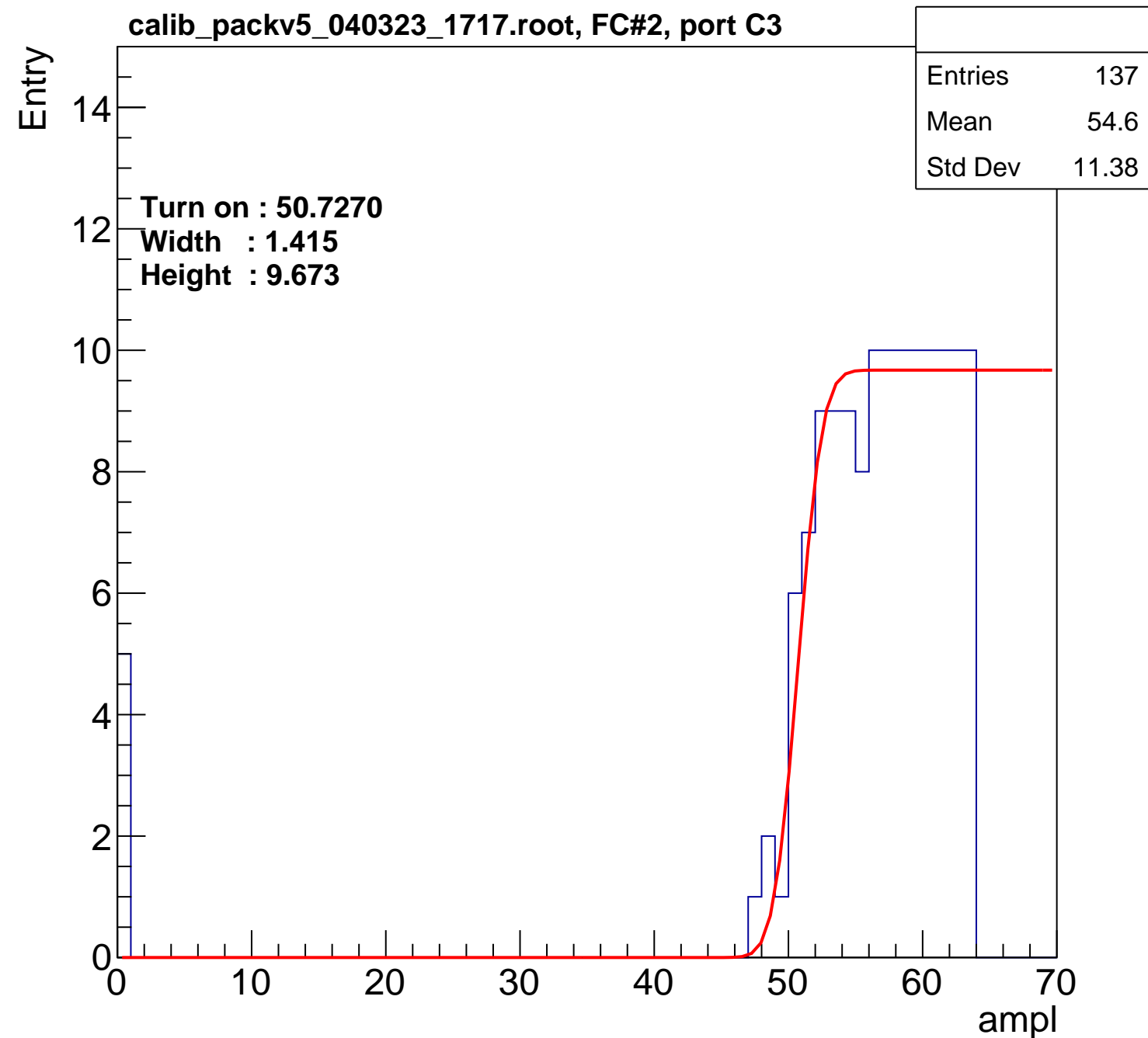
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.7270  
Width : 1.415  
Height : 9.673

Entries	137
Mean	54.6
Std Dev	11.38

ampl



# B0L103S, U26-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

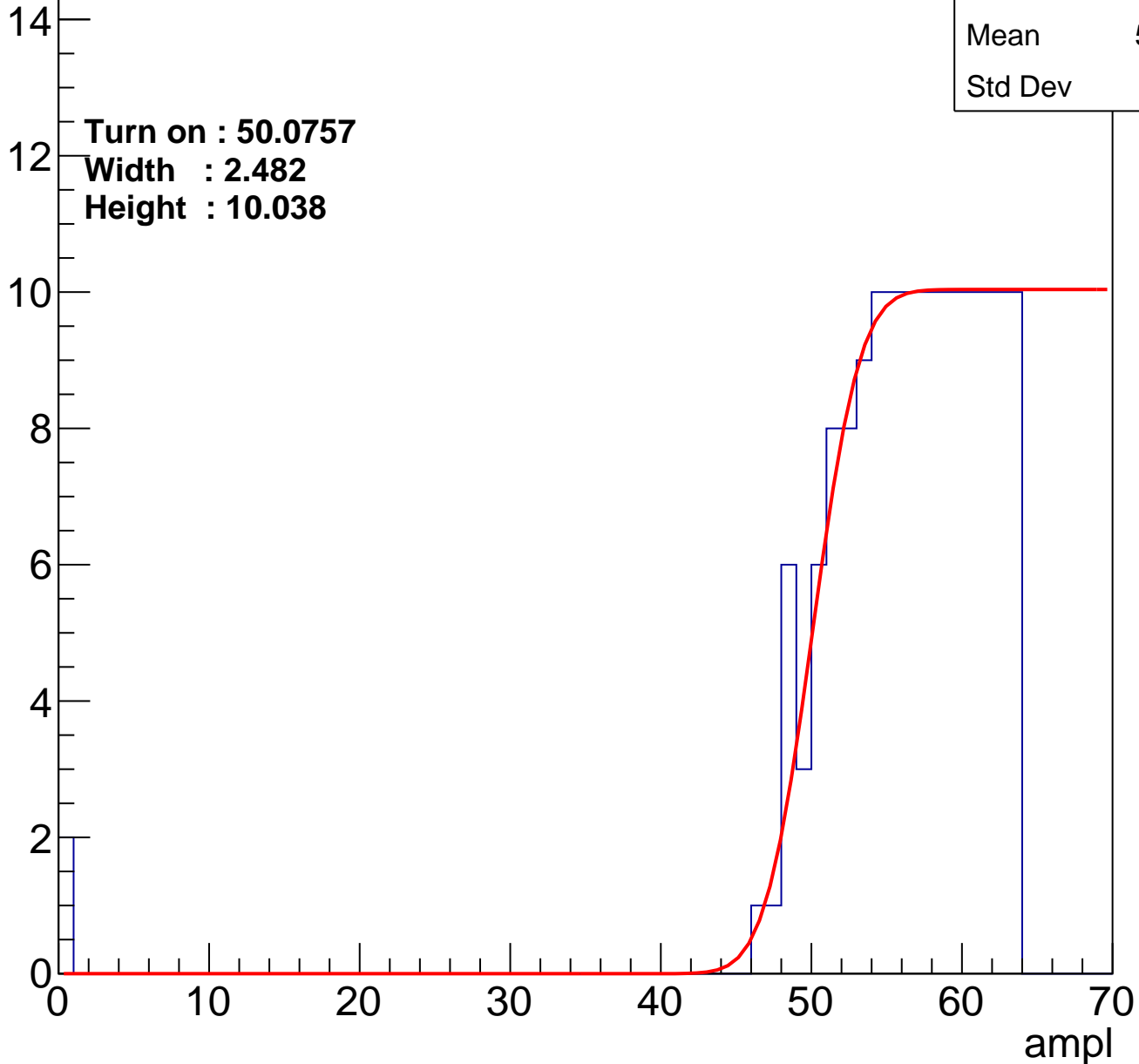
Entry

Entries	144
Mean	55.41
Std Dev	7.91

Turn on : 50.0757

Width : 2.482

Height : 10.038



# B0L103S, U26-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.83
Std Dev	8.107

Turn on : 50.9839

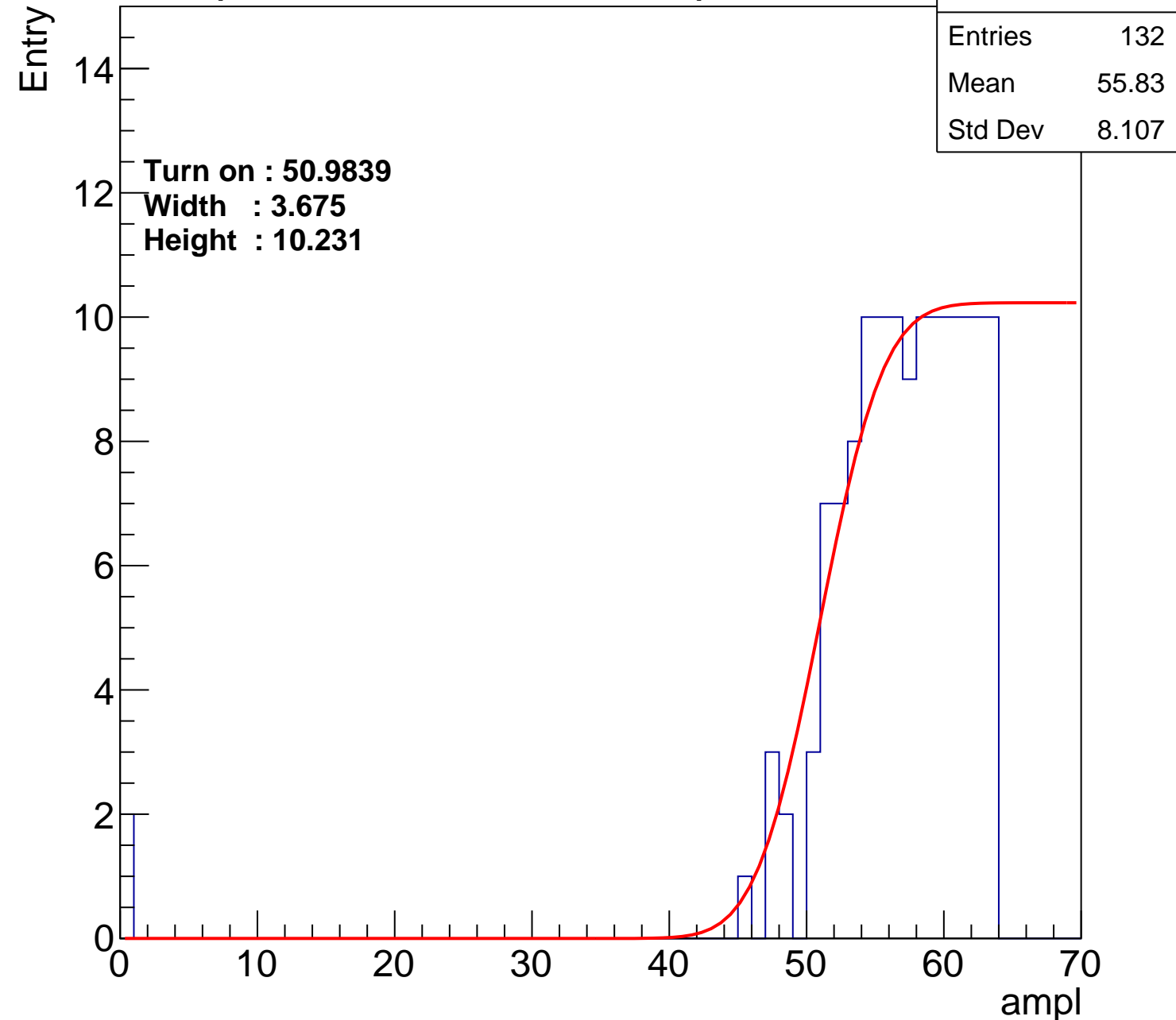
Width : 3.675

Height : 10.231

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	54.64
Std Dev	11.38

Turn on : 51.0885

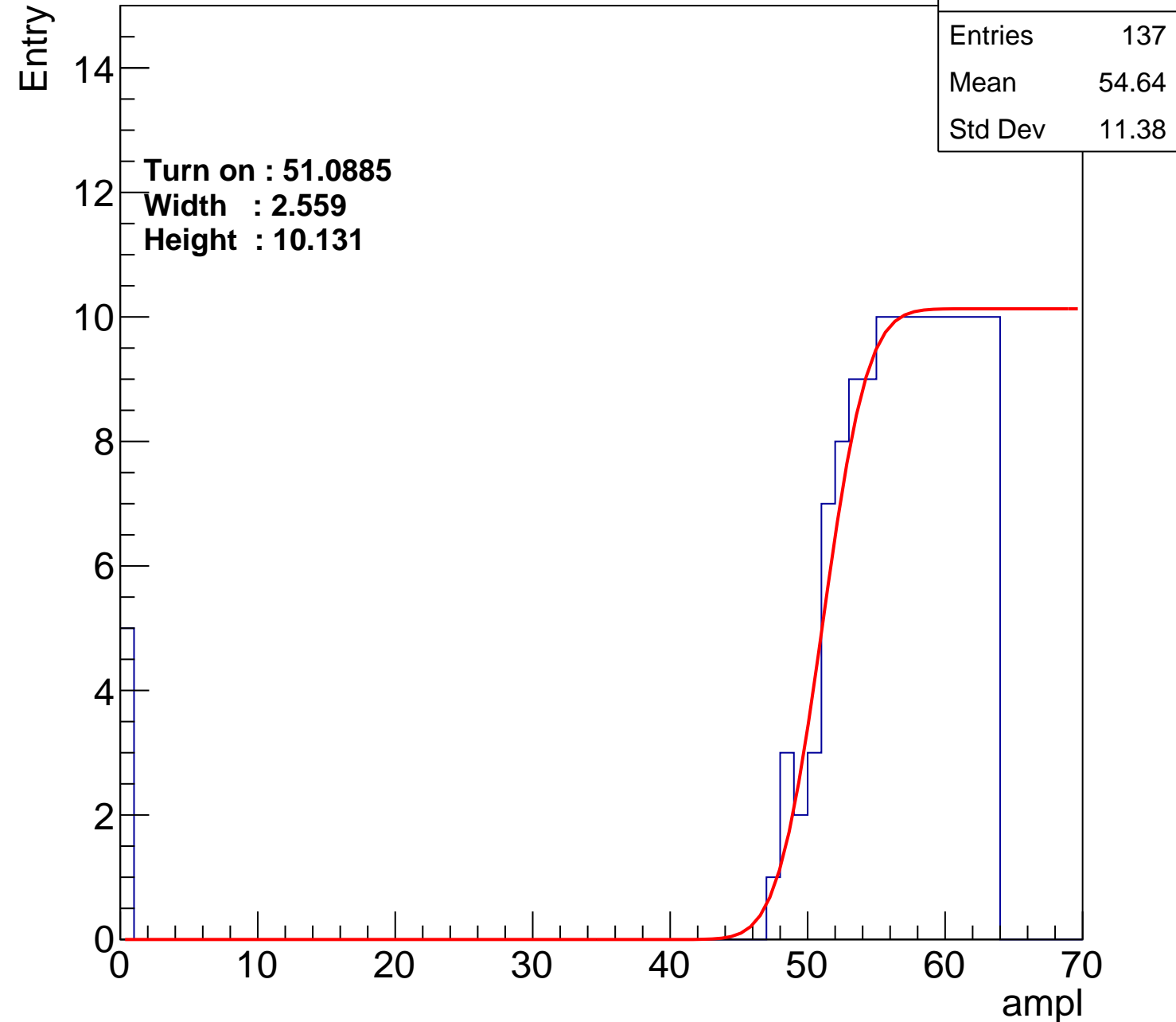
Width : 2.559

Height : 10.131

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.25
Std Dev	8.286

Turn on : 53.2897

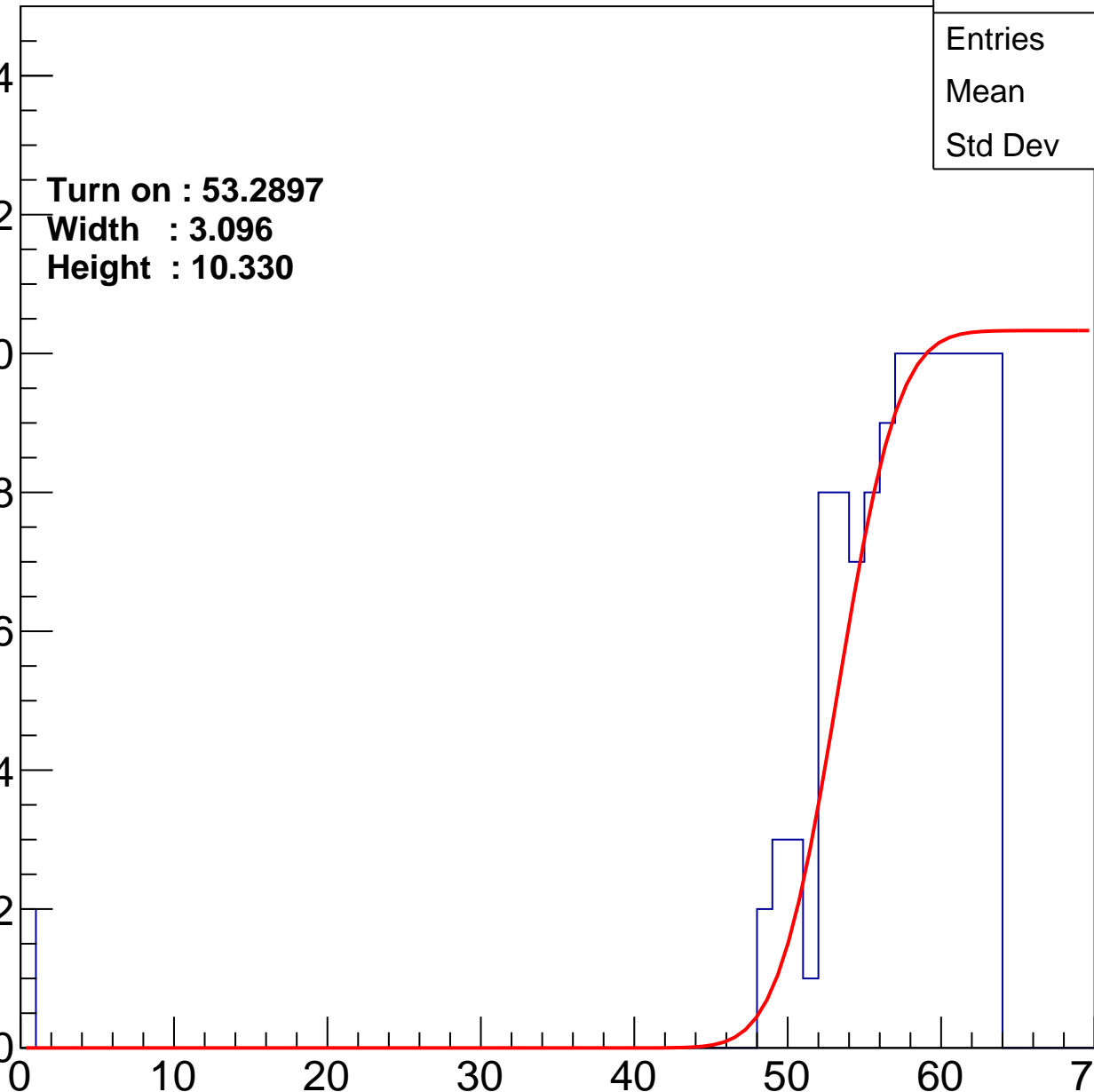
Width : 3.096

Height : 10.330

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch13

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	54.86
Std Dev	10.31

Turn on : 51.1885

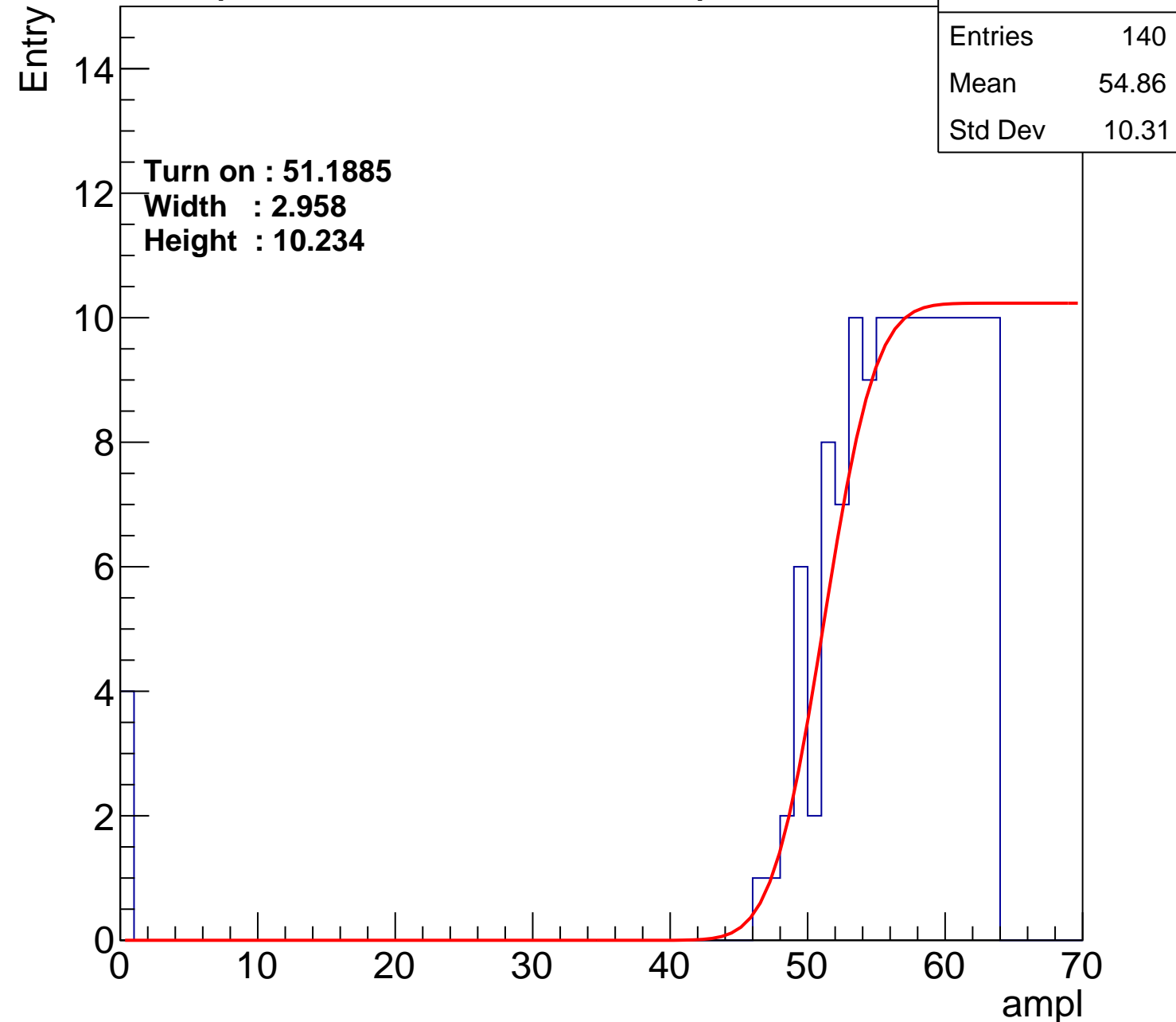
Width : 2.958

Height : 10.234

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch14

calib\_packv5\_040323\_1717.root, FC#2, port C3

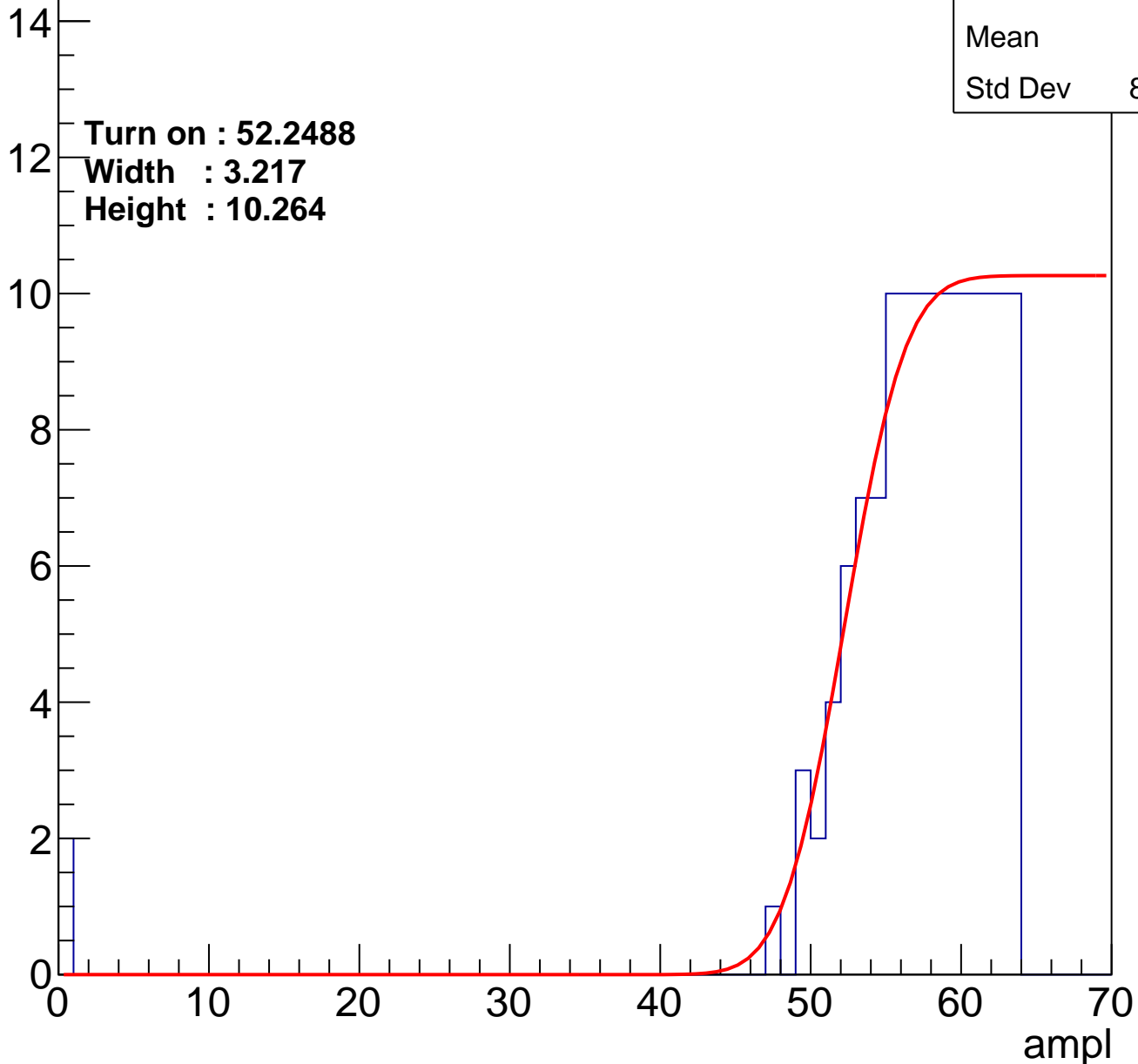
Entries	122
Mean	56.3
Std Dev	8.227

Turn on : 52.2488

Width : 3.217

Height : 10.264

Entry





# B0L103S, U26-ch15

calib\_packv5\_040323\_1717.root, FC#2, port C3

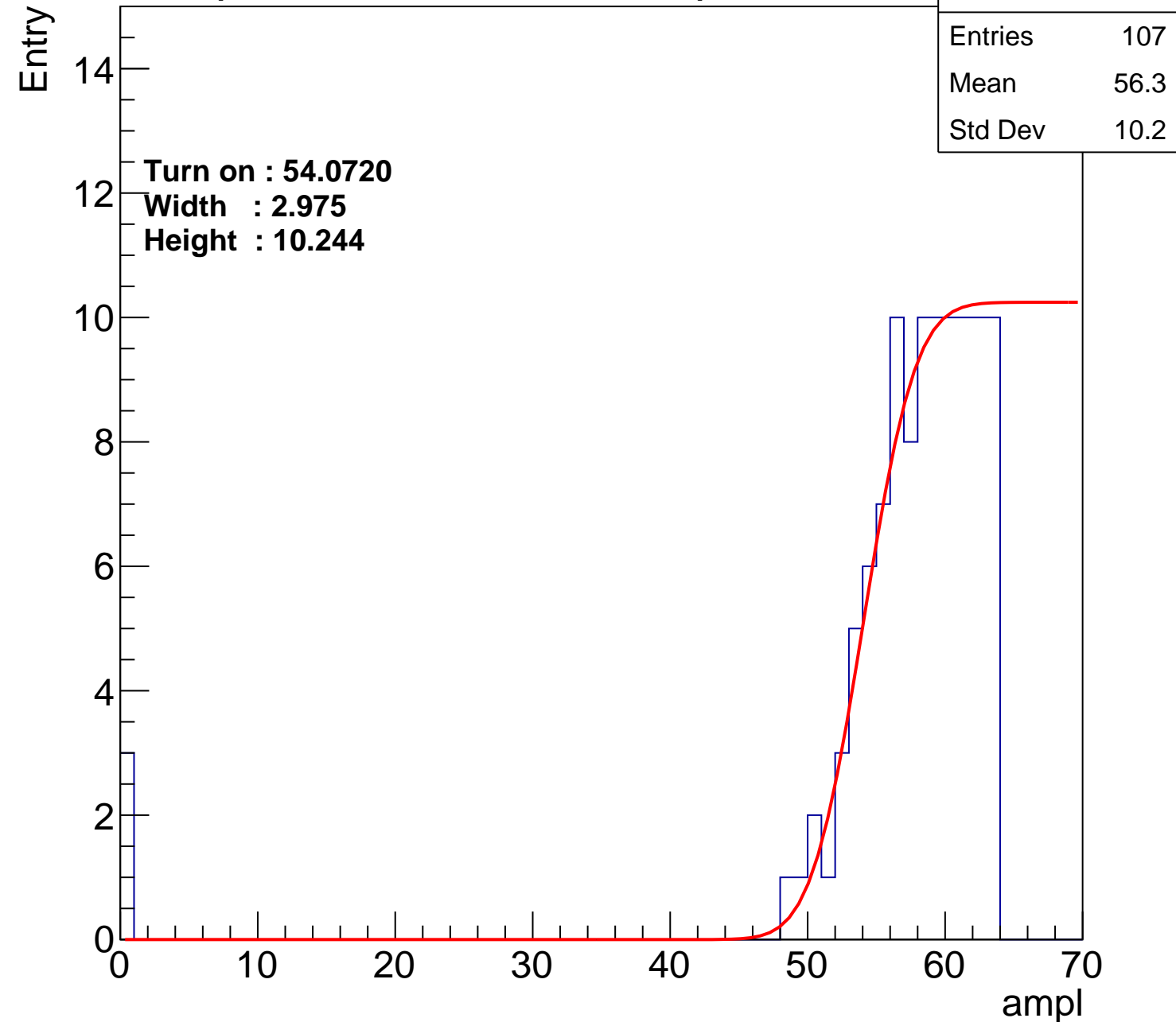
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 54.0720  
Width : 2.975  
Height : 10.244

Entries	107
Mean	56.3
Std Dev	10.2

ampl



# B0L103S, U26-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	56.51
Std Dev	6.509

Turn on : 52.6874

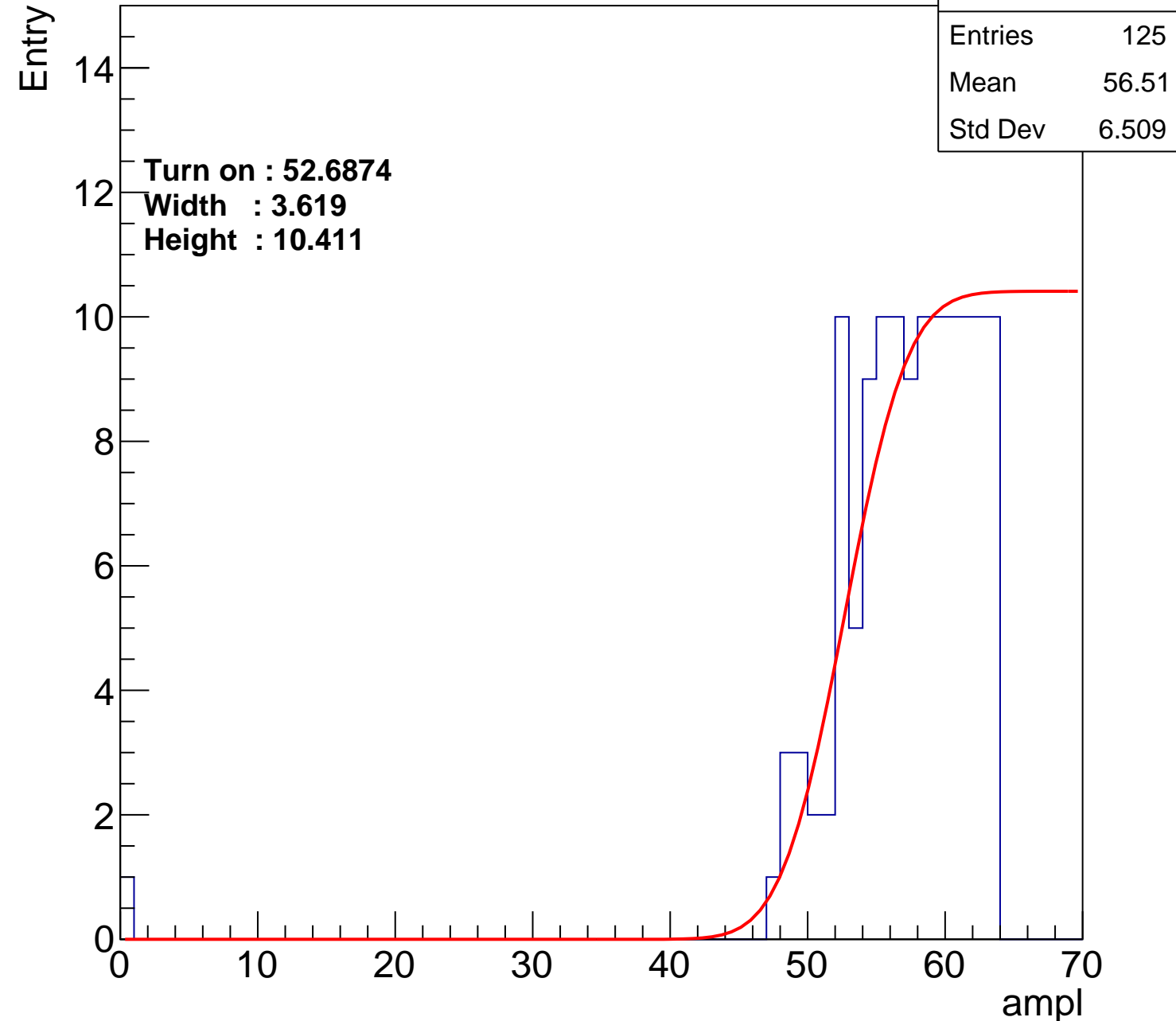
Width : 3.619

Height : 10.411

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.97
Std Dev	10.42

Turn on : 50.7078

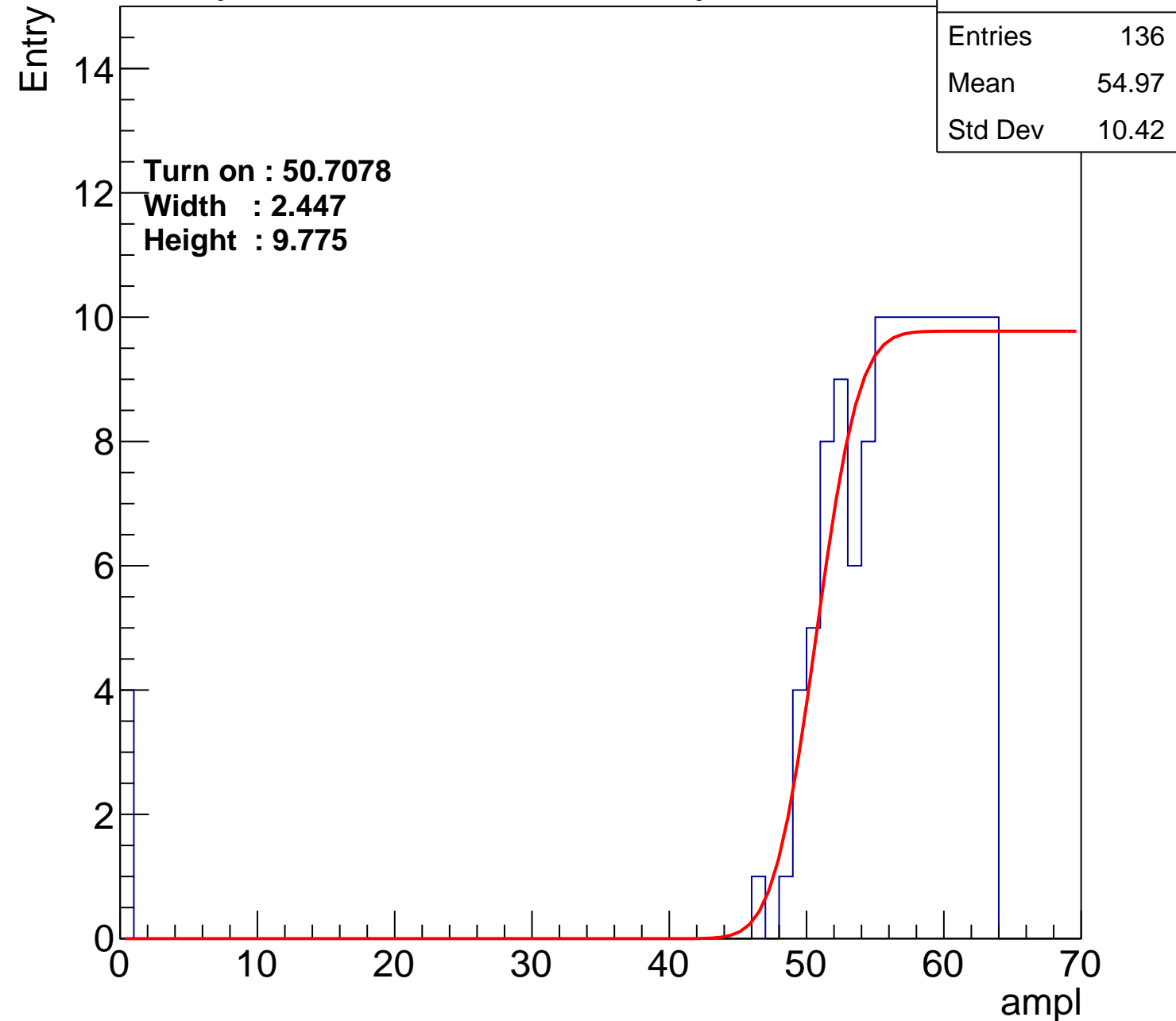
Width : 2.447

Height : 9.775

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch18

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	54.86
Std Dev	10.41

**Turn on : 51.1472**

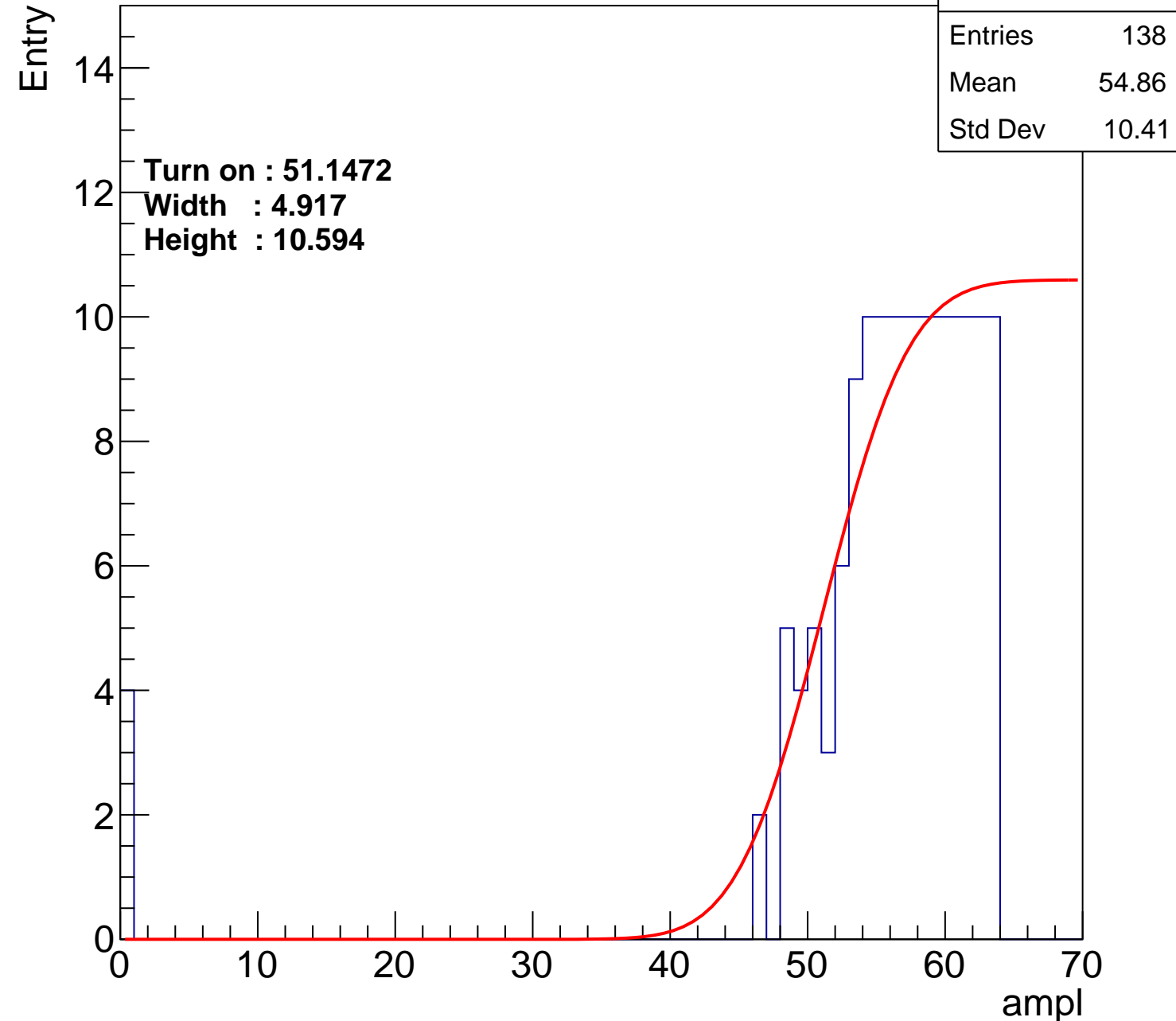
**Width : 4.917**

**Height : 10.594**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.42
Std Dev	8.28

Turn on : 52.4869

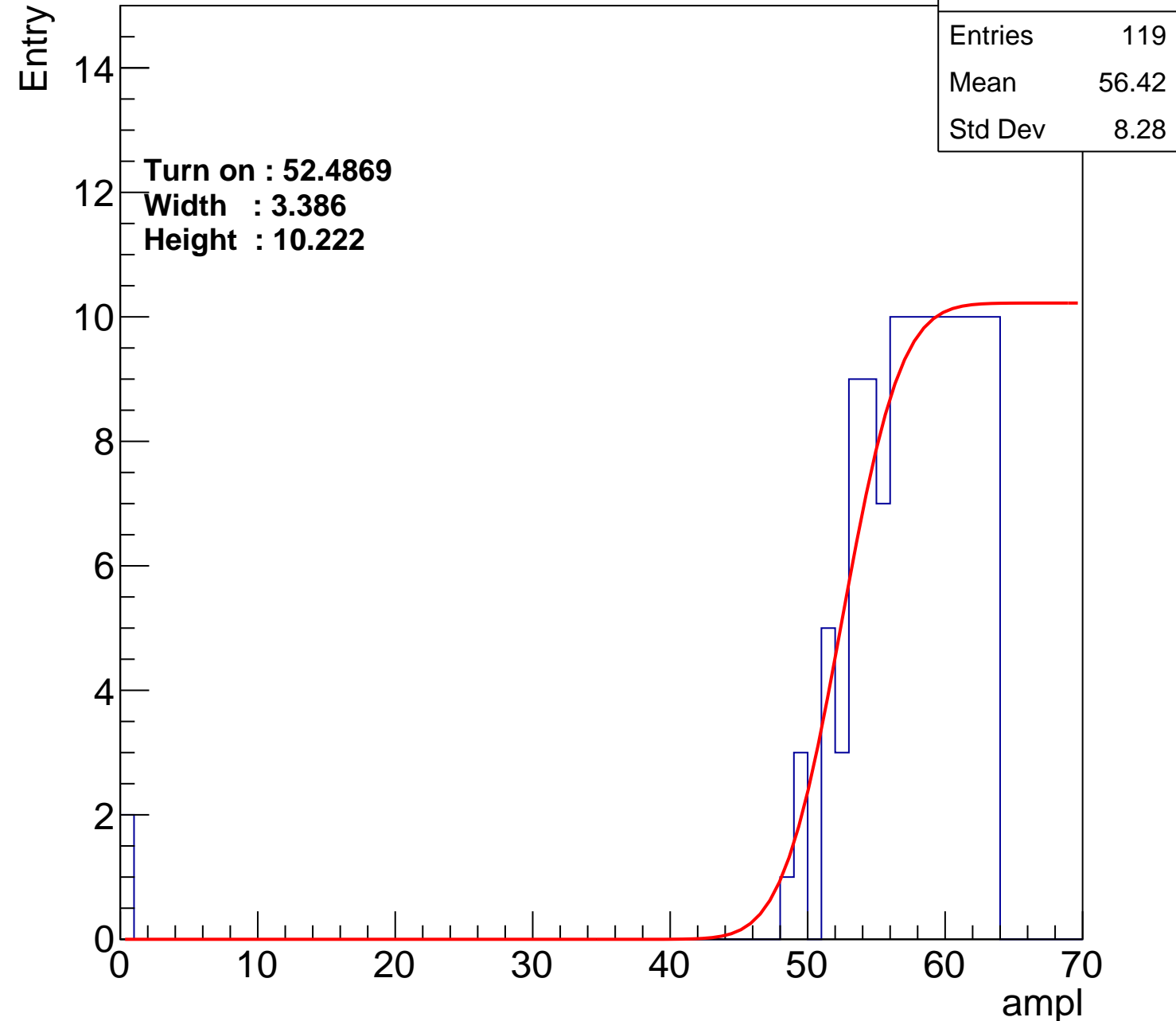
Width : 3.386

Height : 10.222

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	54.72
Std Dev	10.38

Turn on : 50.5026

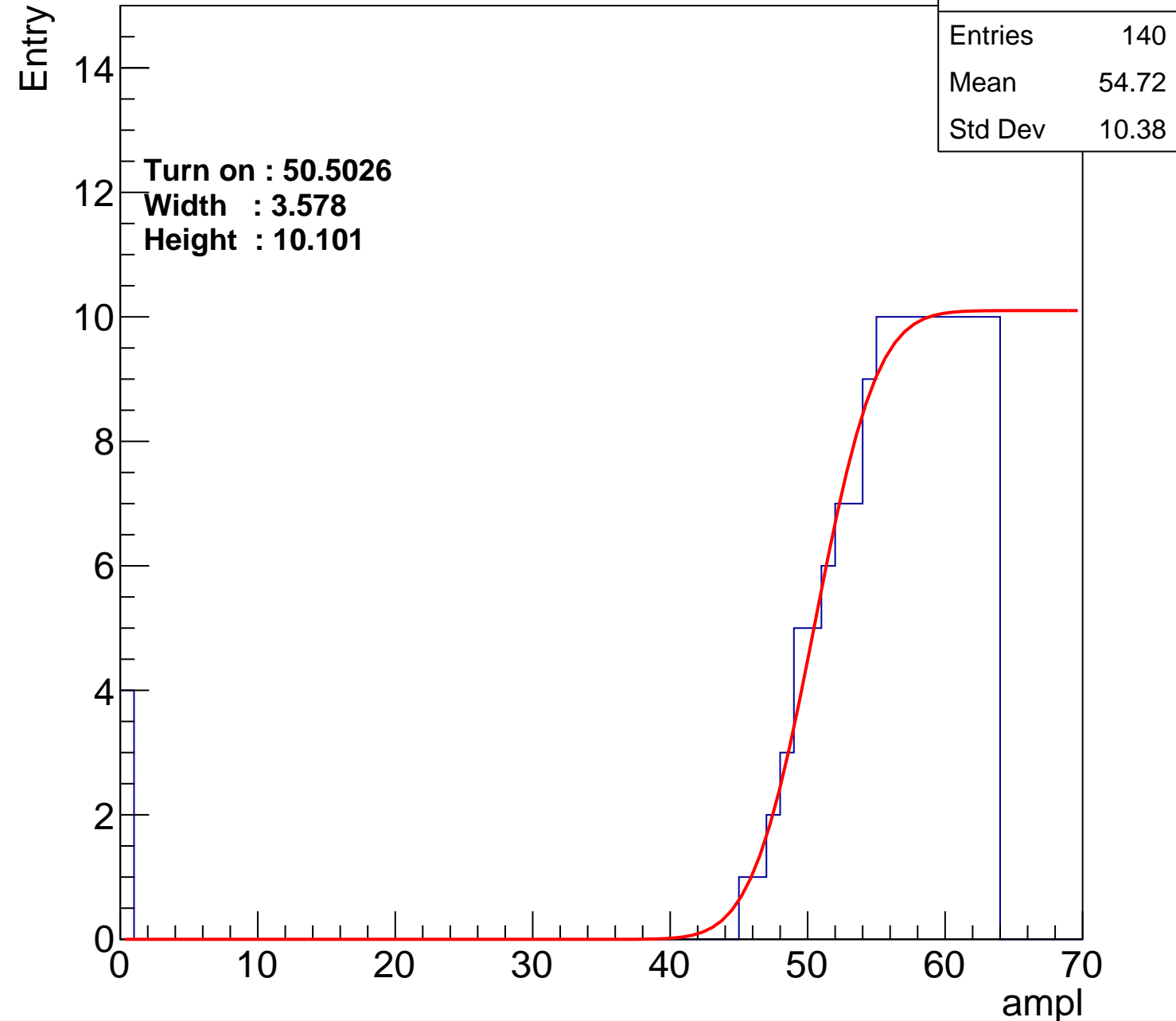
Width : 3.578

Height : 10.101

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.25
Std Dev	8.268

Turn on : 53.0188

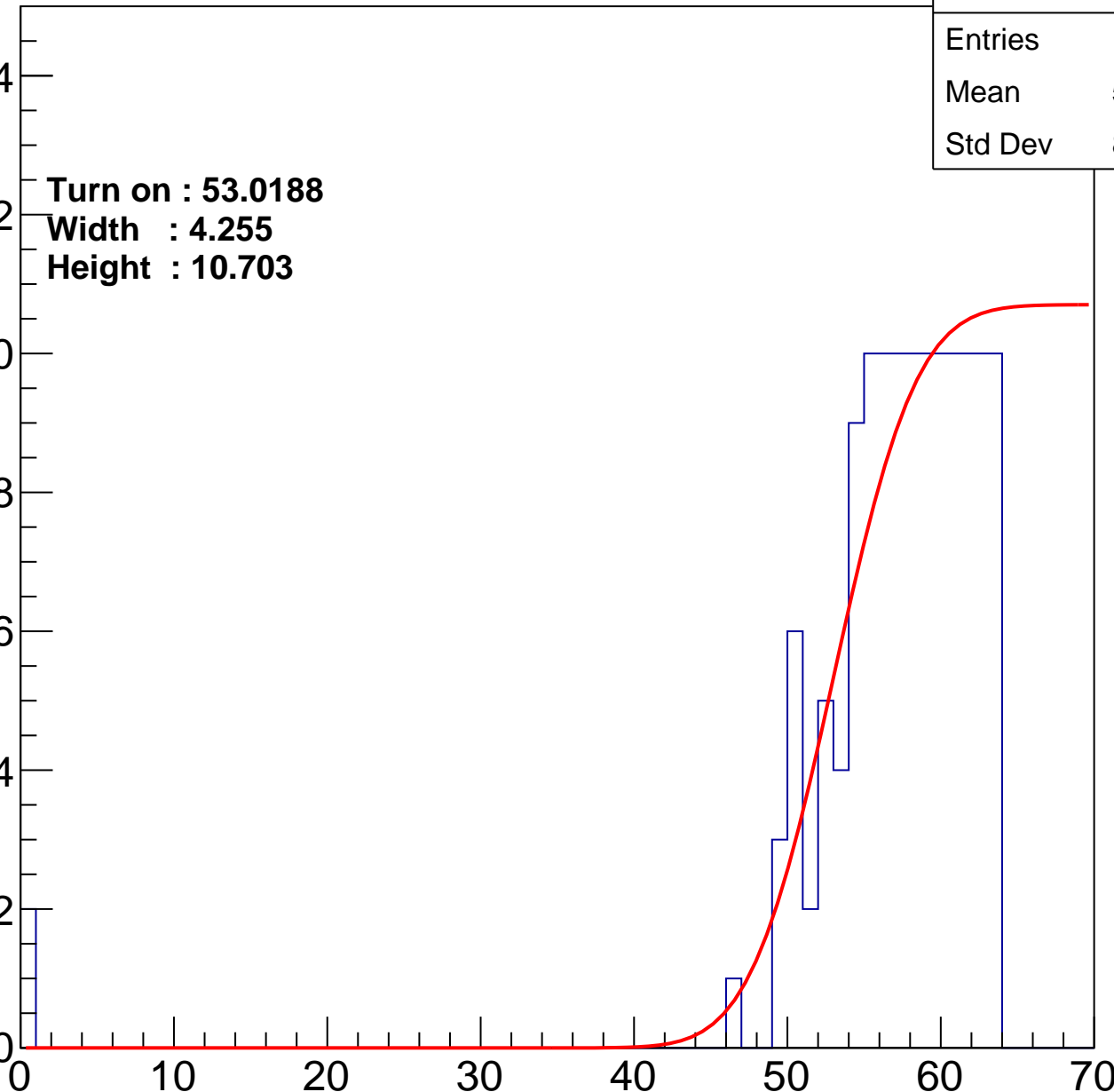
Width : 4.255

Height : 10.703

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch22

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.62
Std Dev	10.92

**Turn on : 52.5677**

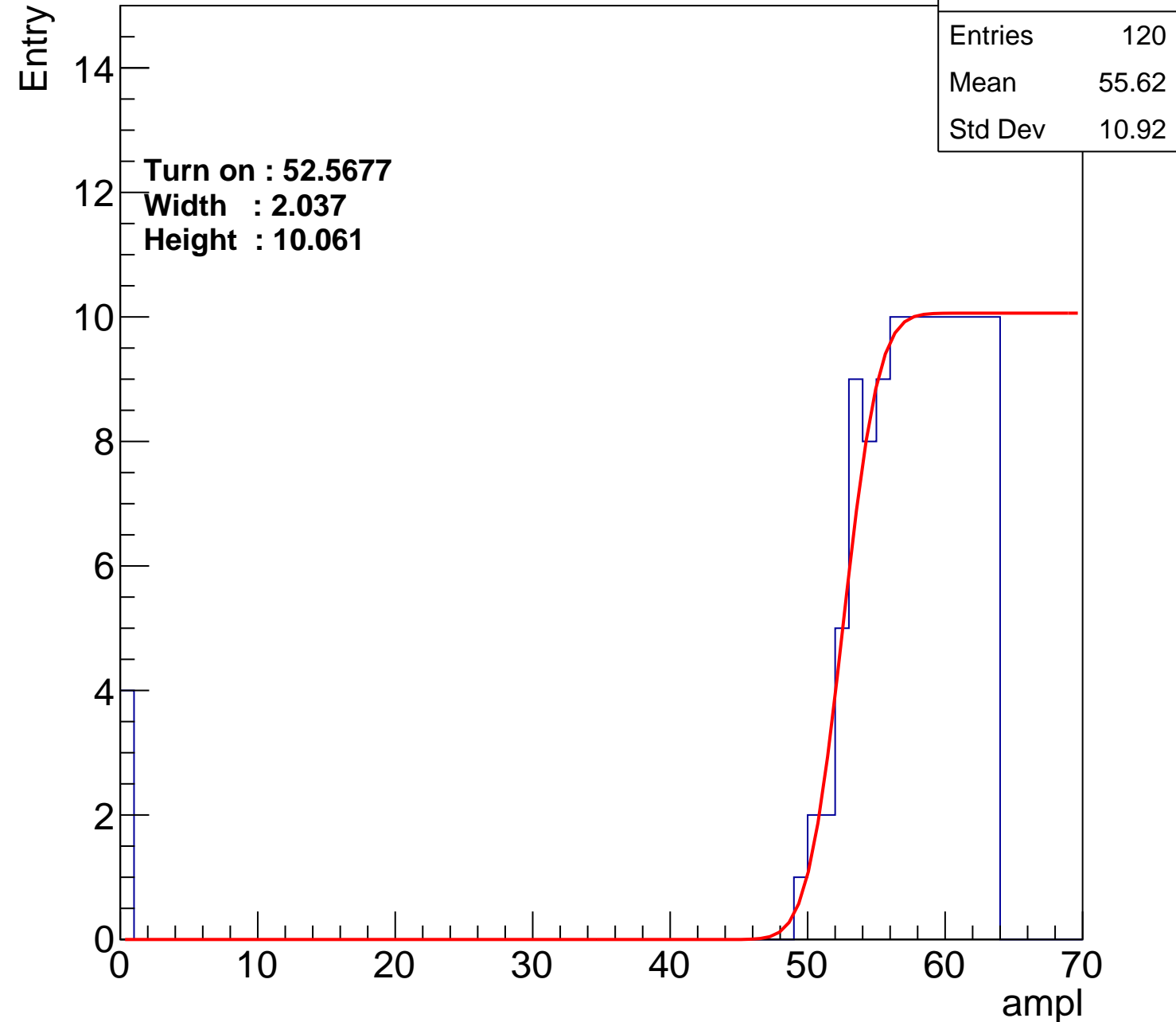
**Width : 2.037**

**Height : 10.061**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

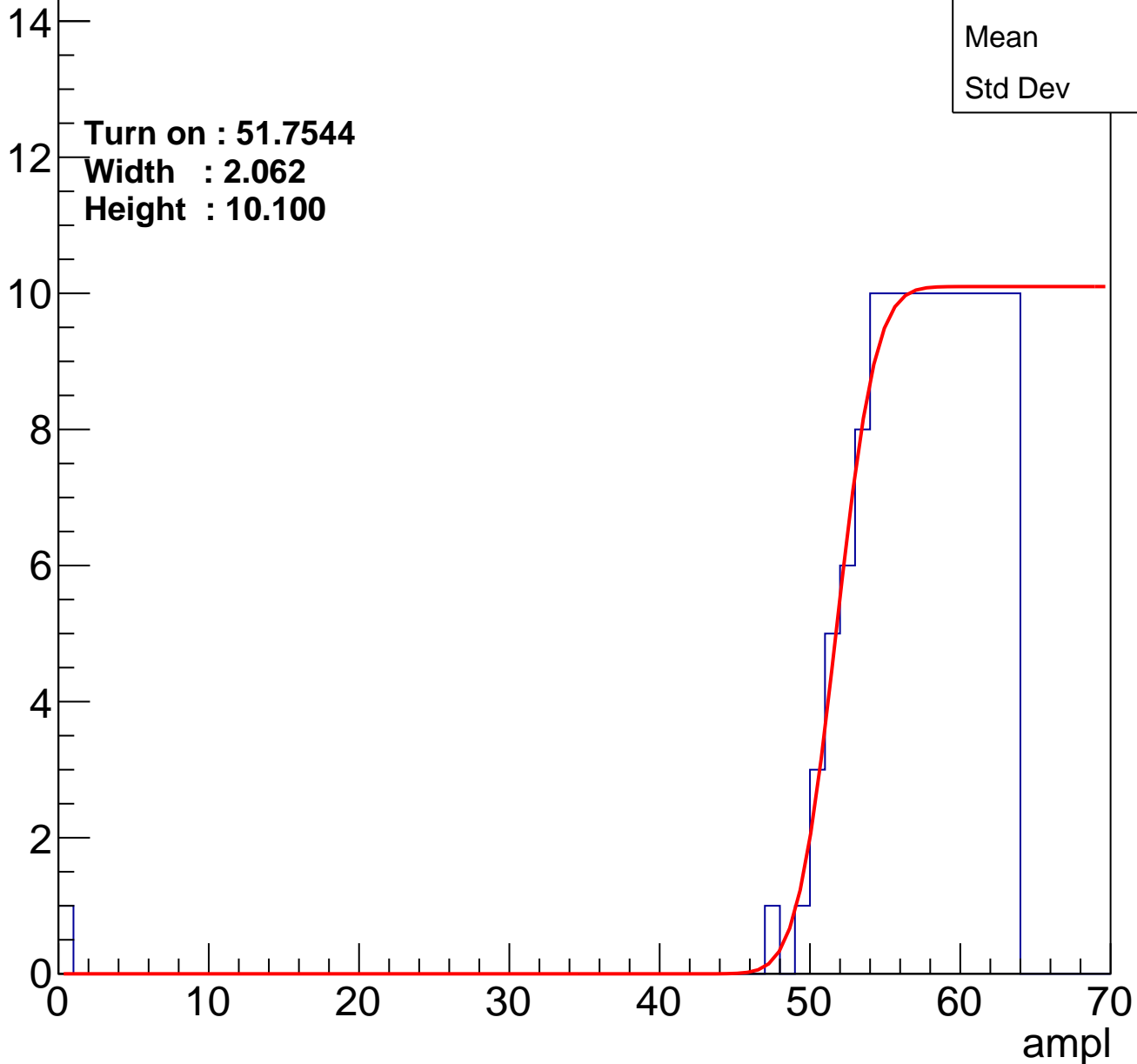
Entries	125
Mean	56.7
Std Dev	6.36

Turn on : 51.7544

Width : 2.062

Height : 10.100

Entry



# B0L103S, U26-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

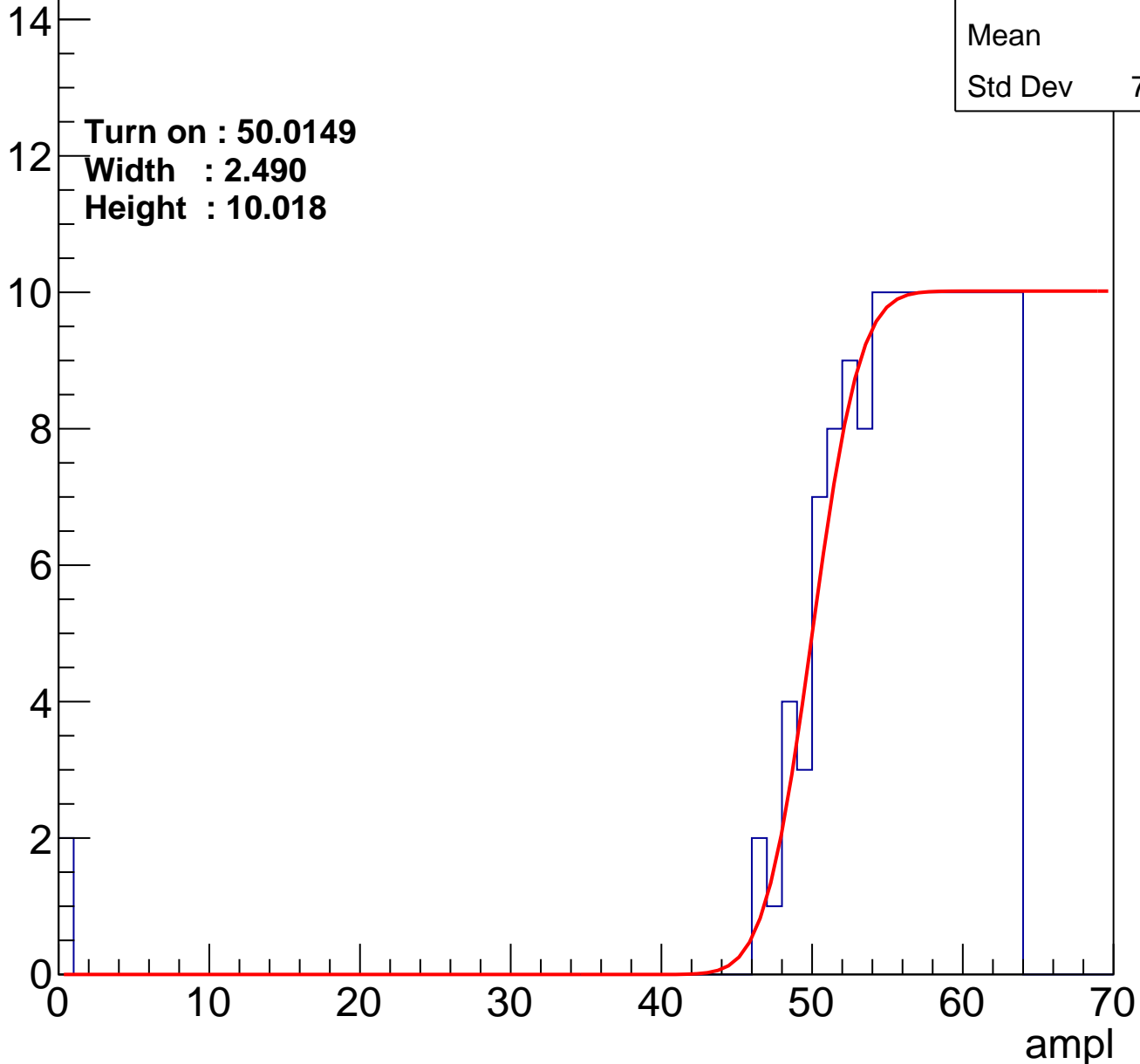
Entries	144
Mean	55.4
Std Dev	7.916

Turn on : 50.0149

Width : 2.490

Height : 10.018

Entry



# B0L103S, U26-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

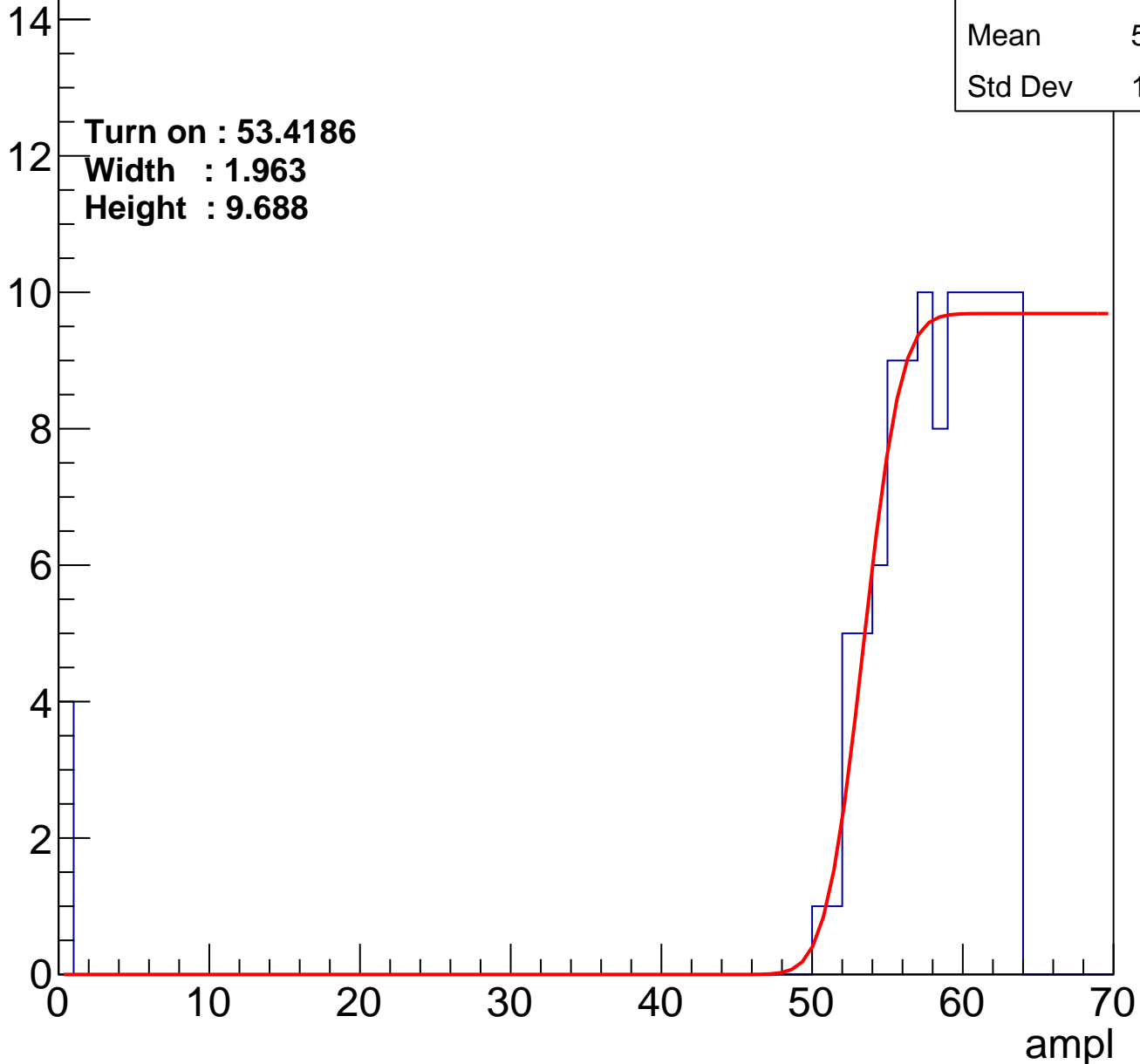
Entries	108
Mean	55.86
Std Dev	11.45

Turn on : 53.4186

Width : 1.963

Height : 9.688

Entry



# B0L103S, U26-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	100
Mean	57.72
Std Dev	6.659

Turn on : 54.8588

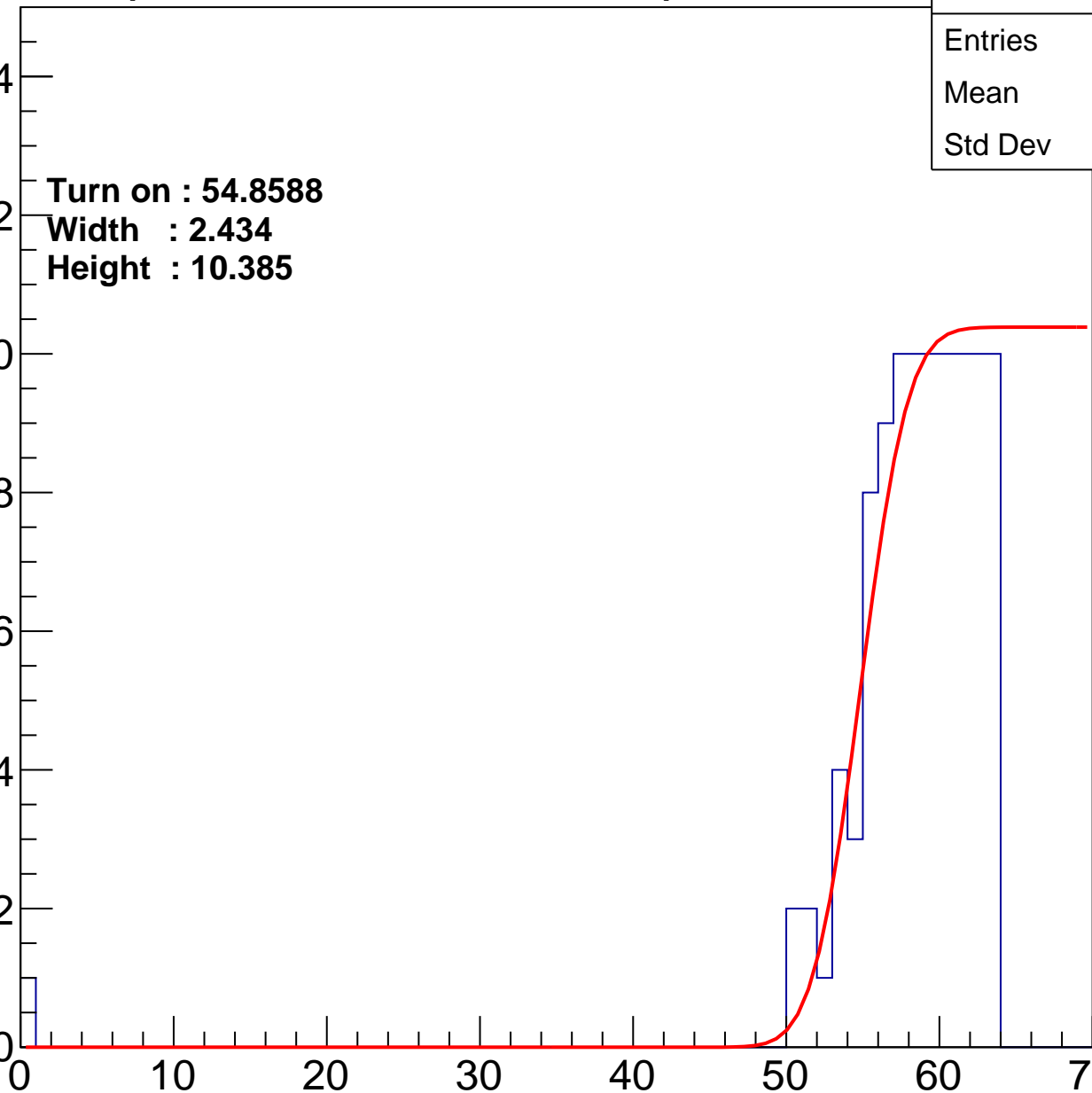
Width : 2.434

Height : 10.385

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.5
Std Dev	10.92

Turn on : 52.1922

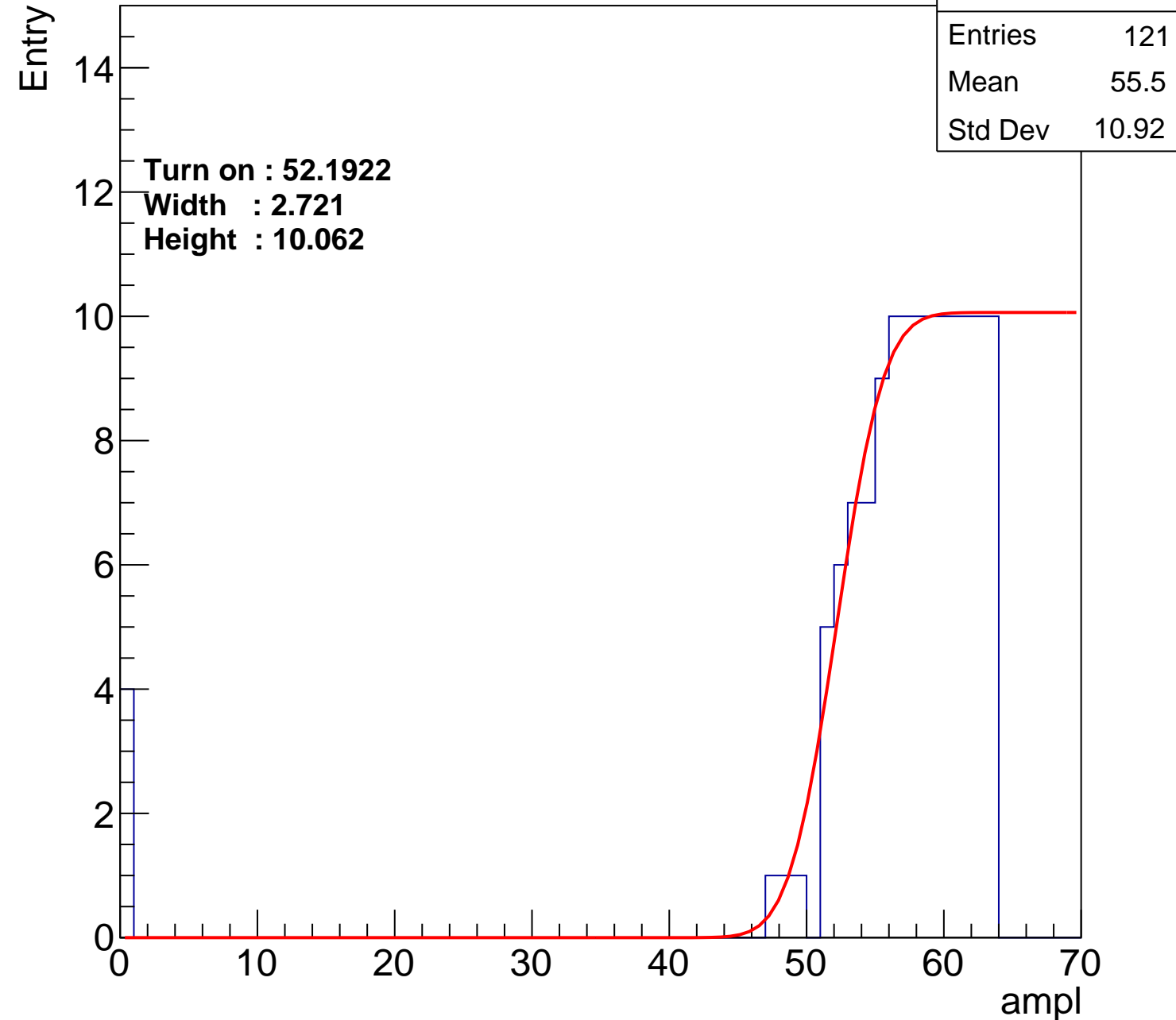
Width : 2.721

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

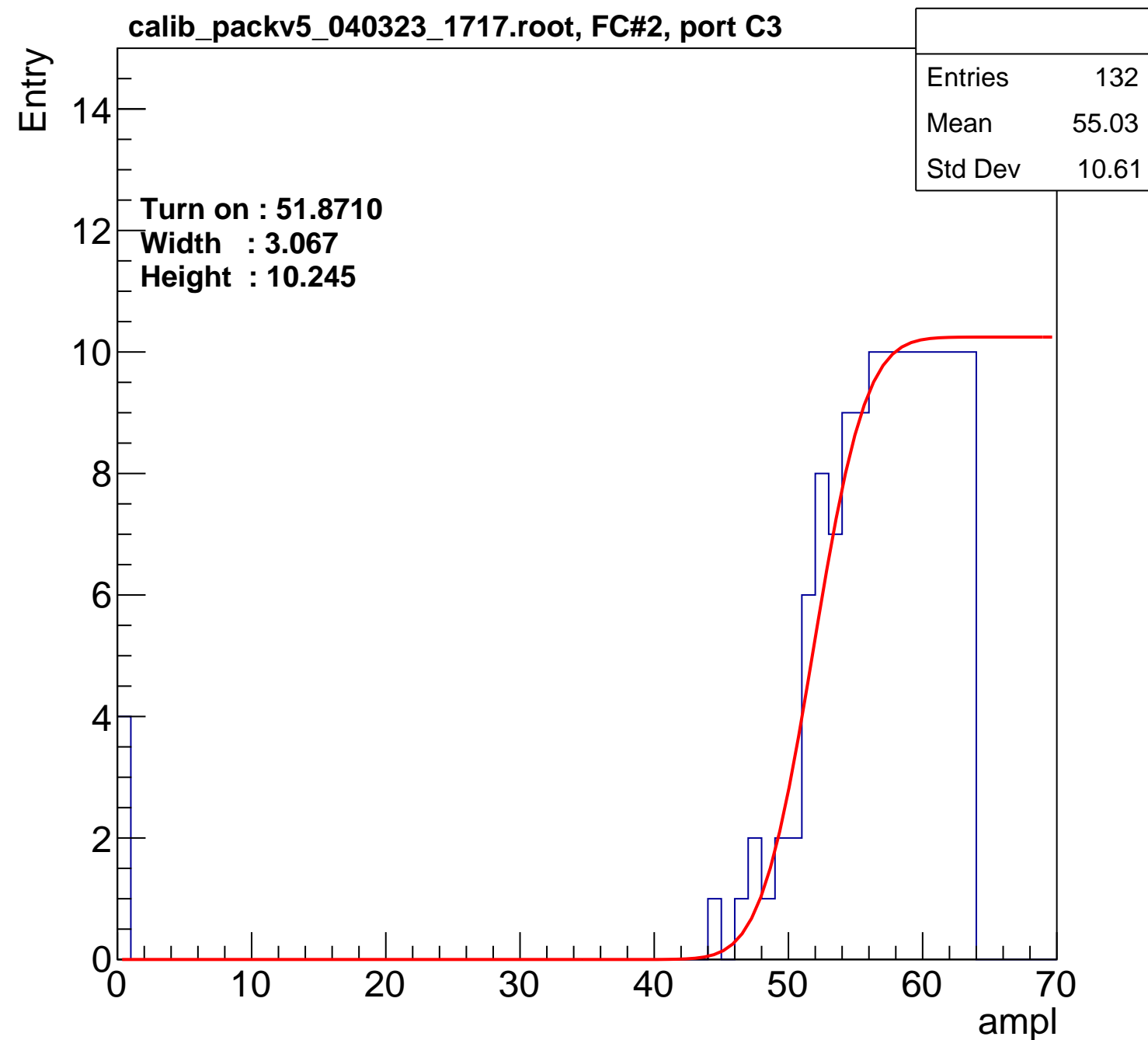
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 51.8710  
Width : 3.067  
Height : 10.245

Entries	132
Mean	55.03
Std Dev	10.61

ampl



# B0L103S, U26-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.39
Std Dev	9.437

Turn on : 50.9124

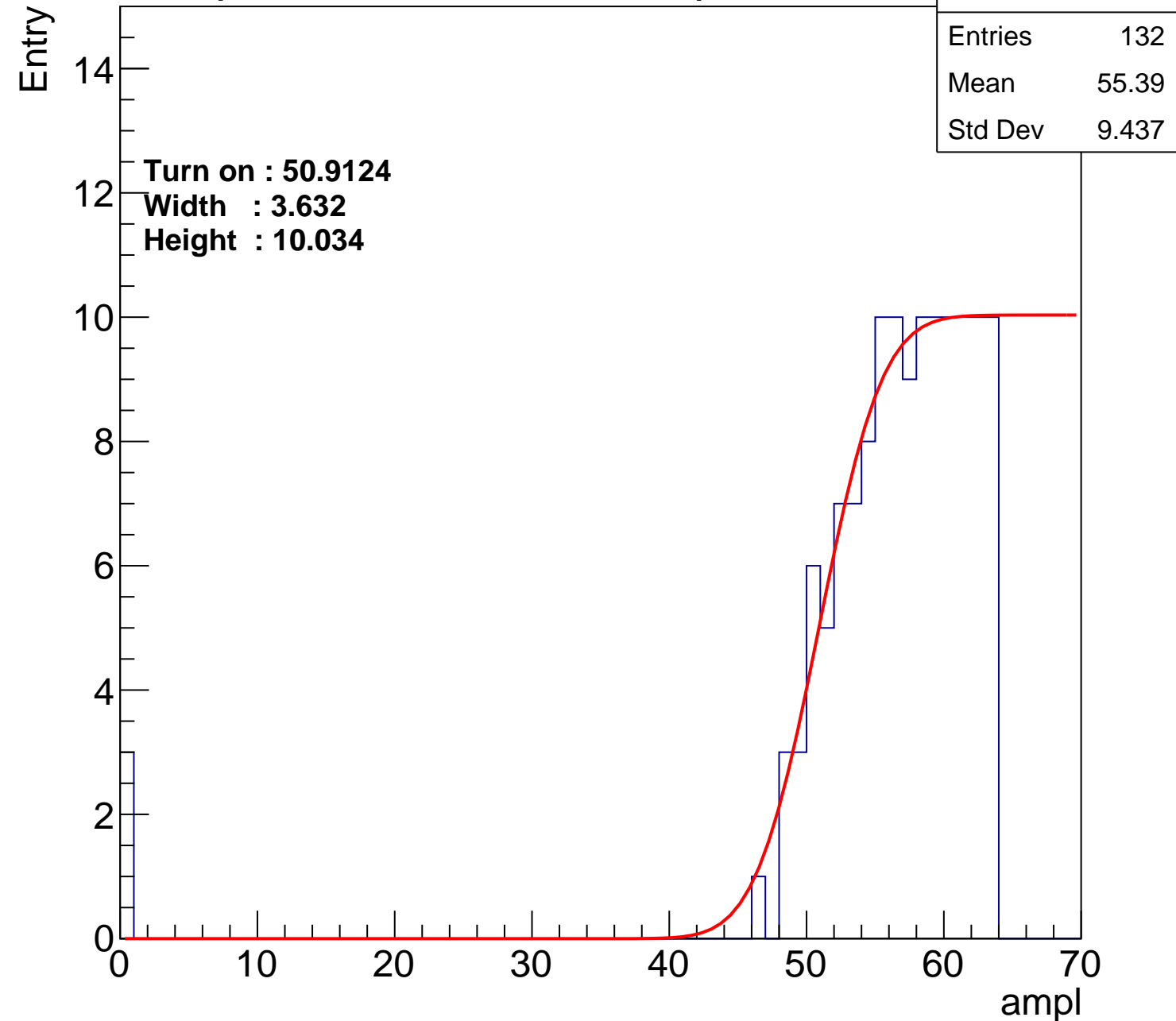
Width : 3.632

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	55.82
Std Dev	6.379

Turn on : 49.9283

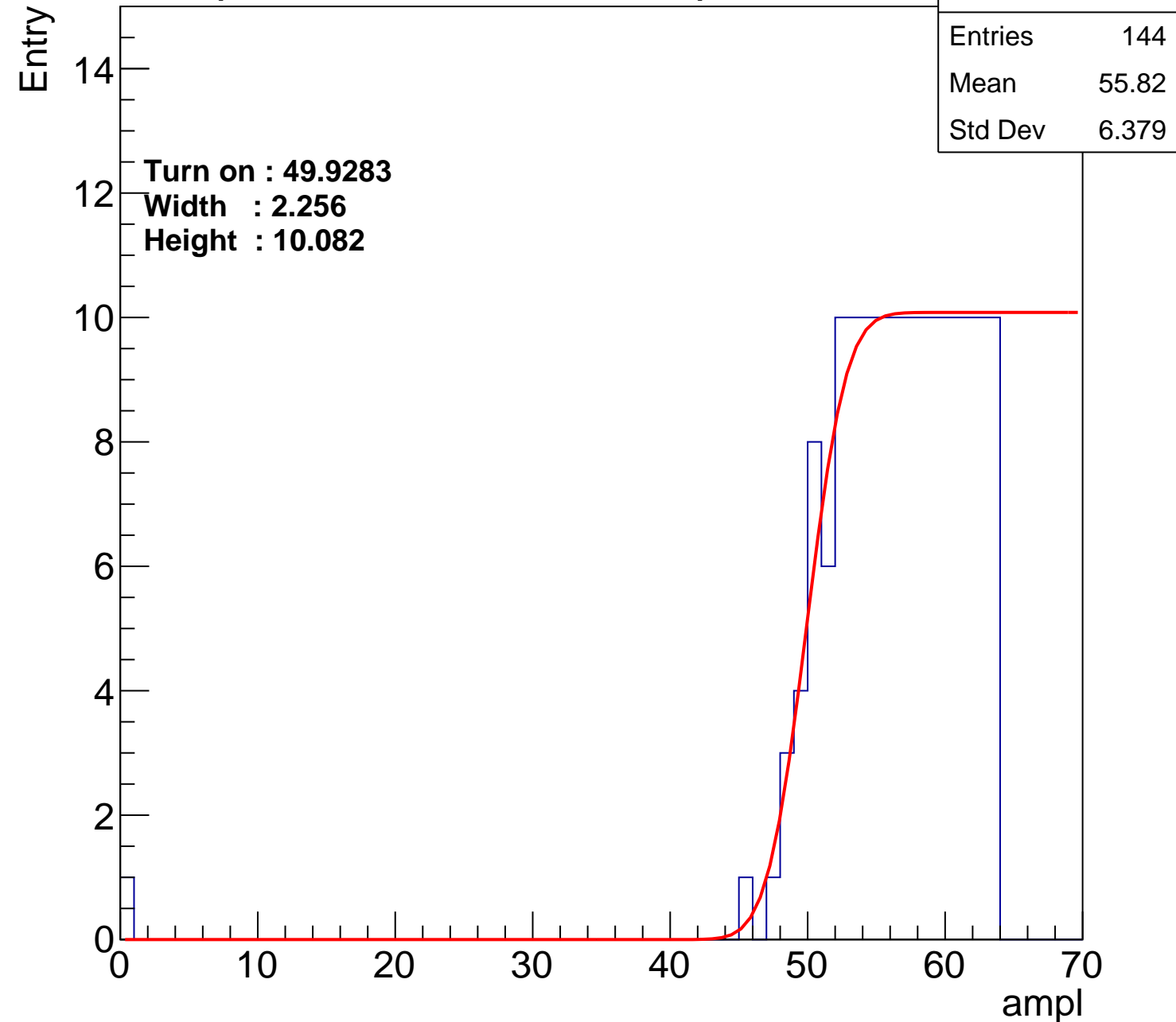
Width : 2.256

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	55.7
Std Dev	11.24

Turn on : 52.9439

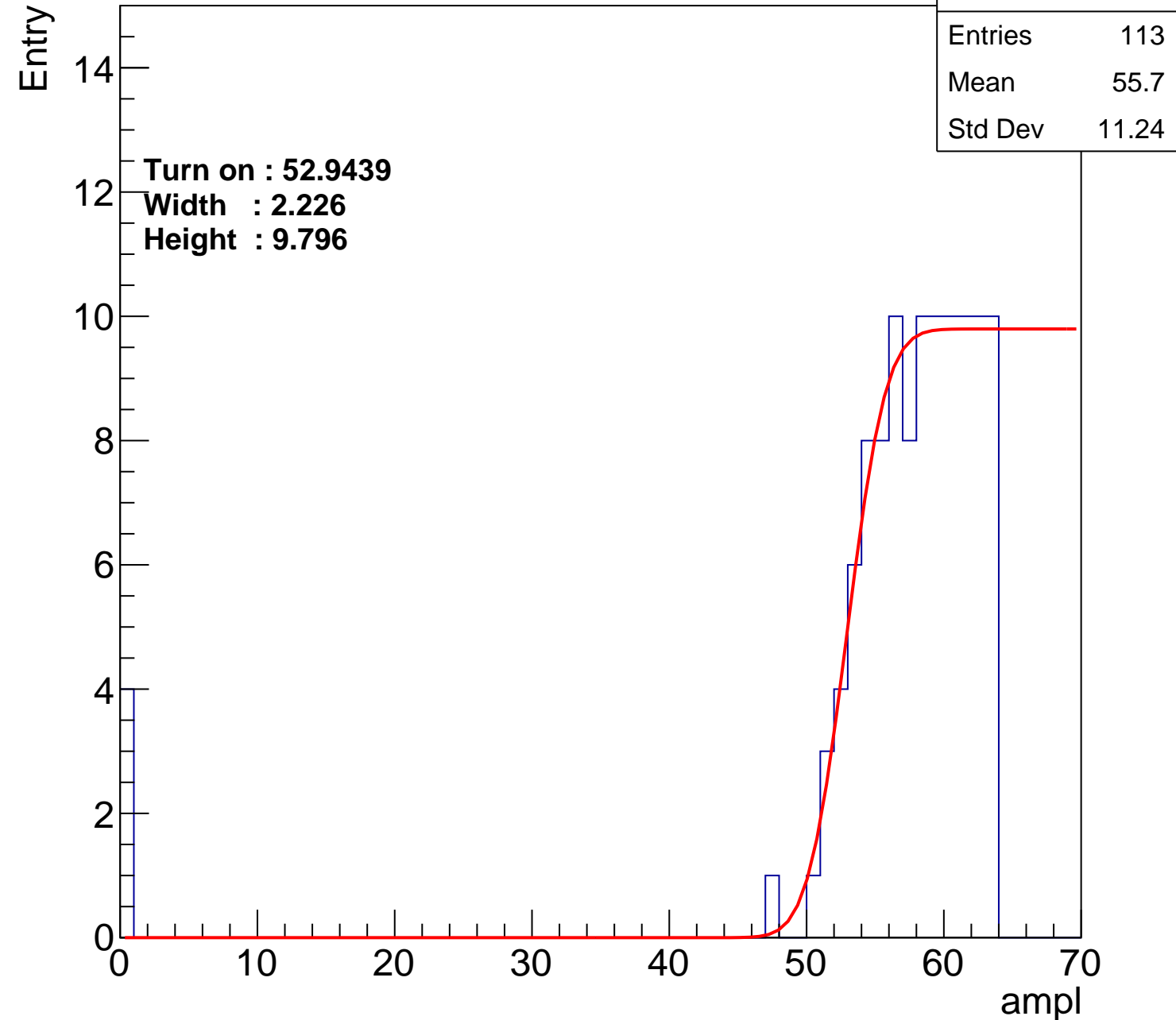
Width : 2.226

Height : 9.796

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	54.75
Std Dev	10.48

Turn on : 51.3481

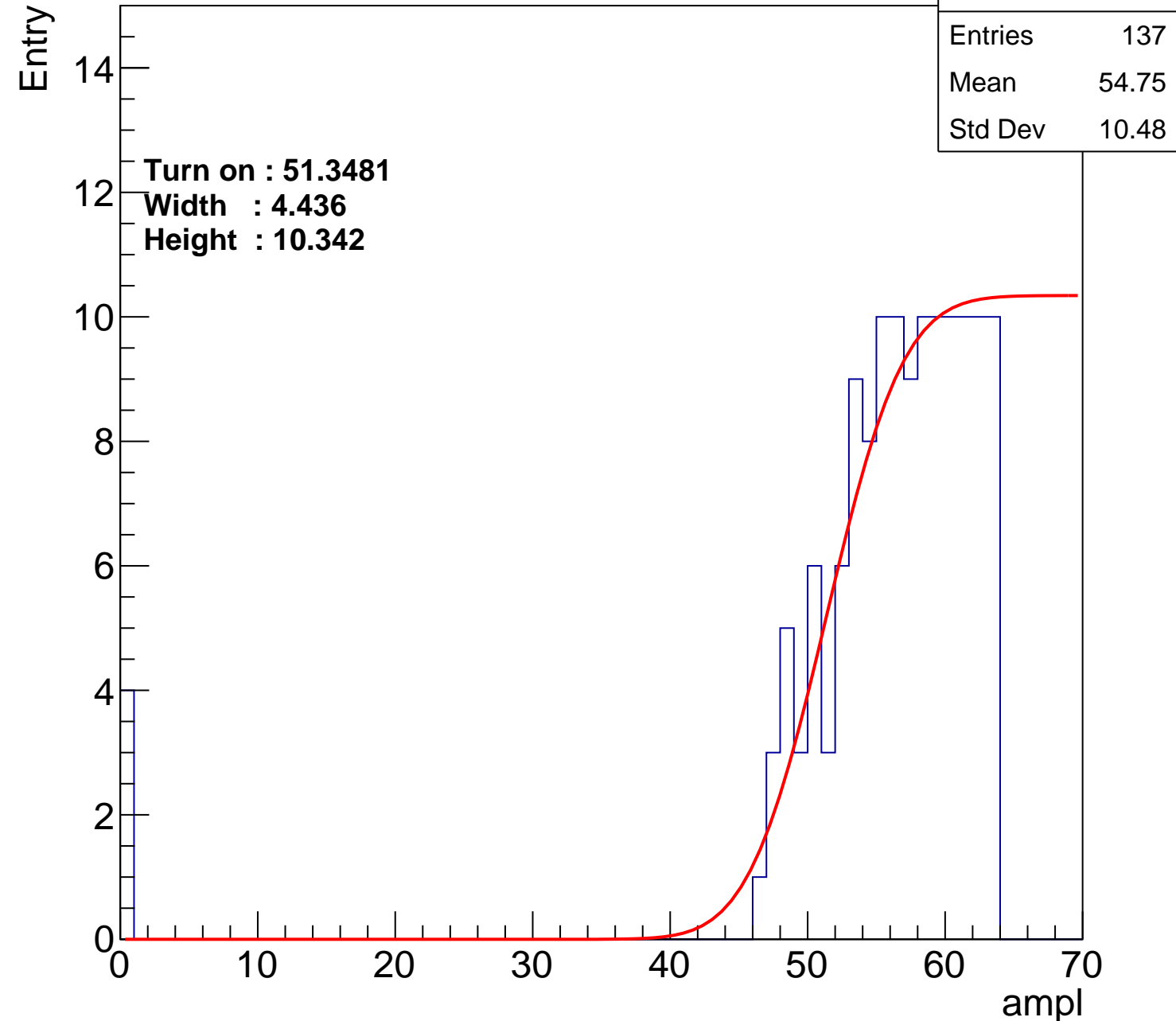
Width : 4.436

Height : 10.342

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch33

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	54.97
Std Dev	11.92

Turn on : 51.9540

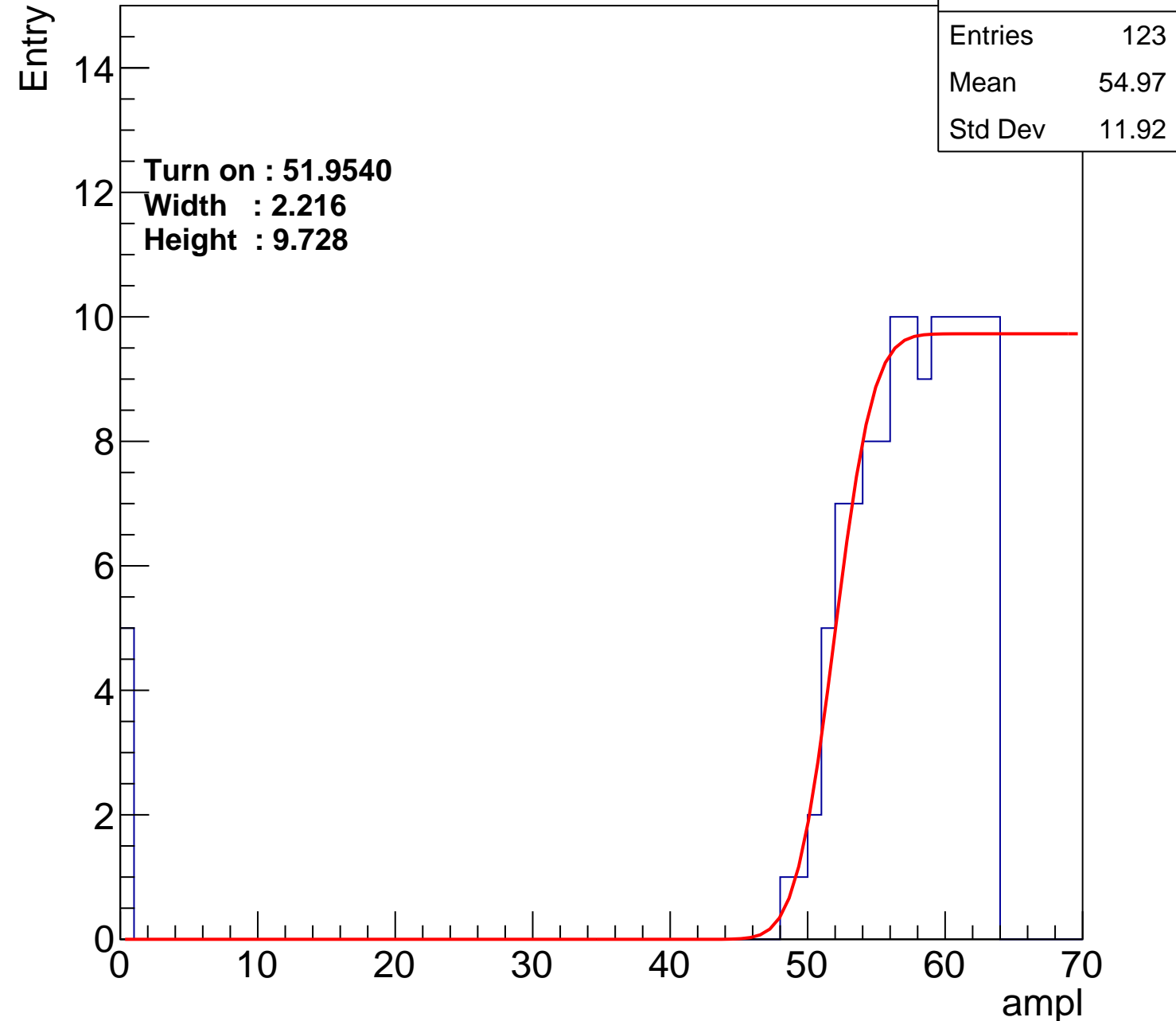
Width : 2.216

Height : 9.728

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.58
Std Dev	8.545

**Turn on : 53.9446**

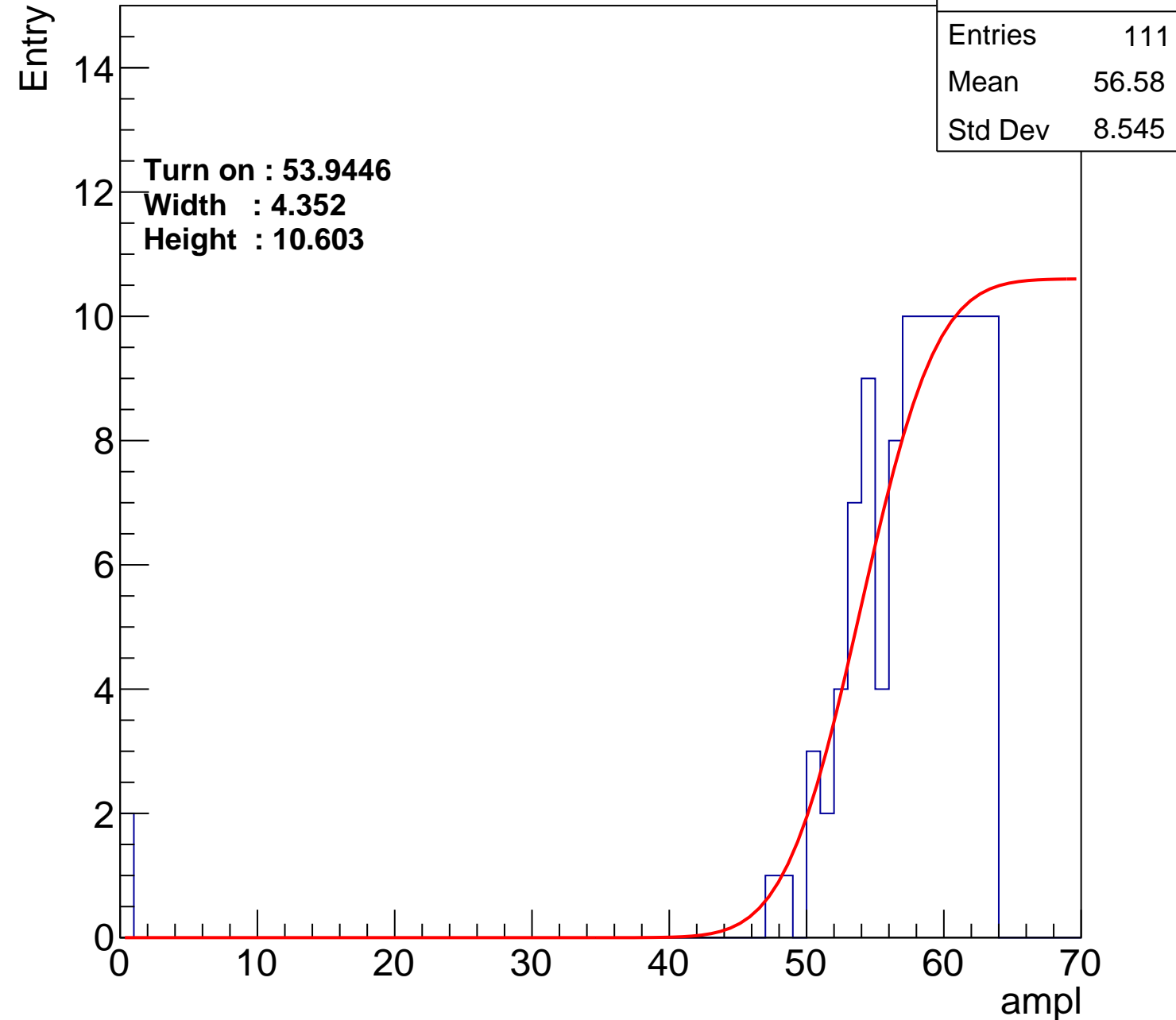
**Width : 4.352**

**Height : 10.603**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	56.66
Std Dev	8.523

Turn on : 53.2401

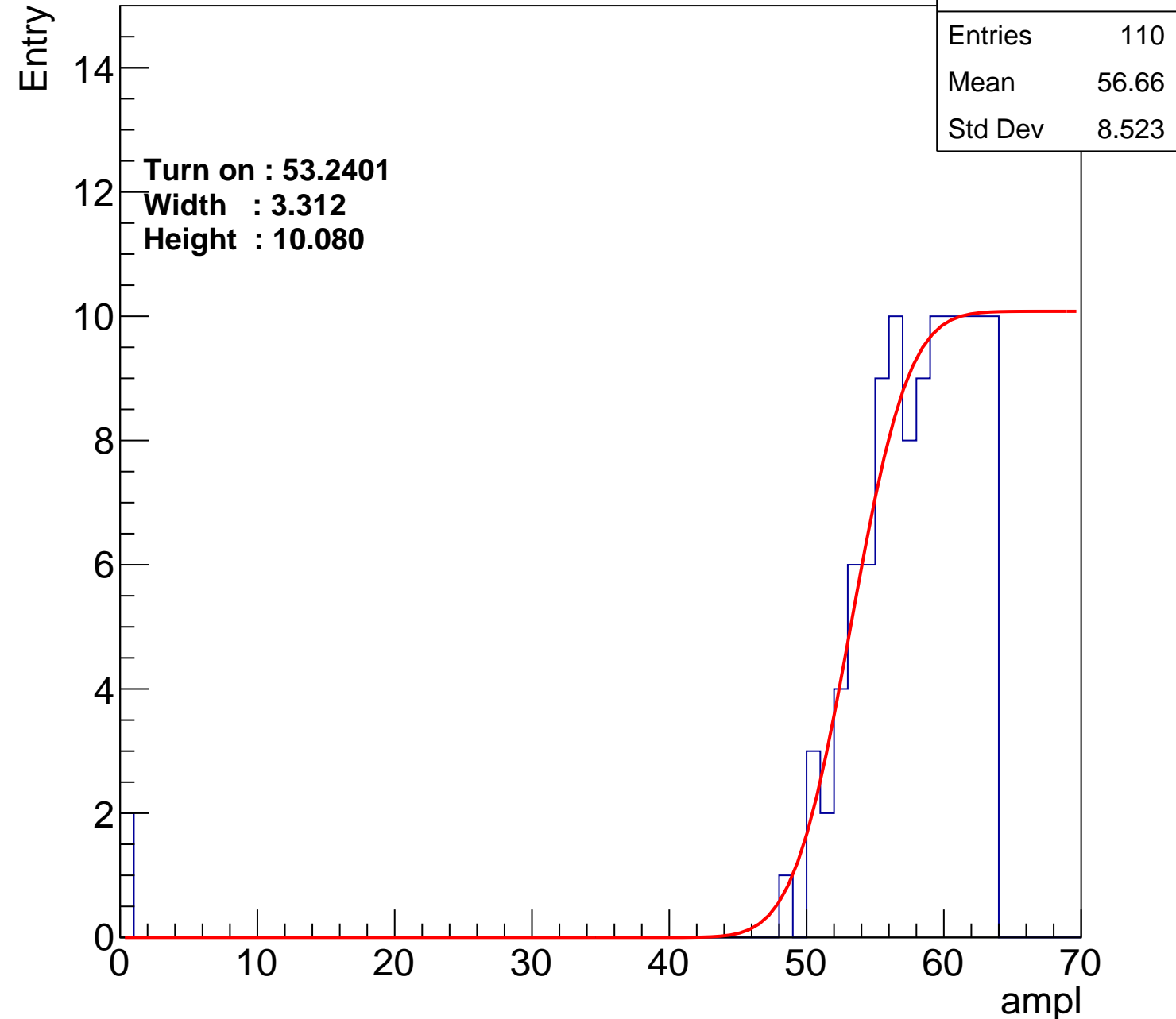
Width : 3.312

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	54.65
Std Dev	13.45

**Turn on : 53.7023**

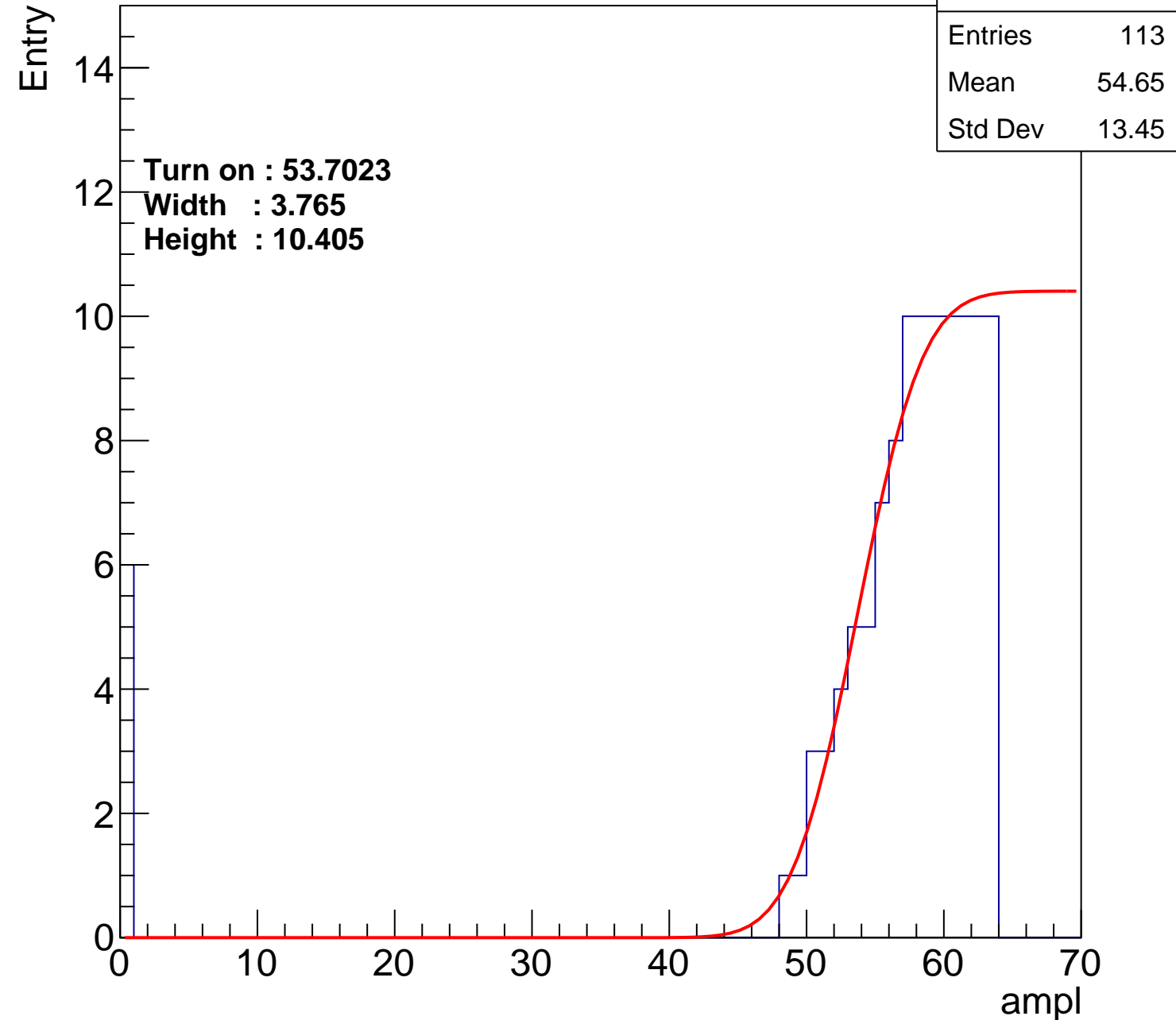
**Width : 3.765**

**Height : 10.405**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.94
Std Dev	10.46

Turn on : 51.2705

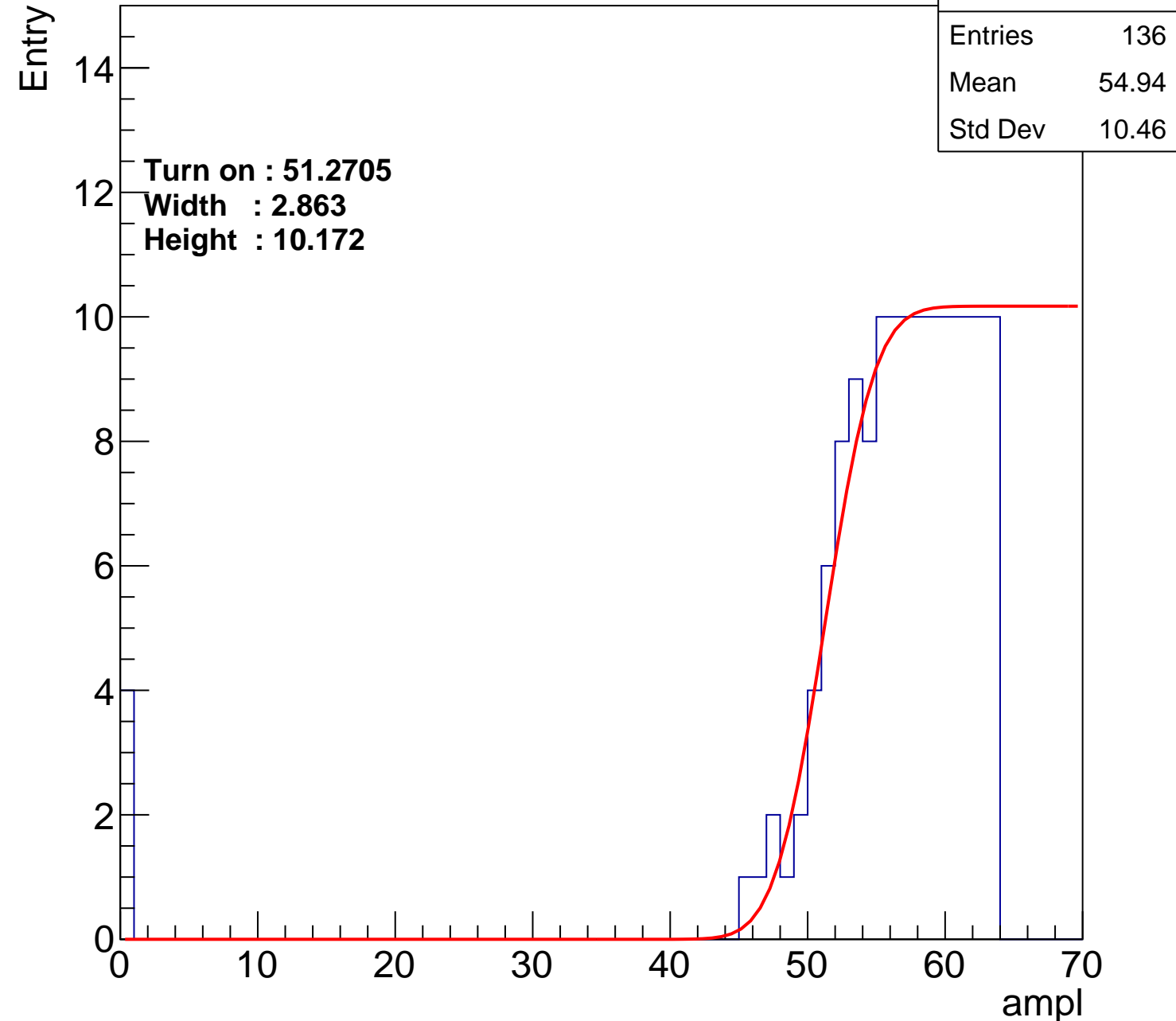
Width : 2.863

Height : 10.172

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

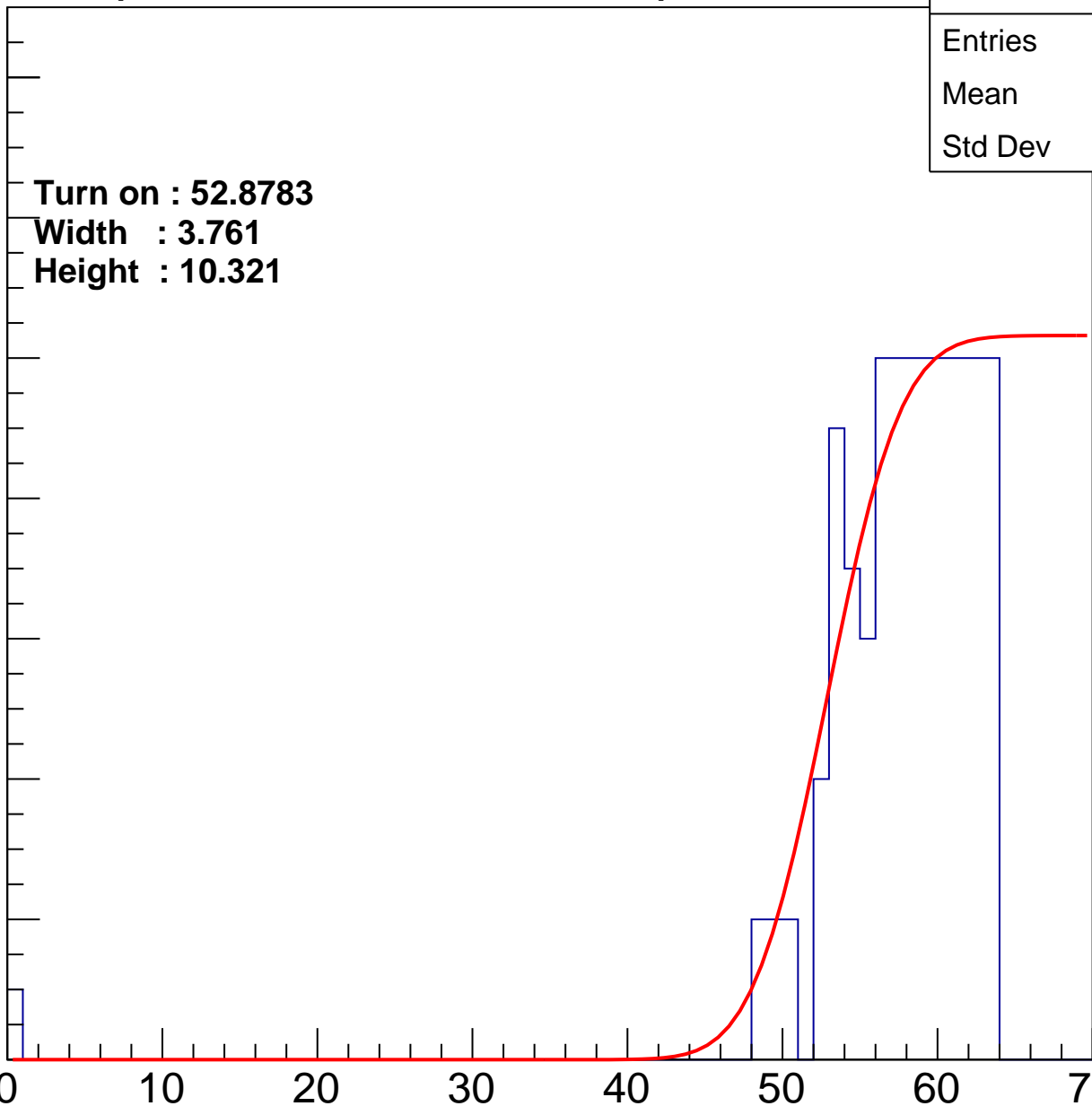
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 52.8783  
Width : 3.761  
Height : 10.321

Entries	113
Mean	57.05
Std Dev	6.577

ampl





# B0L103S, U26-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

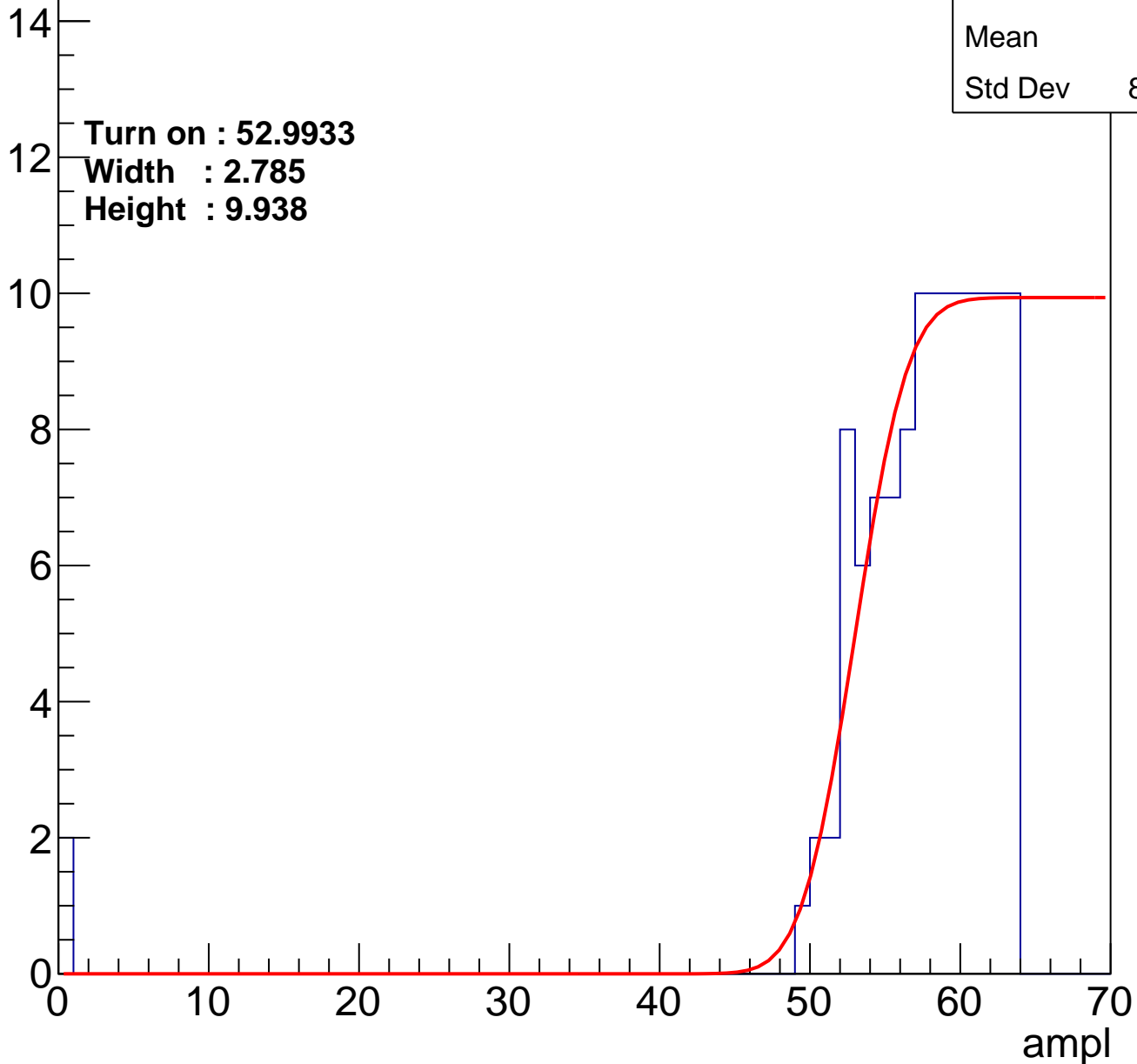
Entries	113
Mean	56.6
Std Dev	8.424

Turn on : 52.9933

Width : 2.785

Height : 9.938

Entry



# B0L103S, U26-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

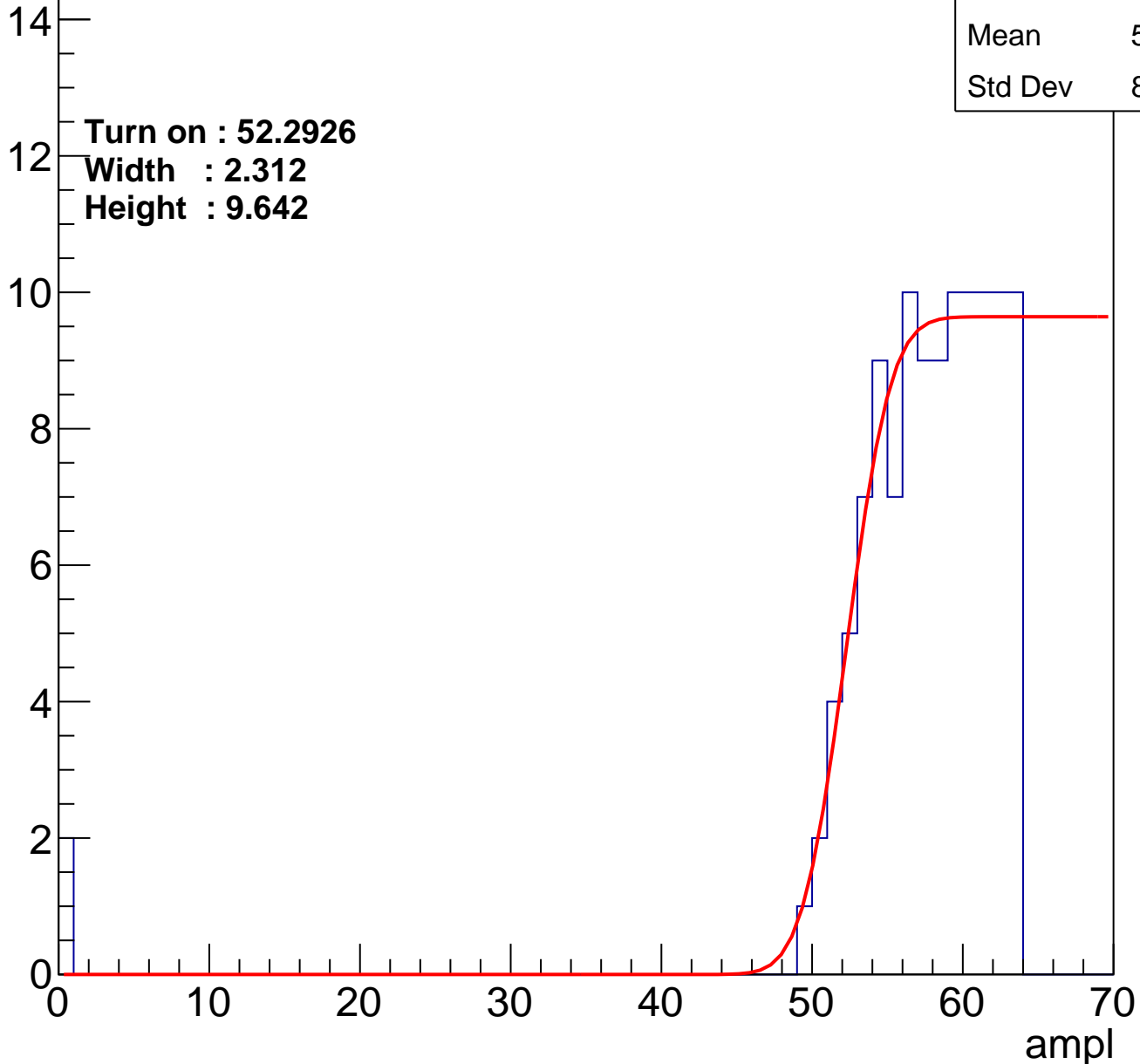
Entries	115
Mean	56.52
Std Dev	8.363

Turn on : 52.2926

Width : 2.312

Height : 9.642

Entry



# B0L103S, U26-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

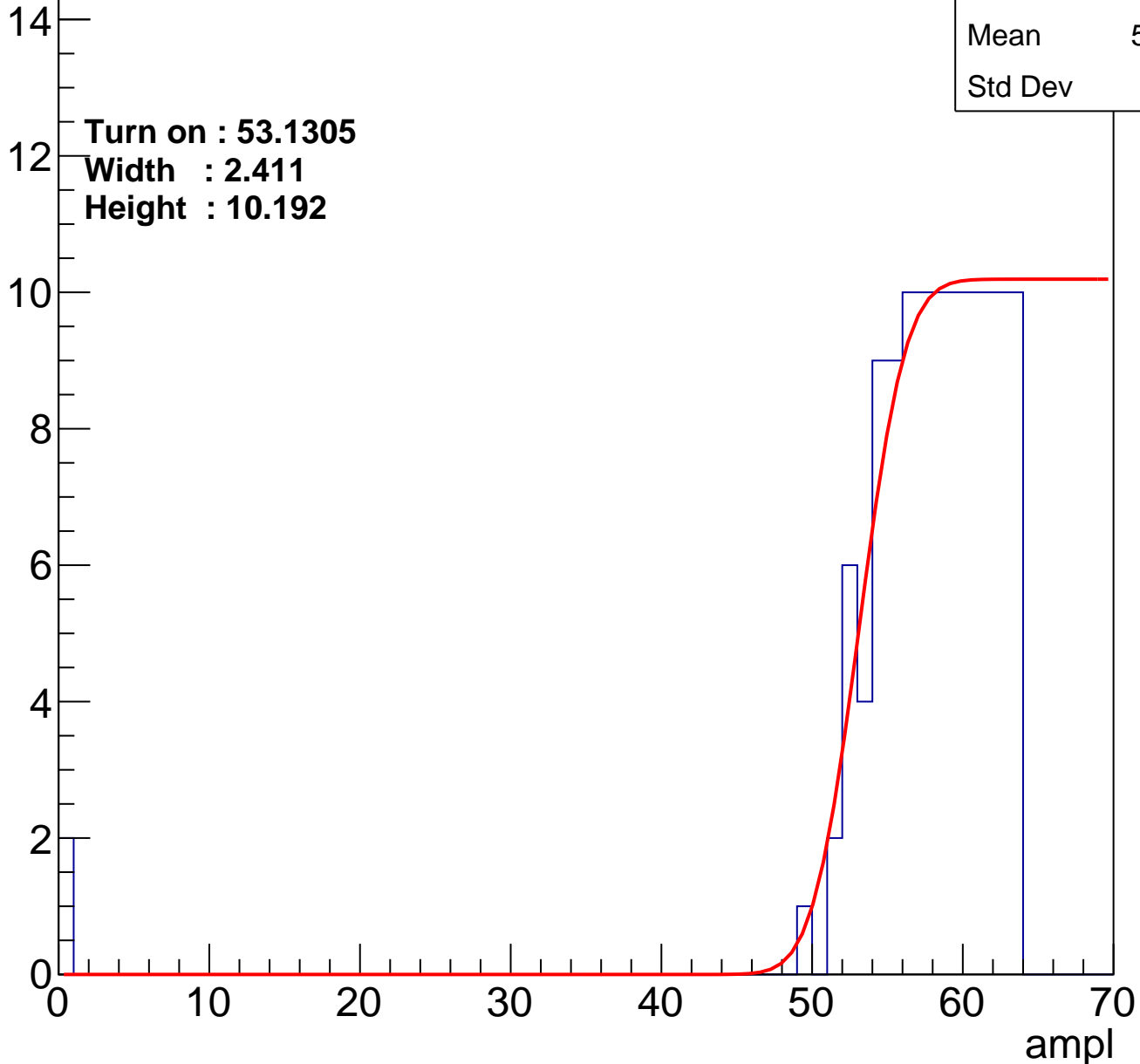
Entries	113
Mean	56.78
Std Dev	8.35

Turn on : 53.1305

Width : 2.411

Height : 10.192

Entry



# B0L103S, U26-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

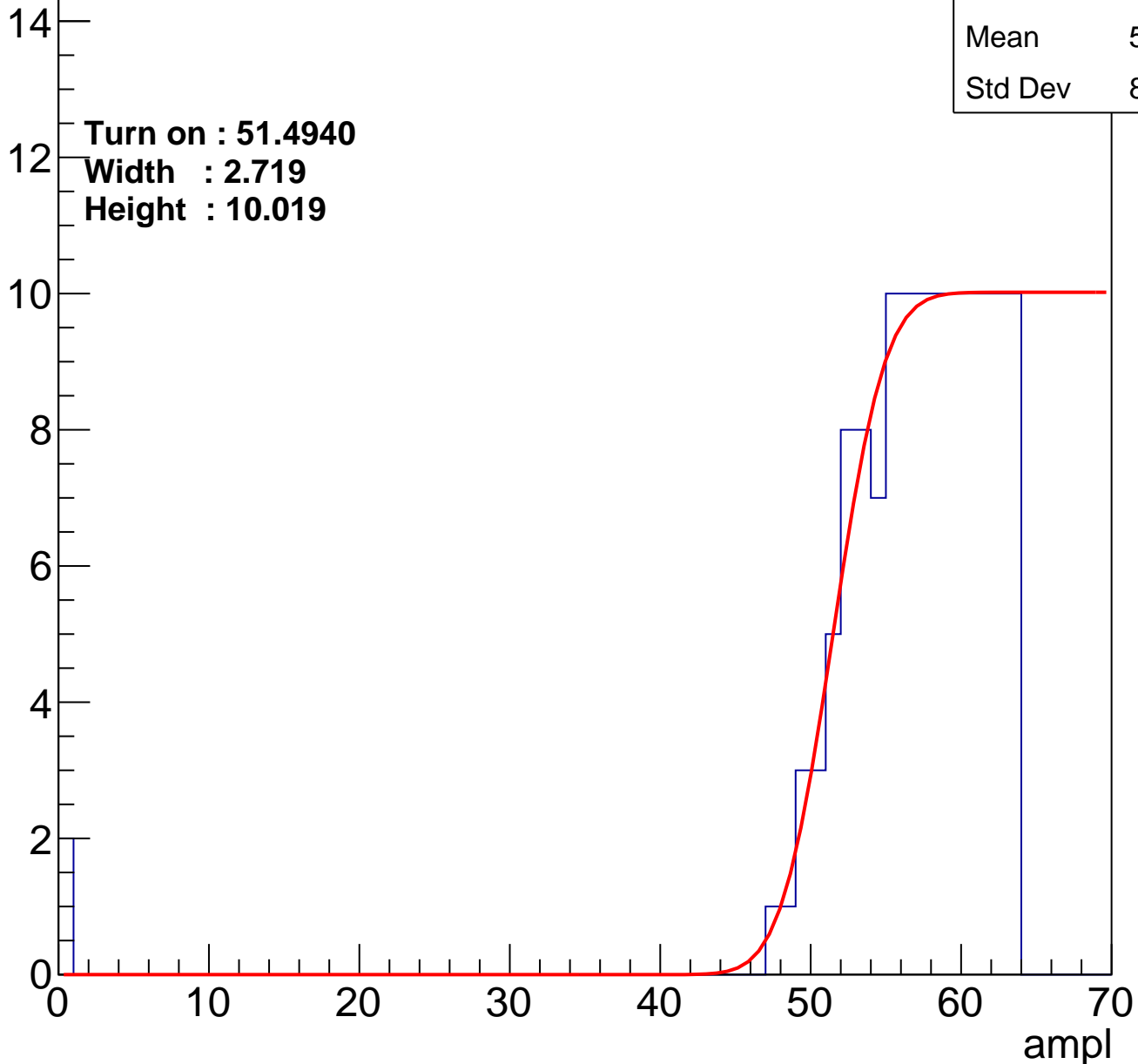
Entries	128
Mean	56.05
Std Dev	8.118

Turn on : 51.4940

Width : 2.719

Height : 10.019

Entry



# B0L103S, U26-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.6
Std Dev	11.02

Turn on : 52.6837

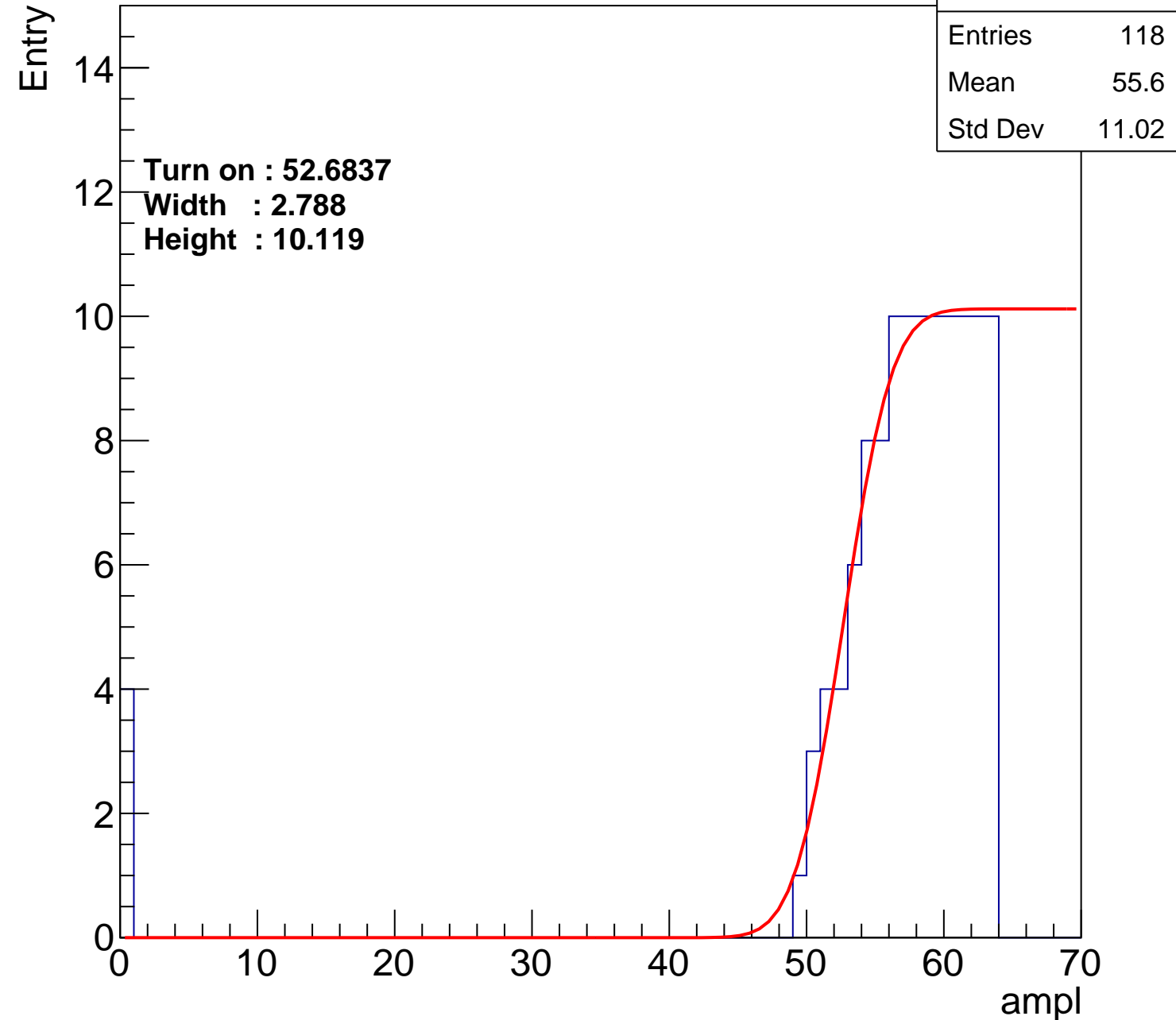
Width : 2.788

Height : 10.119

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch44

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	55.15
Std Dev	12.24

Turn on : 53.1579

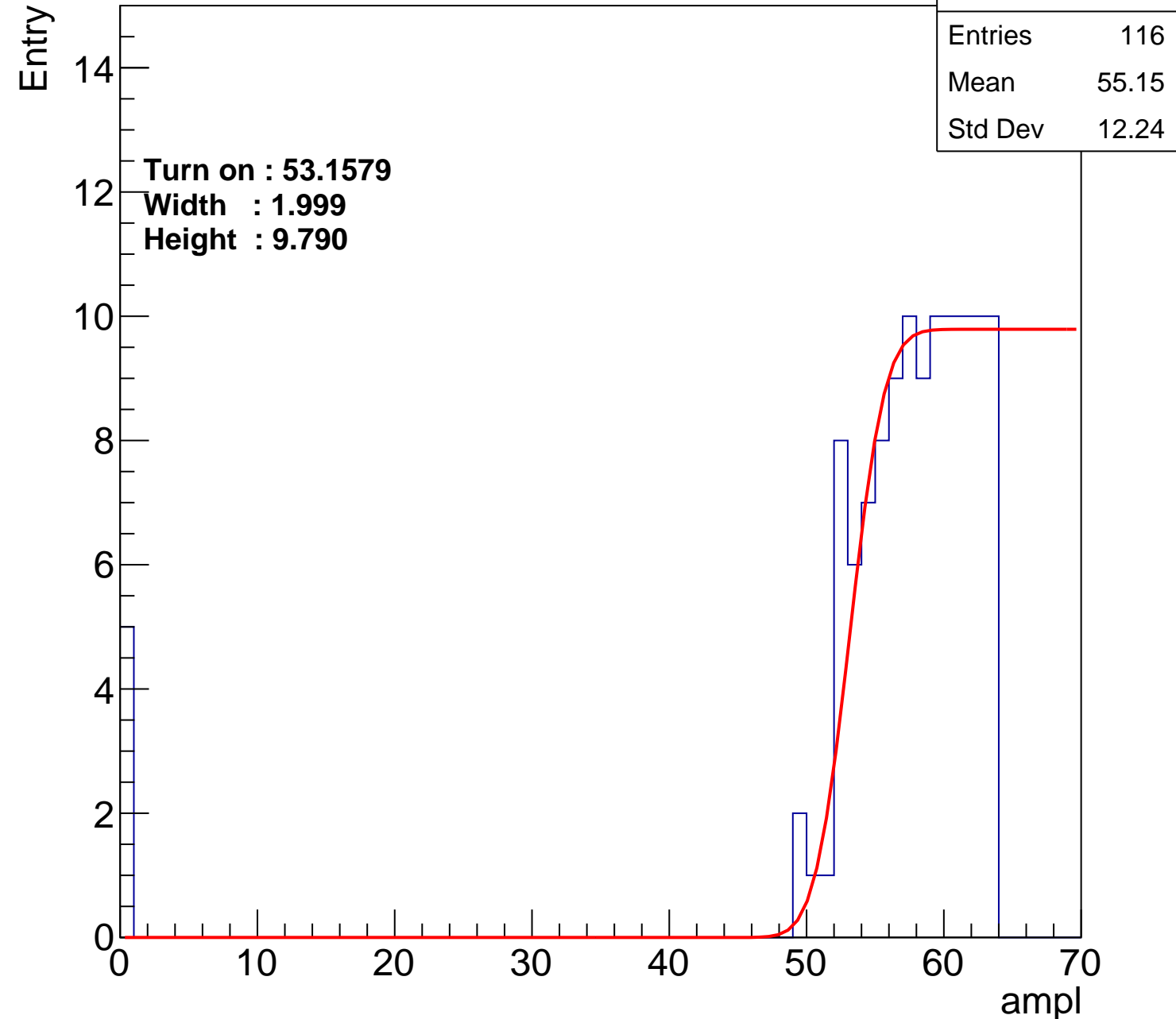
Width : 1.999

Height : 9.790

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

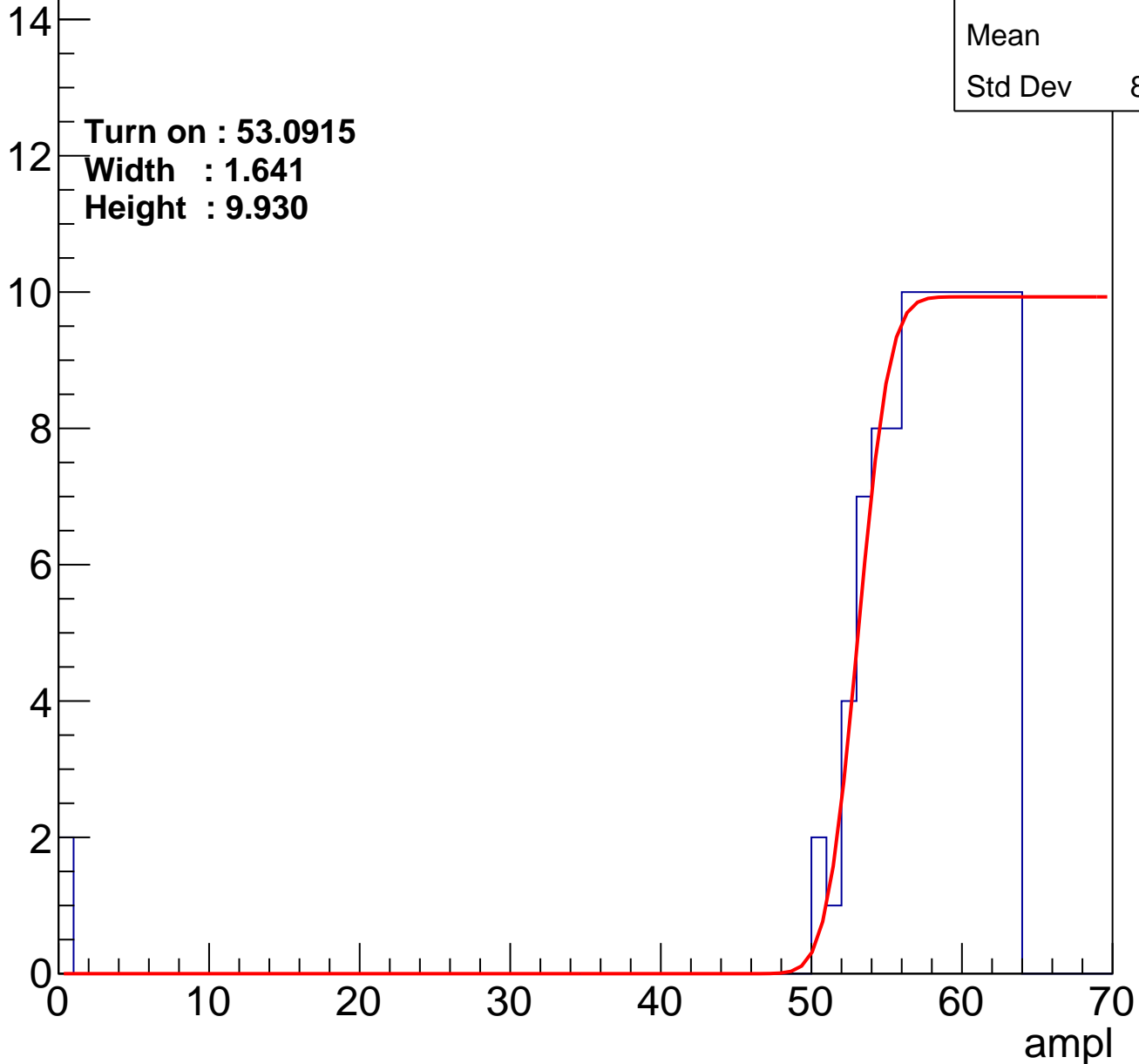
Entries	112
Mean	56.8
Std Dev	8.379

Turn on : 53.0915

Width : 1.641

Height : 9.930

Entry



# B0L103S, U26-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.74
Std Dev	6.635

Turn on : 53.0418

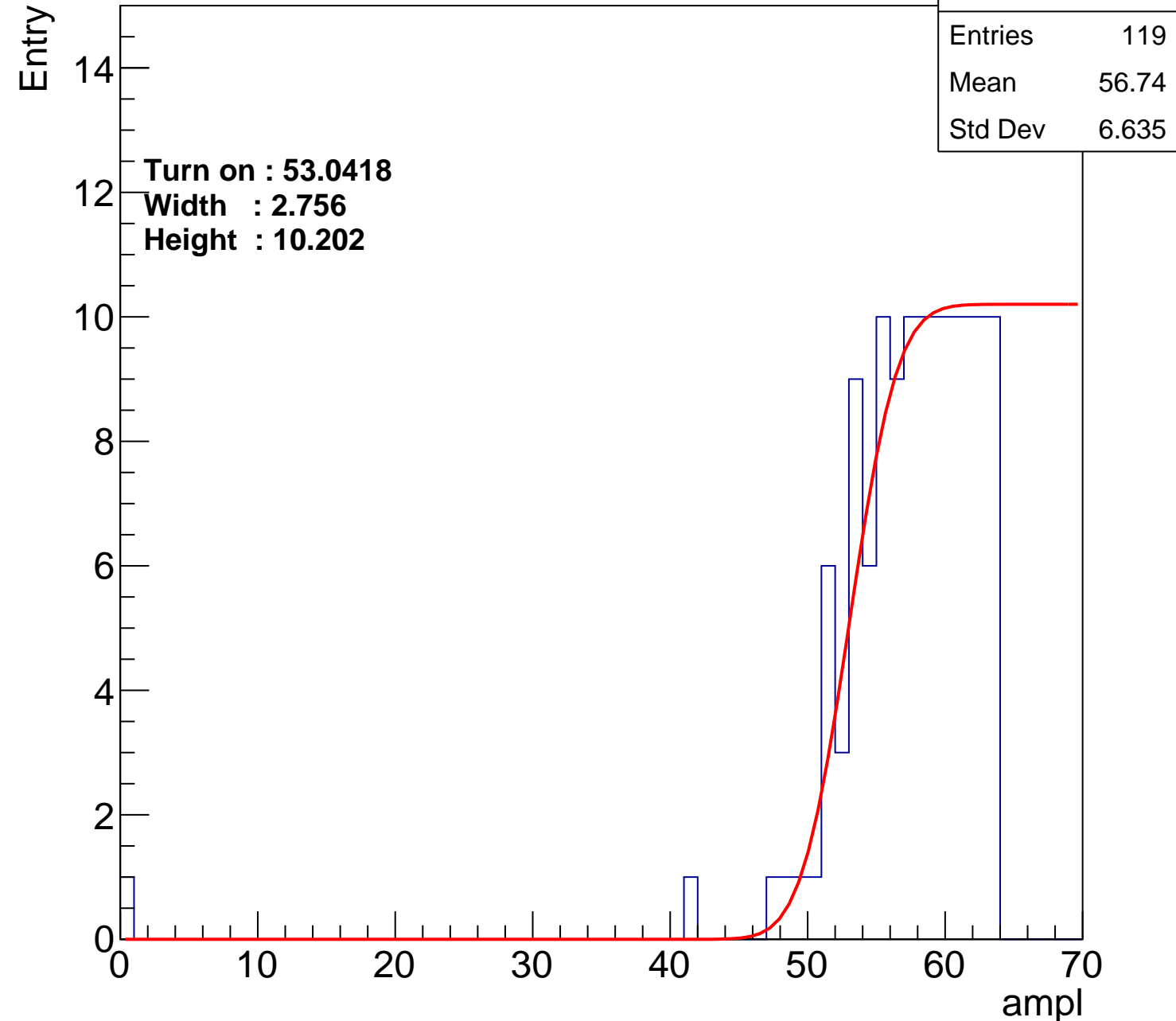
Width : 2.756

Height : 10.202

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	55.8
Std Dev	11.49

Turn on : 54.2568

Width : 2.887

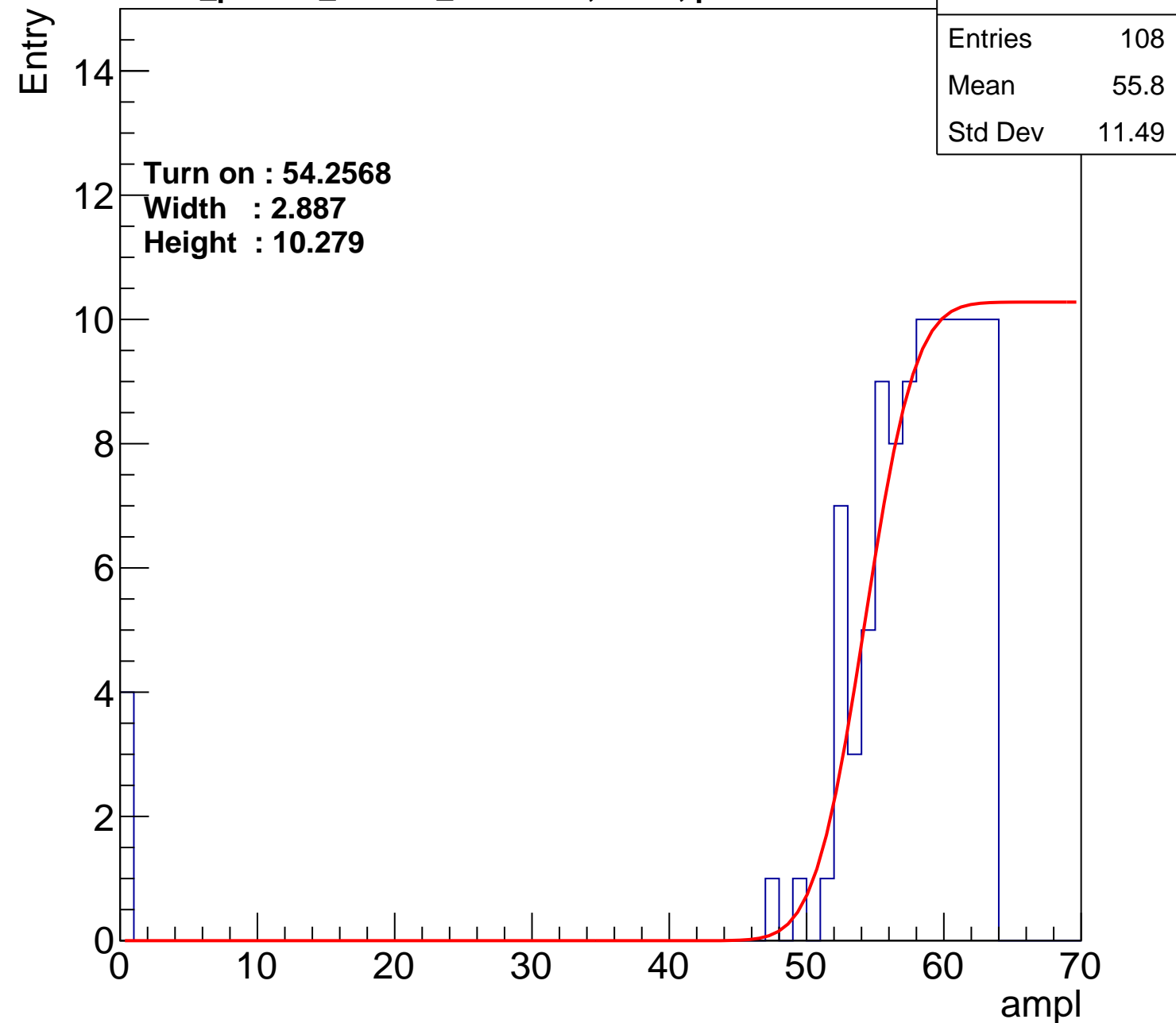
Height : 10.279

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U26-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.91
Std Dev	8.144

Turn on : 51.7042

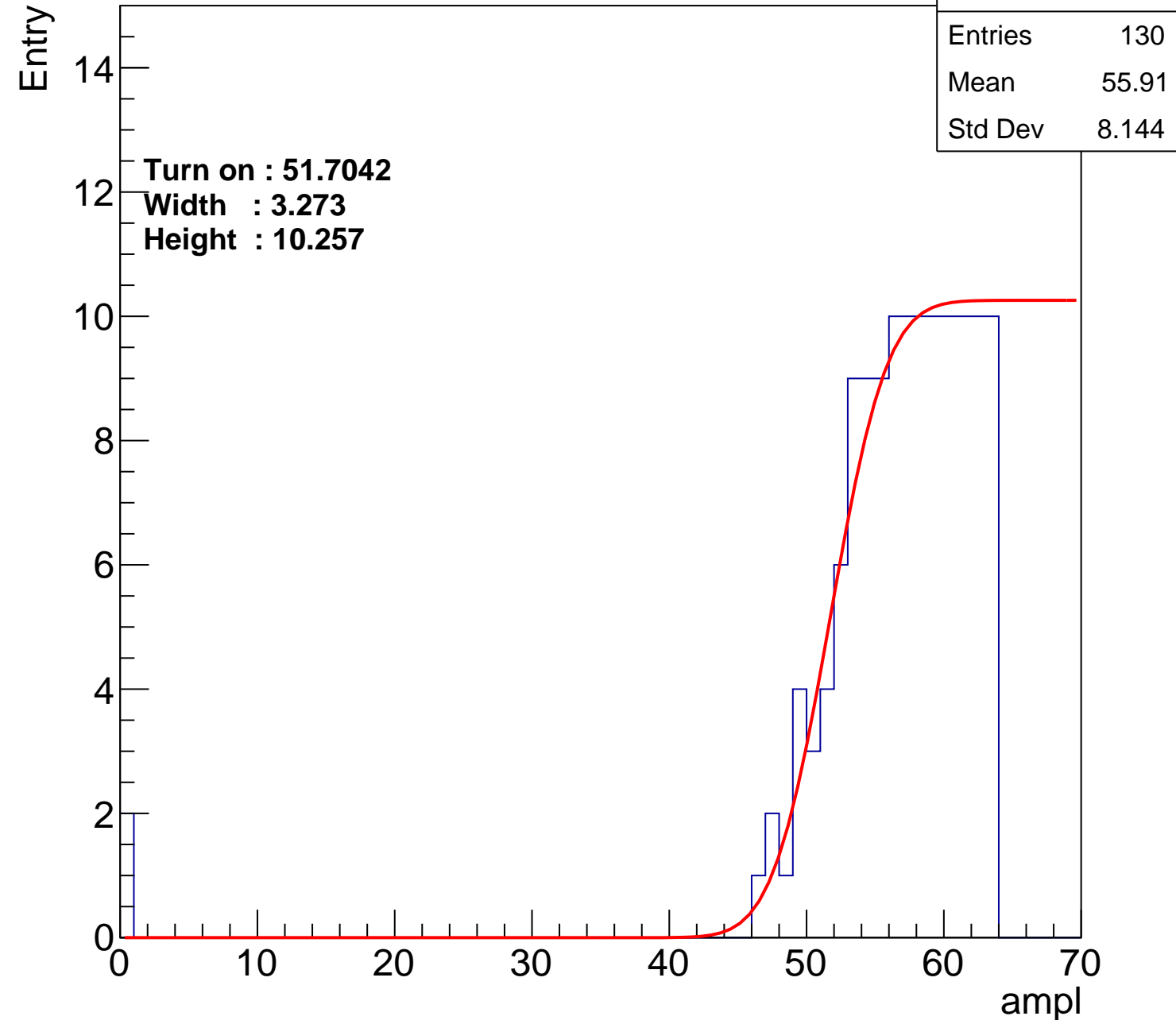
Width : 3.273

Height : 10.257

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.53
Std Dev	9.339

Turn on : 51.1421

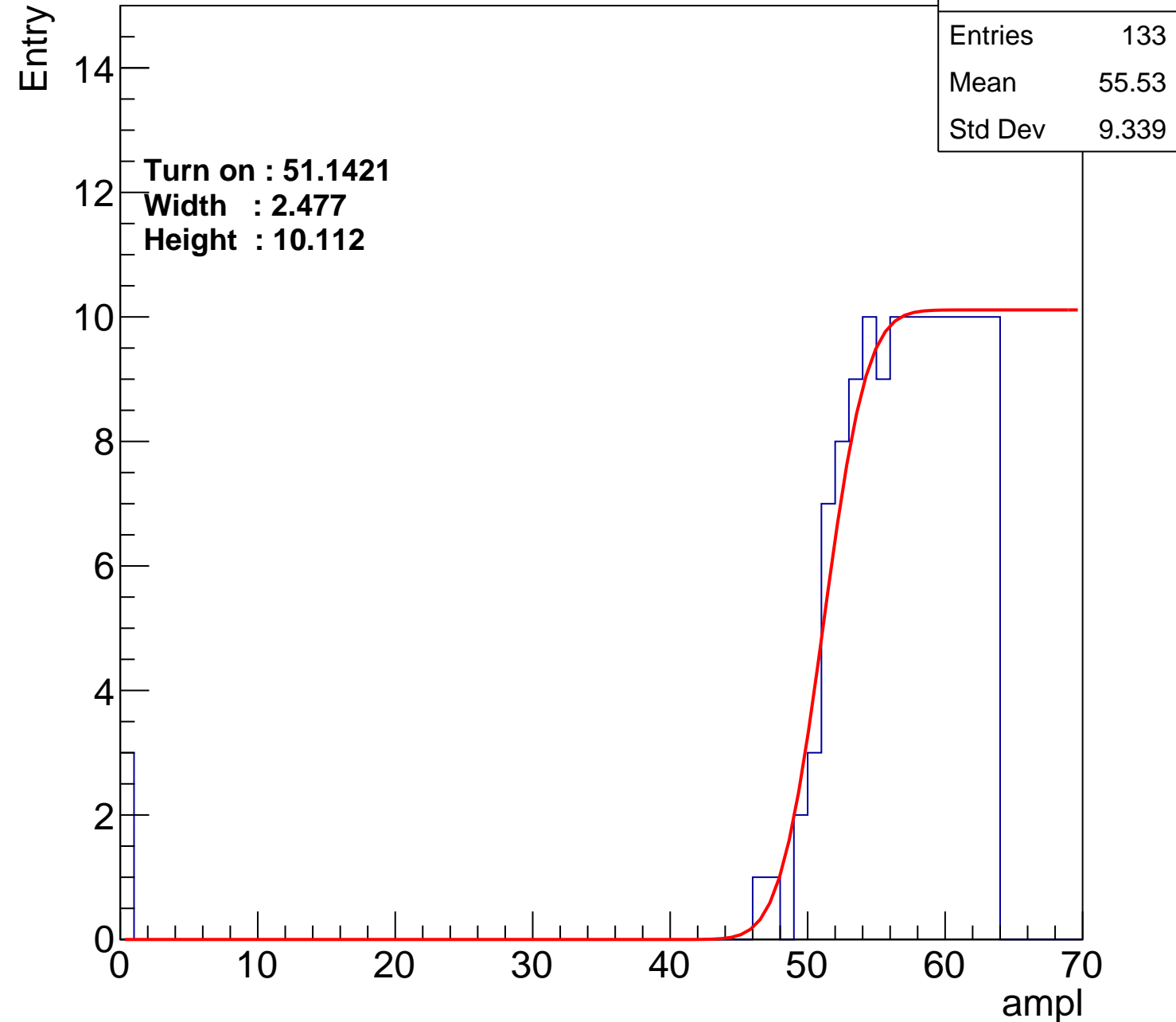
Width : 2.477

Height : 10.112

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

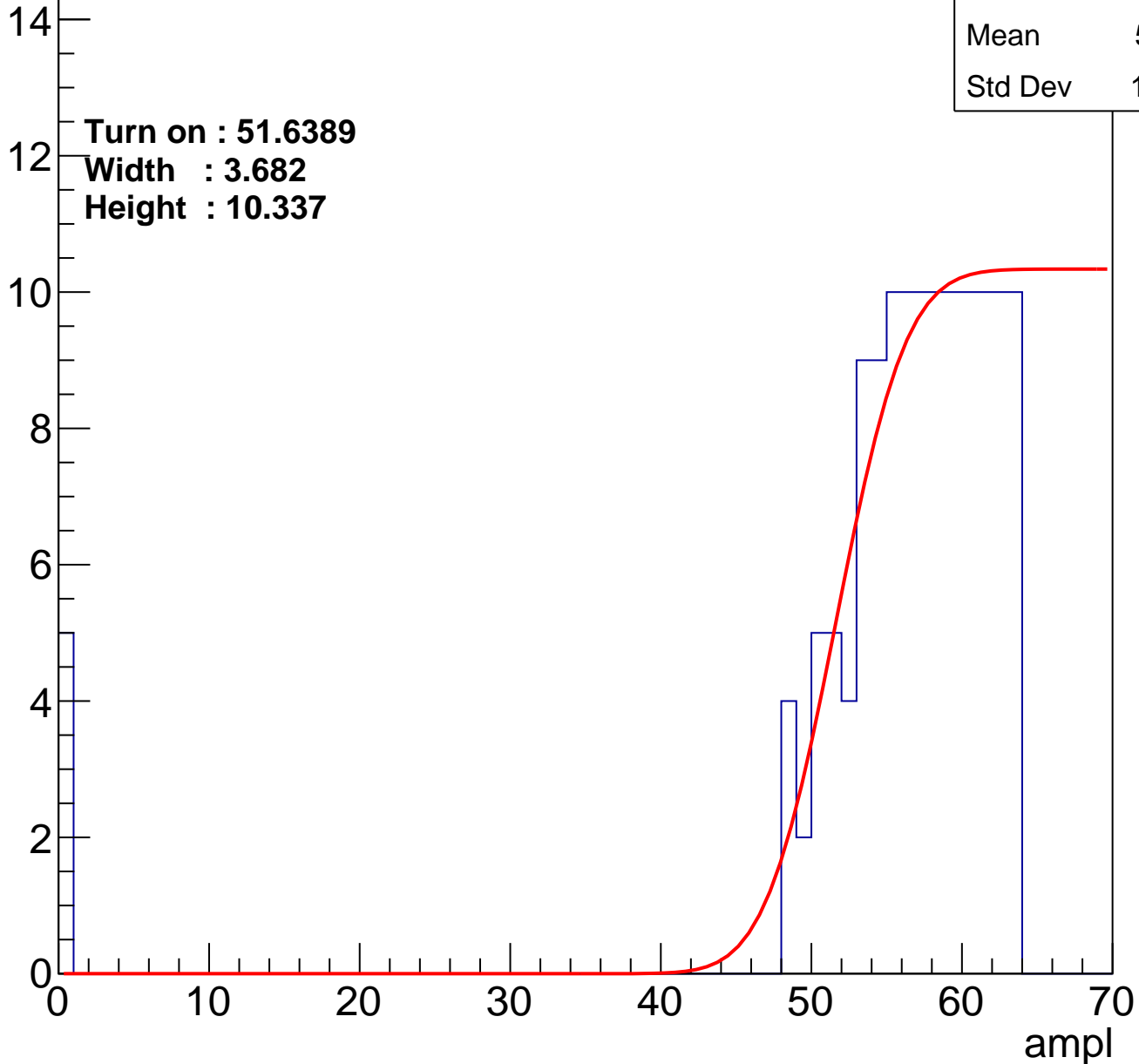
Entries	133
Mean	54.71
Std Dev	11.54

Turn on : 51.6389

Width : 3.682

Height : 10.337

Entry



# B0L103S, U26-ch51

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	56.21
Std Dev	6.52

Turn on : 51.1929

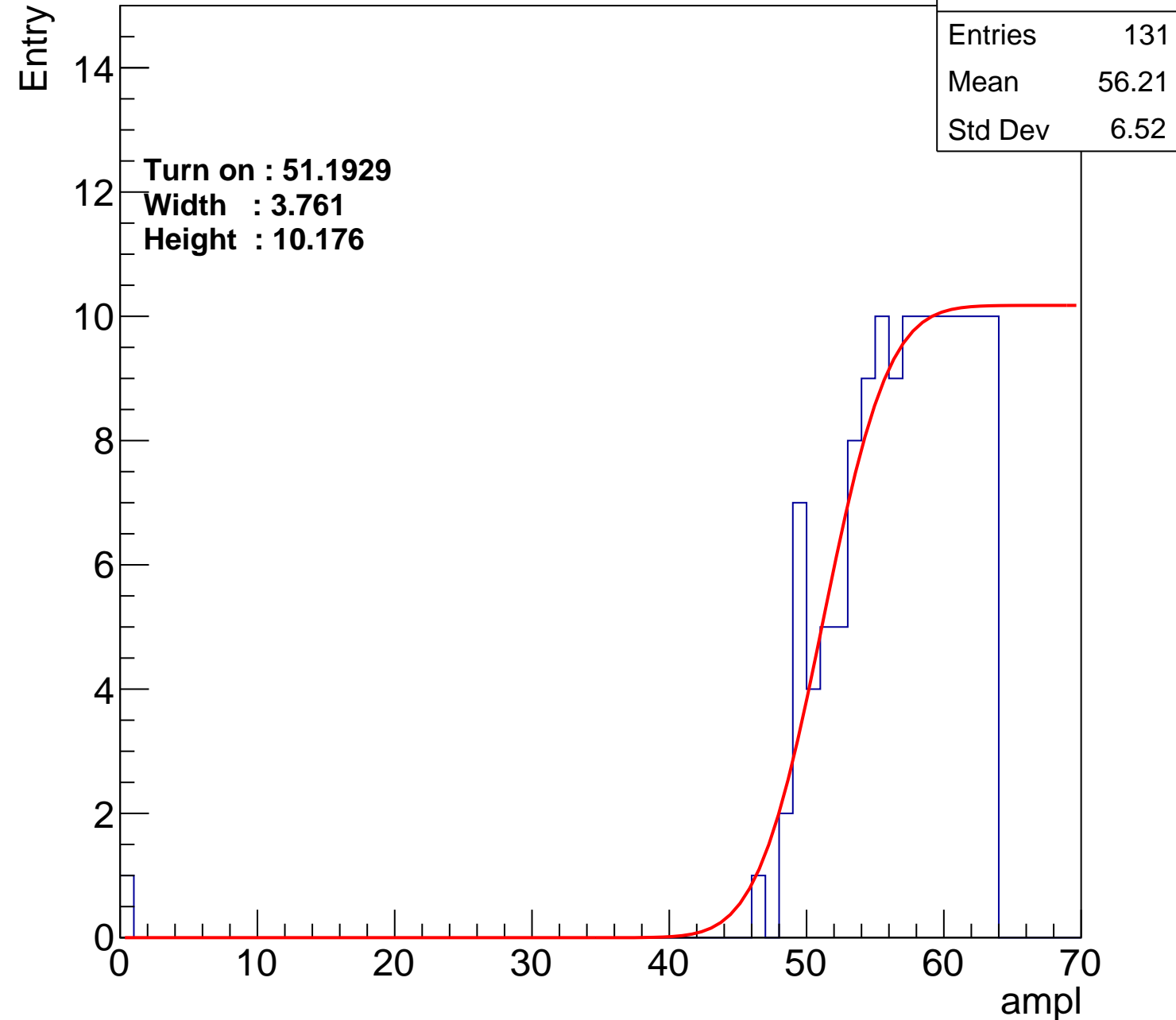
Width : 3.761

Height : 10.176

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.46
Std Dev	10.83

Turn on : 52.5633

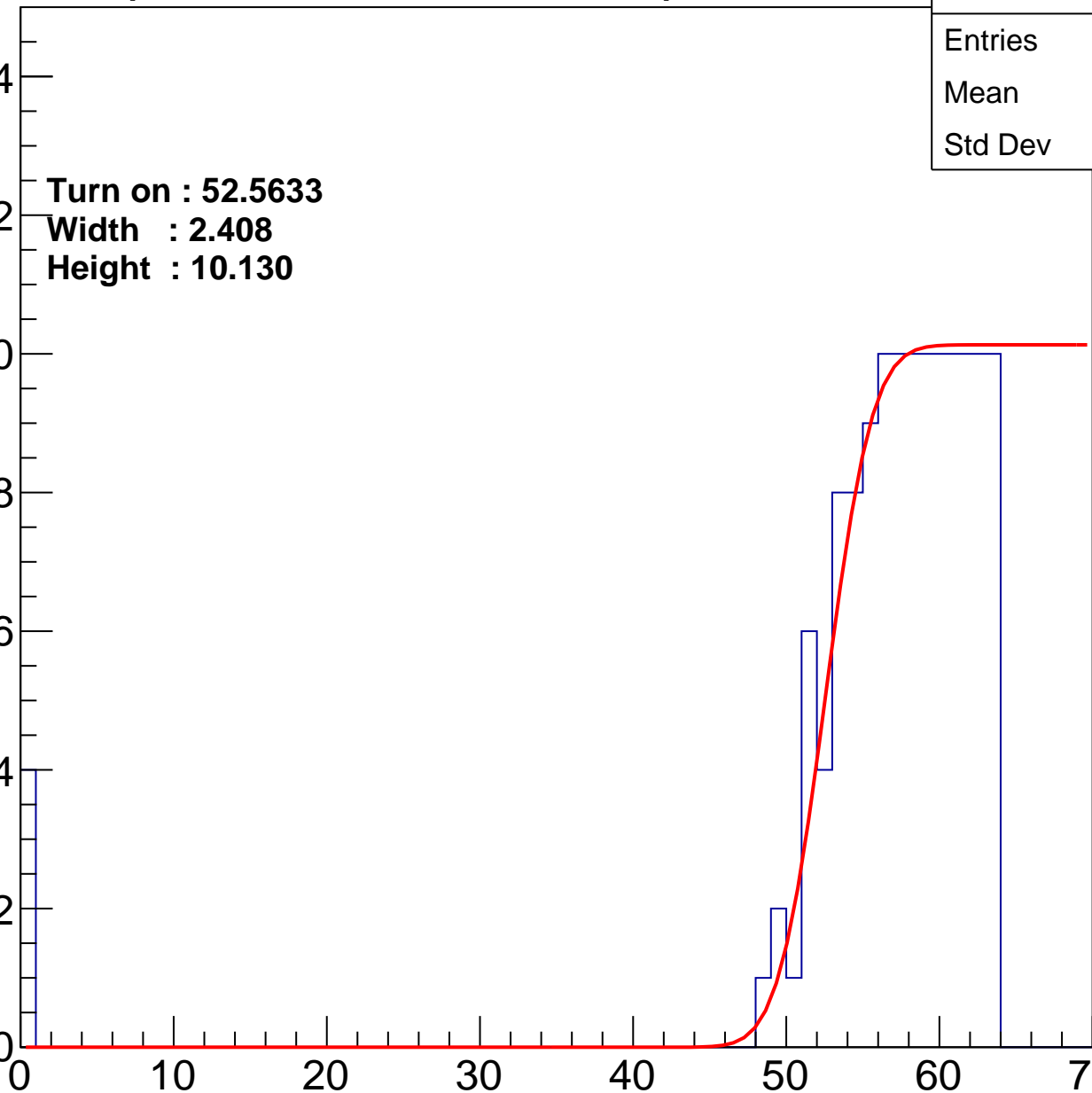
Width : 2.408

Height : 10.130

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.09
Std Dev	10.59

Turn on : 51.2081

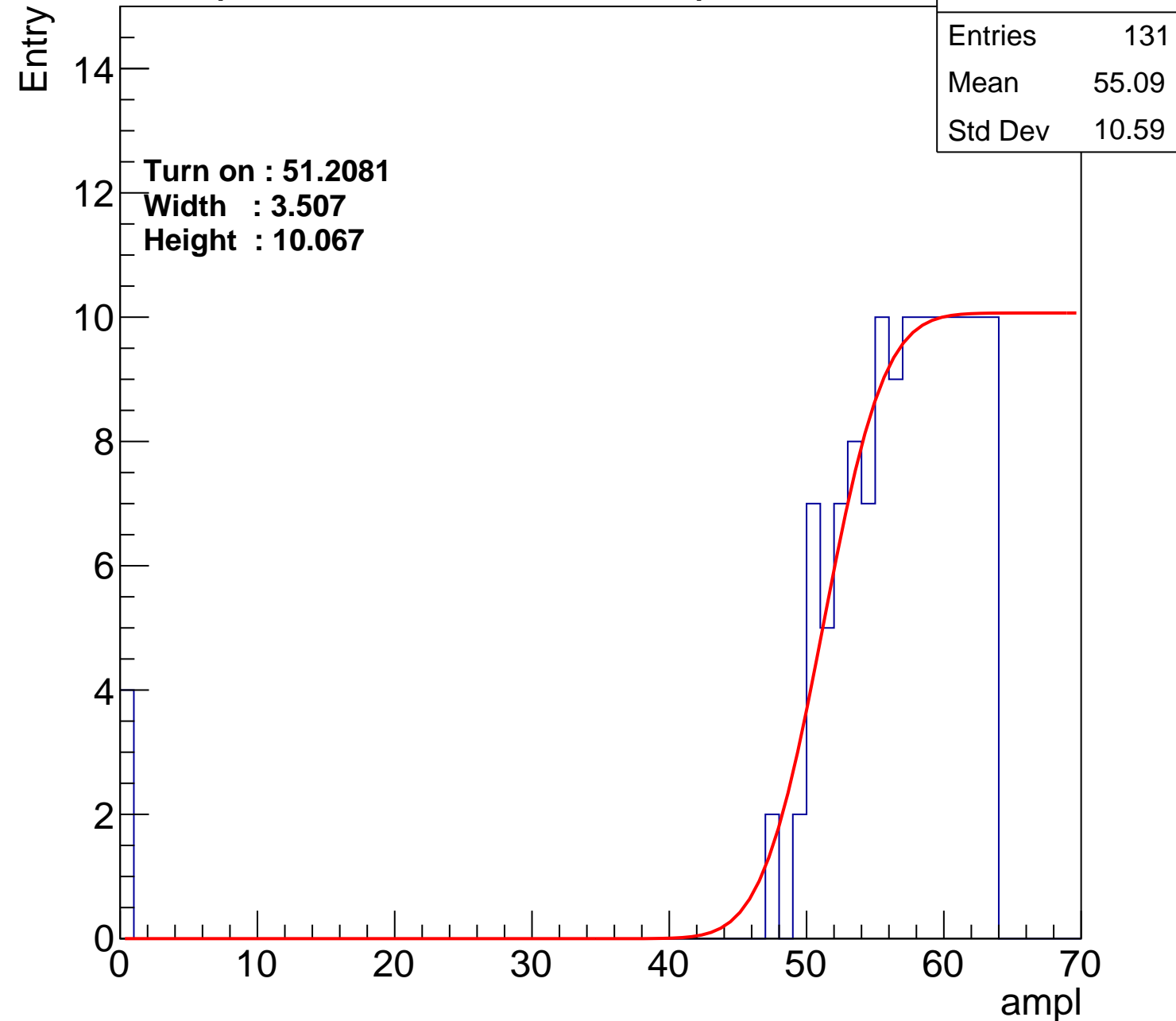
Width : 3.507

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.91
Std Dev	6.573

Turn on : 53.2156

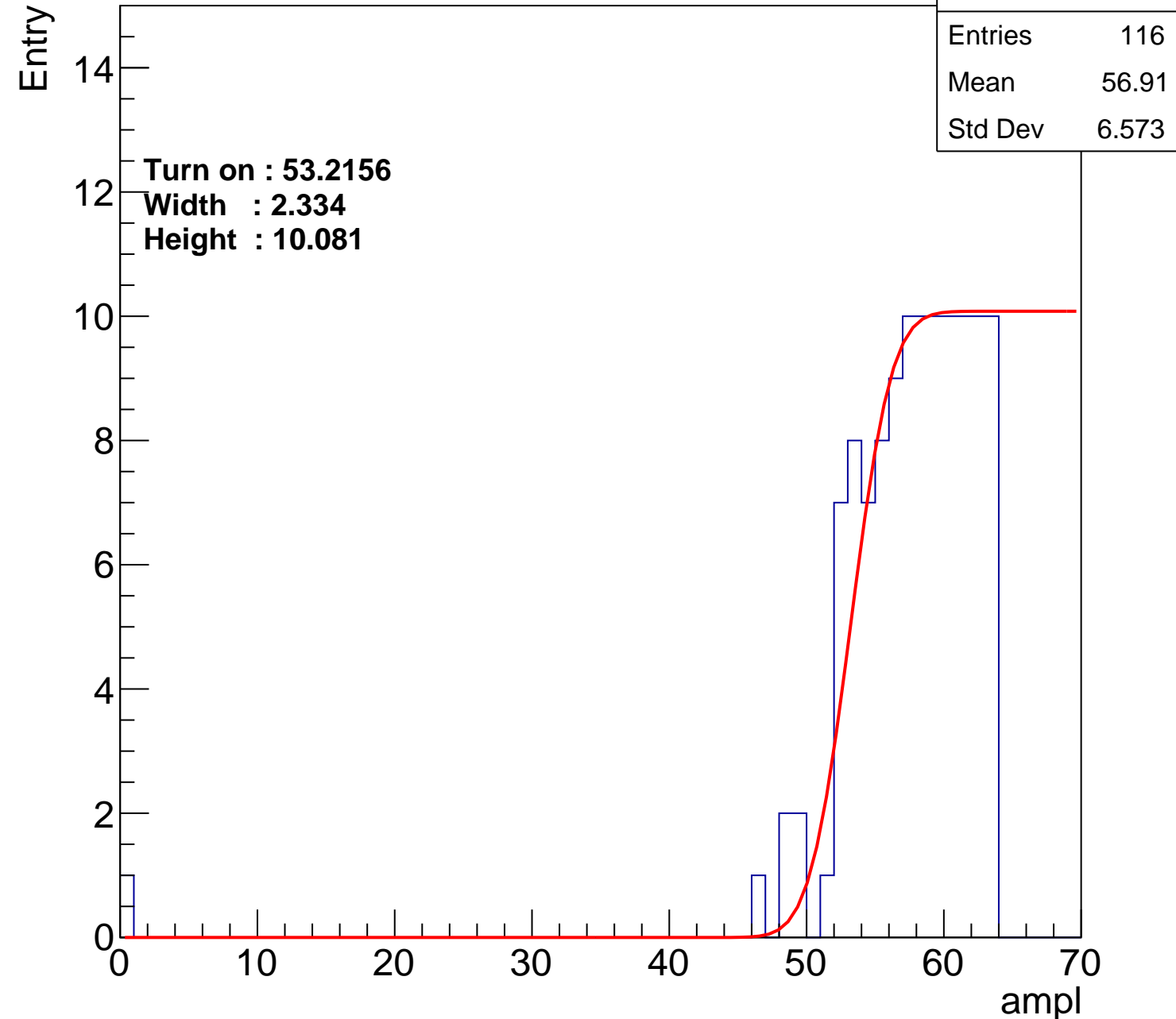
Width : 2.334

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.41
Std Dev	9.272

Turn on : 50.7883

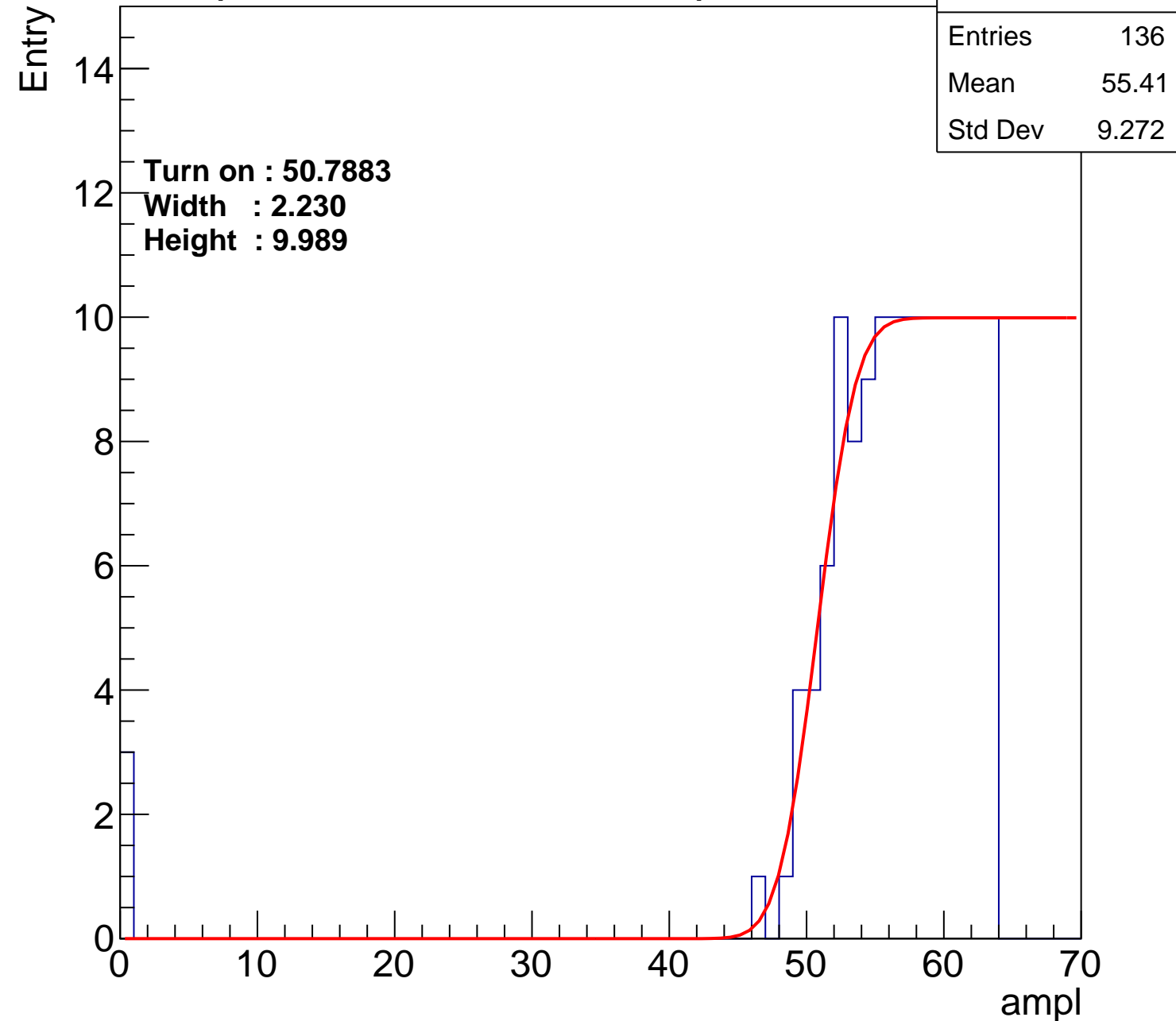
Width : 2.230

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.63
Std Dev	8.518

Turn on : 53.5091

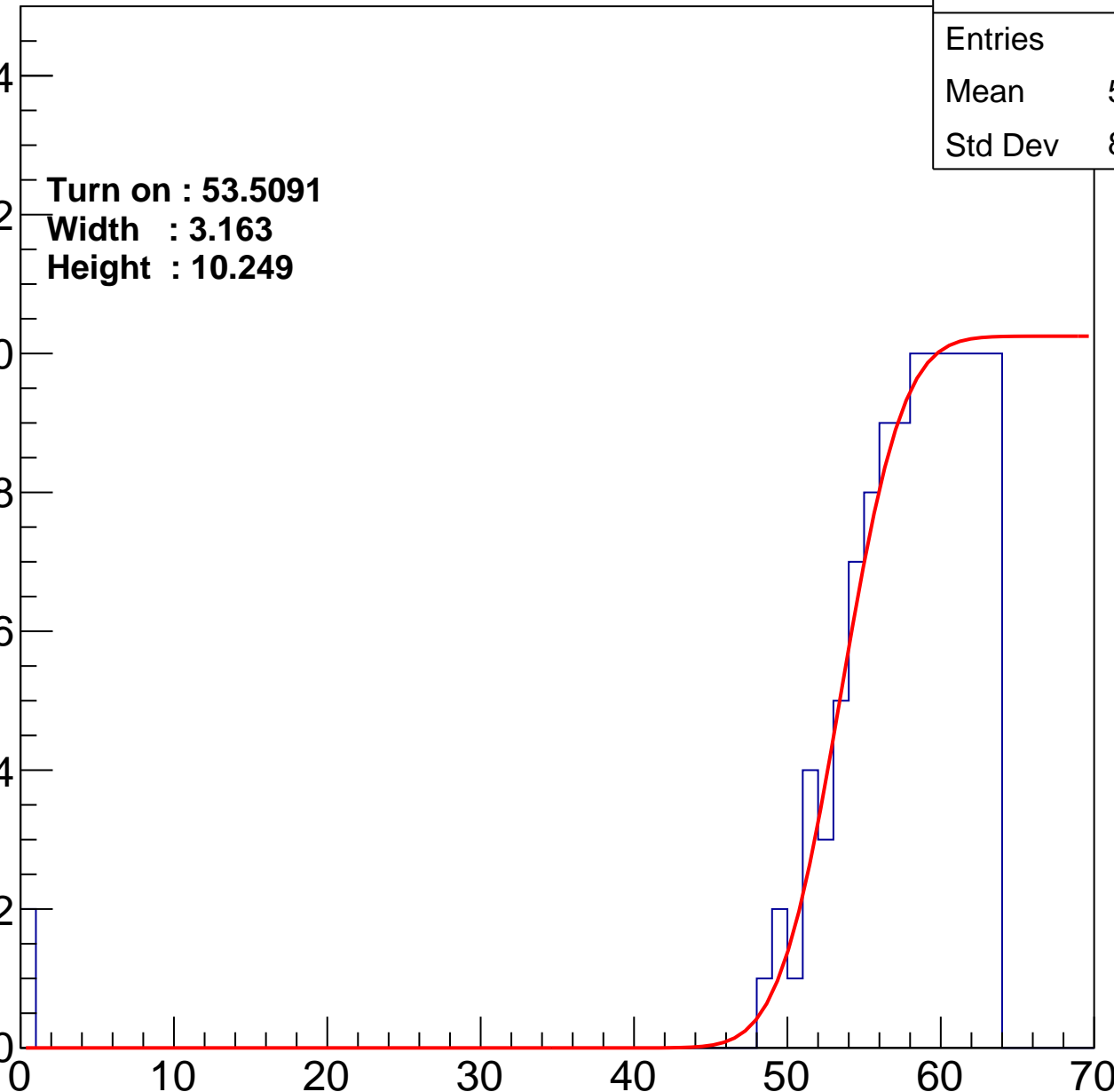
Width : 3.163

Height : 10.249

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	101
Mean	56.49
Std Dev	10.42

Turn on : 54.4414

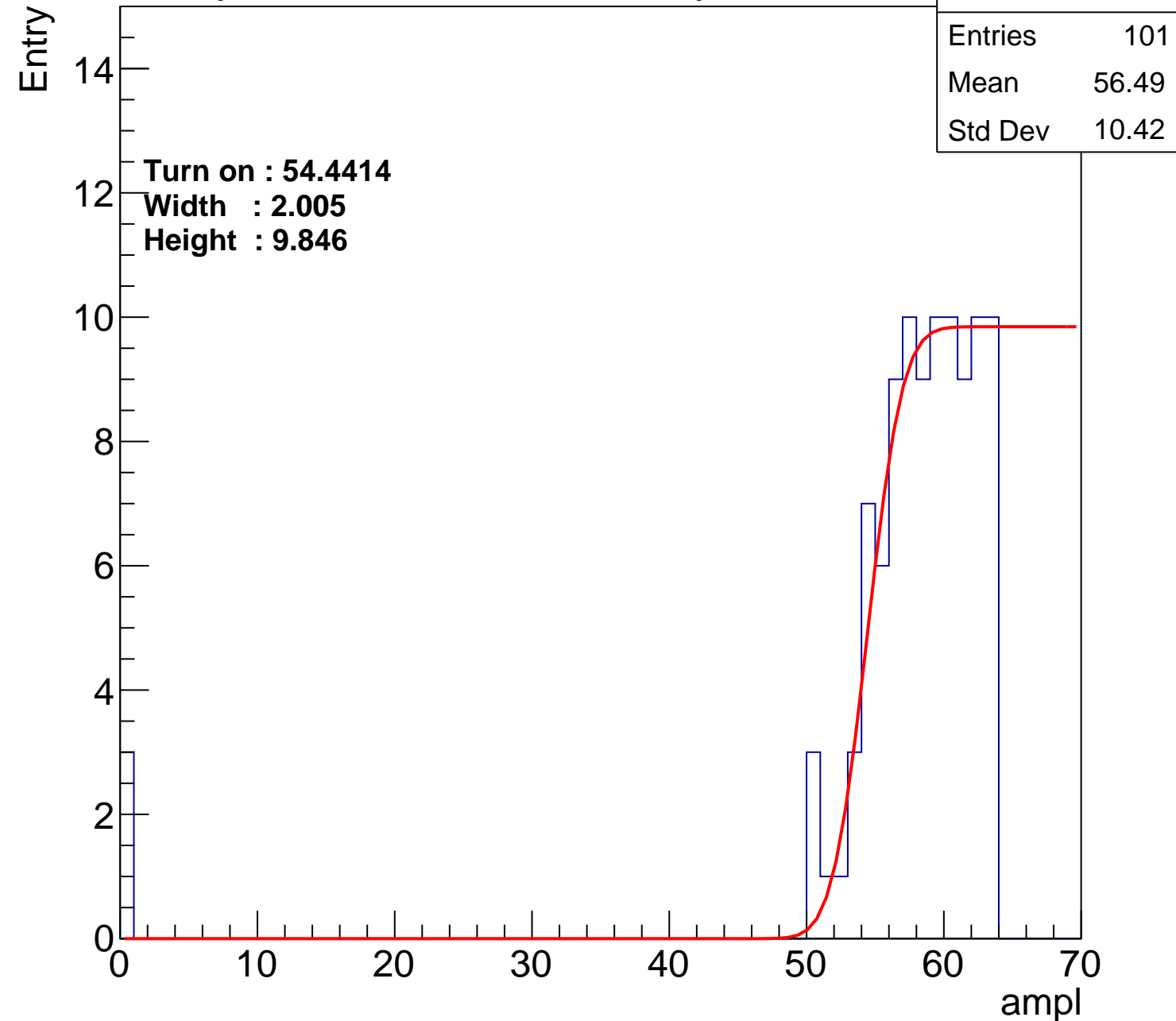
Width : 2.005

Height : 9.846

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

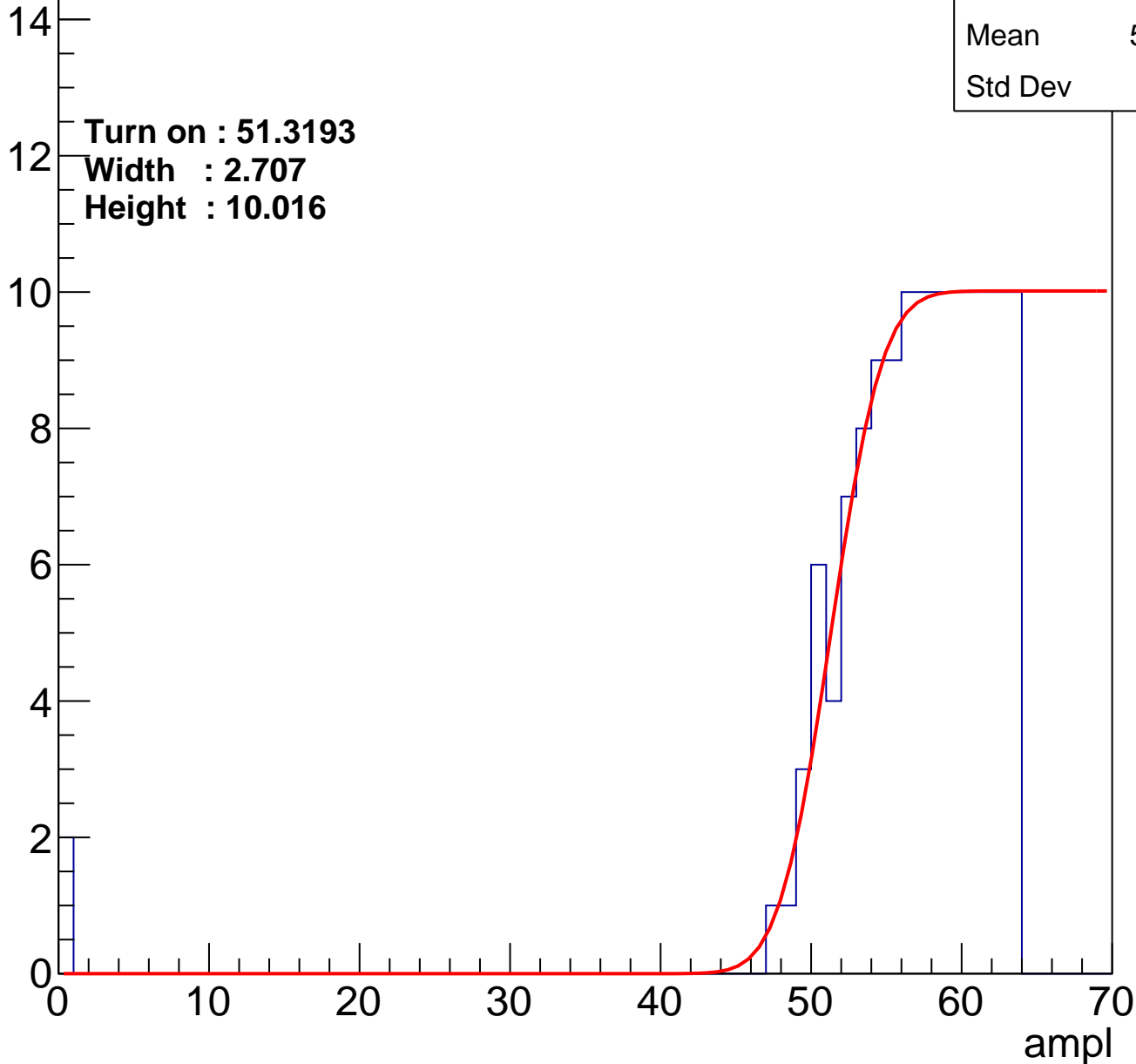
Entries	130
Mean	55.96
Std Dev	8.09

Turn on : 51.3193

Width : 2.707

Height : 10.016

Entry



# B0L103S, U26-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

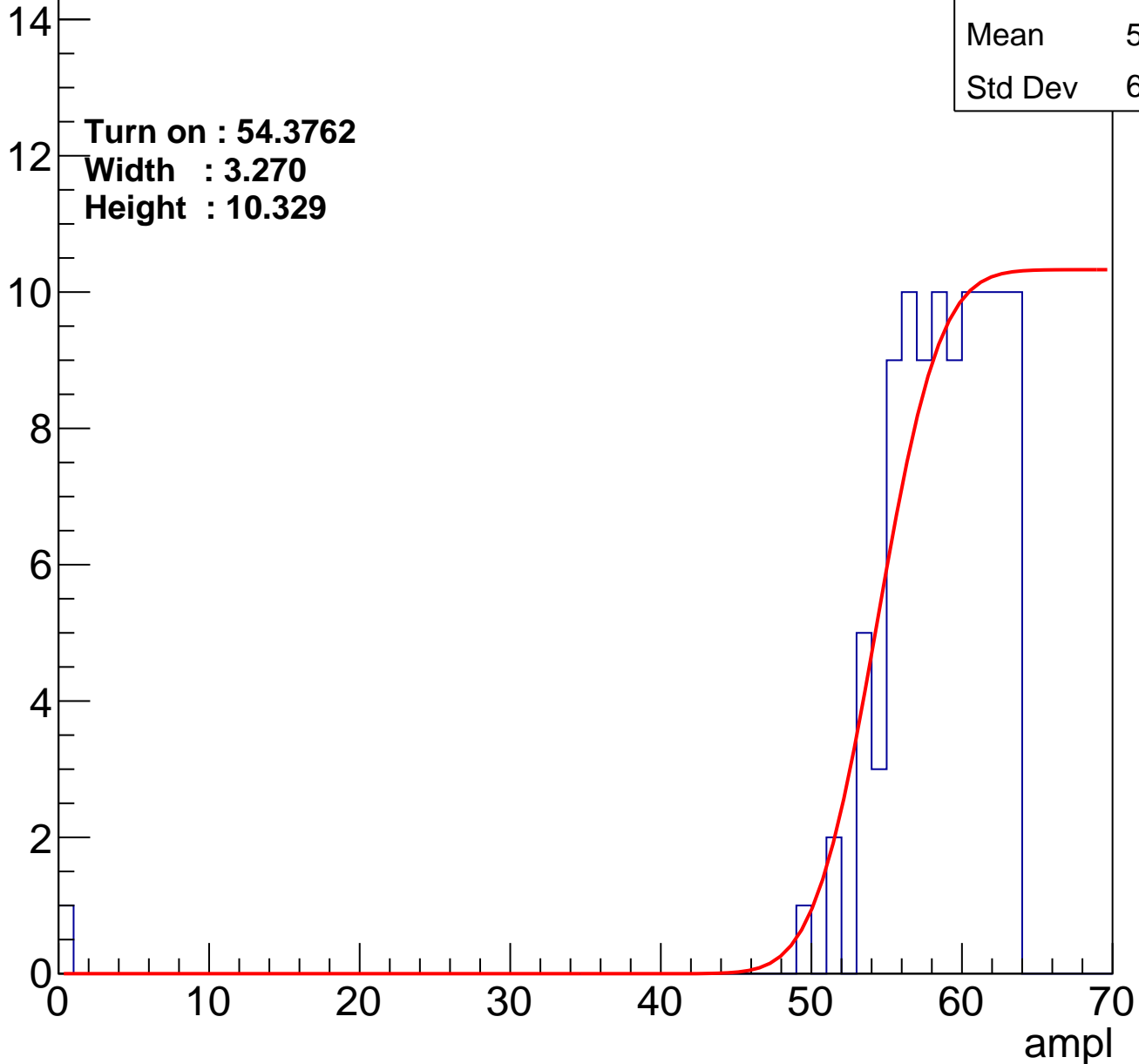
Entries	99
Mean	57.75
Std Dev	6.658

Turn on : 54.3762

Width : 3.270

Height : 10.329

Entry



# B0L103S, U26-ch60

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.62
Std Dev	9.426

Turn on : 51.5599

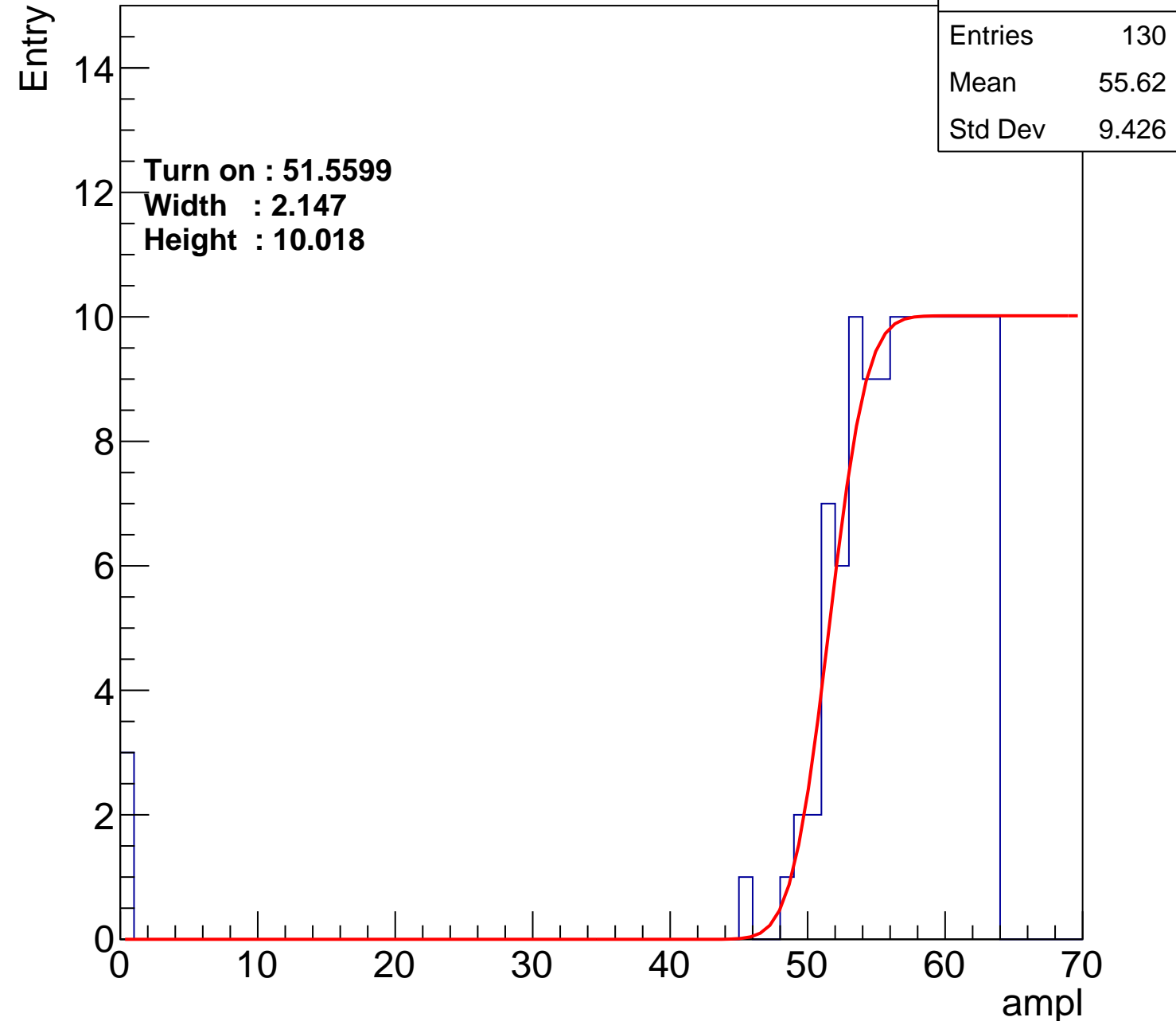
Width : 2.147

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

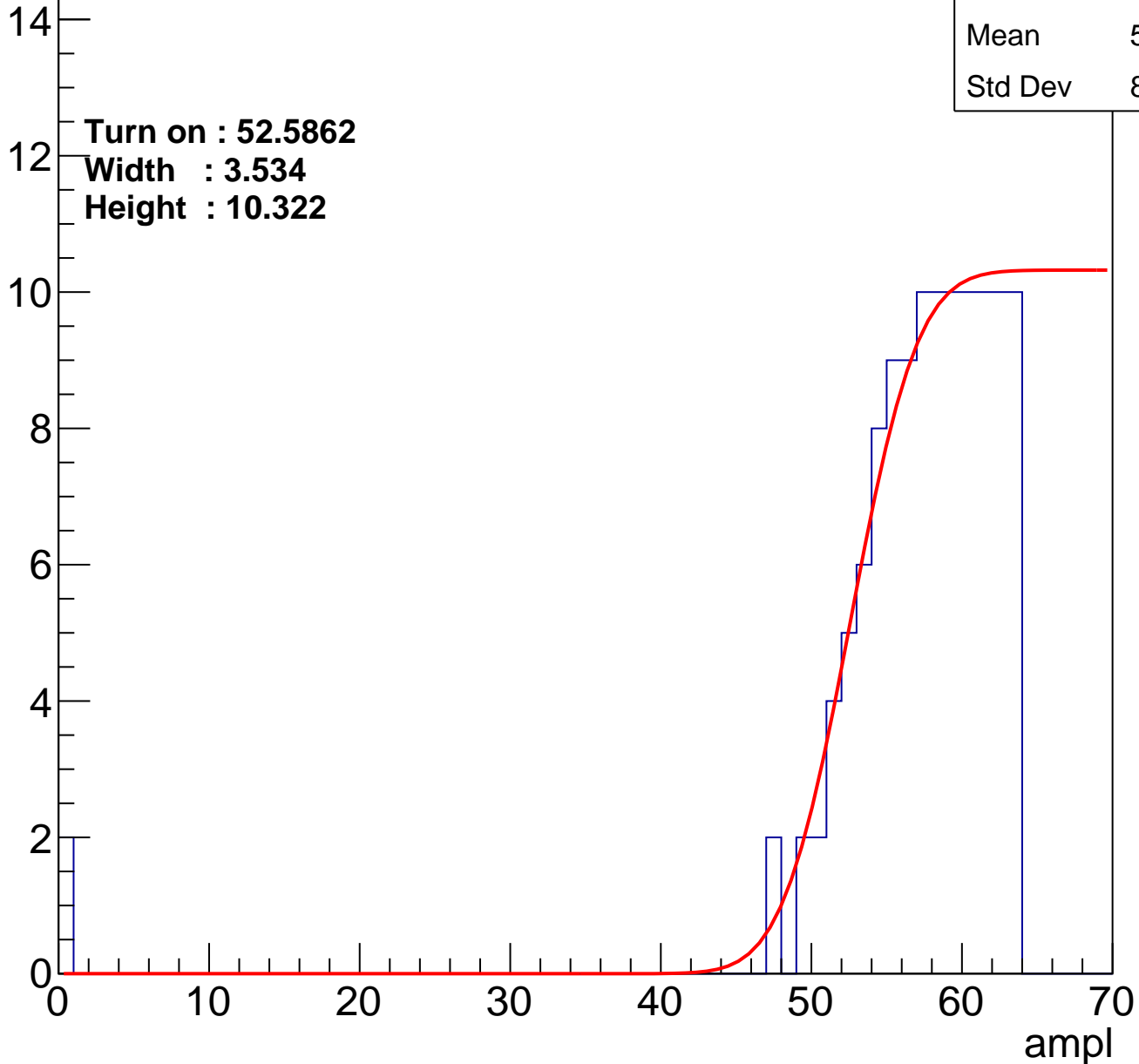
Entries	119
Mean	56.34
Std Dev	8.334

Turn on : 52.5862

Width : 3.534

Height : 10.322

Entry



# B0L103S, U26-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.7
Std Dev	10.35

Turn on : 50.5587

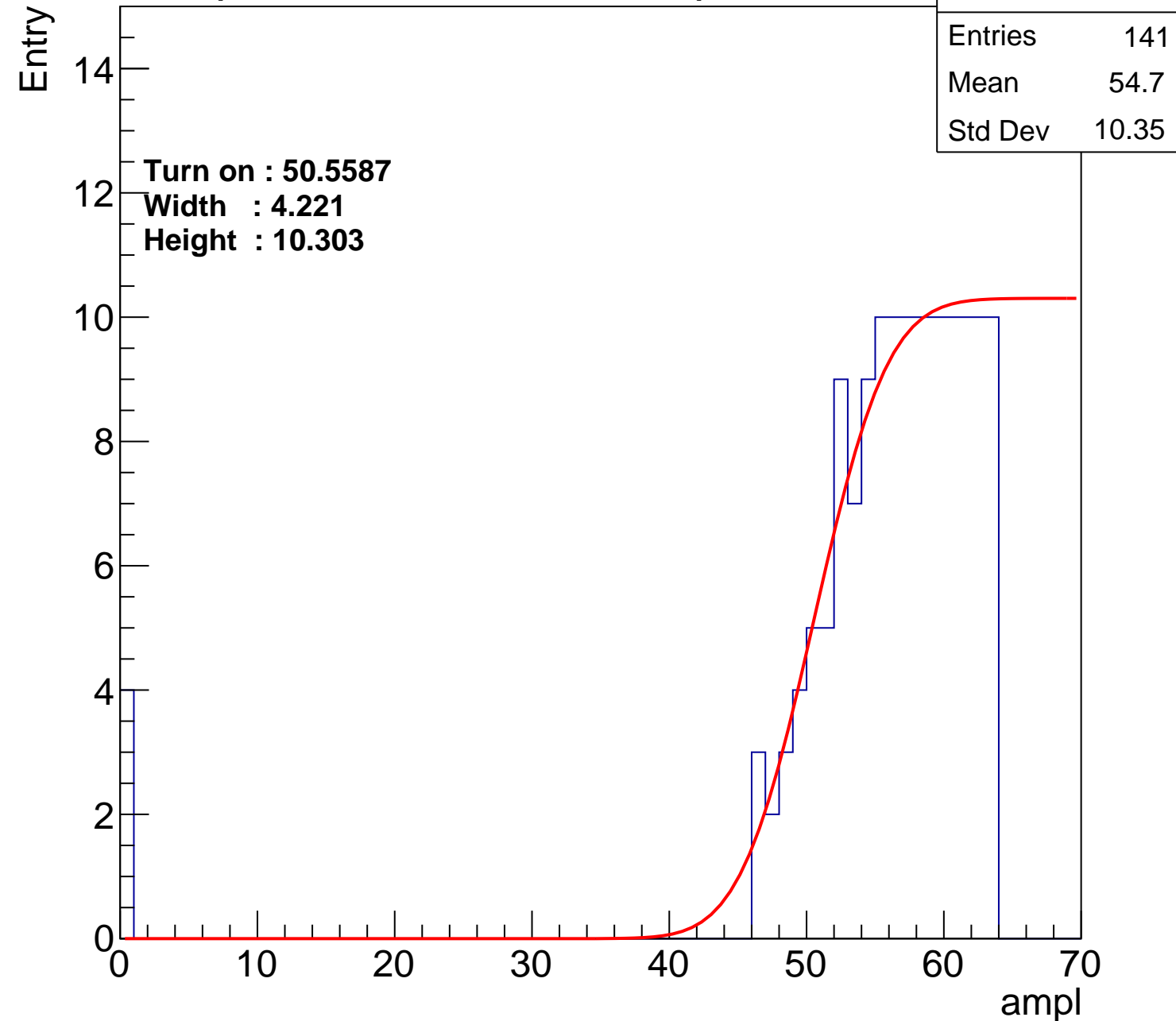
Width : 4.221

Height : 10.303

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch63

calib\_packv5\_040323\_1717.root, FC#2, port C3

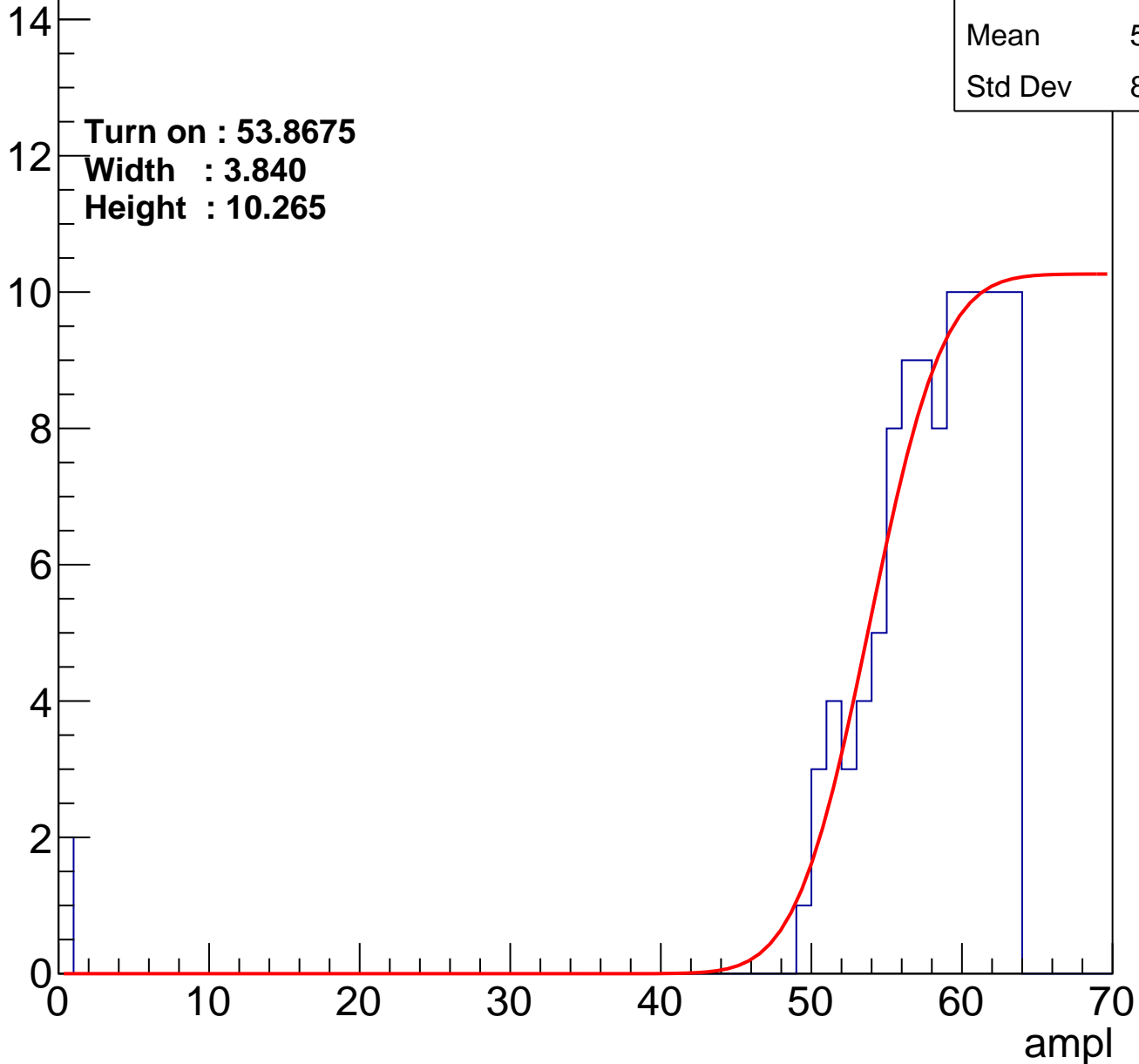
Entries	106
Mean	56.72
Std Dev	8.675

**Turn on : 53.8675**

**Width : 3.840**

**Height : 10.265**

Entry



# B0L103S, U26-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	54.57
Std Dev	11.39

Turn on : 51.6395

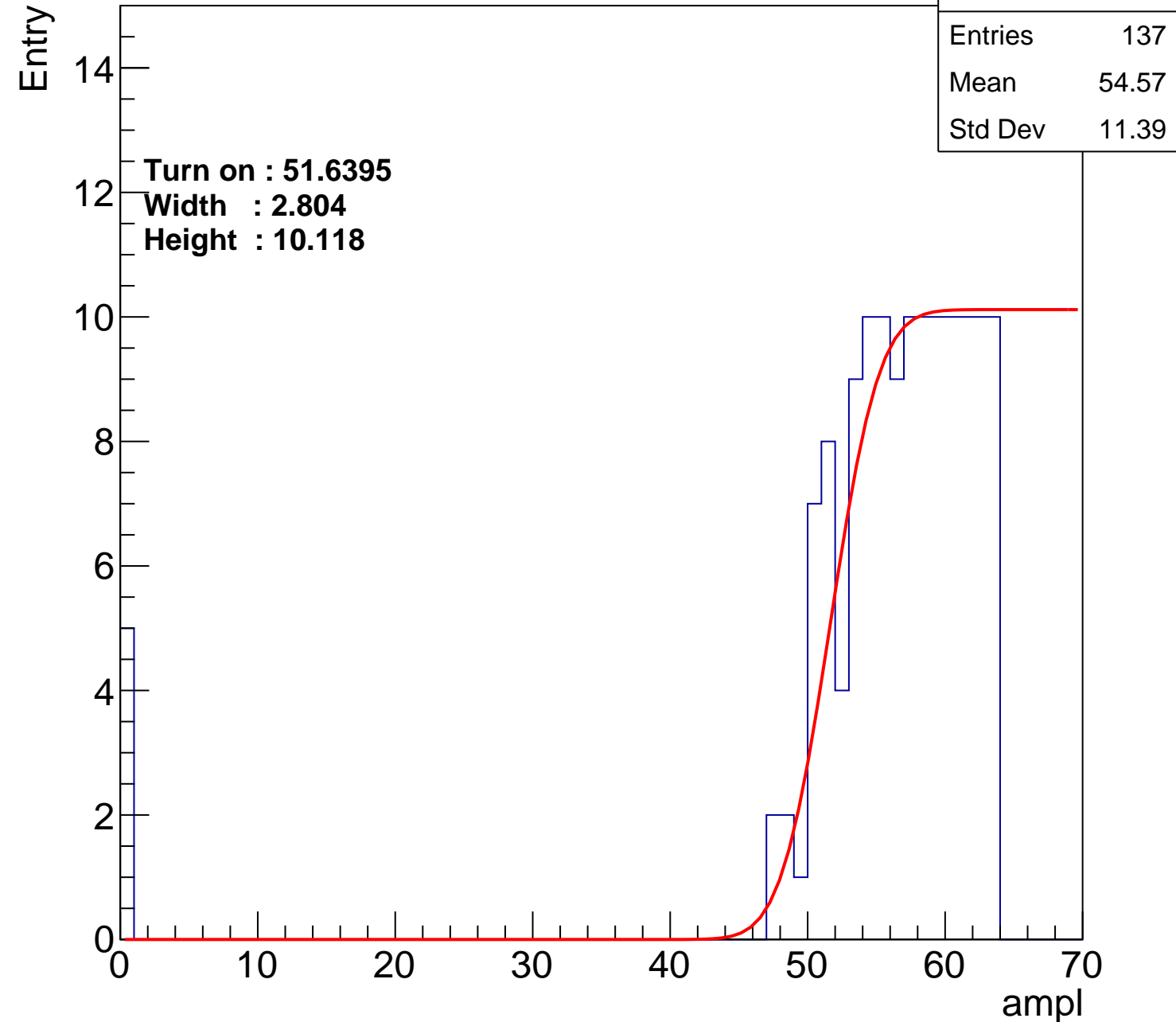
Width : 2.804

Height : 10.118

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	149
Mean	53.84
Std Dev	11.88

Turn on : 49.8552

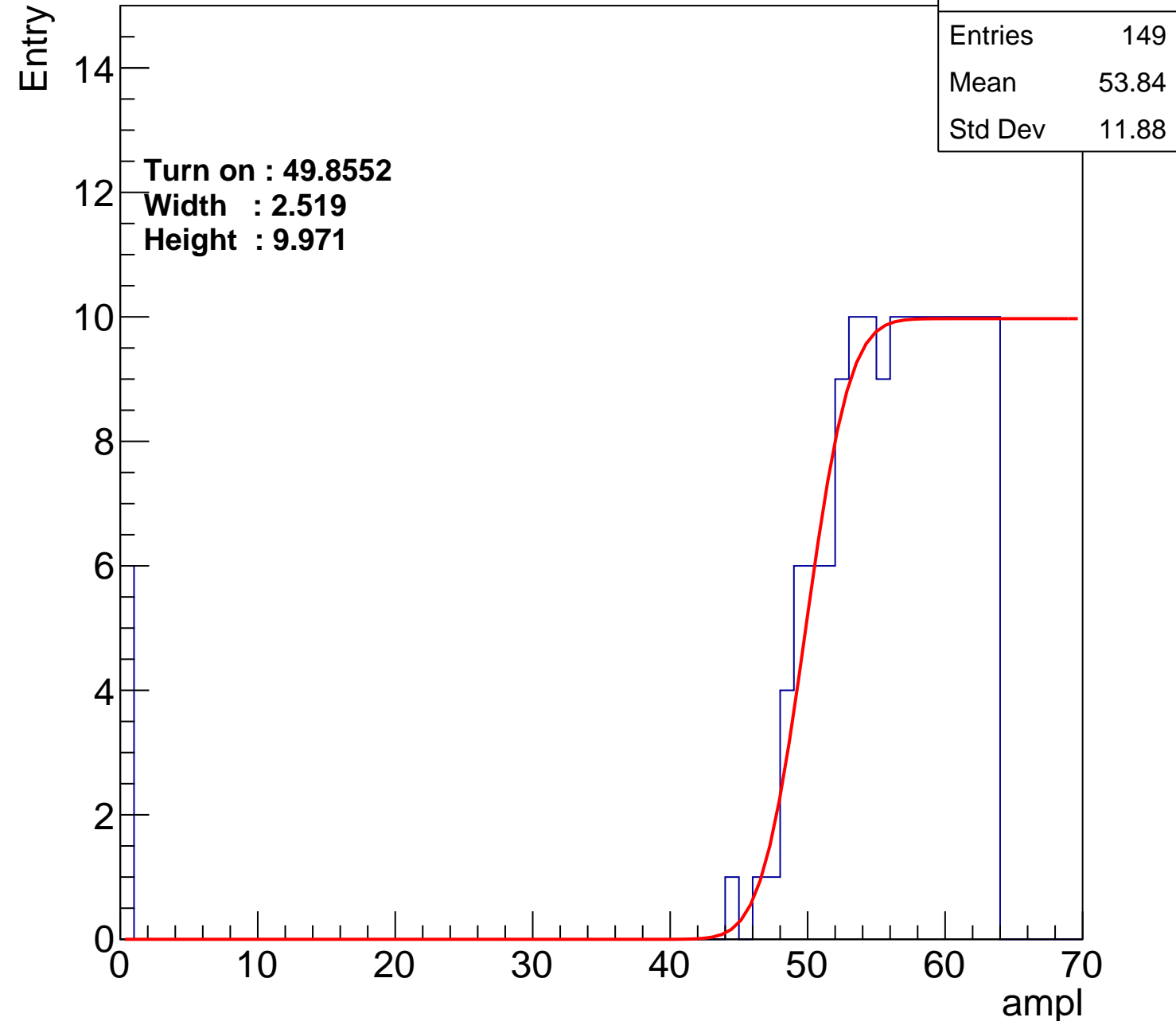
Width : 2.519

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

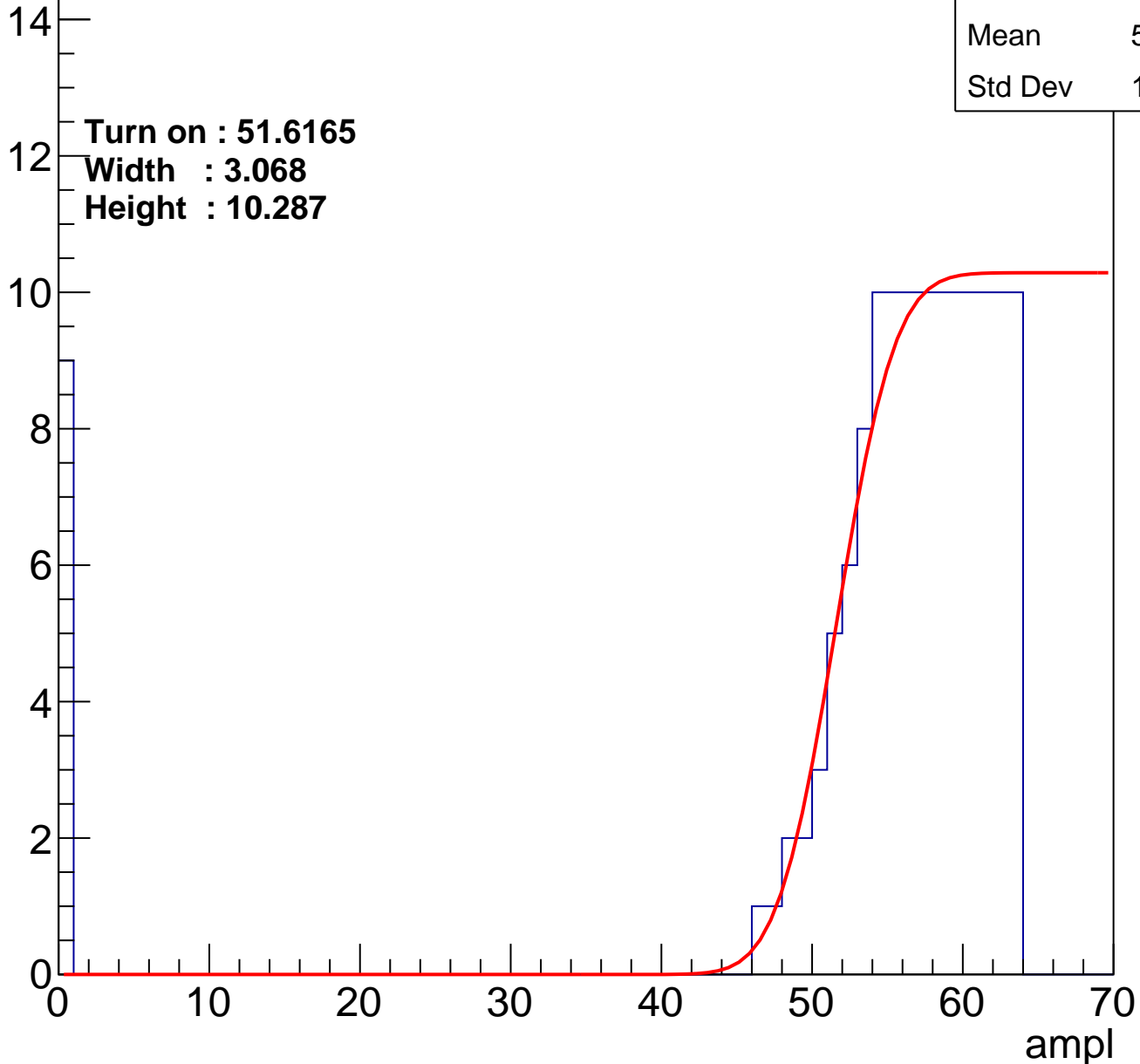
Entries	137
Mean	53.12
Std Dev	14.64

Turn on : 51.6165

Width : 3.068

Height : 10.287

Entry



# B0L103S, U26-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

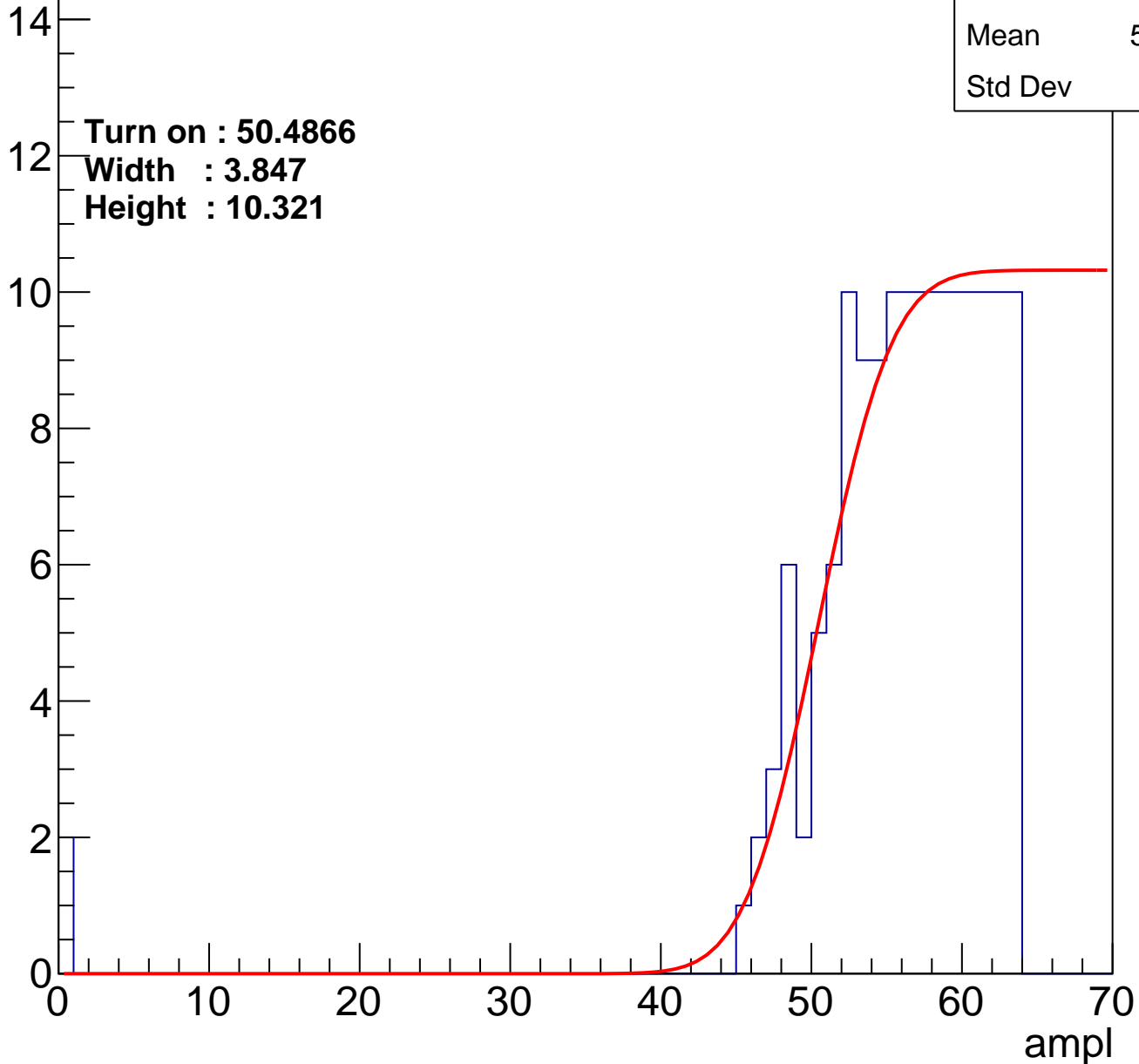
Entries	145
Mean	55.26
Std Dev	7.99

Turn on : 50.4866

Width : 3.847

Height : 10.321

Entry



# B0L103S, U26-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.58
Std Dev	8.324

Turn on : 53.0066

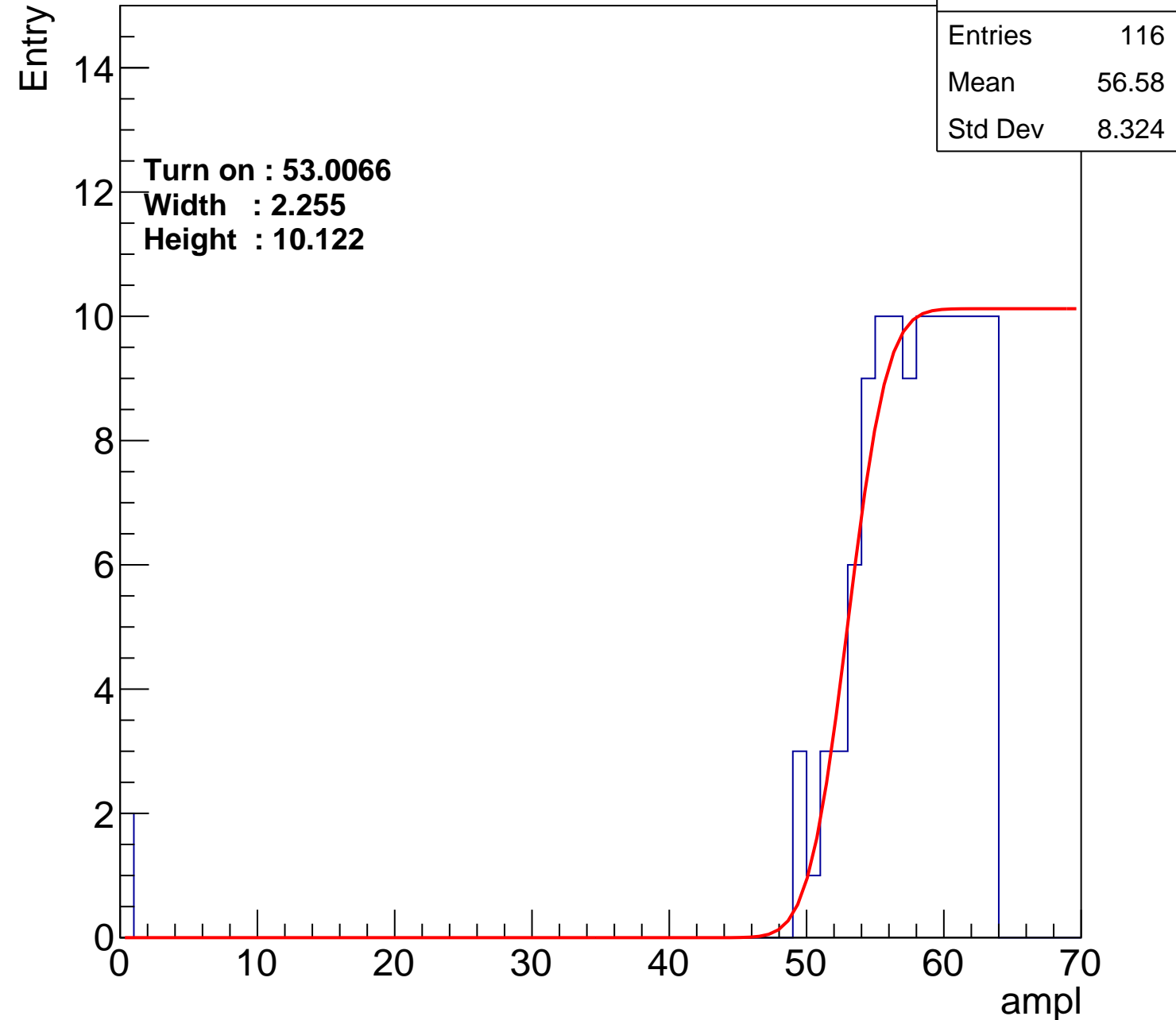
Width : 2.255

Height : 10.122

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

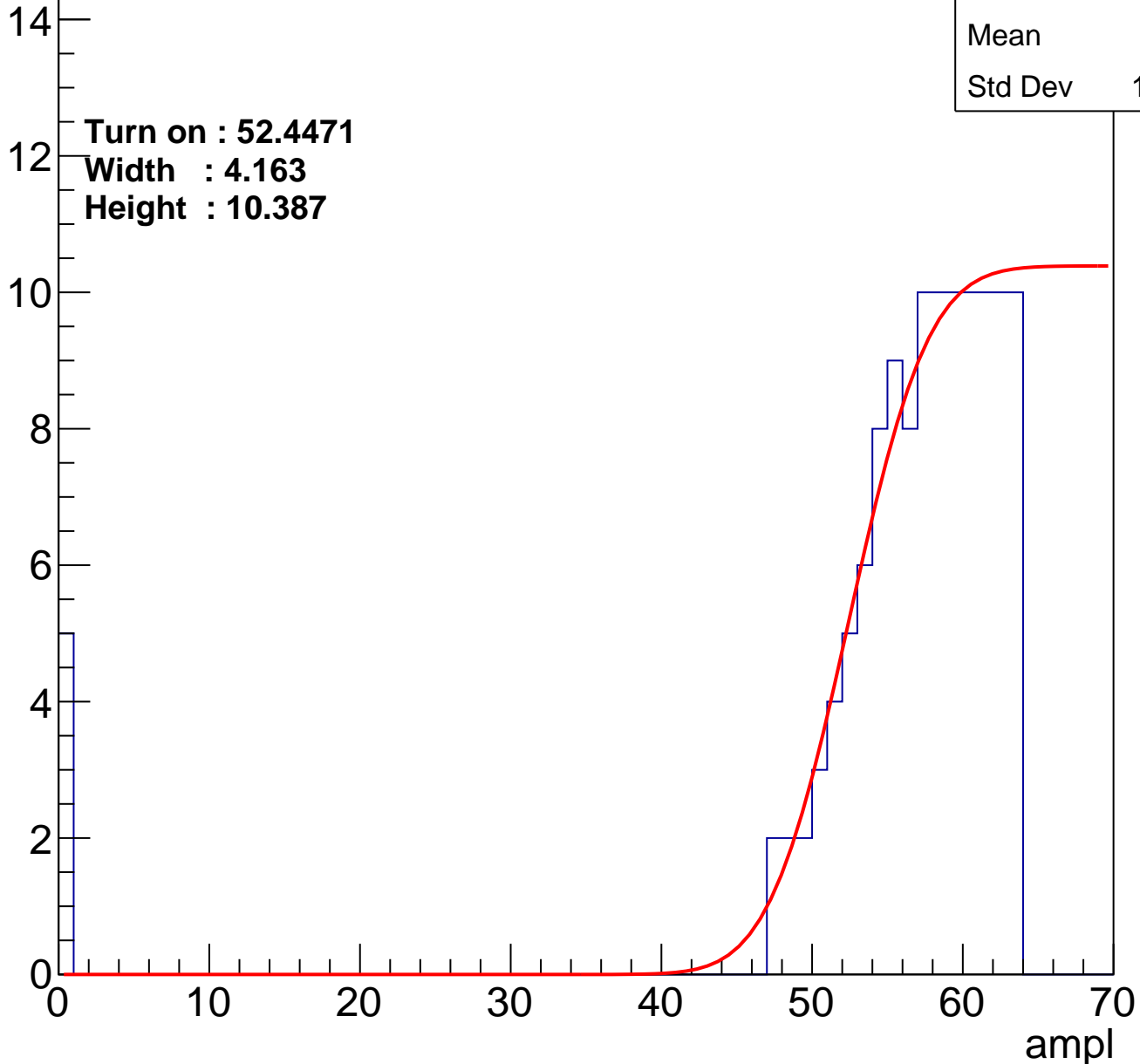
Entries	124
Mean	54.8
Std Dev	11.94

Turn on : 52.4471

Width : 4.163

Height : 10.387

Entry



# B0L103S, U26-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	56.44
Std Dev	6.522

Turn on : 50.4026

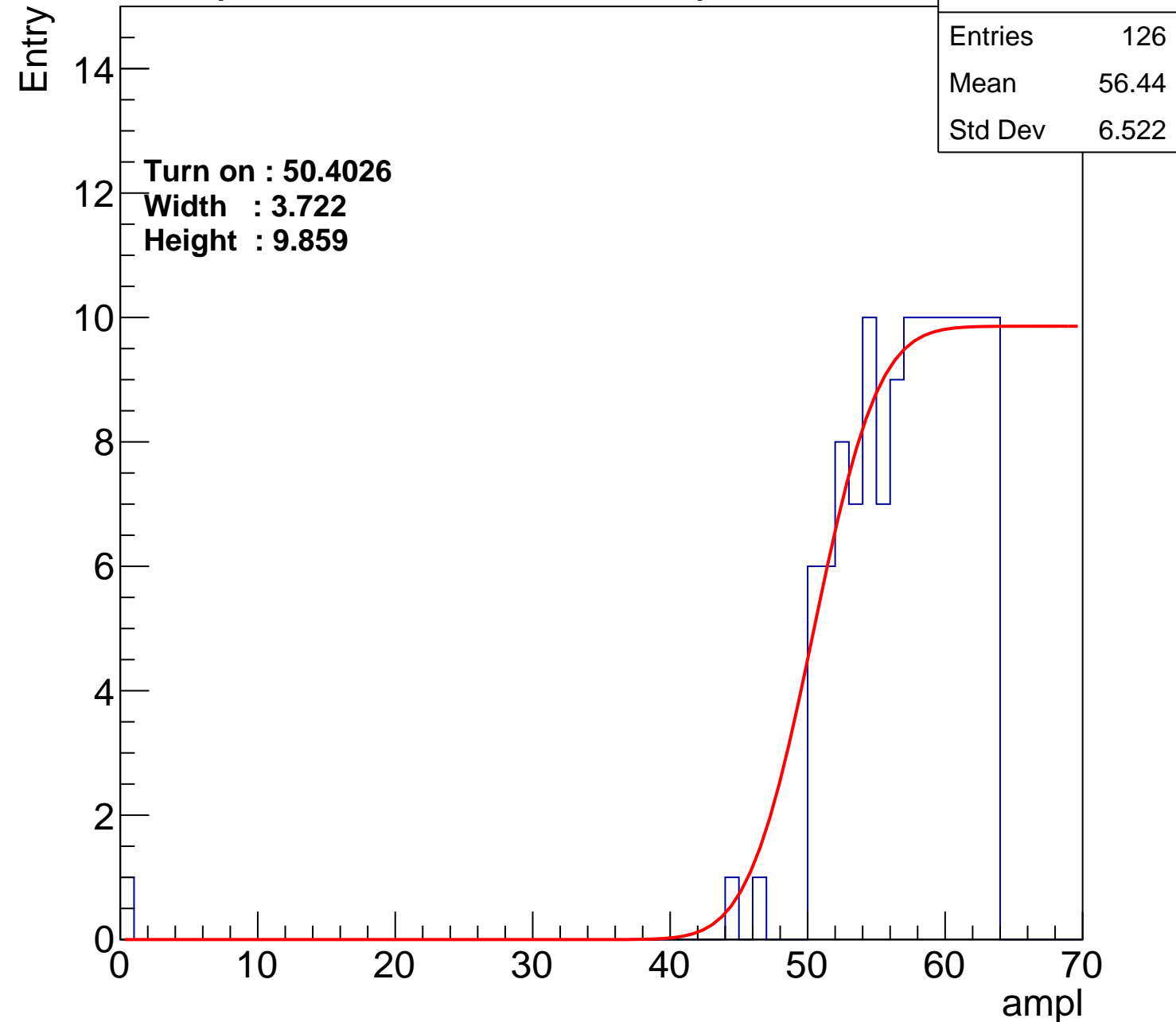
Width : 3.722

Height : 9.859

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	55.91
Std Dev	9.943

Turn on : 53.7490

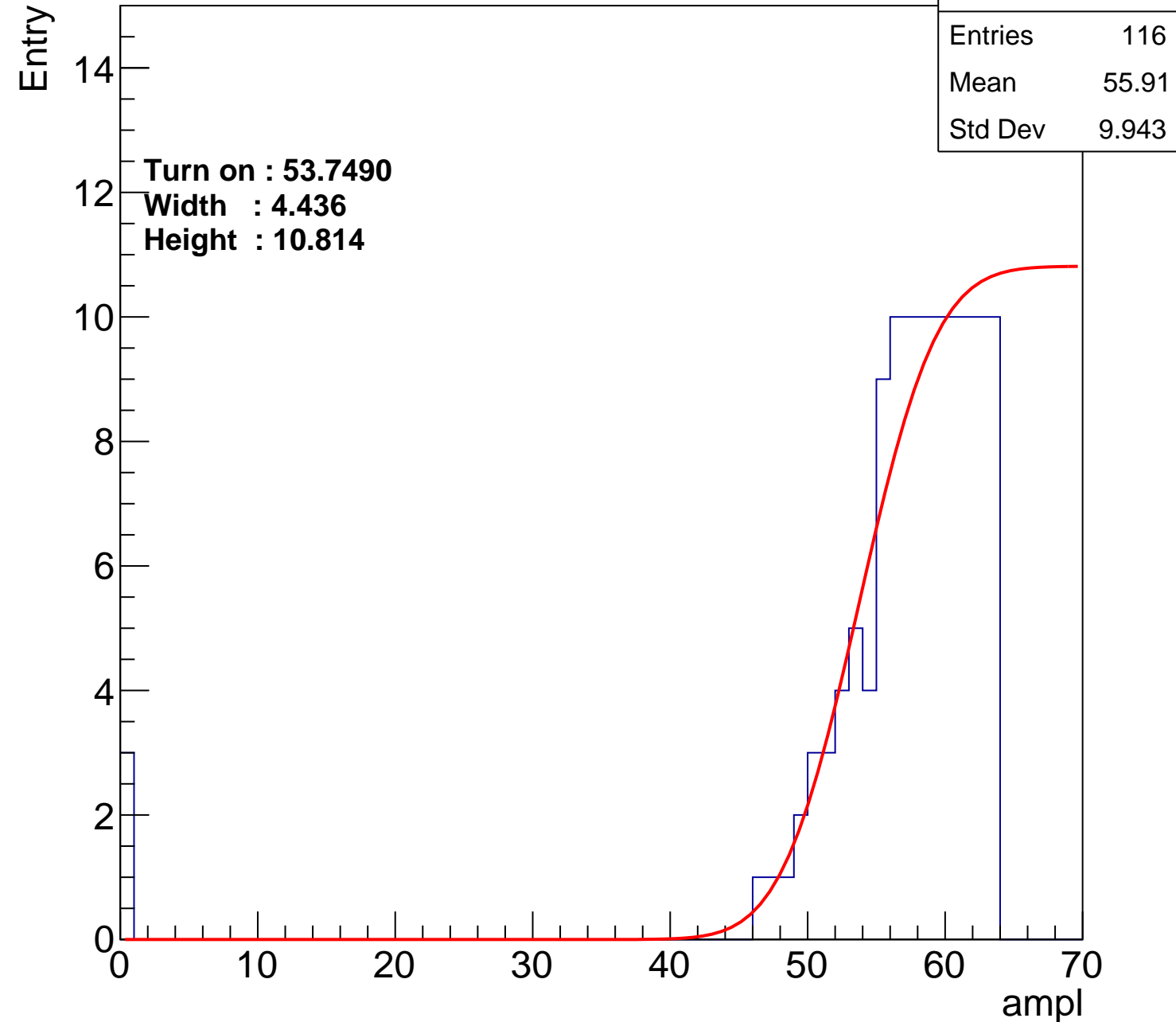
Width : 4.436

Height : 10.814

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	55.23
Std Dev	12.73

Turn on : 54.6393

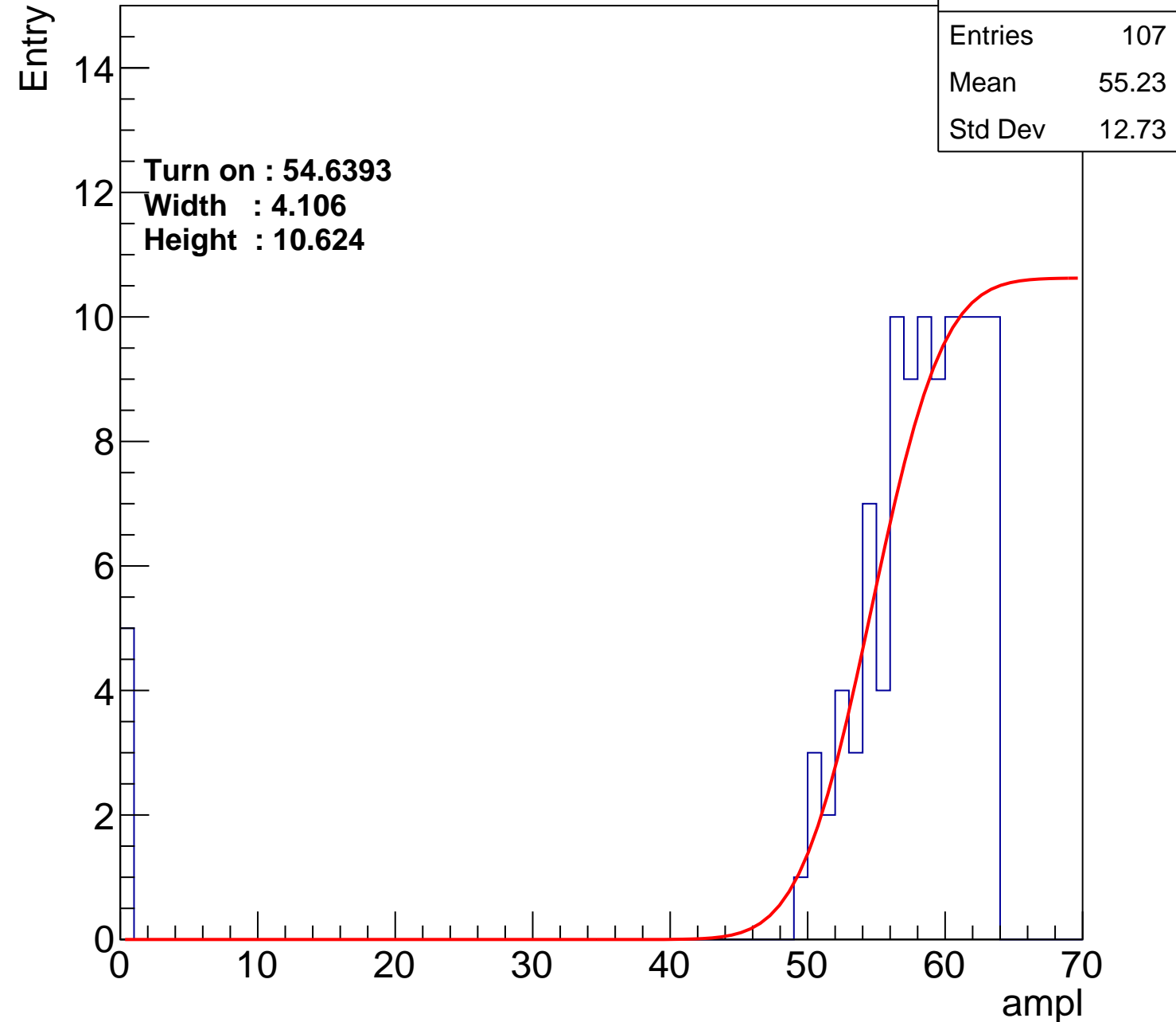
Width : 4.106

Height : 10.624

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.81
Std Dev	6.513

Turn on : 52.6259

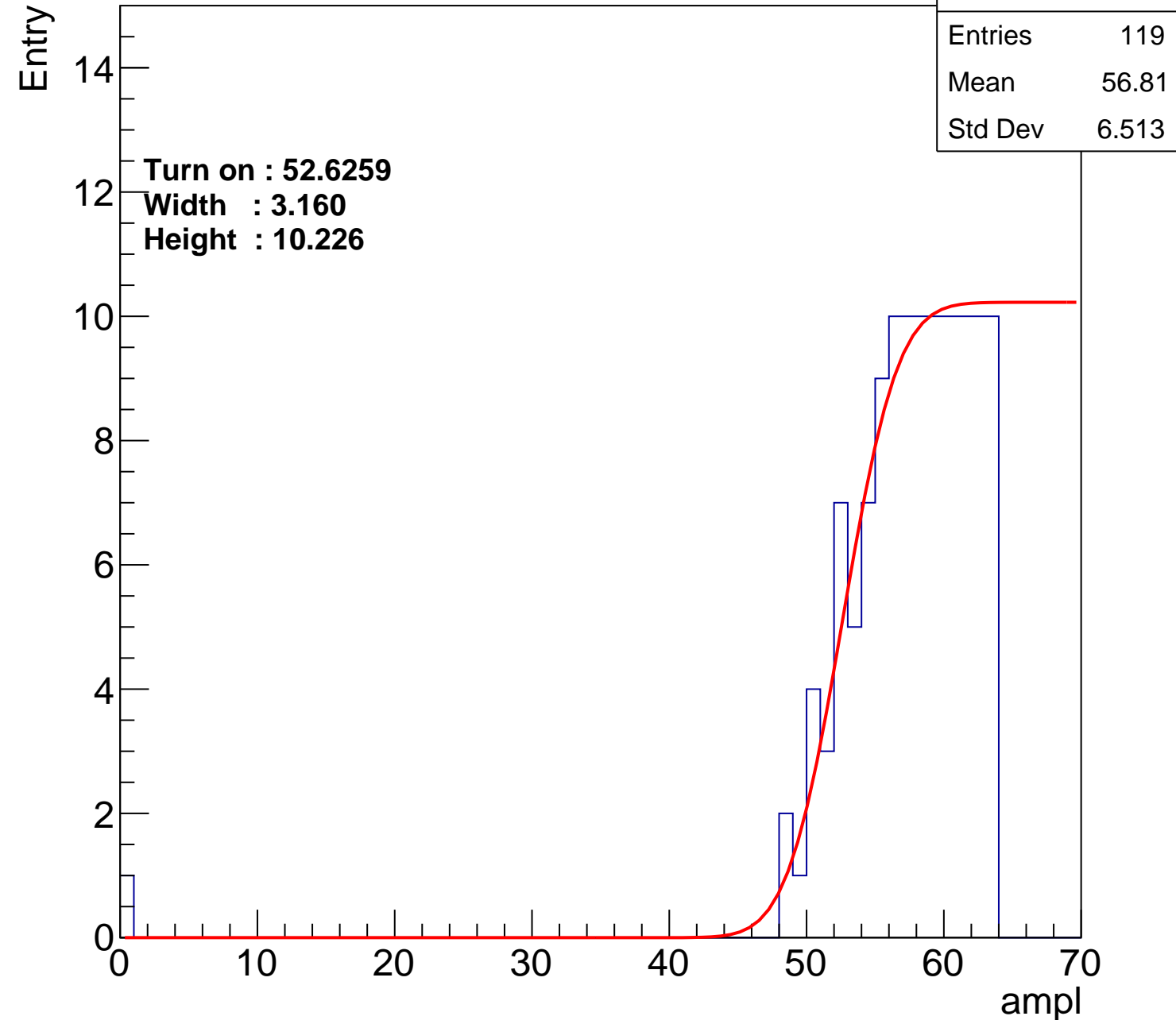
Width : 3.160

Height : 10.226

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.44
Std Dev	9.301

Turn on : 50.9214

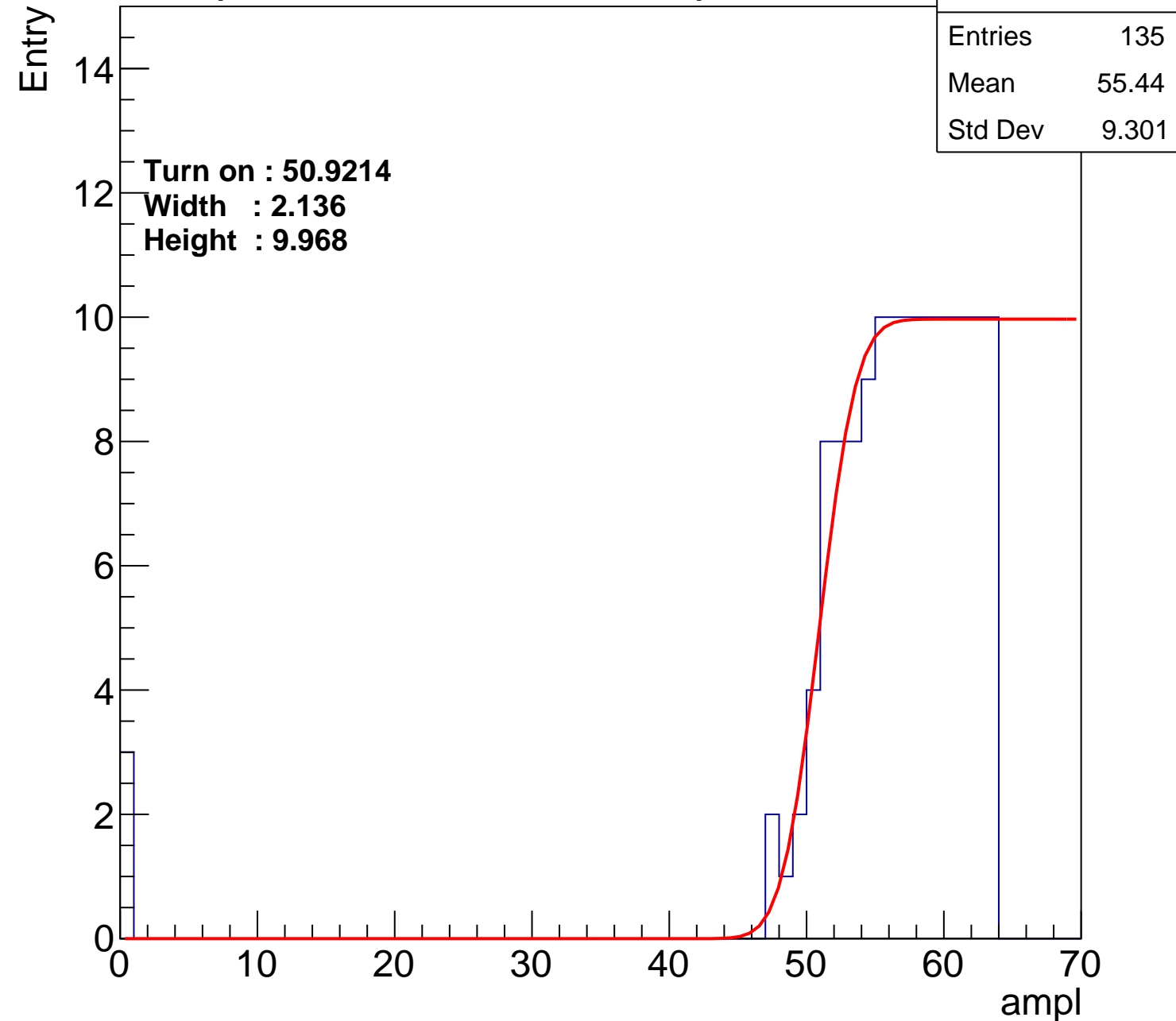
Width : 2.136

Height : 9.968

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	55.65
Std Dev	11.55

Turn on : 54.0073

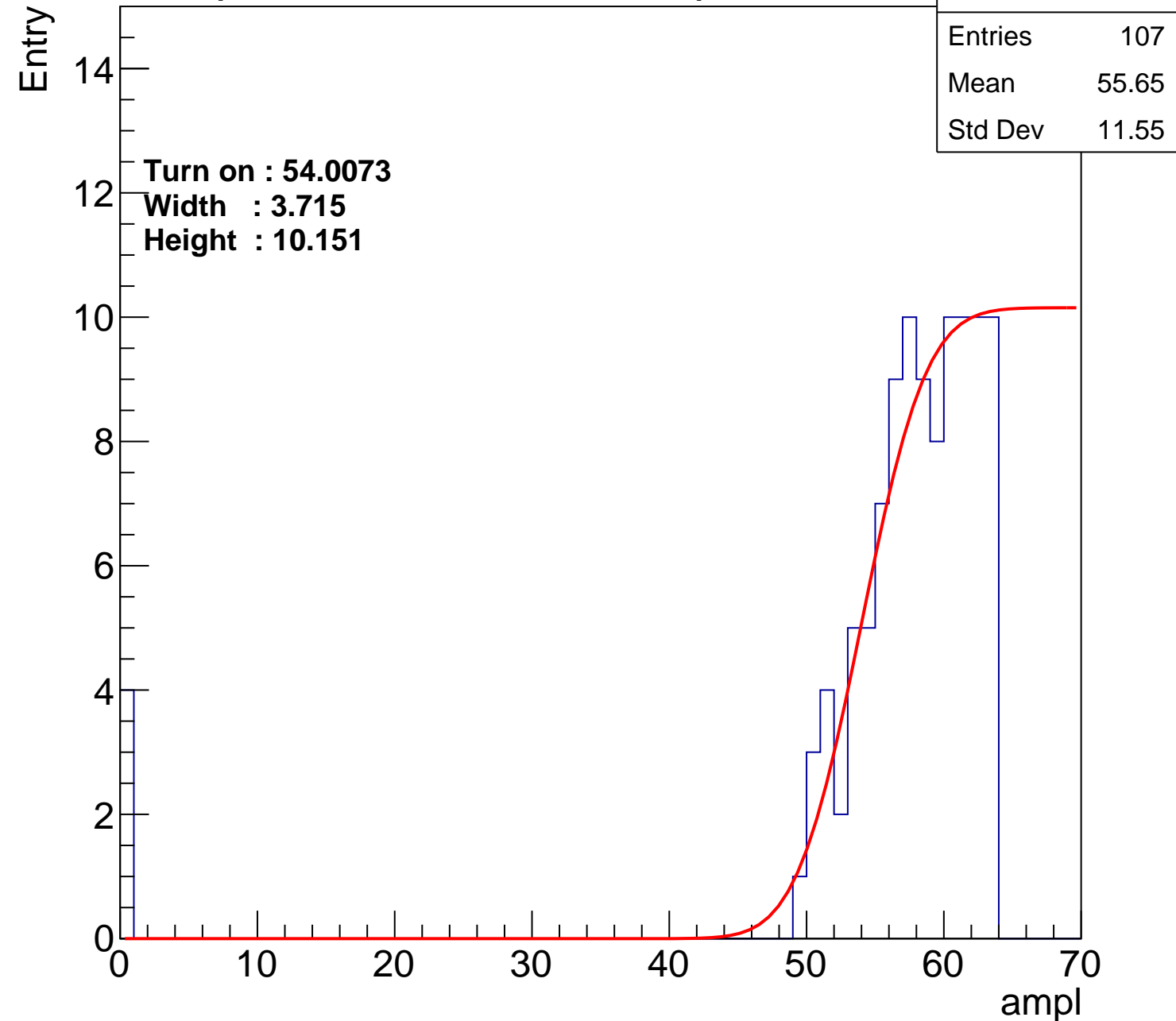
Width : 3.715

Height : 10.151

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

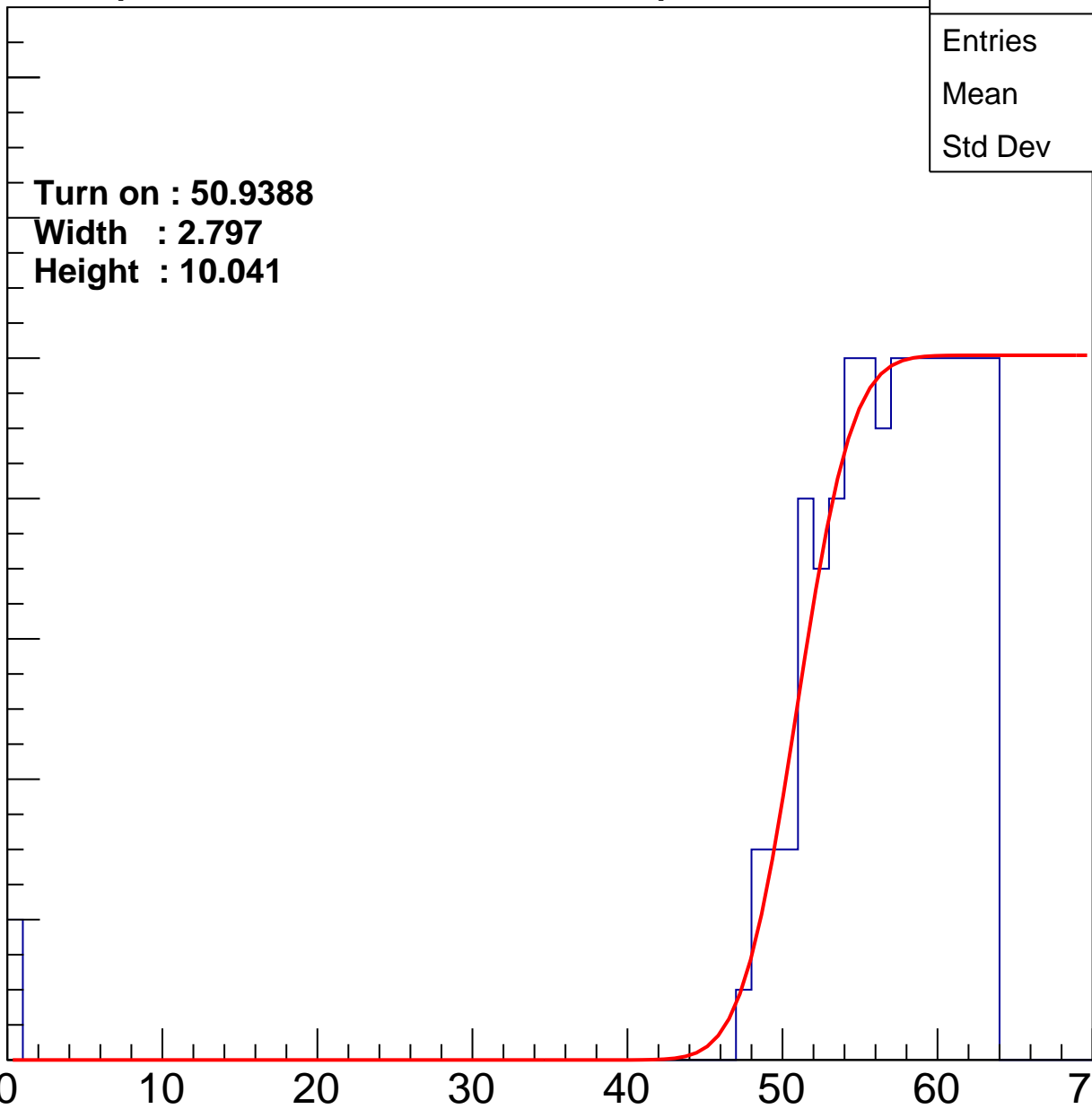
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.9388  
Width : 2.797  
Height : 10.041

Entries	134
Mean	55.81
Std Dev	8.025

ampl



# B0L103S, U26-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	55.54
Std Dev	11.1

Turn on : 52.7806

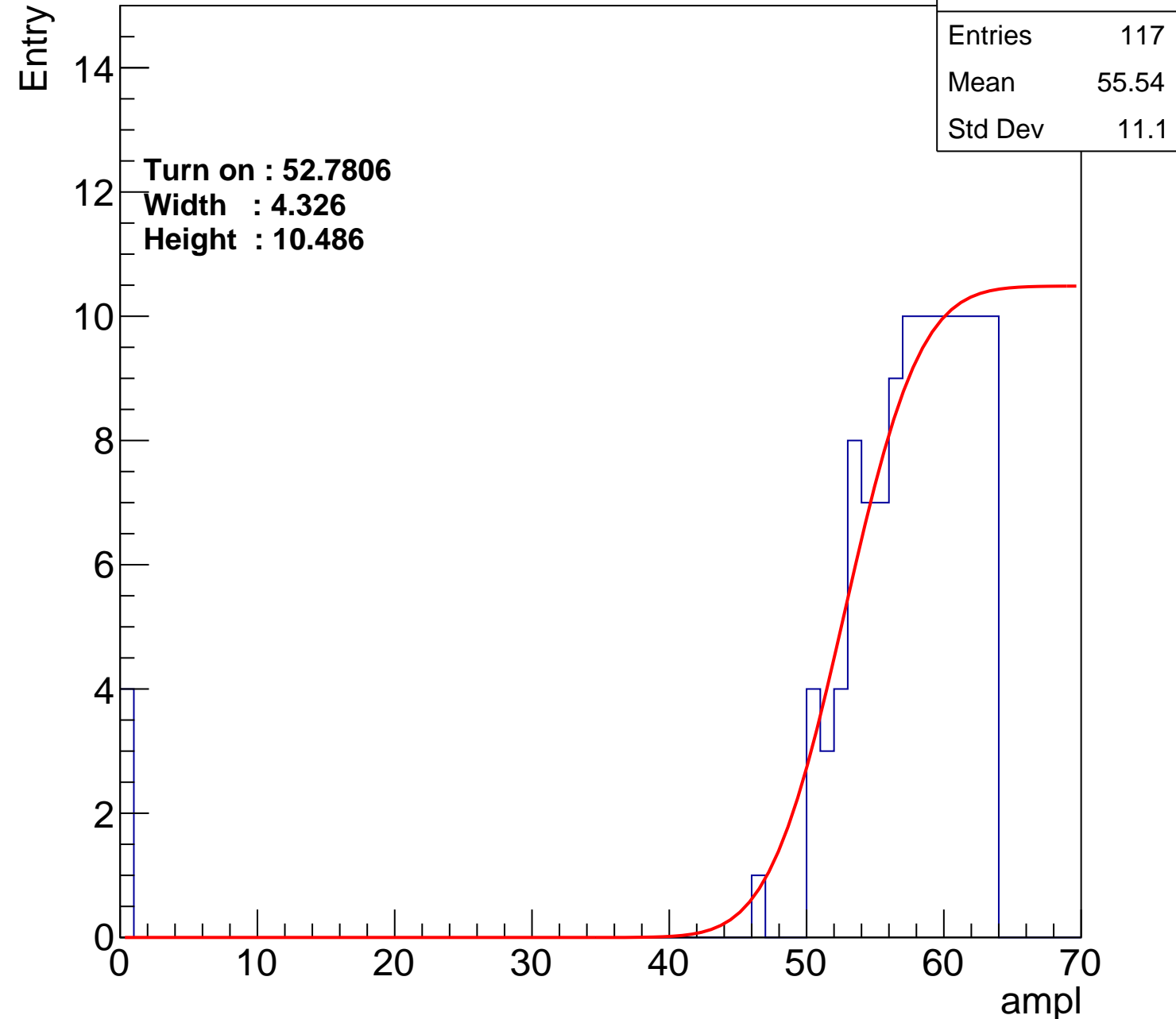
Width : 4.326

Height : 10.486

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.96
Std Dev	9.167

Turn on : 49.6372

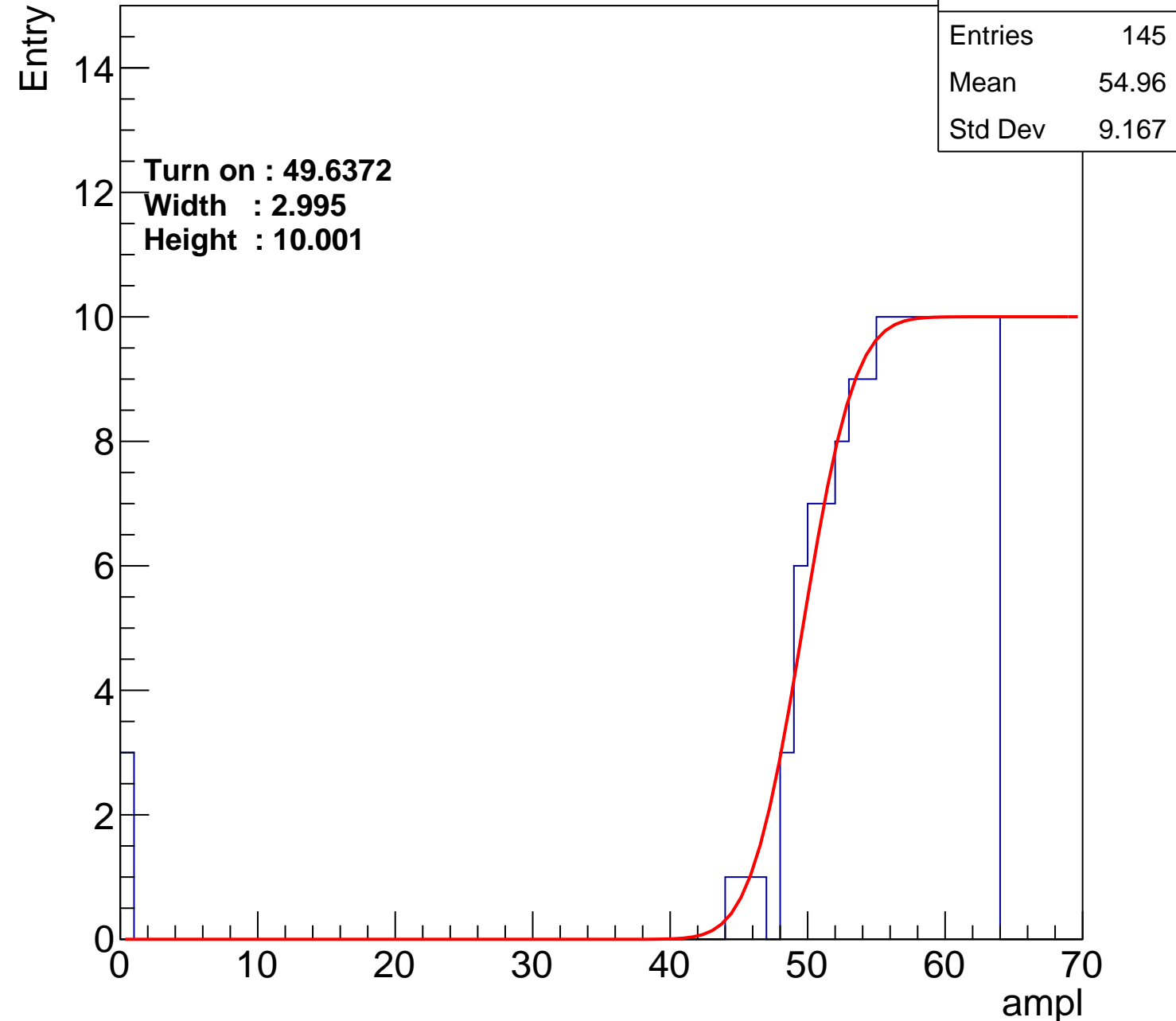
Width : 2.995

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	55.14
Std Dev	12.44

Turn on : 52.9725

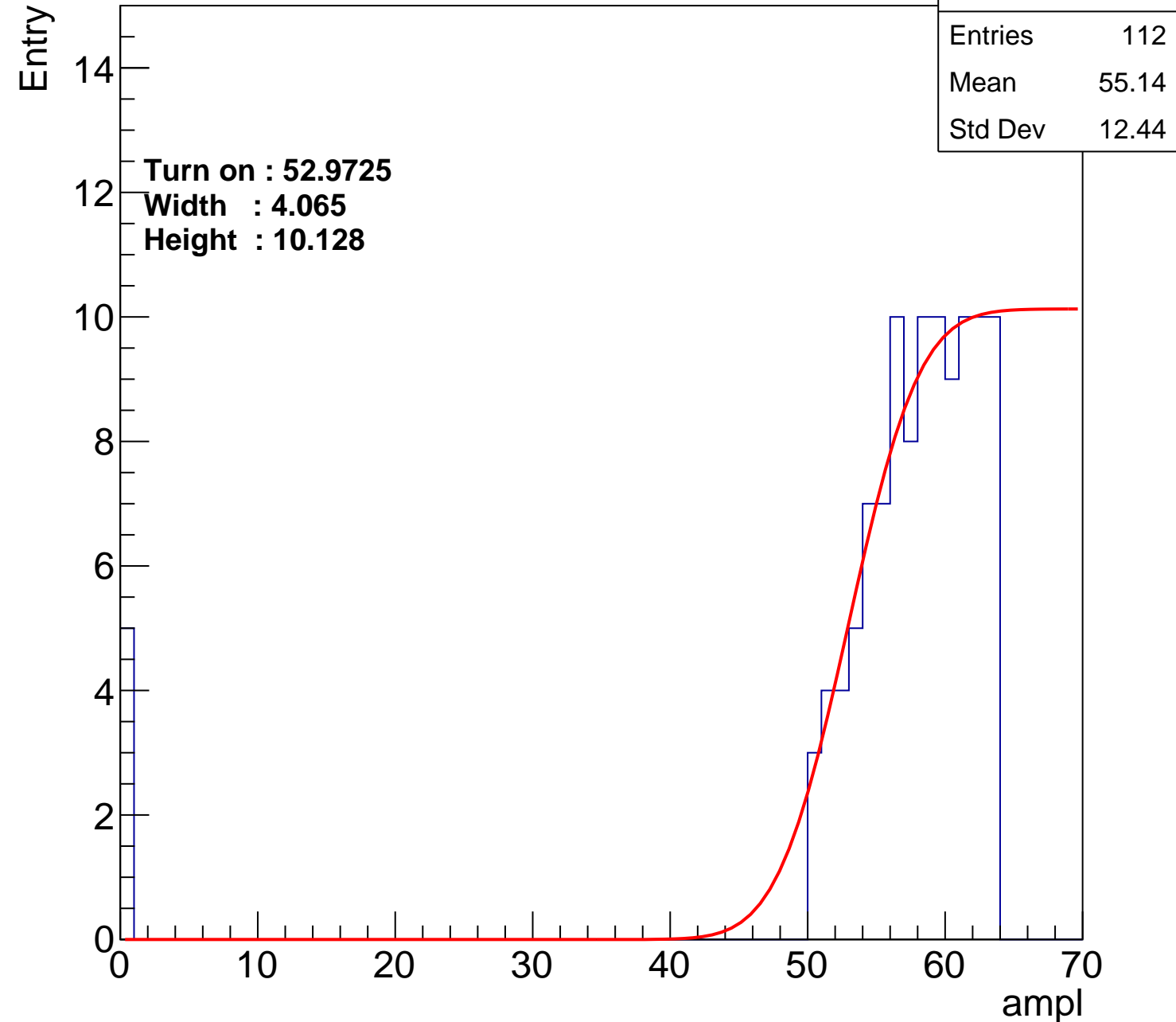
Width : 4.065

Height : 10.128

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.29
Std Dev	8.439

Turn on : 51.8346

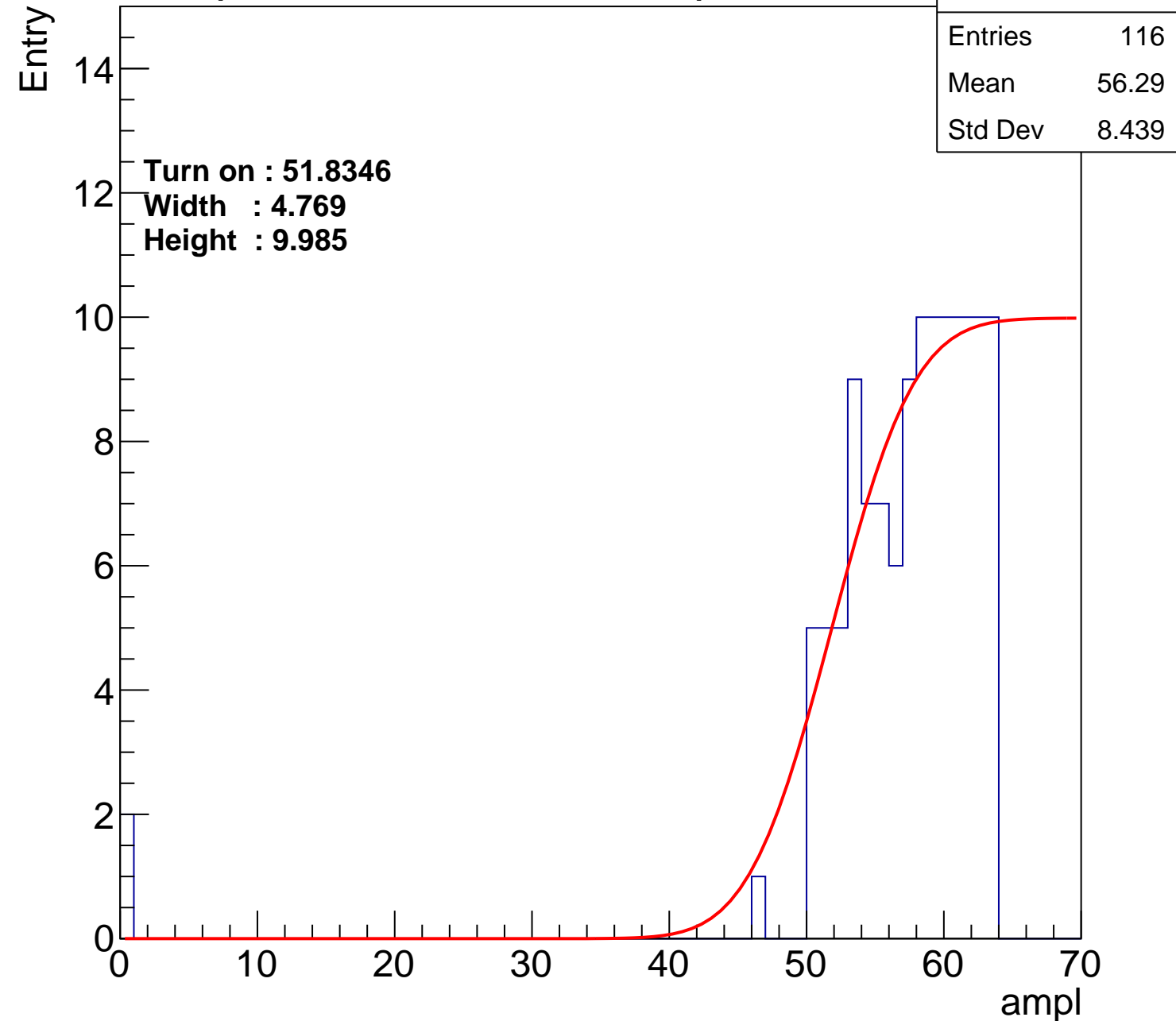
Width : 4.769

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch81

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	55.55
Std Dev	9.568

Turn on : 51.5347

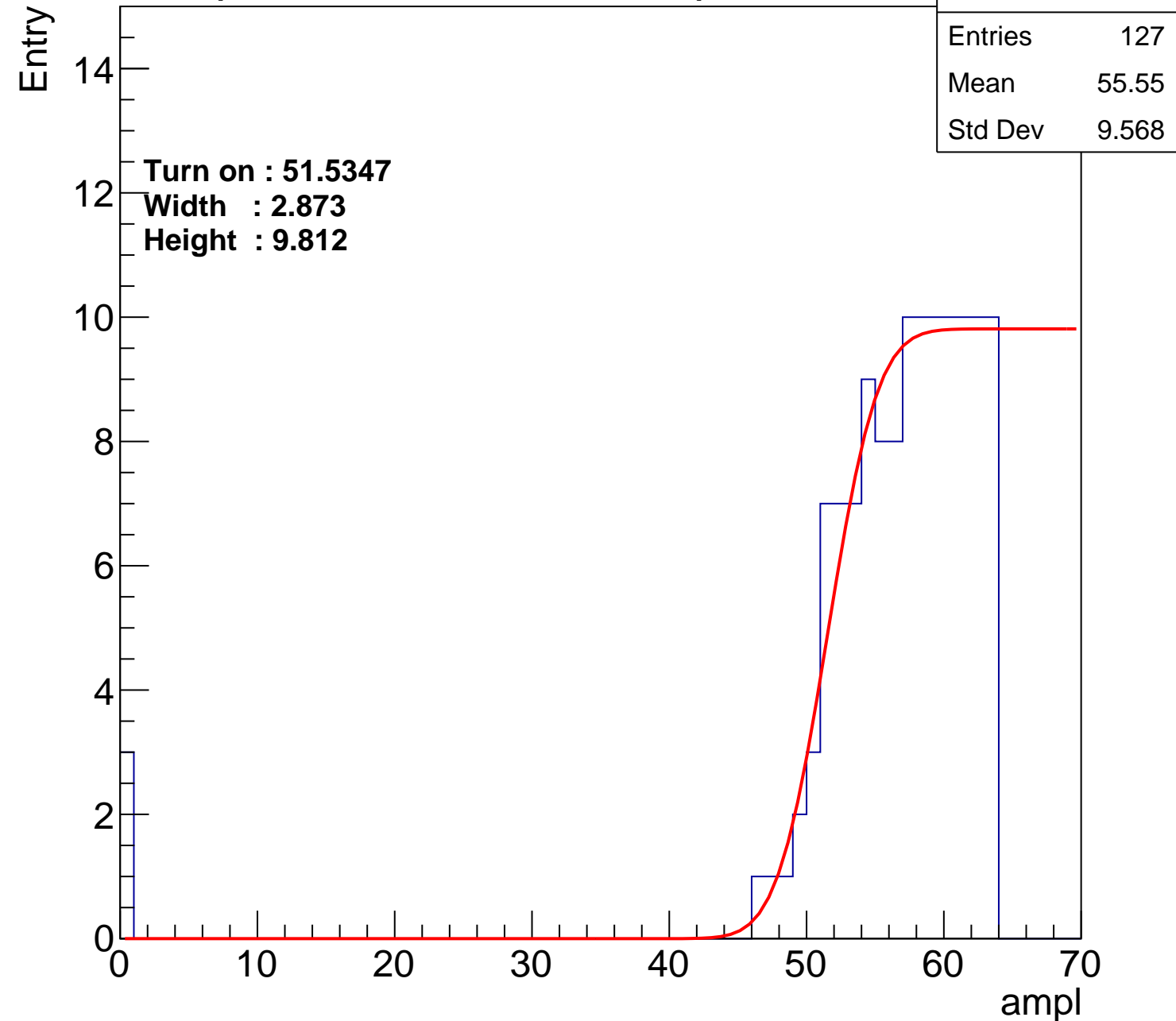
Width : 2.873

Height : 9.812

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

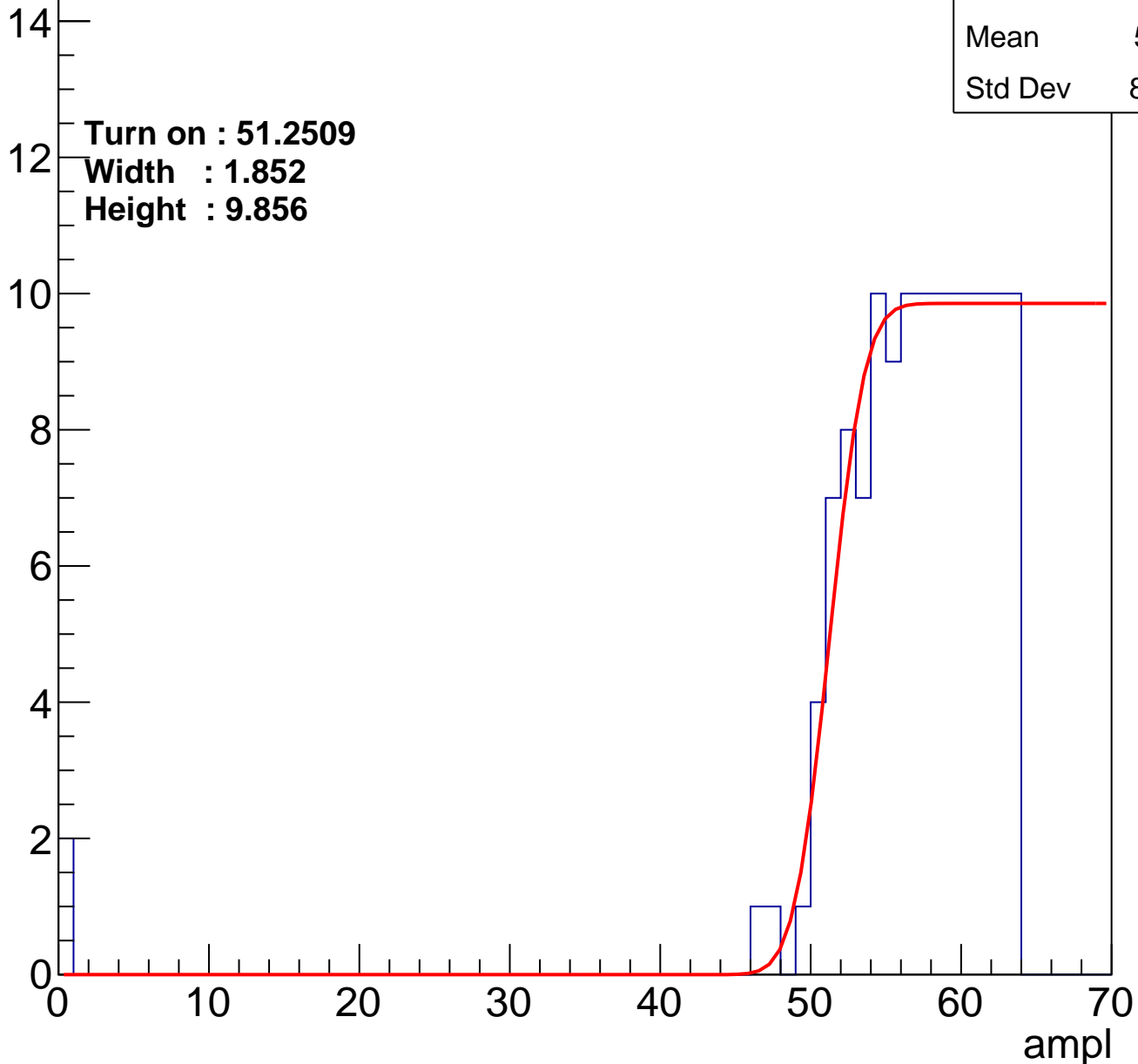
Entries	130
Mean	56.01
Std Dev	8.067

Turn on : 51.2509

Width : 1.852

Height : 9.856

Entry



# B0L103S, U26-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.99
Std Dev	8.174

**Turn on : 51.9435**

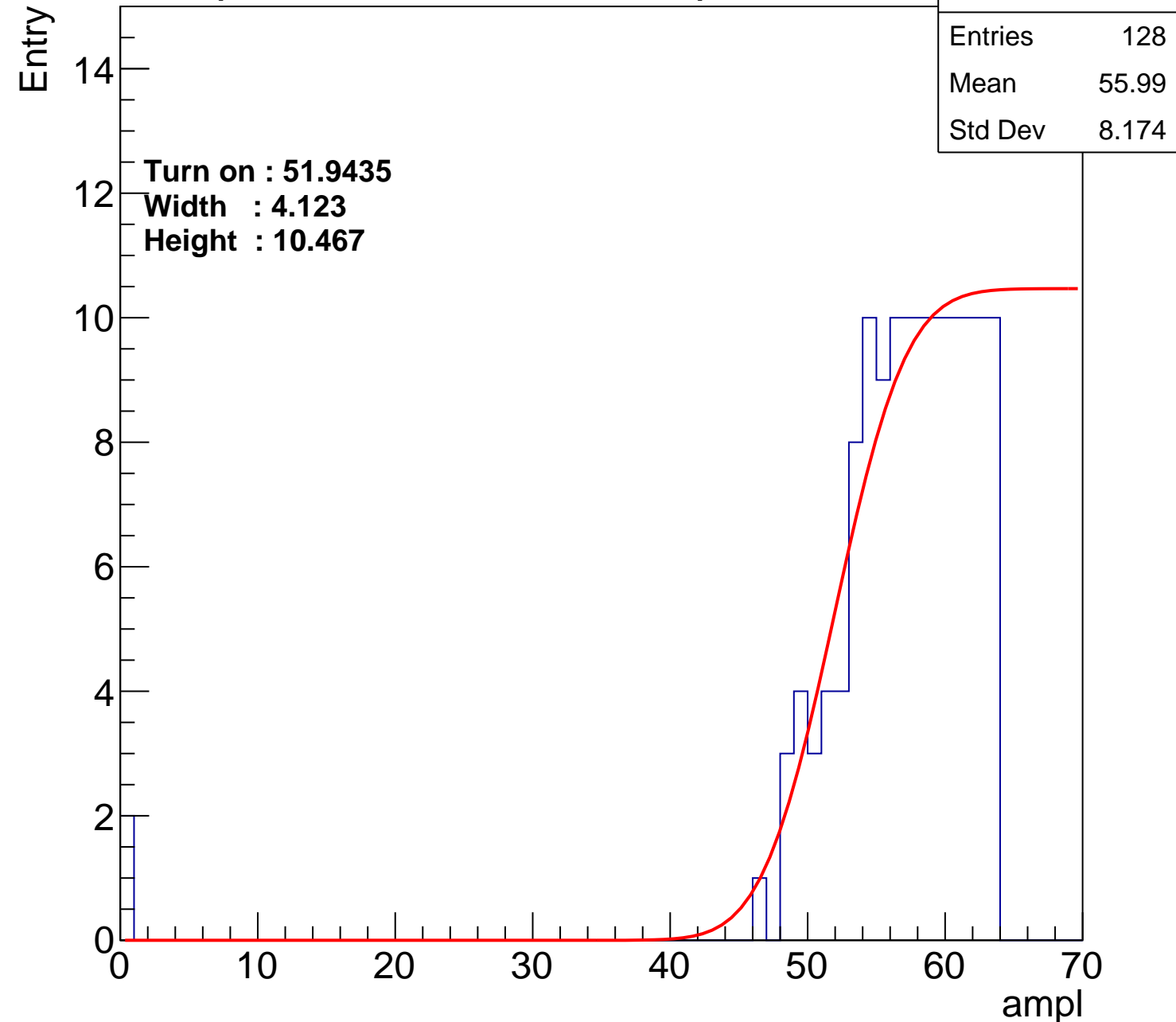
**Width : 4.123**

**Height : 10.467**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.89
Std Dev	8.058

Turn on : 51.1337

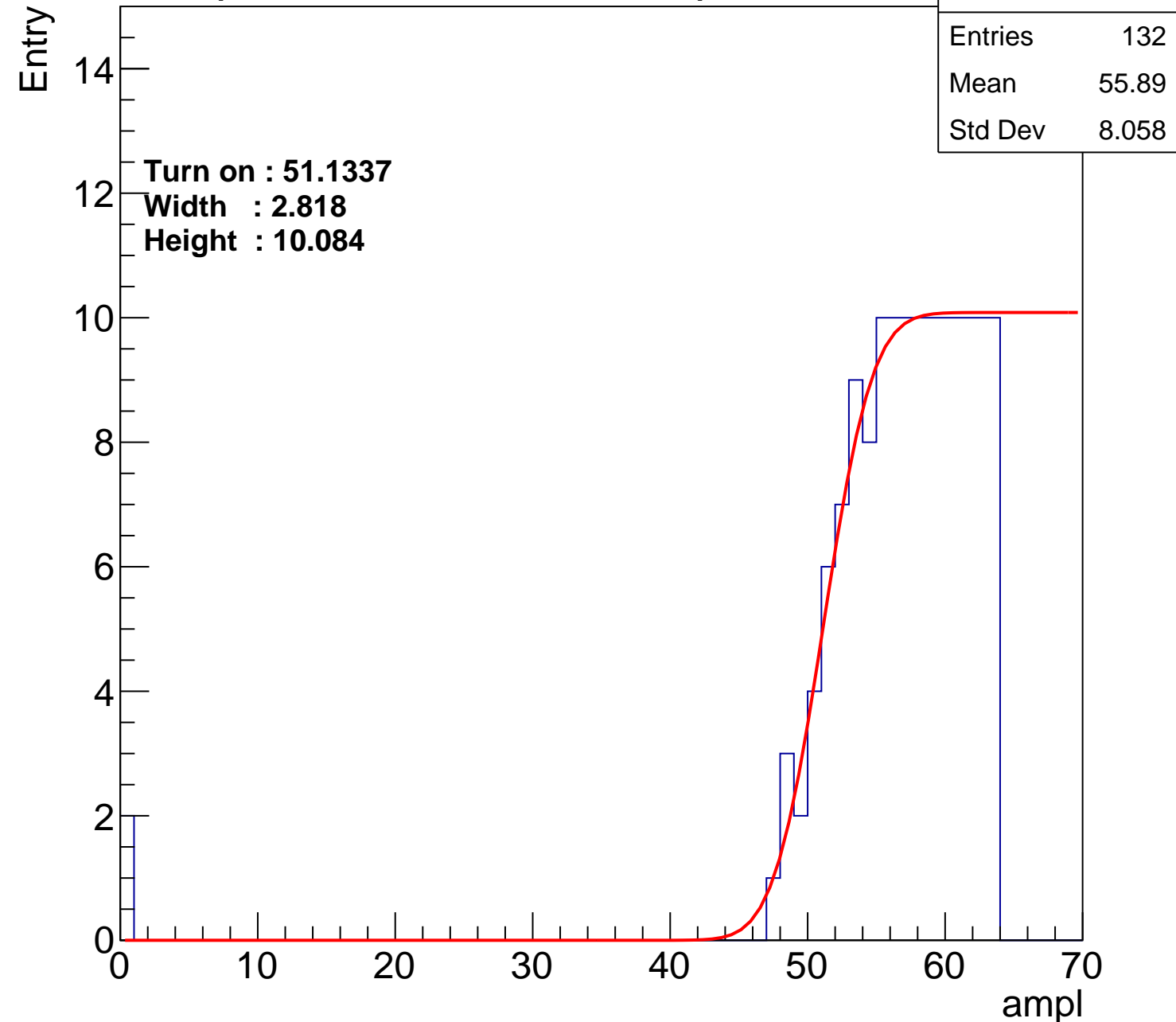
Width : 2.818

Height : 10.084

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	54.51
Std Dev	11.43

Turn on : 51.4016

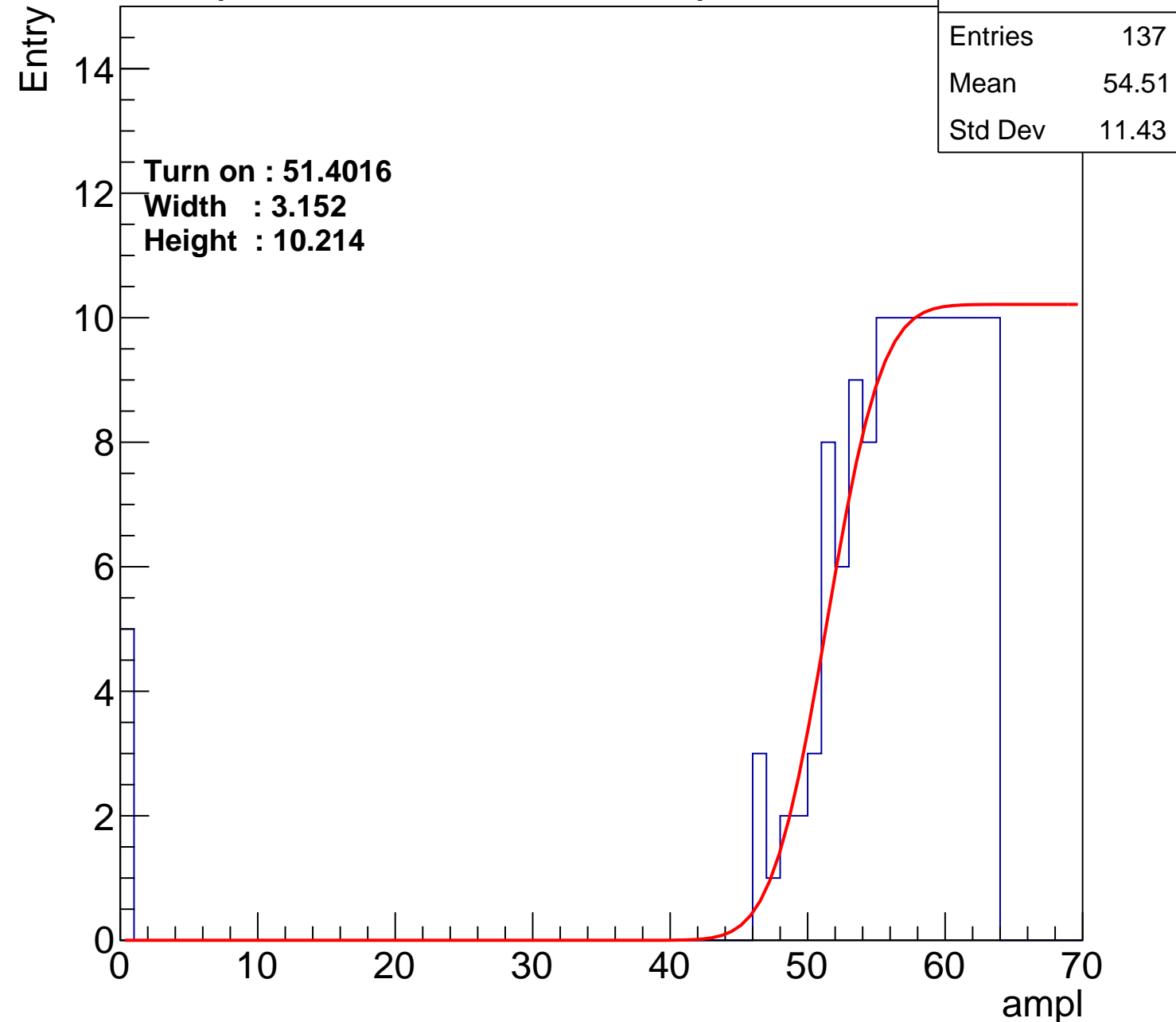
Width : 3.152

Height : 10.214

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.9
Std Dev	10.46

Turn on : 50.9021

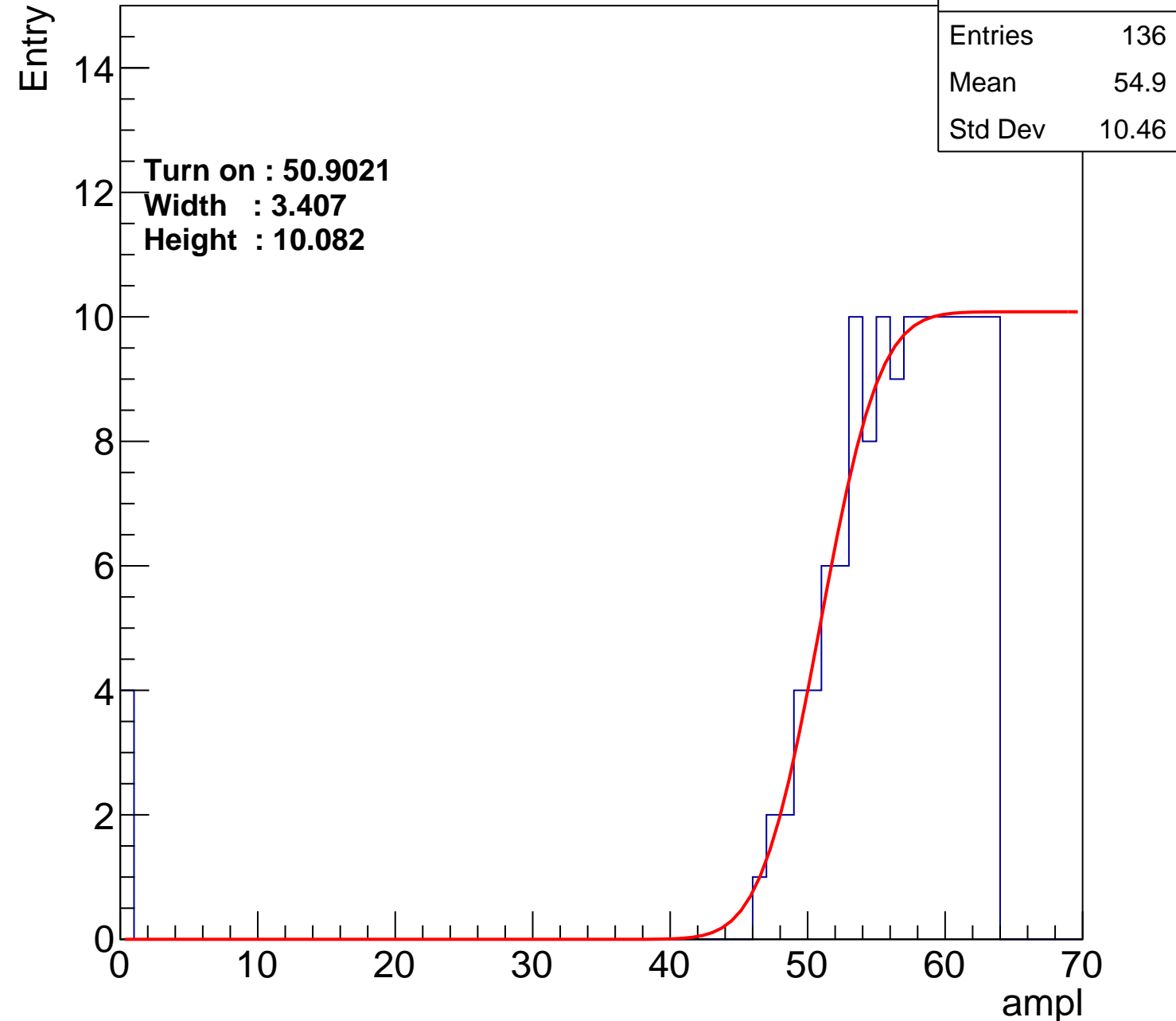
Width : 3.407

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.18
Std Dev	9.393

Turn on : 51.2320

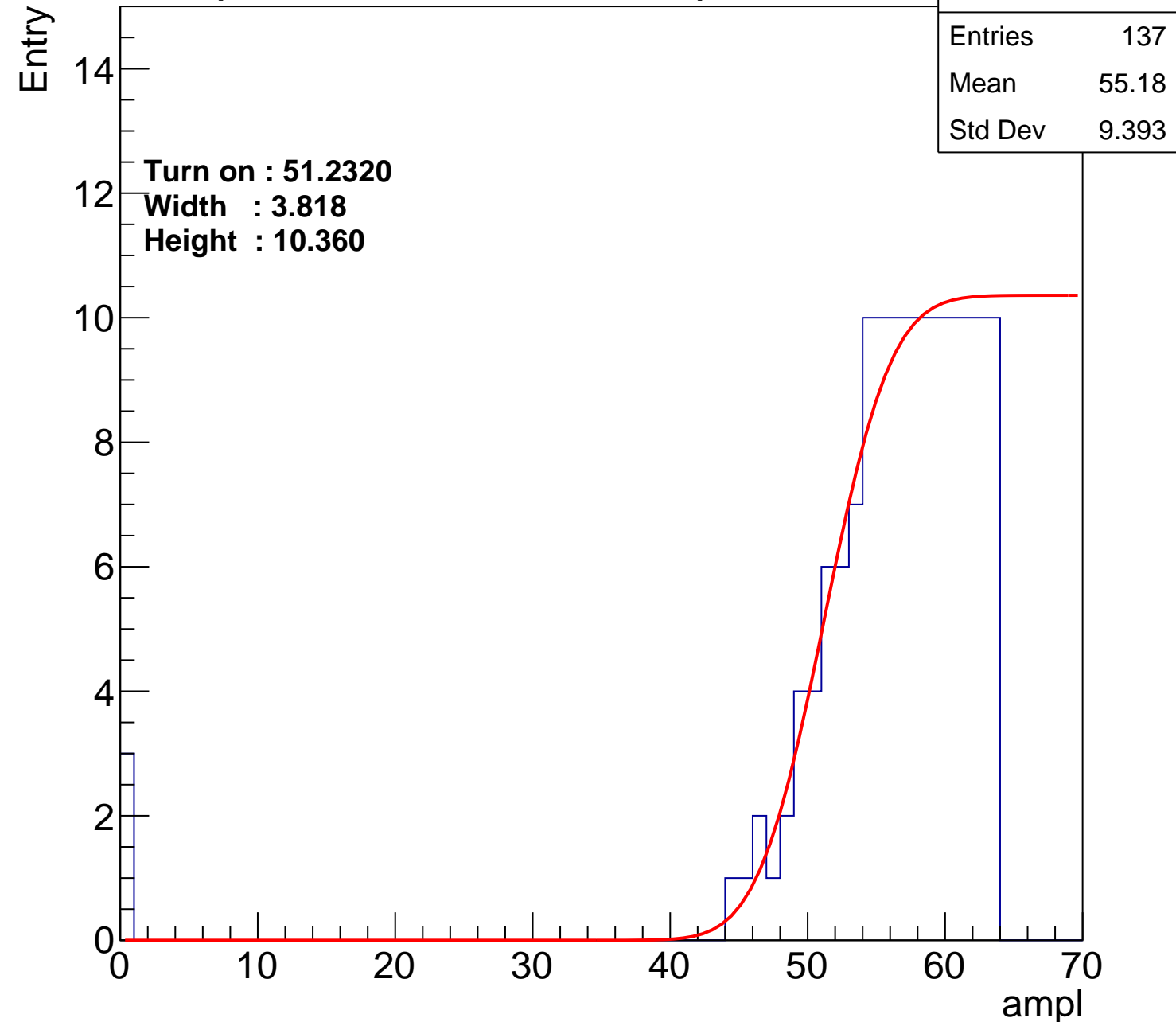
Width : 3.818

Height : 10.360

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	54.57
Std Dev	11.42

Turn on : 51.4416

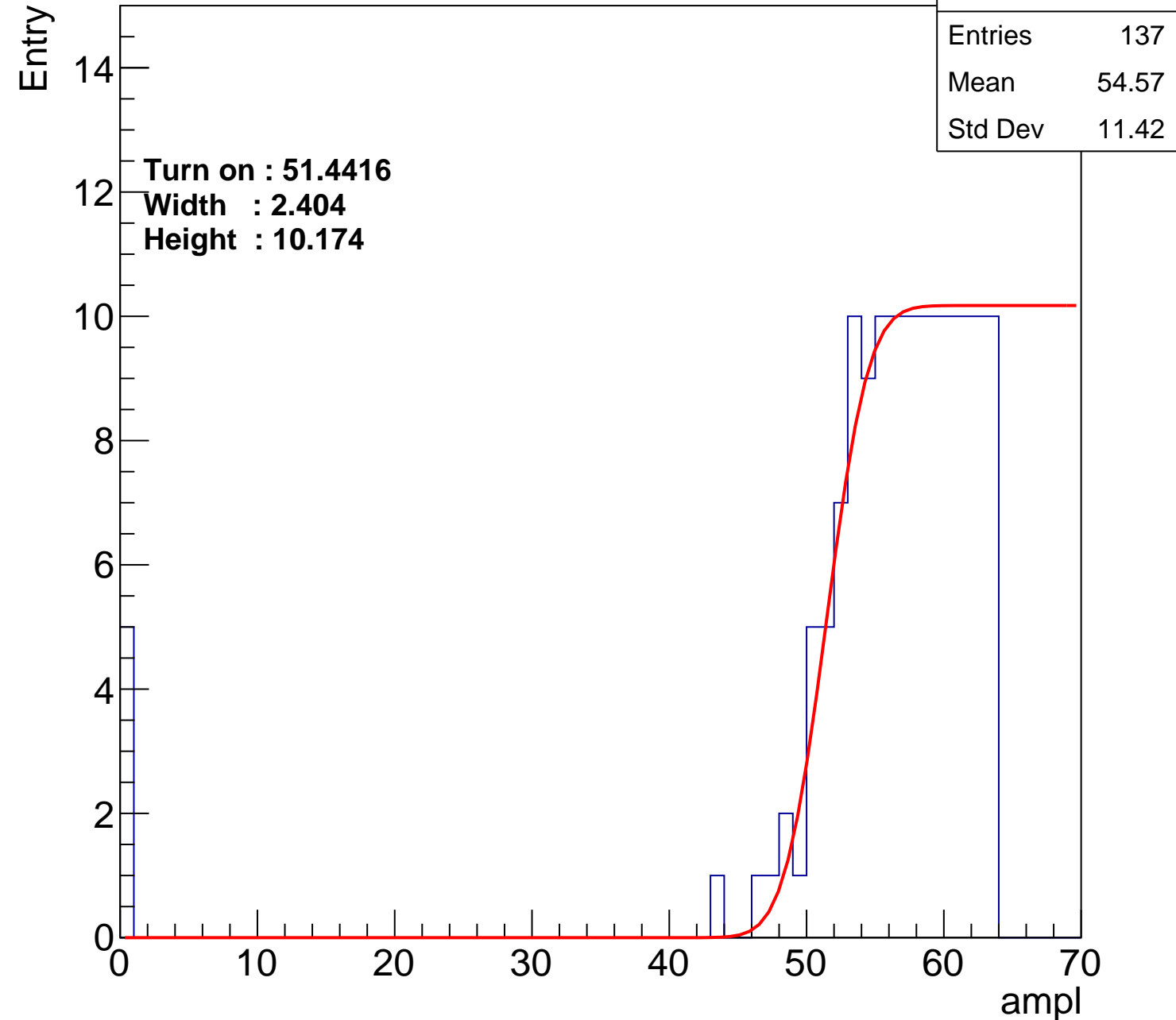
Width : 2.404

Height : 10.174

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	55.72
Std Dev	11.39

Turn on : 53.6316

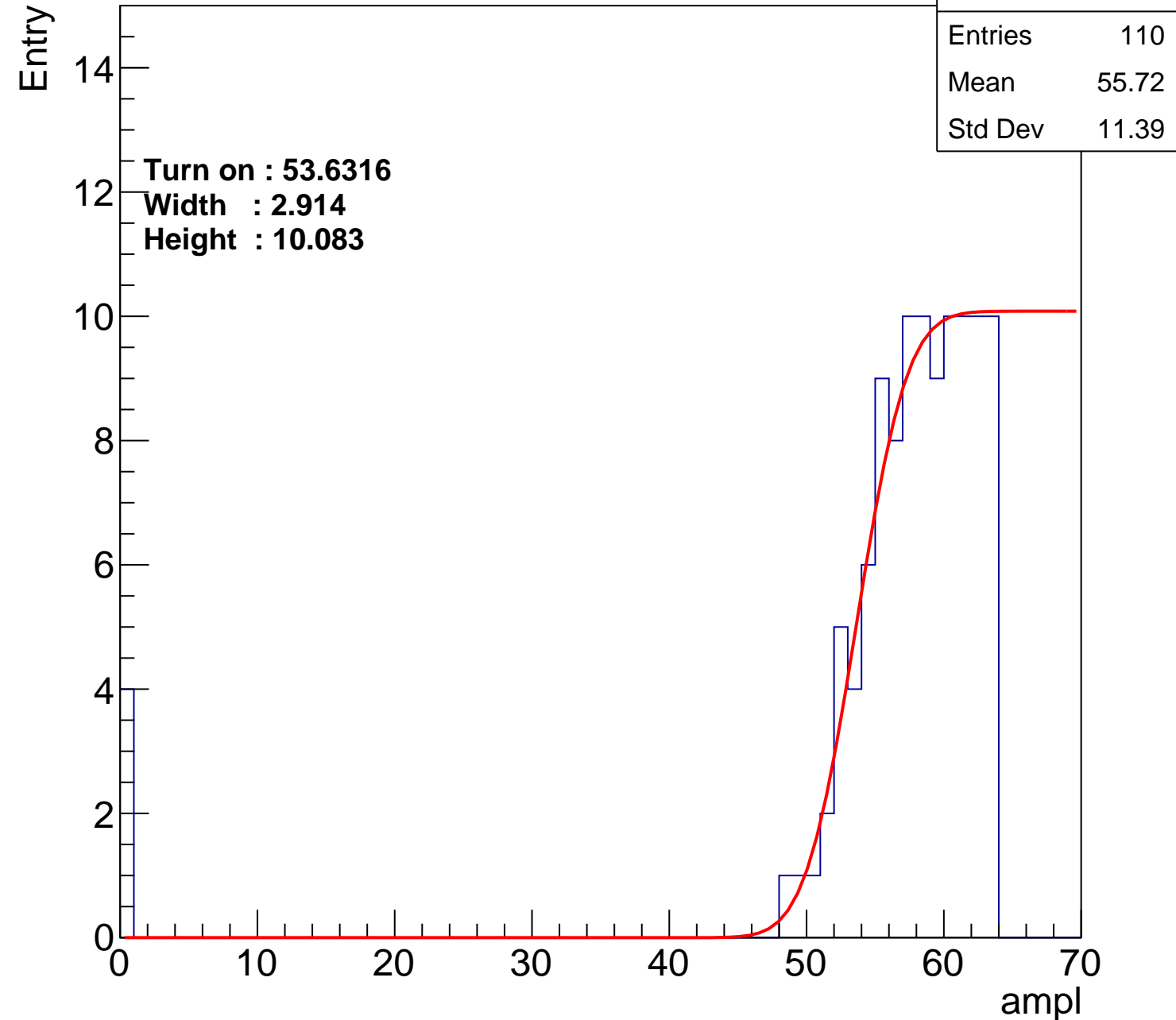
Width : 2.914

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

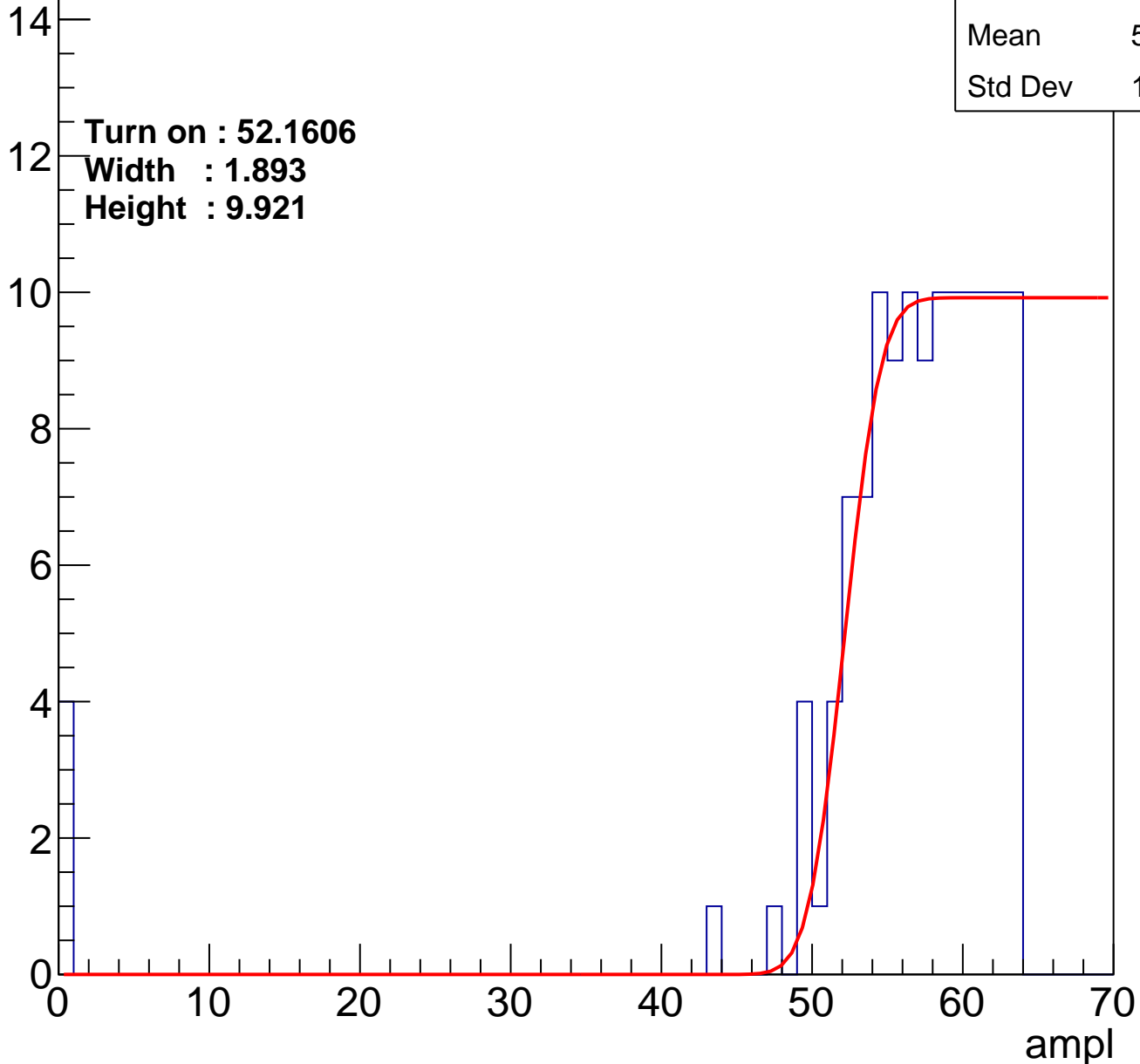
Entries	127
Mean	55.22
Std Dev	10.75

Turn on : 52.1606

Width : 1.893

Height : 9.921

Entry



# B0L103S, U26-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.94
Std Dev	9.197

Turn on : 49.7814

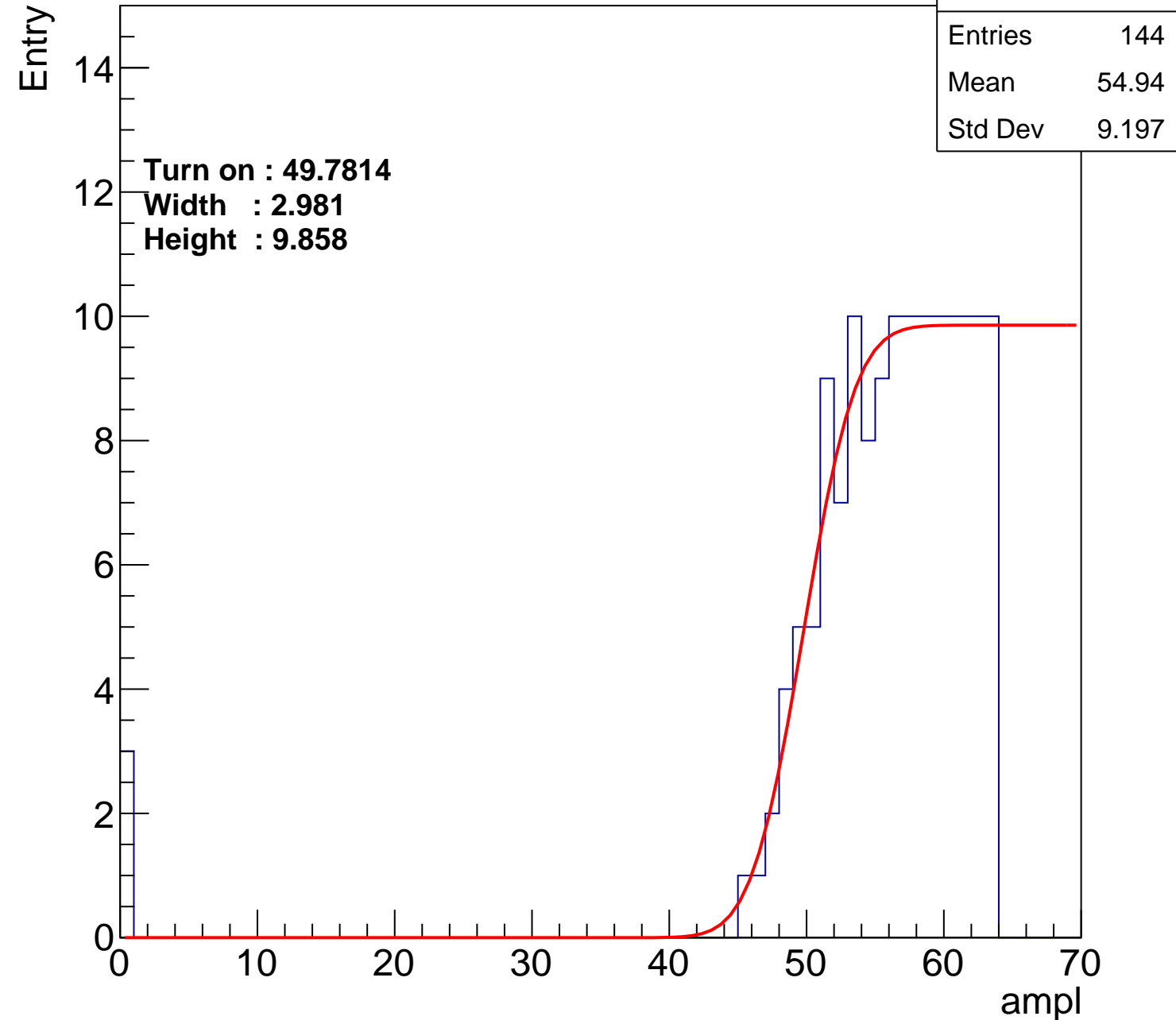
Width : 2.981

Height : 9.858

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	56.05
Std Dev	6.531

Turn on : 51.2211

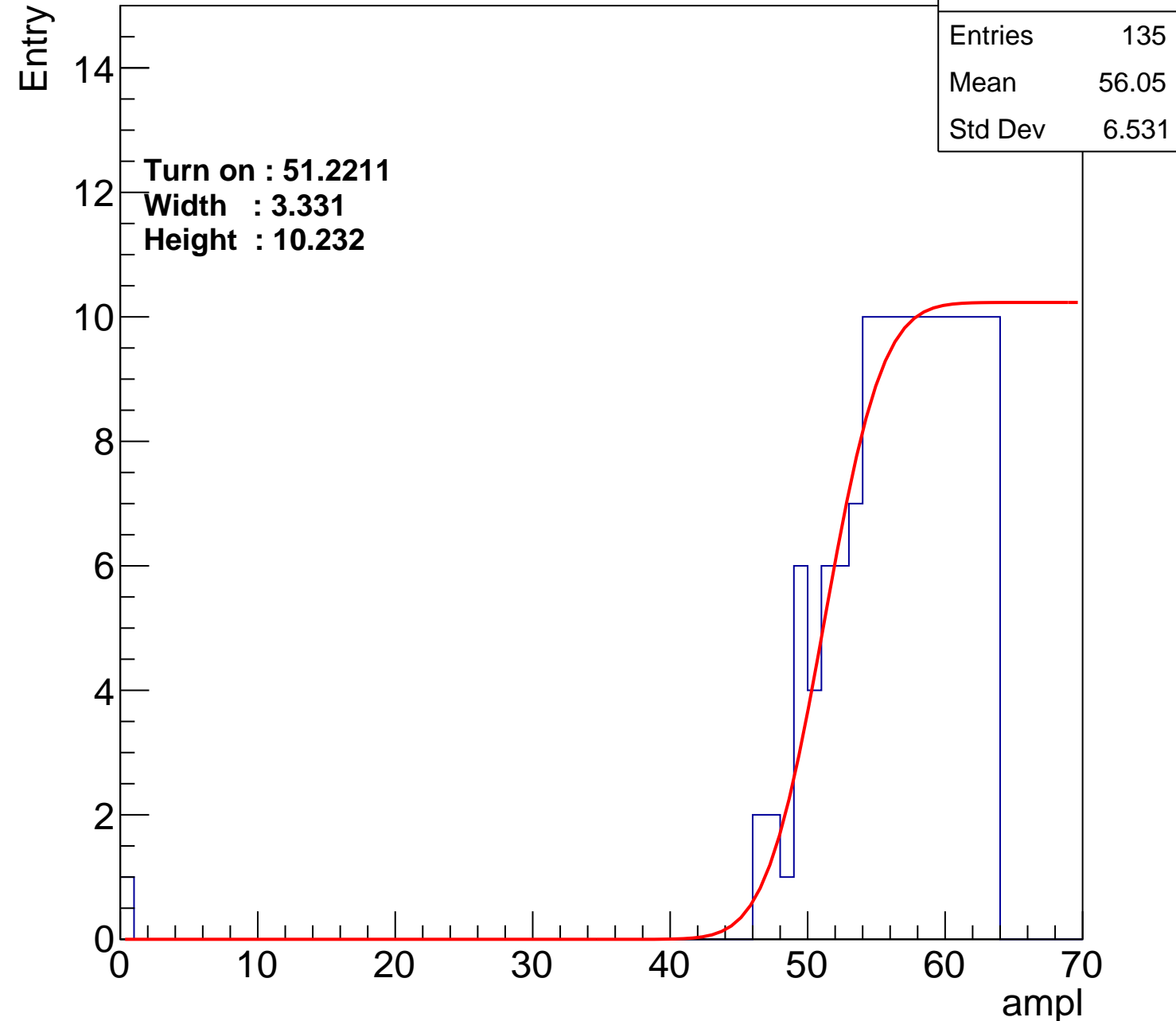
Width : 3.331

Height : 10.232

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

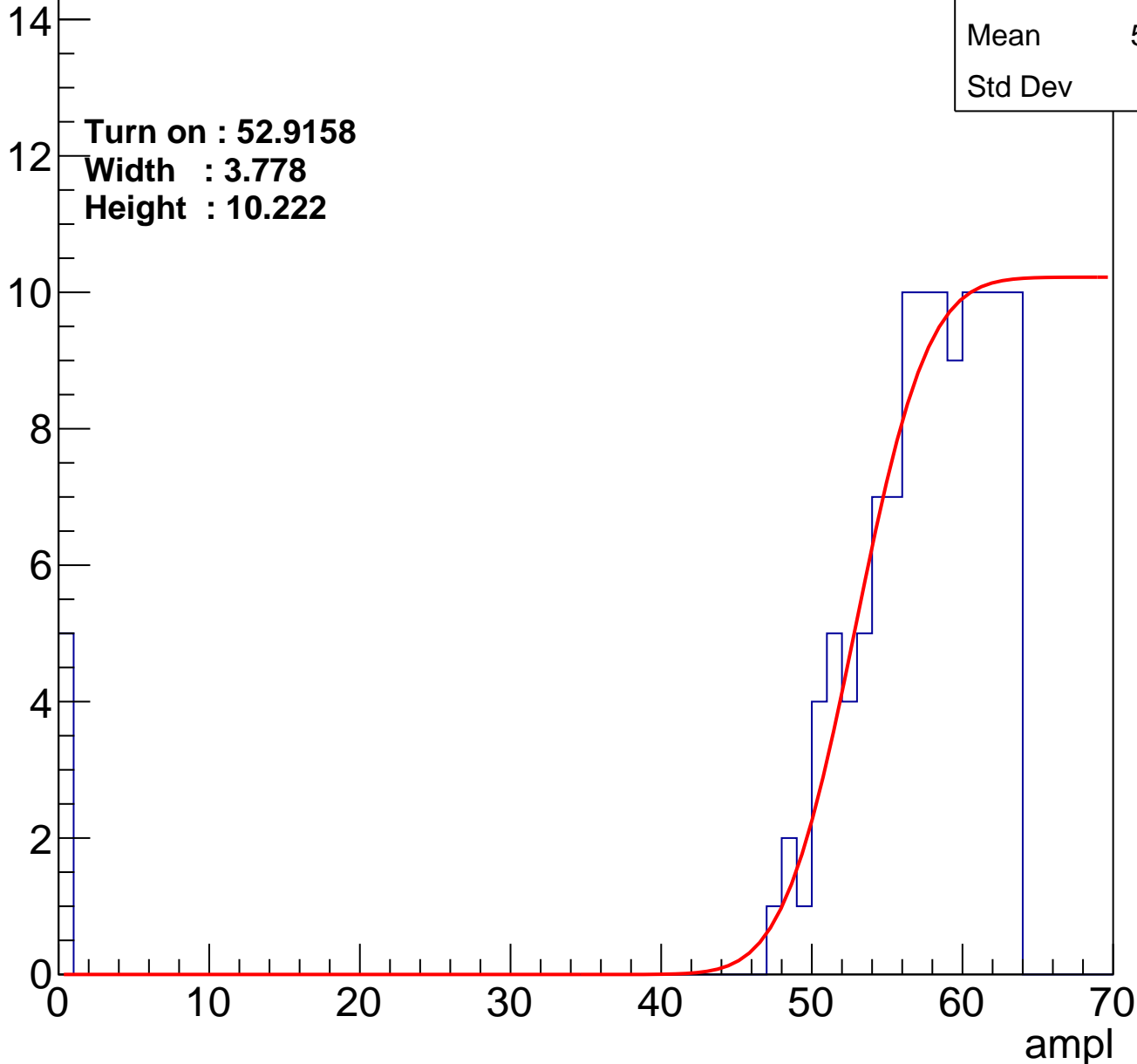
Entry

Entries	120
Mean	54.87
Std Dev	12.11

Turn on : 52.9158

Width : 3.778

Height : 10.222



# B0L103S, U26-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	55.54
Std Dev	9.582

Turn on : 51.6849

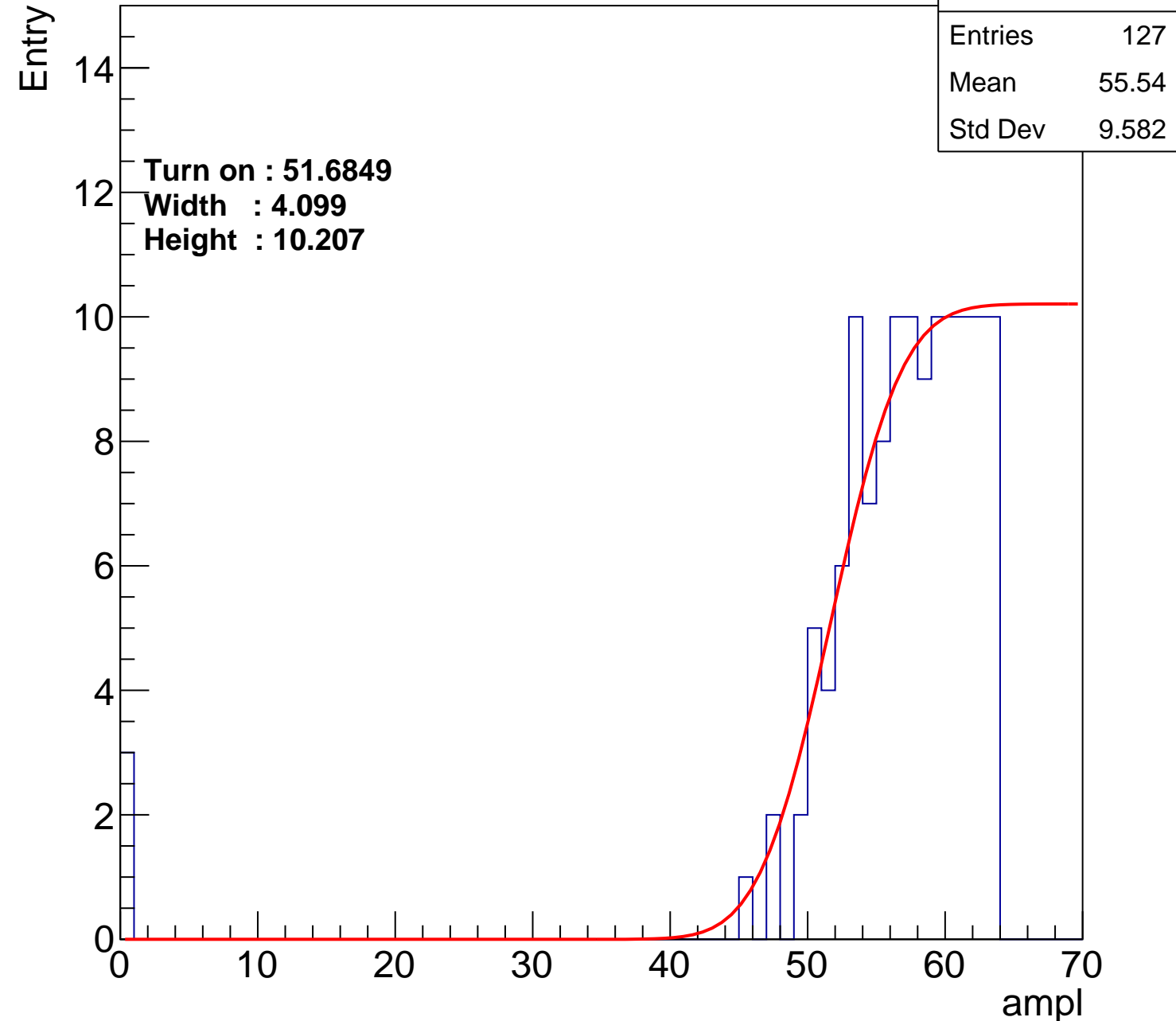
Width : 4.099

Height : 10.207

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.24
Std Dev	11.14

Turn on : 50.6195

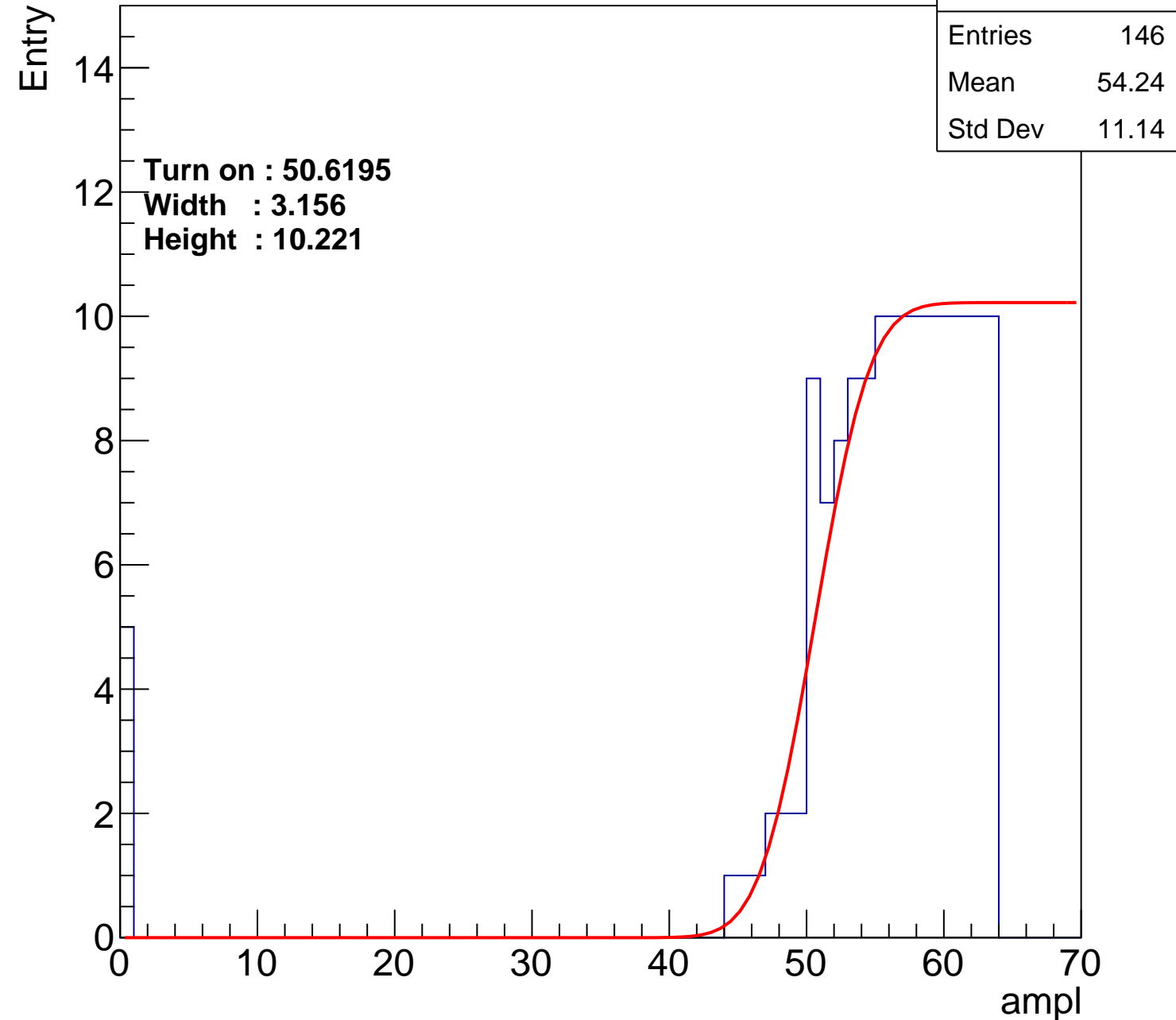
Width : 3.156

Height : 10.221

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	148
Mean	54.83
Std Dev	9.115

**Turn on : 49.7927**

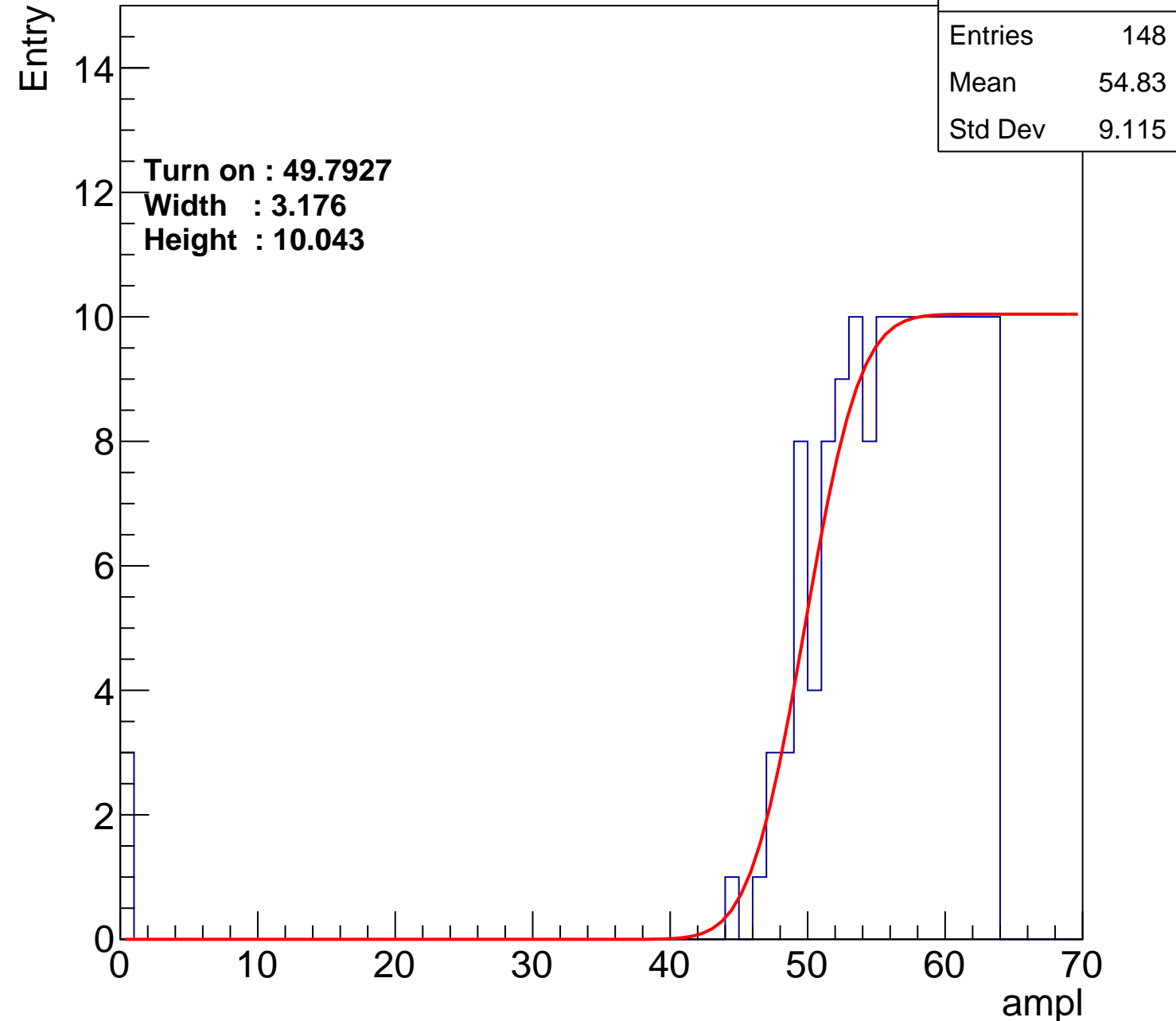
**Width : 3.176**

**Height : 10.043**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.93
Std Dev	10.51

Turn on : 51.3284

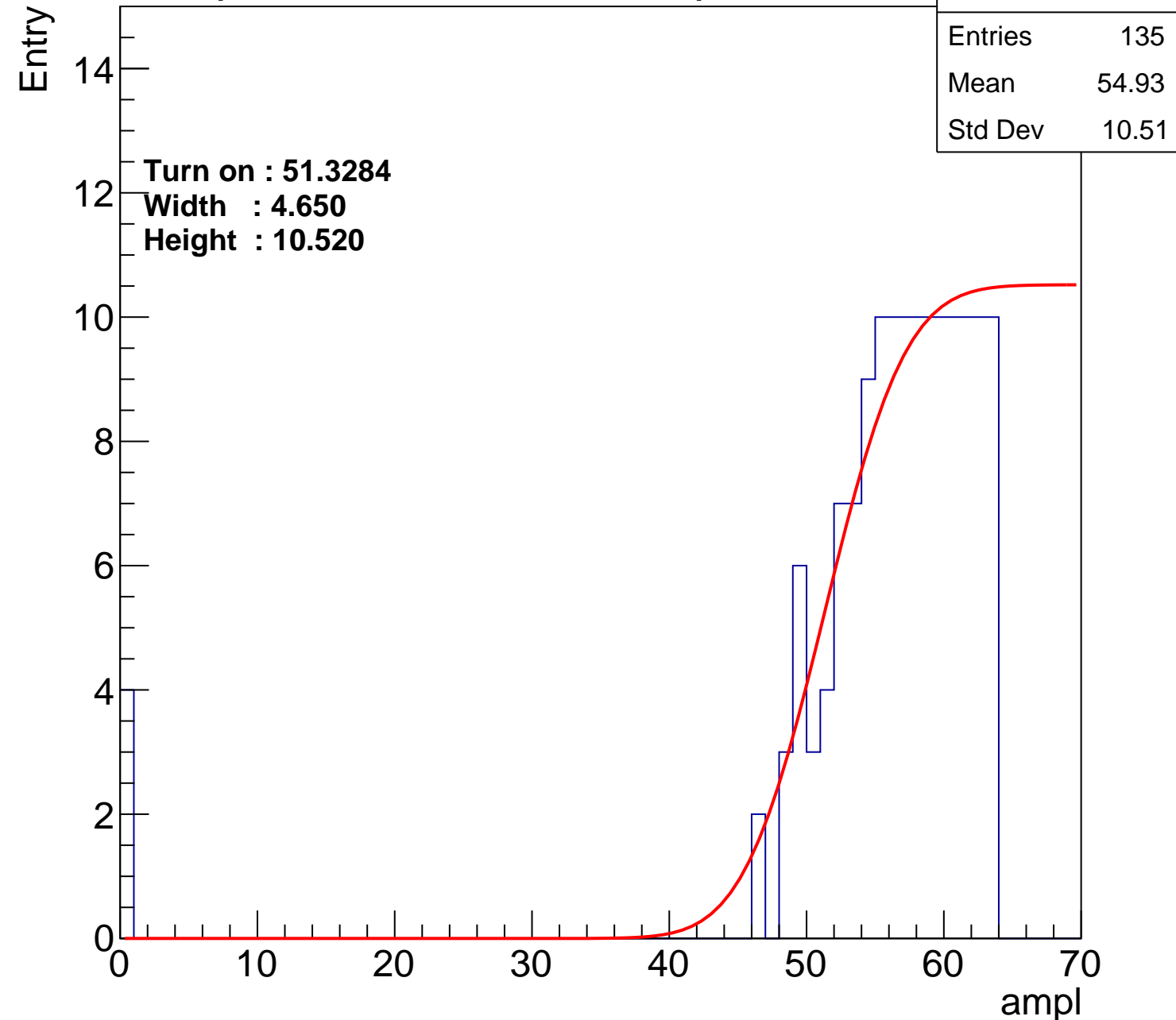
Width : 4.650

Height : 10.520

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

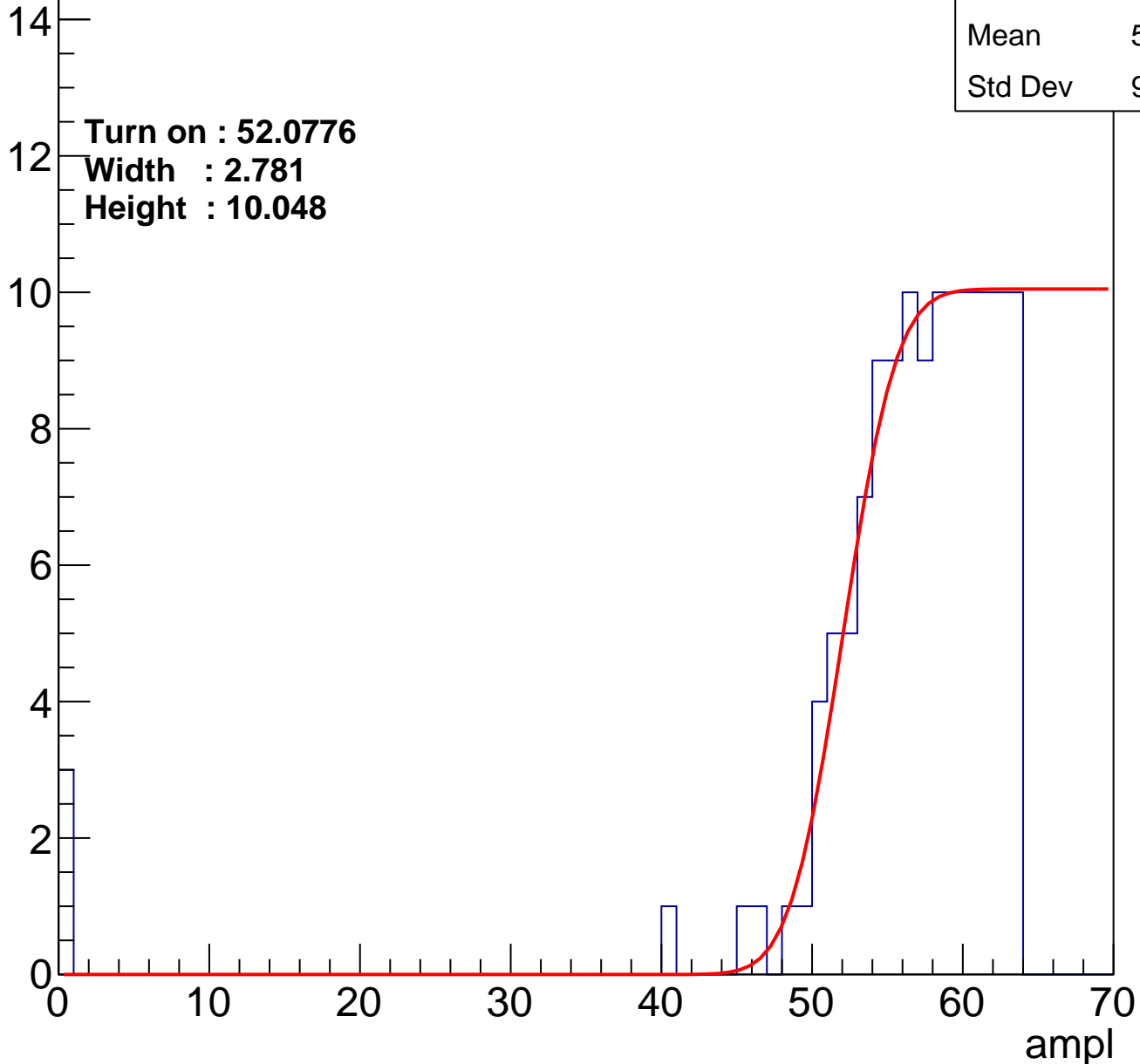
Entries	126
Mean	55.54
Std Dev	9.689

Turn on : 52.0776

Width : 2.781

Height : 10.048

Entry



# B0L103S, U26-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

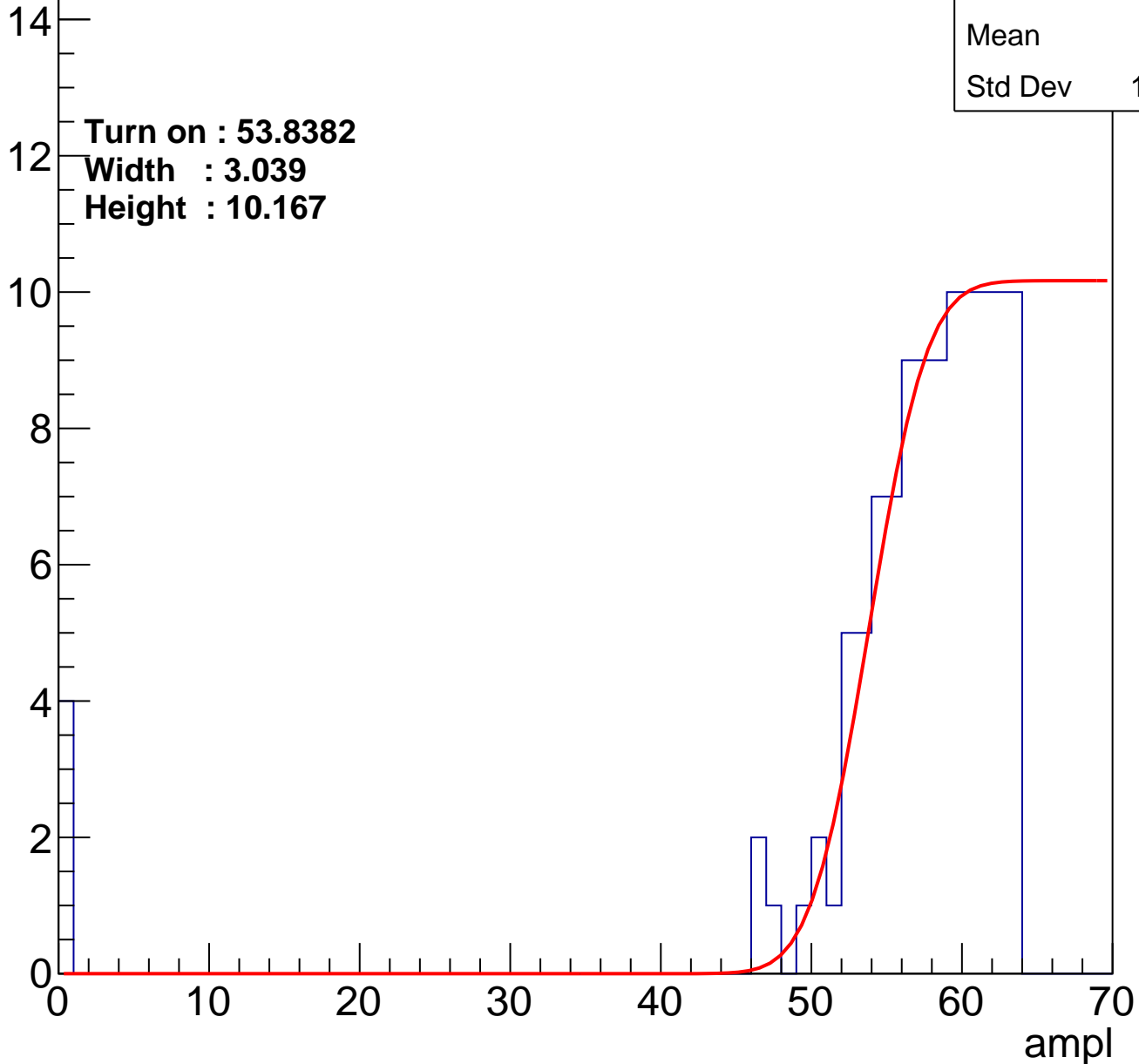
Entries	112
Mean	55.5
Std Dev	11.38

Turn on : 53.8382

Width : 3.039

Height : 10.167

Entry



# B0L103S, U26-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

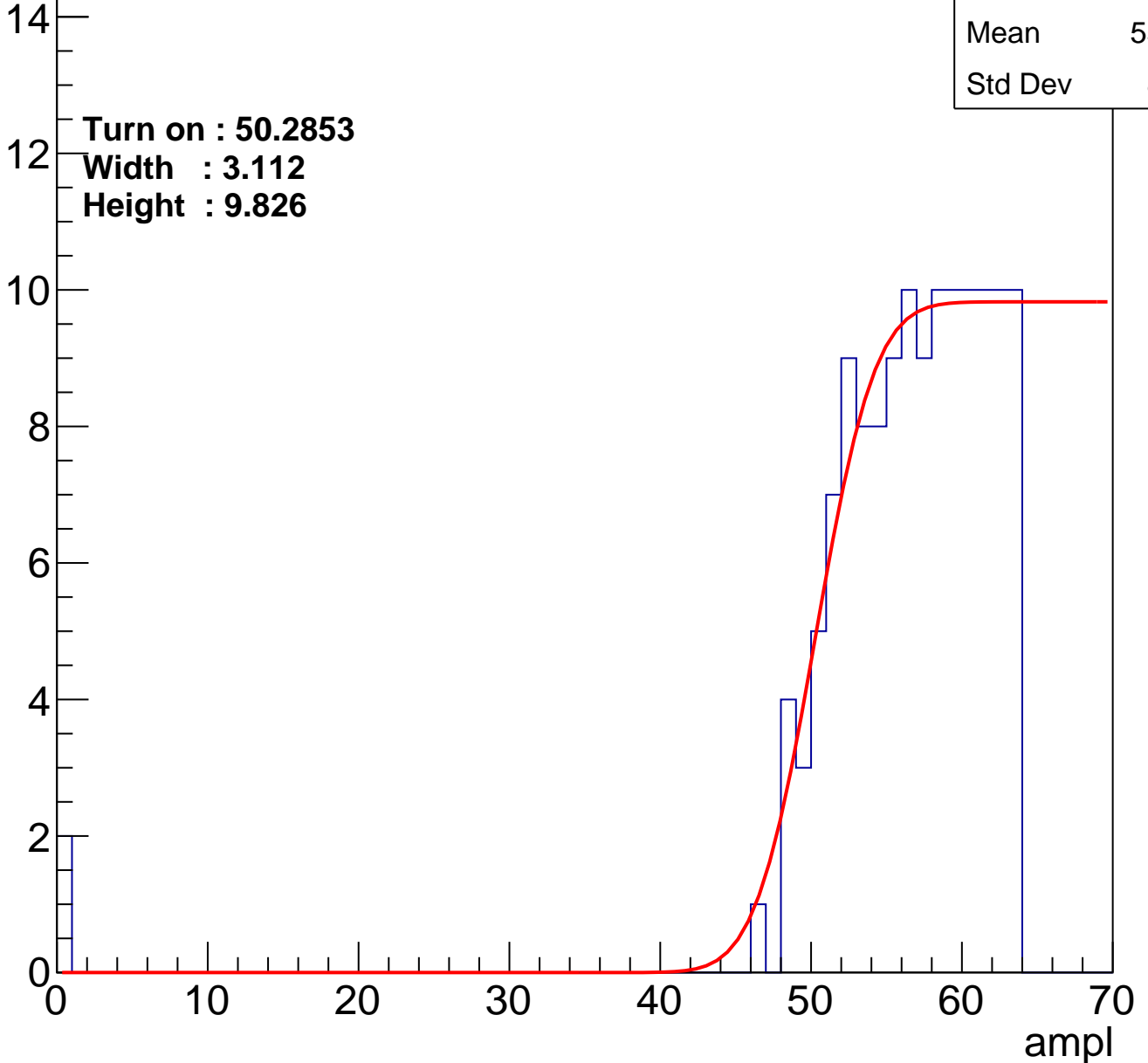
Entries	135
Mean	55.66
Std Dev	8.06

Turn on : 50.2853

Width : 3.112

Height : 9.826

Entry



# B0L103S, U26-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	54.43
Std Dev	11.22

Turn on : 49.5598

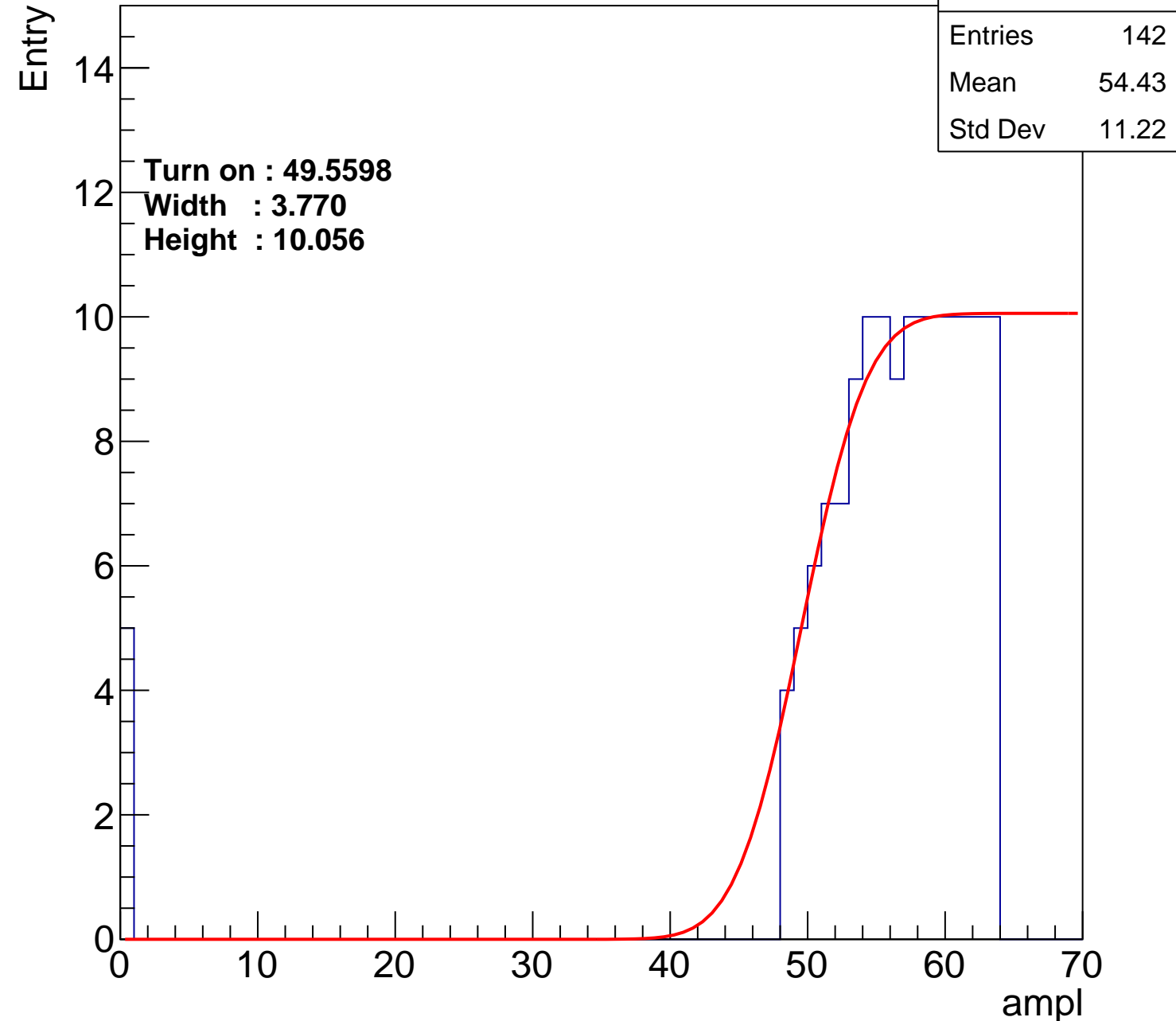
Width : 3.770

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	53.68
Std Dev	14.25

Turn on : 52.0673

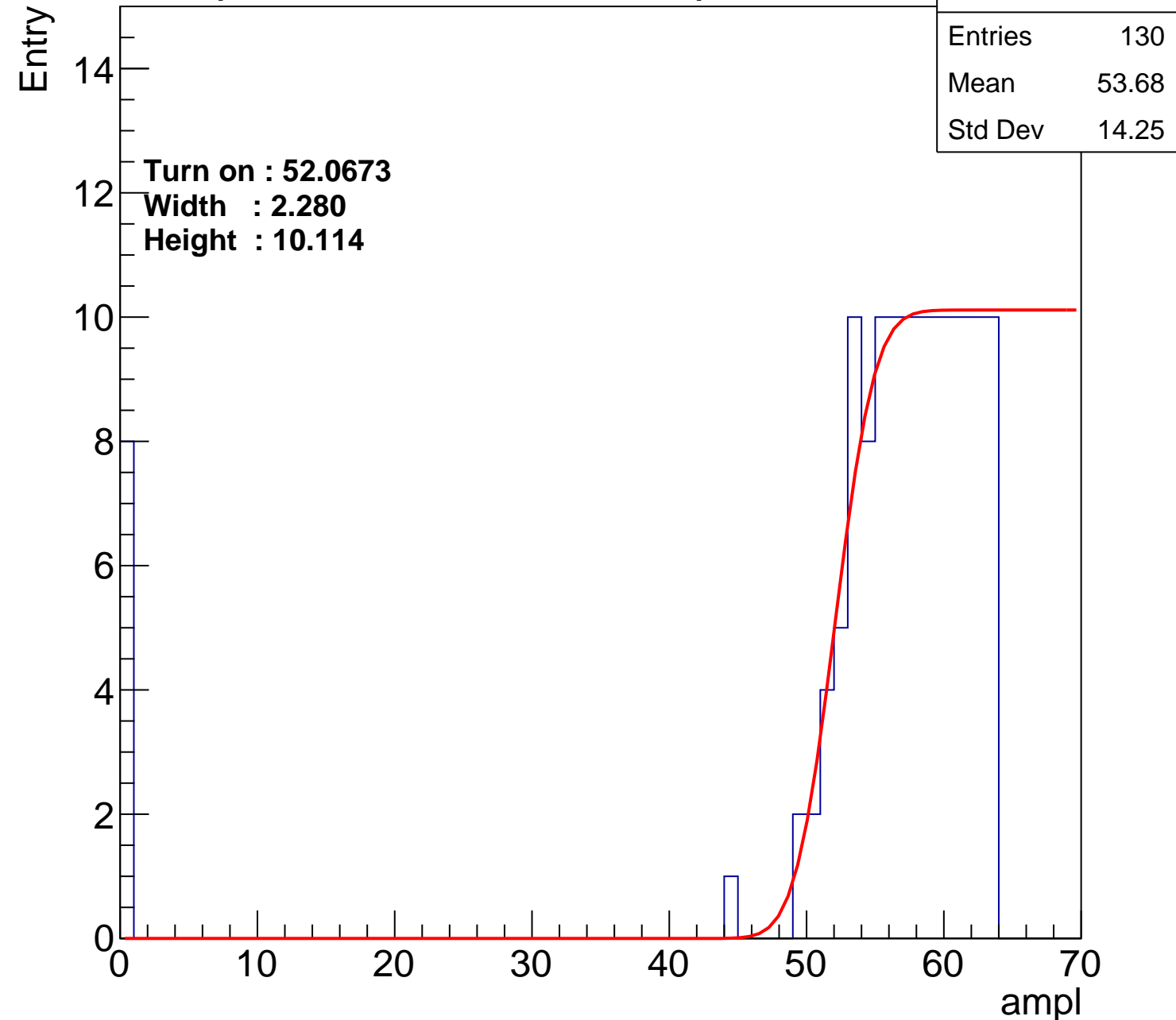
Width : 2.280

Height : 10.114

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	99
Mean	57.58
Std Dev	6.778

**Turn on : 54.6289**

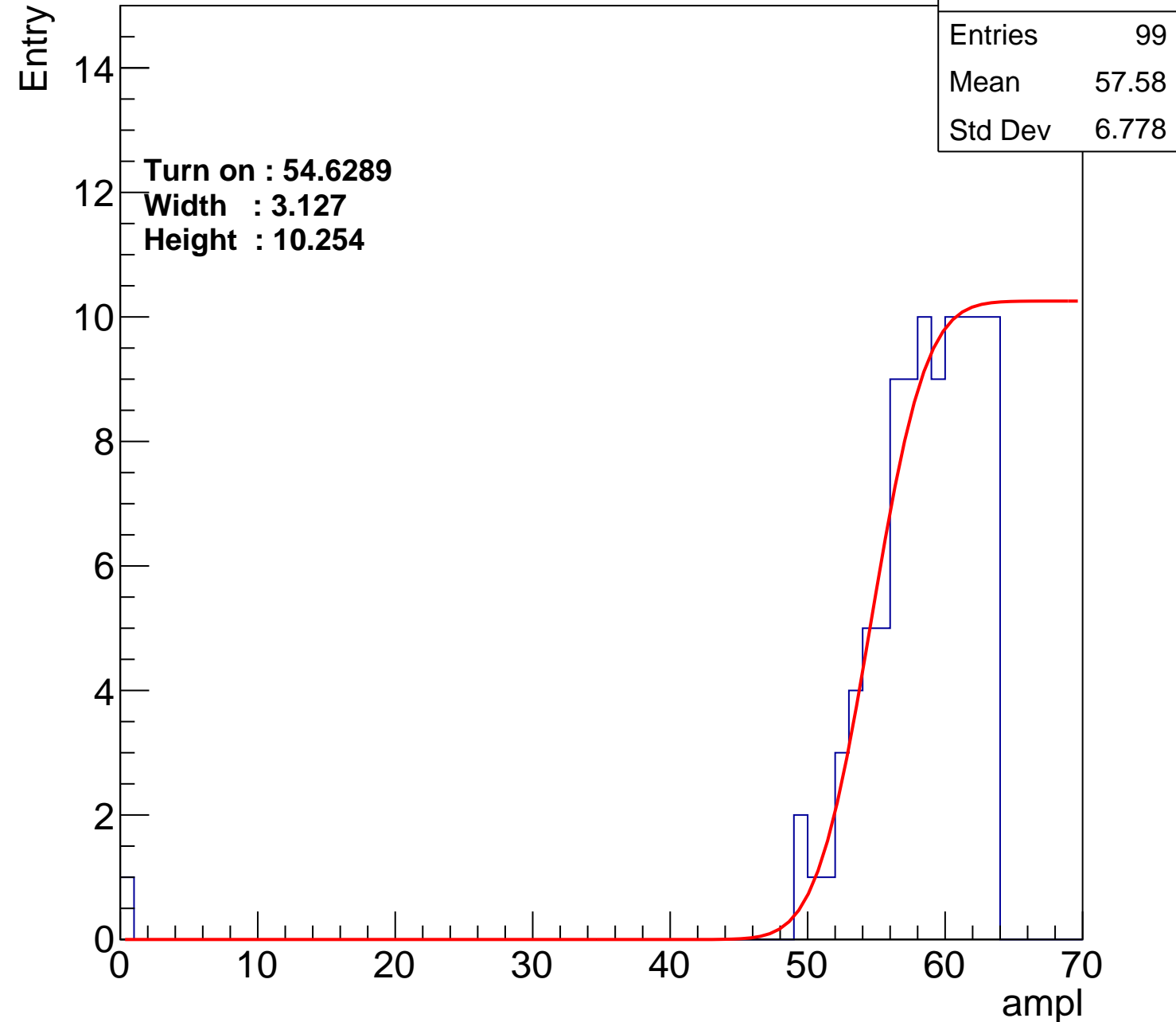
**Width : 3.127**

**Height : 10.254**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.04
Std Dev	10.5

**Turn on : 51.3849**

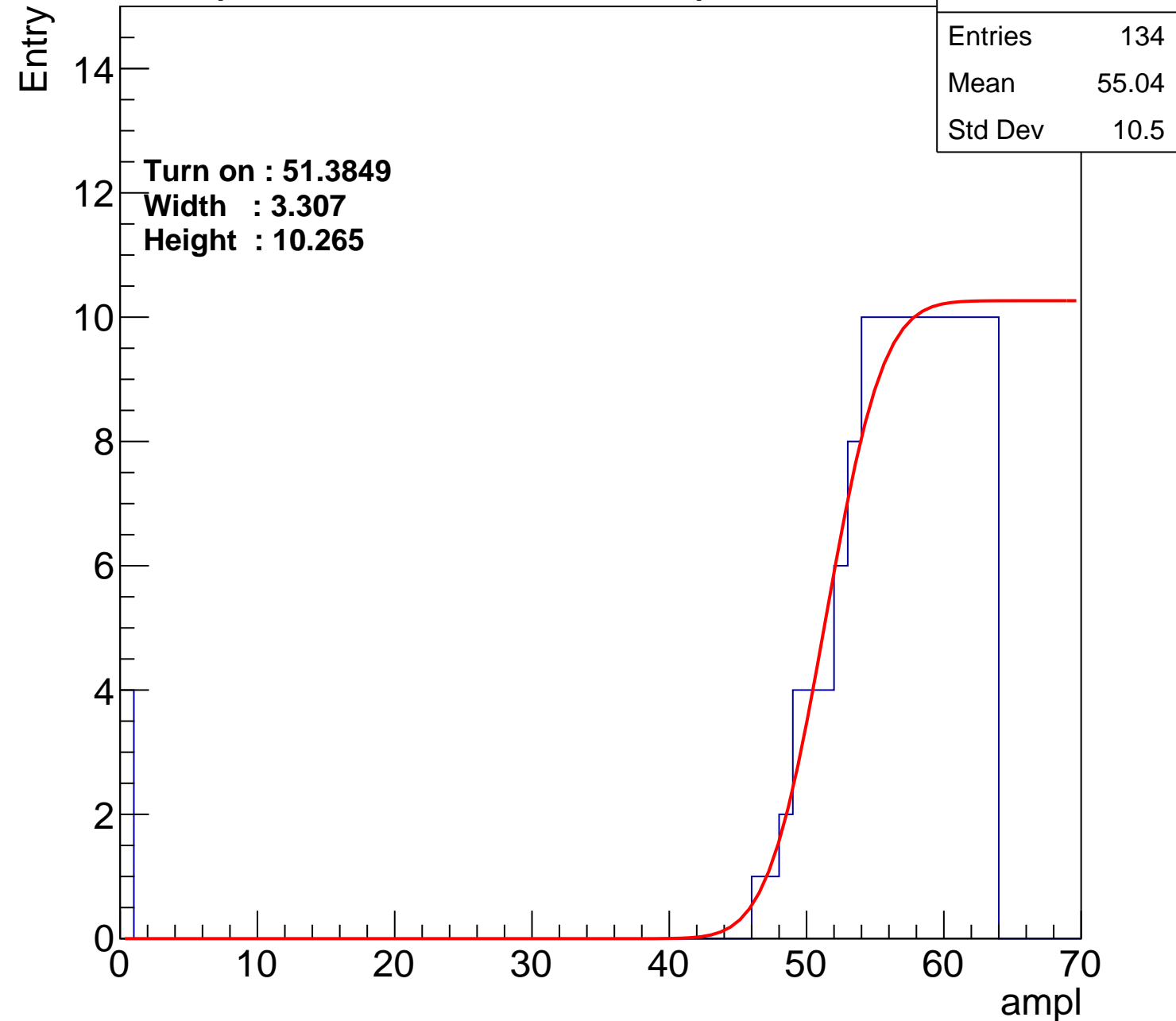
**Width : 3.307**

**Height : 10.265**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

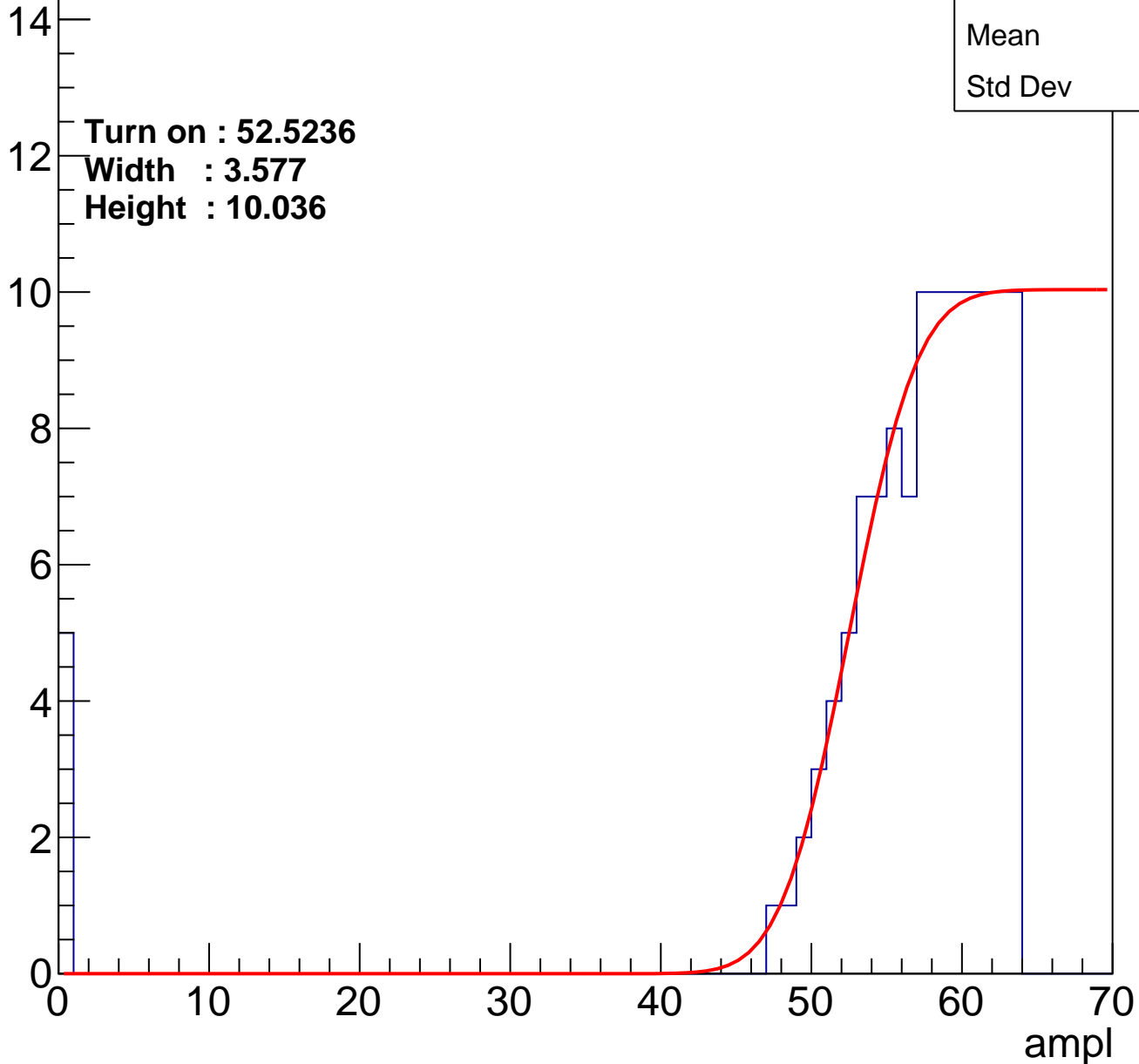
Entries	120
Mean	54.9
Std Dev	12.1

Turn on : 52.5236

Width : 3.577

Height : 10.036

Entry



# B0L103S, U26-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.07
Std Dev	12.12

Turn on : 52.1551

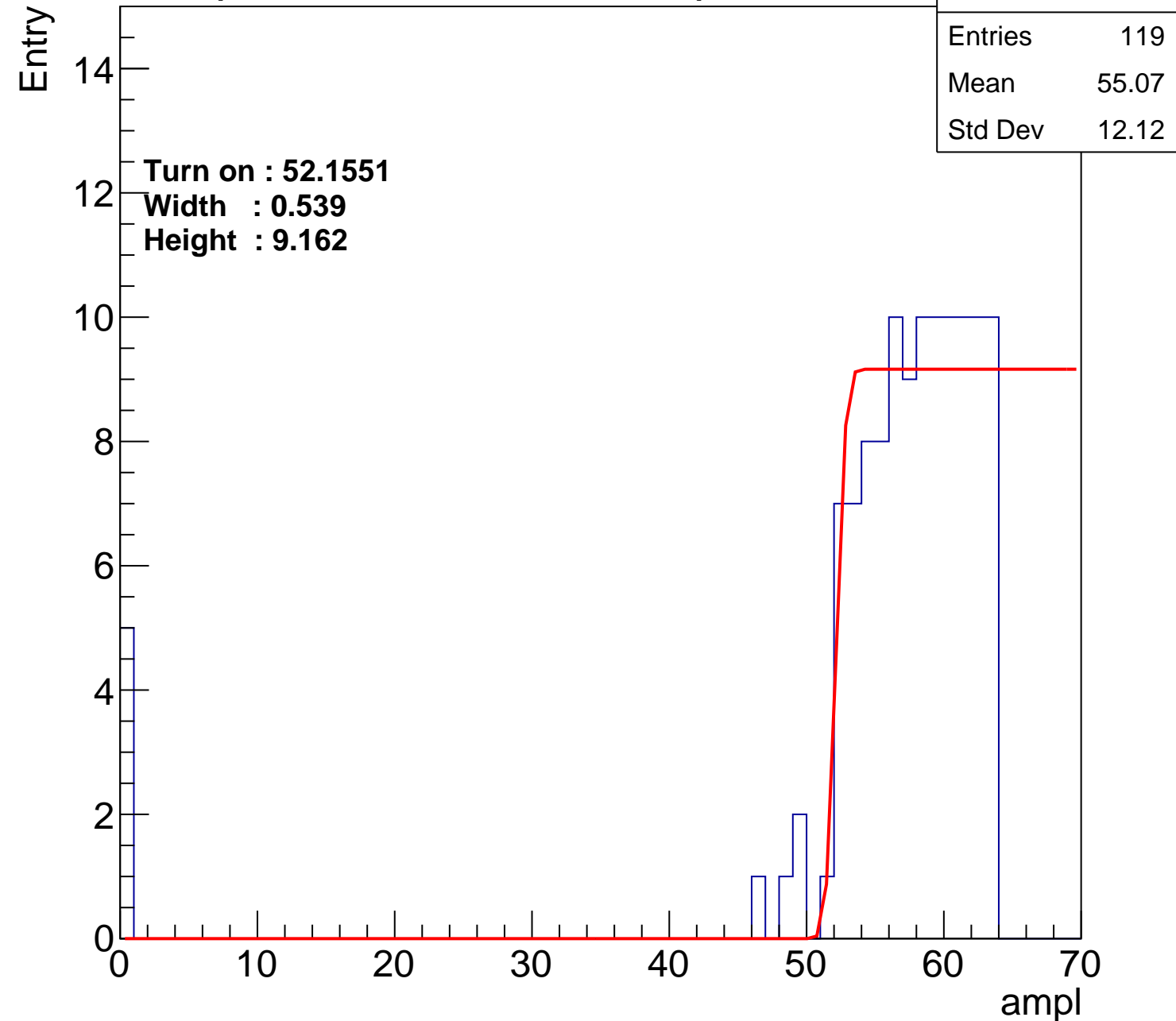
Width : 0.539

Height : 9.162

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.69
Std Dev	10.18

Turn on : 49.7271

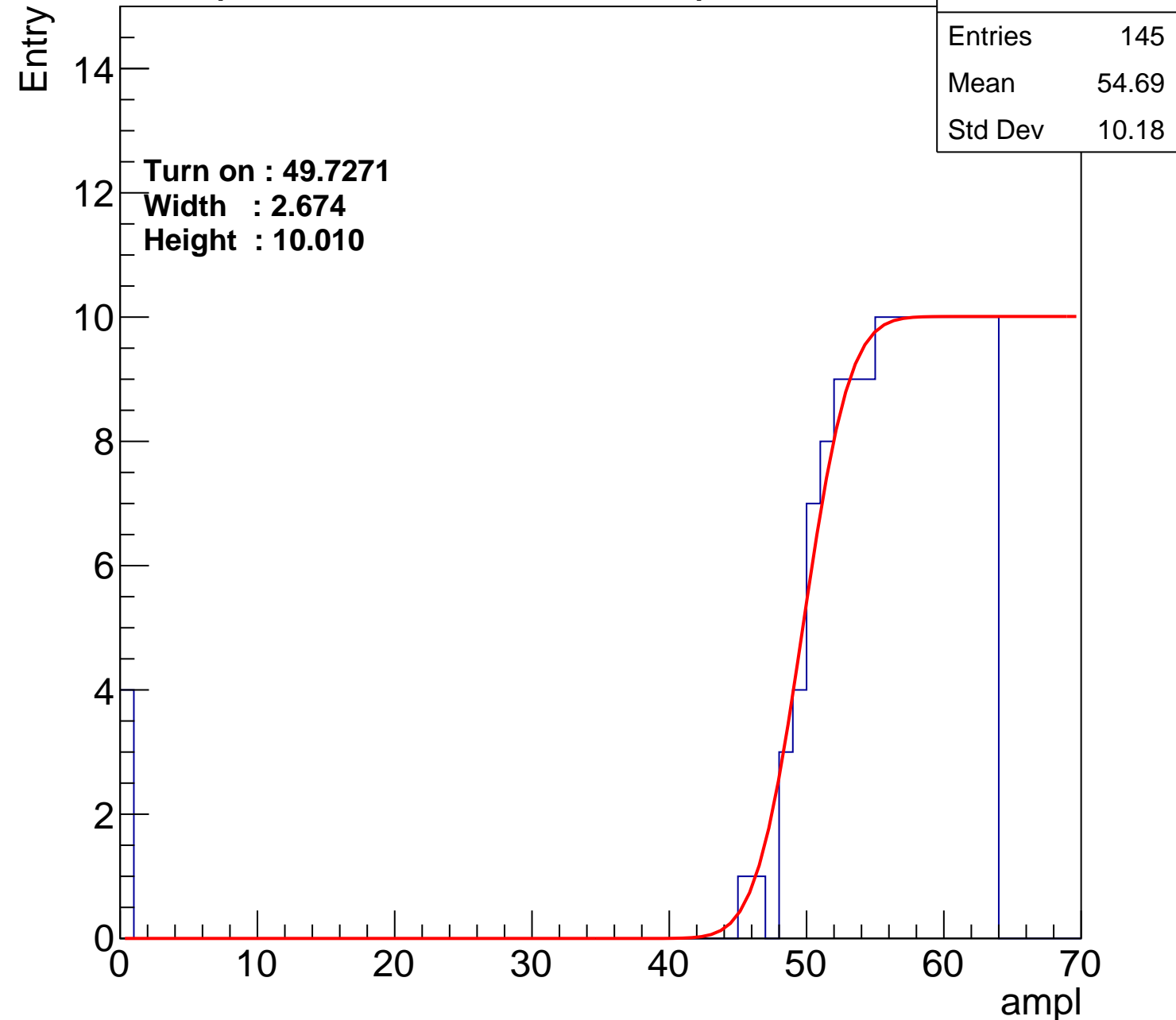
Width : 2.674

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch108

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.6
Std Dev	9.459

Turn on : 52.0316

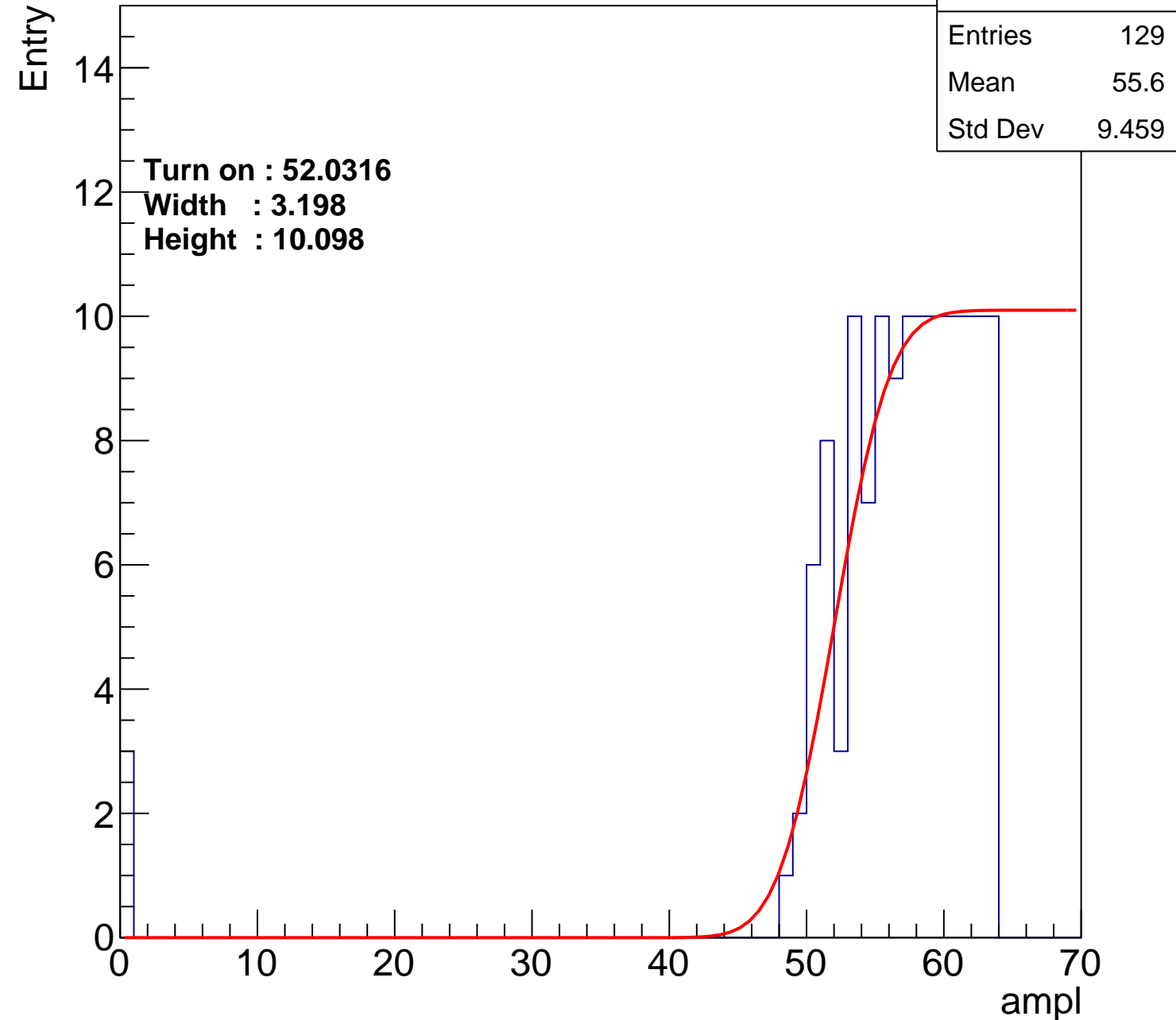
Width : 3.198

Height : 10.098

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.91
Std Dev	8.325

Turn on : 52.1992

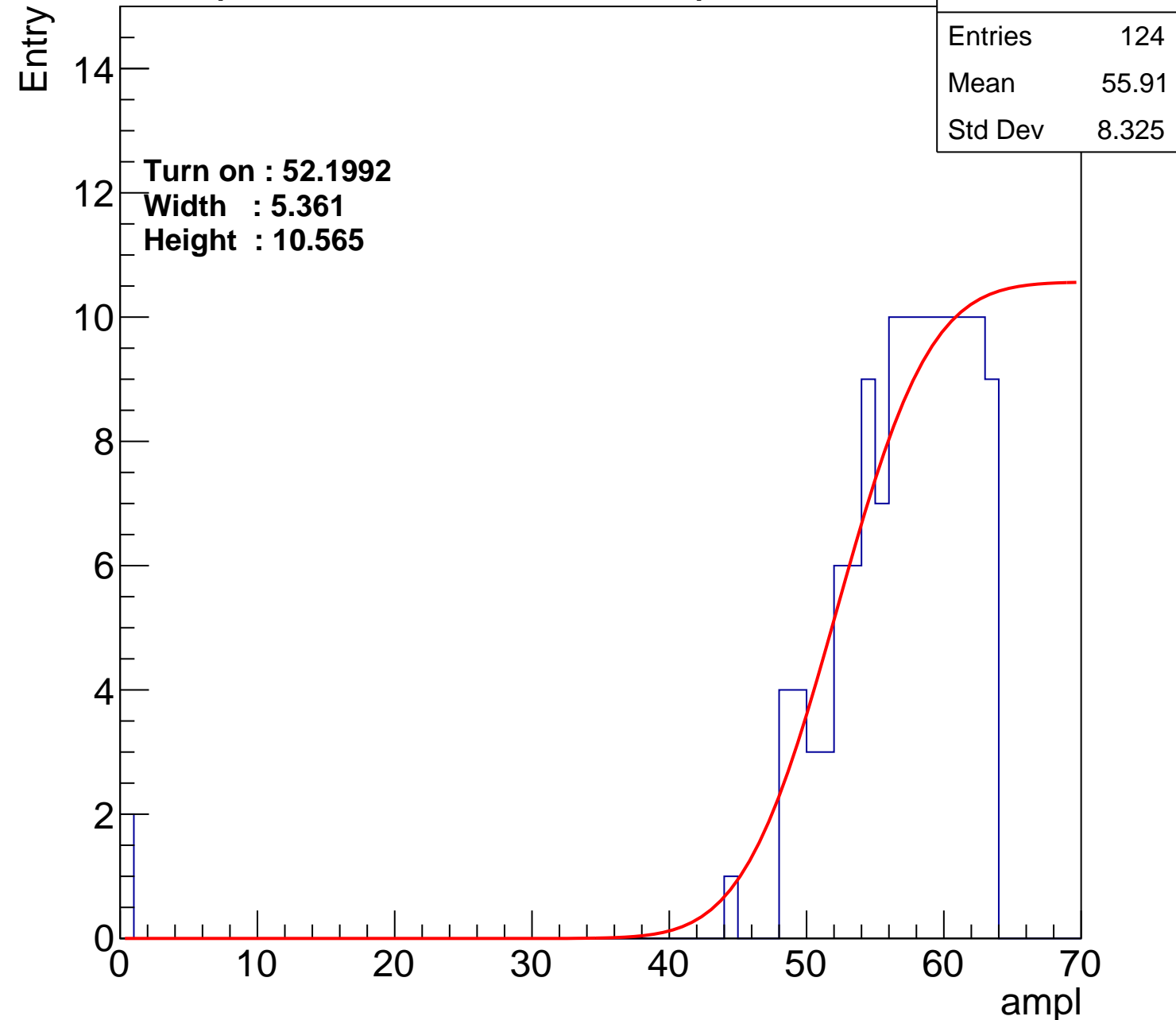
Width : 5.361

Height : 10.565

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.12
Std Dev	9.219

Turn on : 49.5736

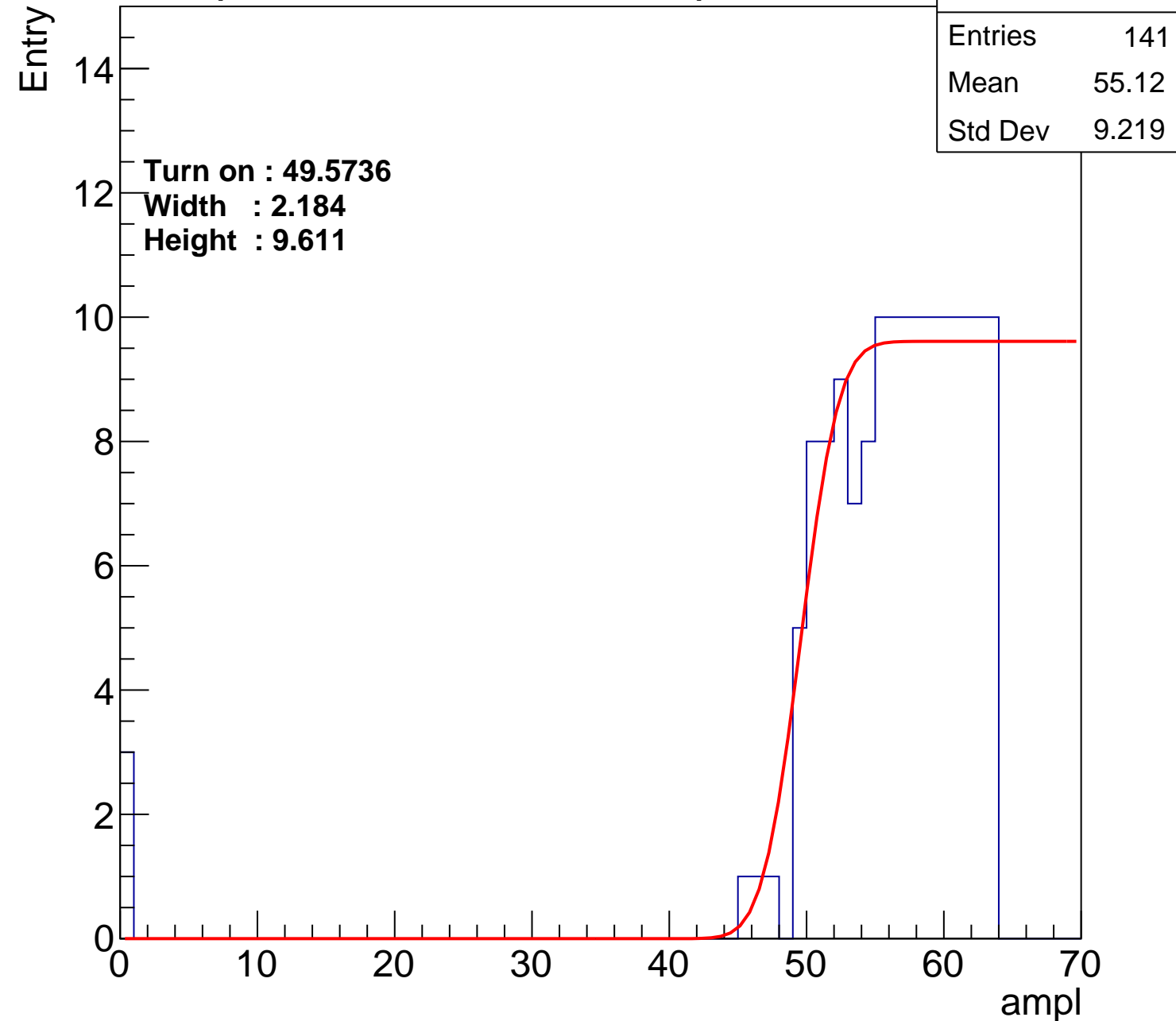
Width : 2.184

Height : 9.611

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch111

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.81
Std Dev	9.691

Turn on : 52.3581

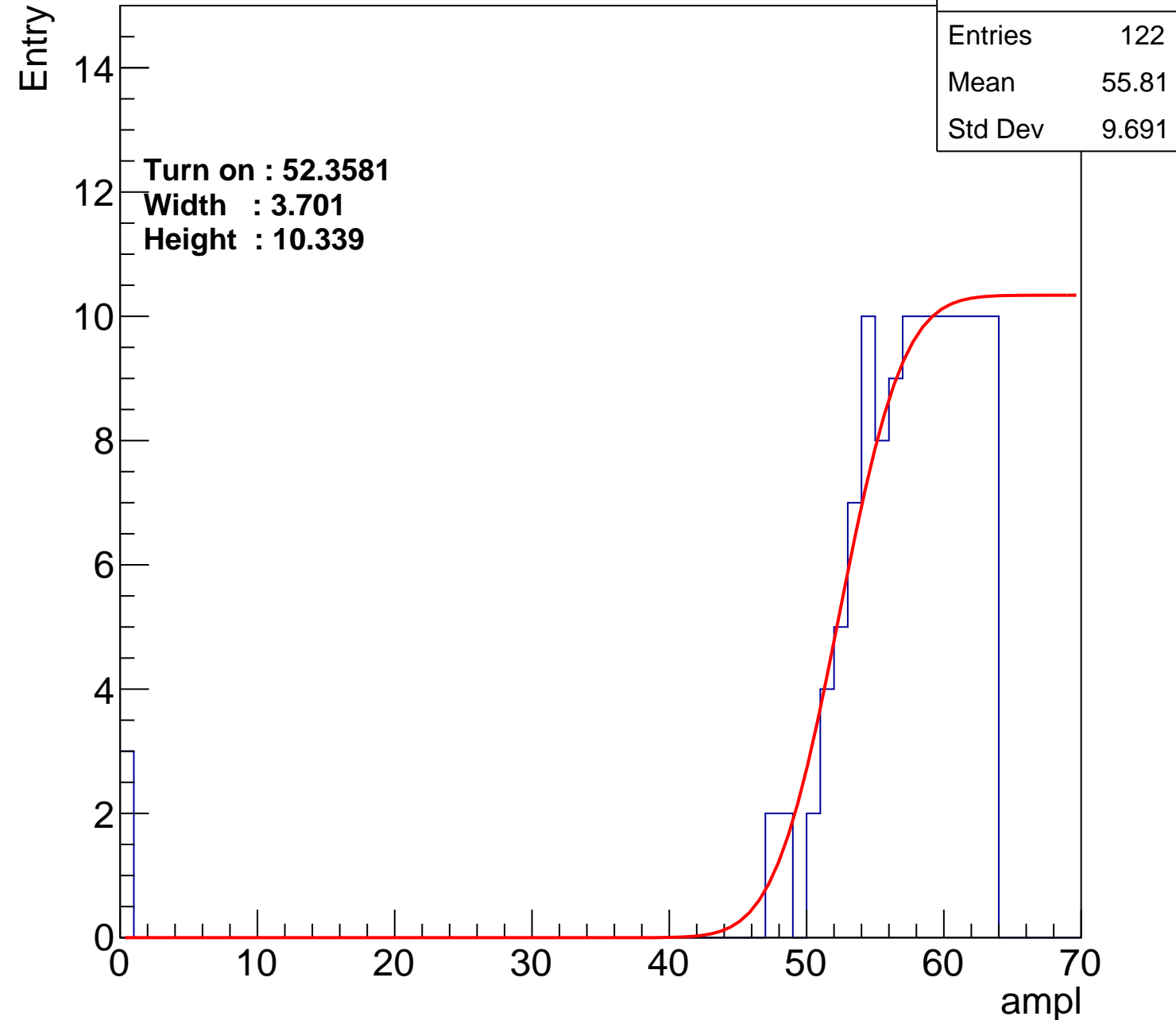
Width : 3.701

Height : 10.339

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	114
Mean	56.04
Std Dev	9.933

Turn on : 53.0065

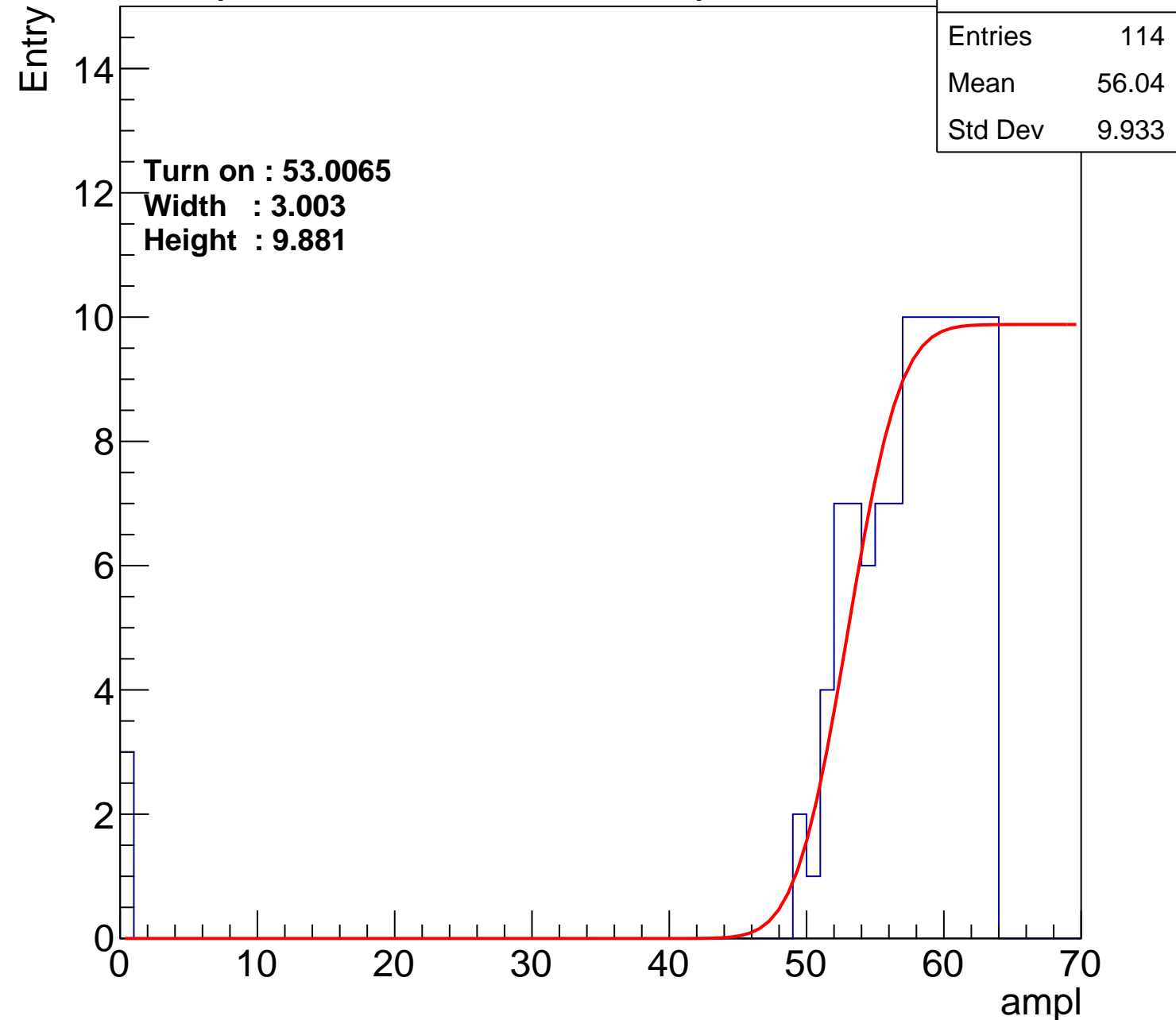
Width : 3.003

Height : 9.881

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.52
Std Dev	9.562

Turn on : 52.4573

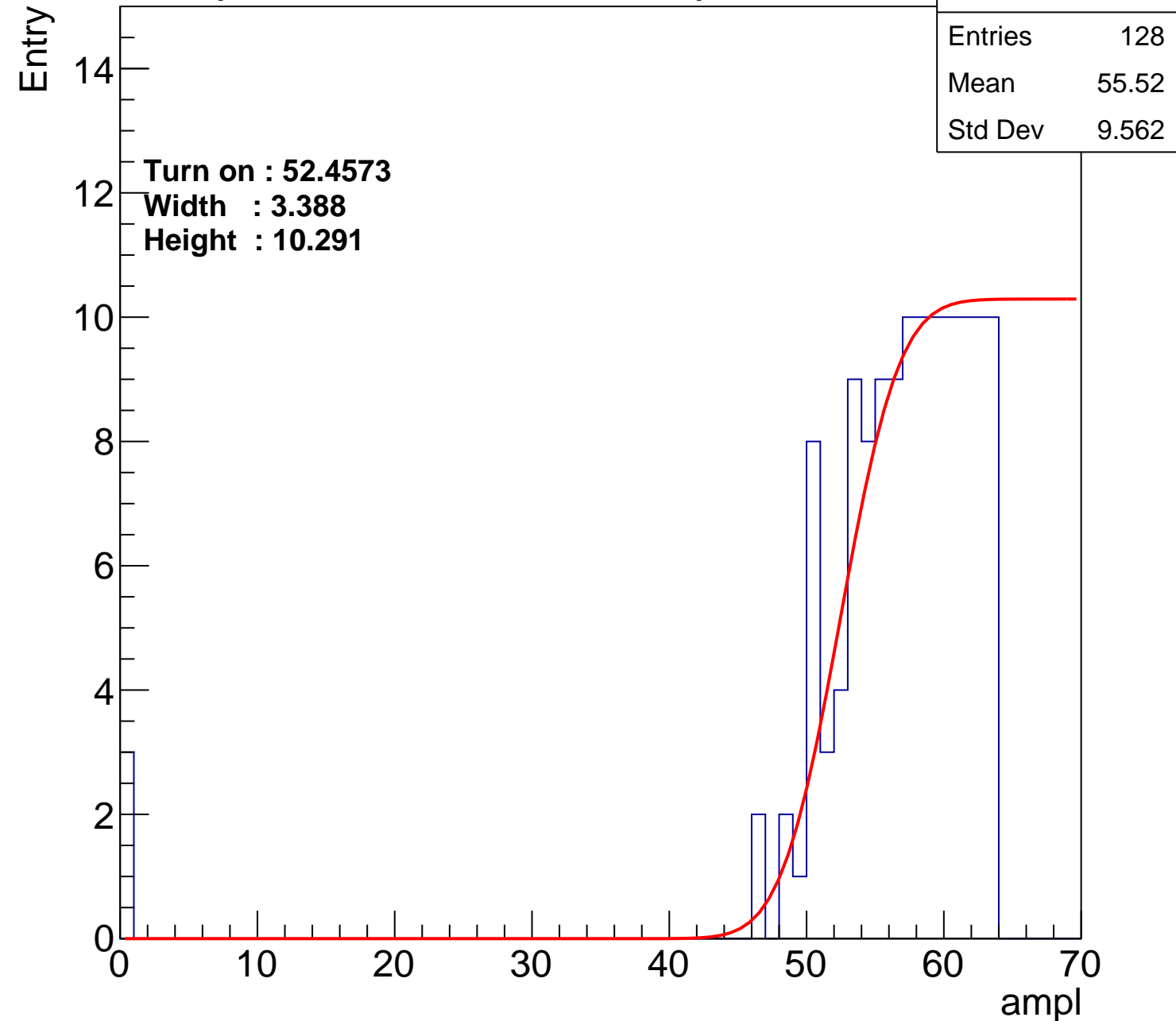
Width : 3.388

Height : 10.291

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	55.69
Std Dev	11.54

Turn on : 54.9405

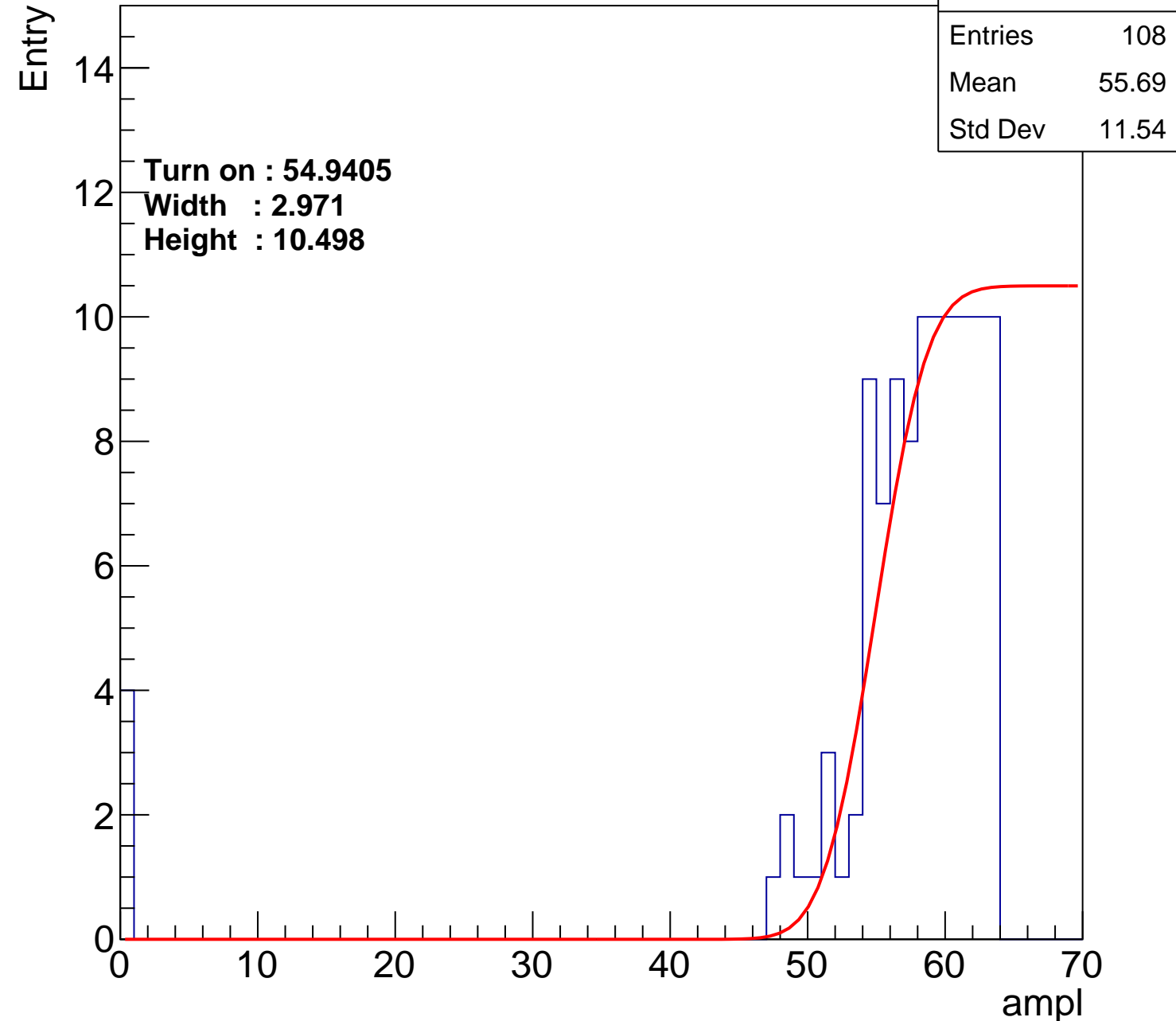
Width : 2.971

Height : 10.498

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.28
Std Dev	9.361

Turn on : 51.3423

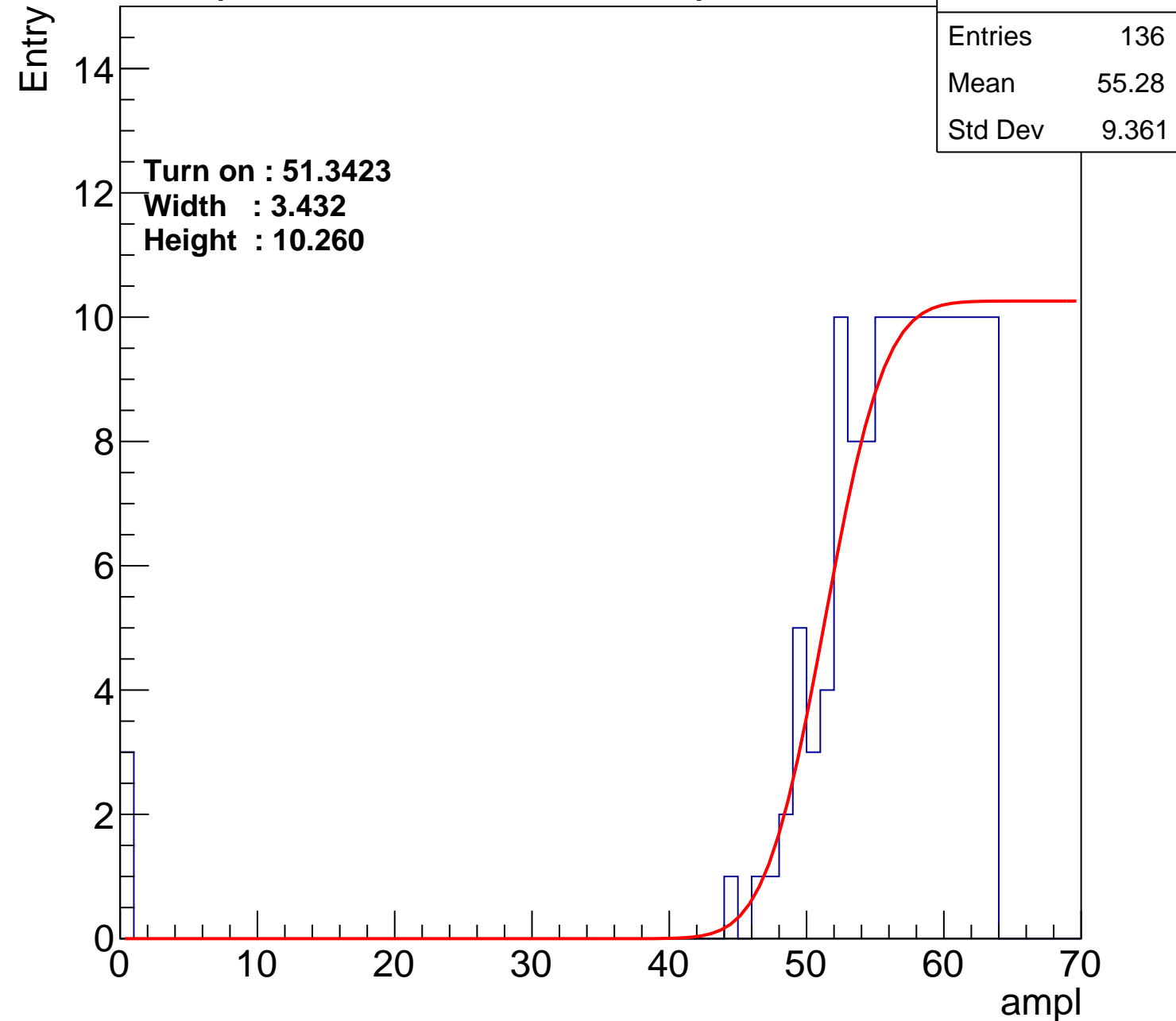
Width : 3.432

Height : 10.260

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

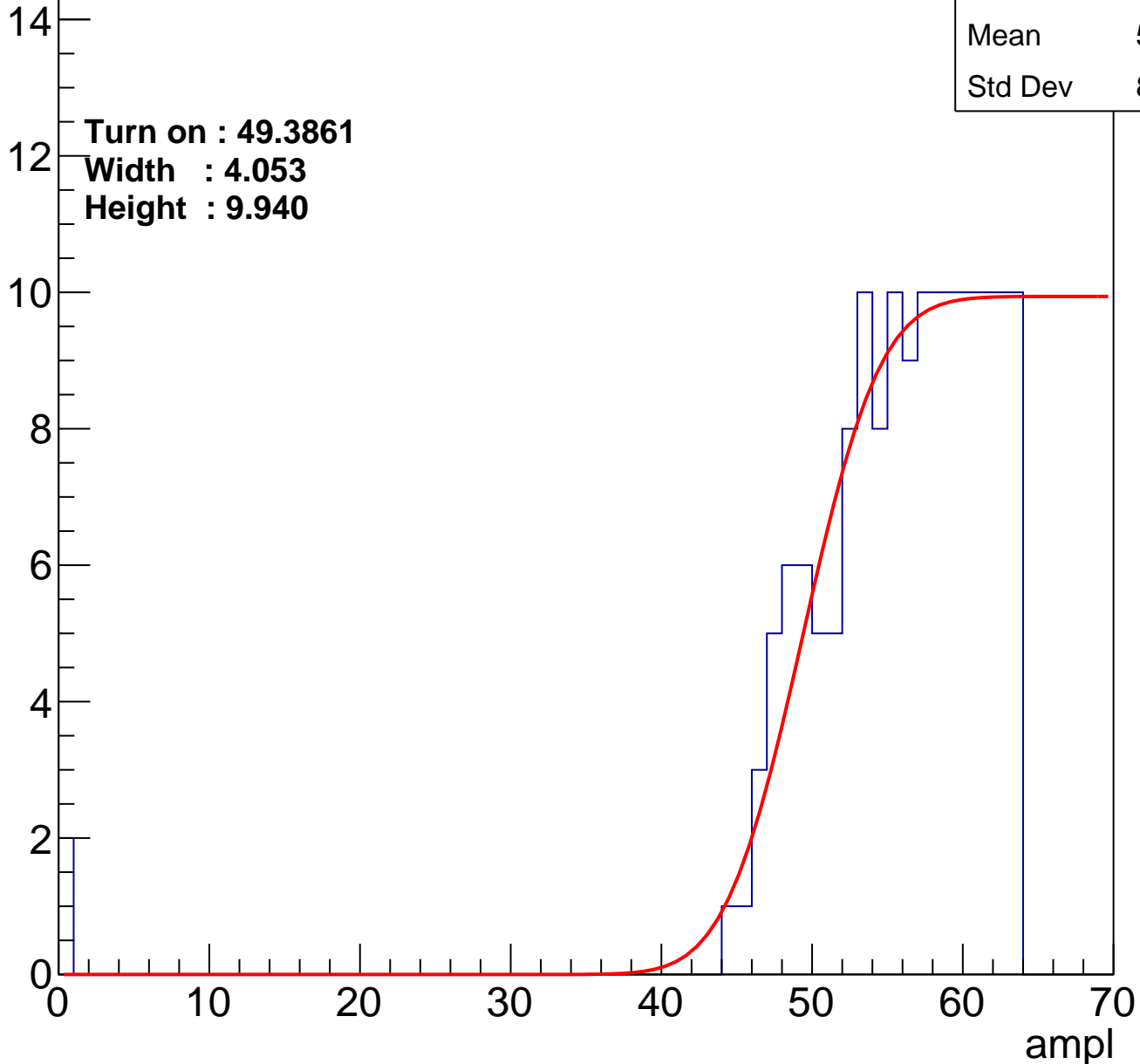
Entries	149
Mean	54.91
Std Dev	8.071

Turn on : 49.3861

Width : 4.053

Height : 9.940

Entry



# B0L103S, U26-ch117

calib\_packv5\_040323\_1717.root, FC#2, port C3

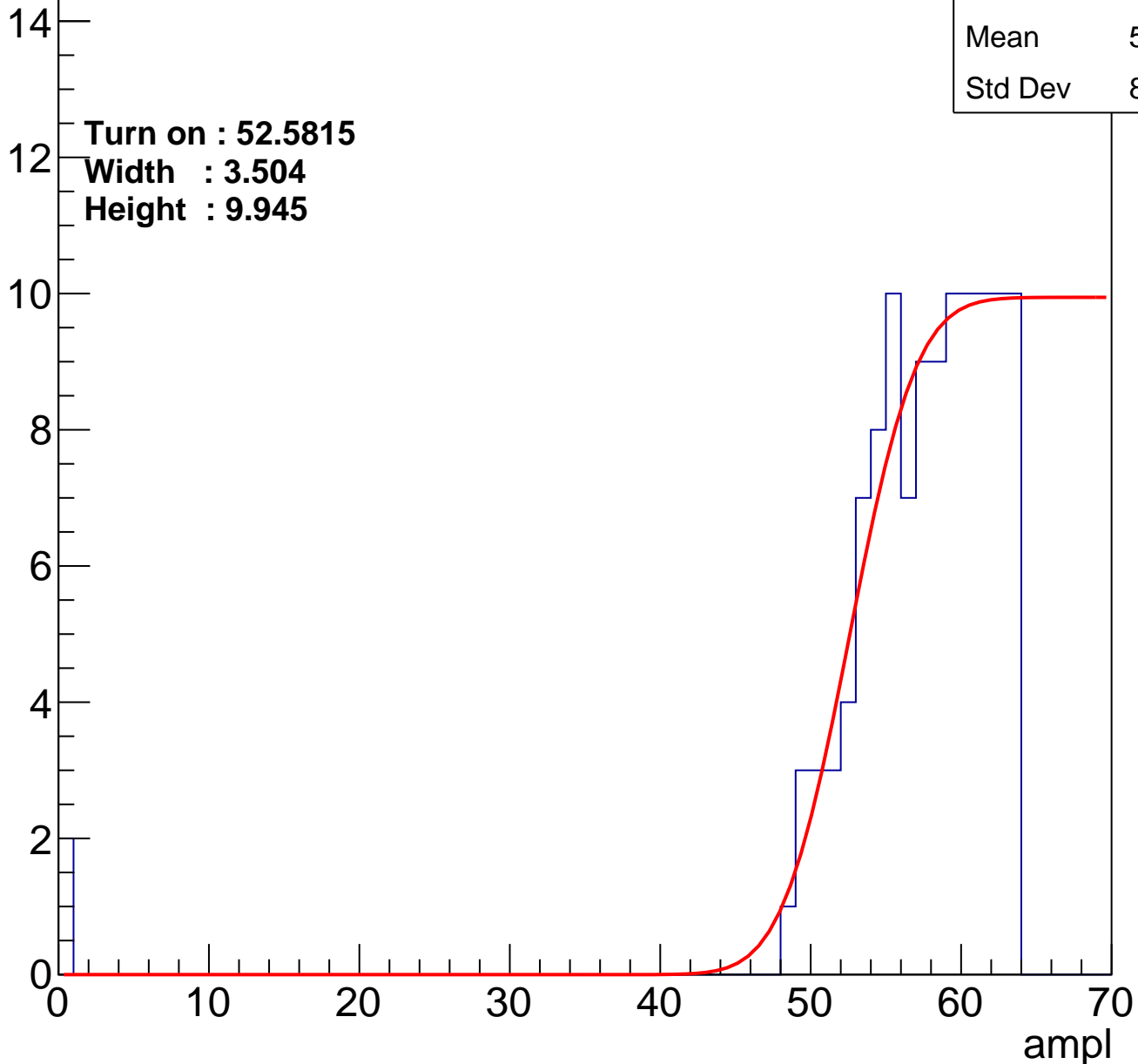
Entries	116
Mean	56.34
Std Dev	8.416

Turn on : 52.5815

Width : 3.504

Height : 9.945

Entry



# B0L103S, U26-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	55.29
Std Dev	7.994

Turn on : 49.9390

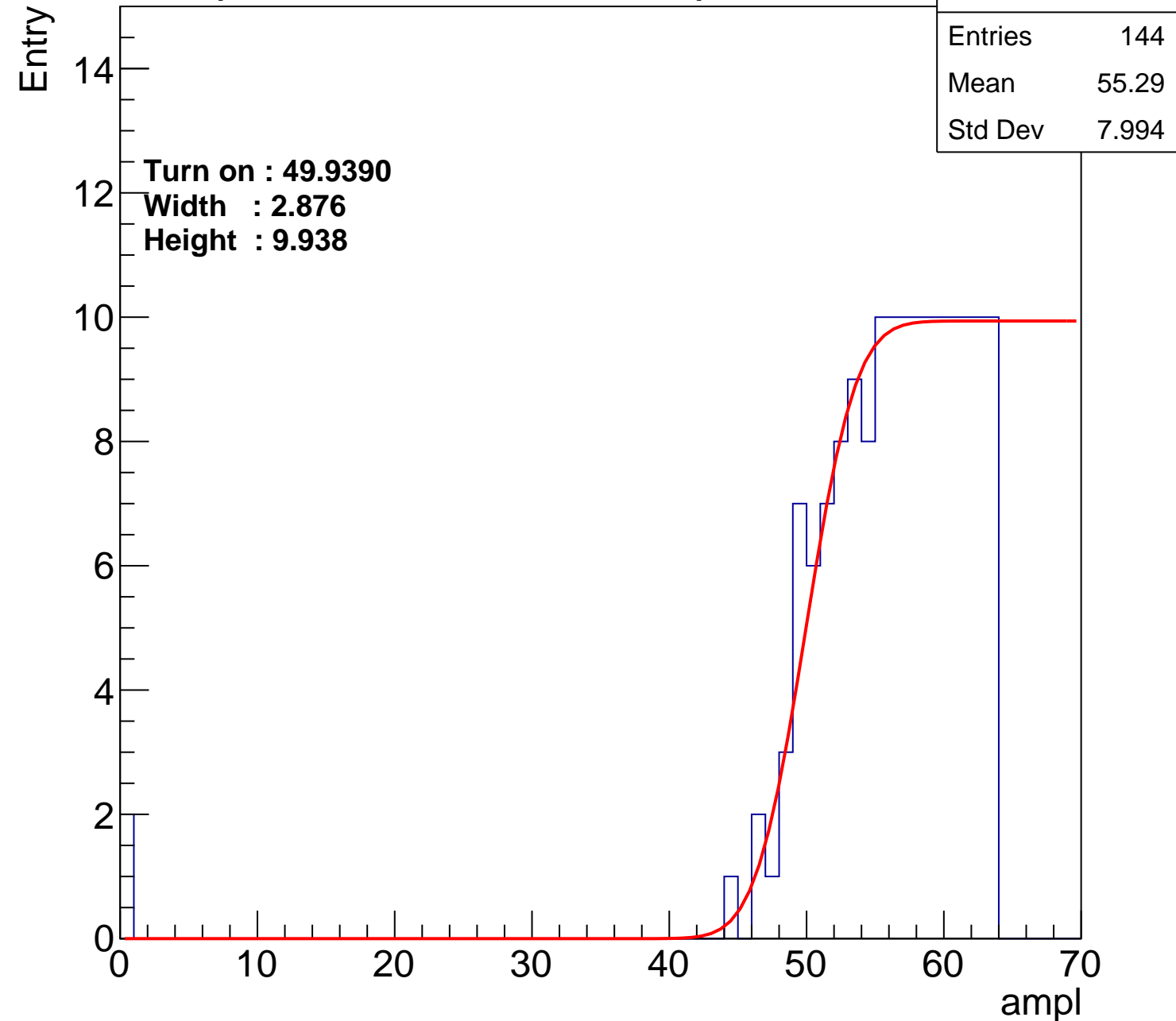
Width : 2.876

Height : 9.938

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	105
Mean	55.79
Std Dev	11.66

**Turn on : 54.4749**

**Width : 3.582**

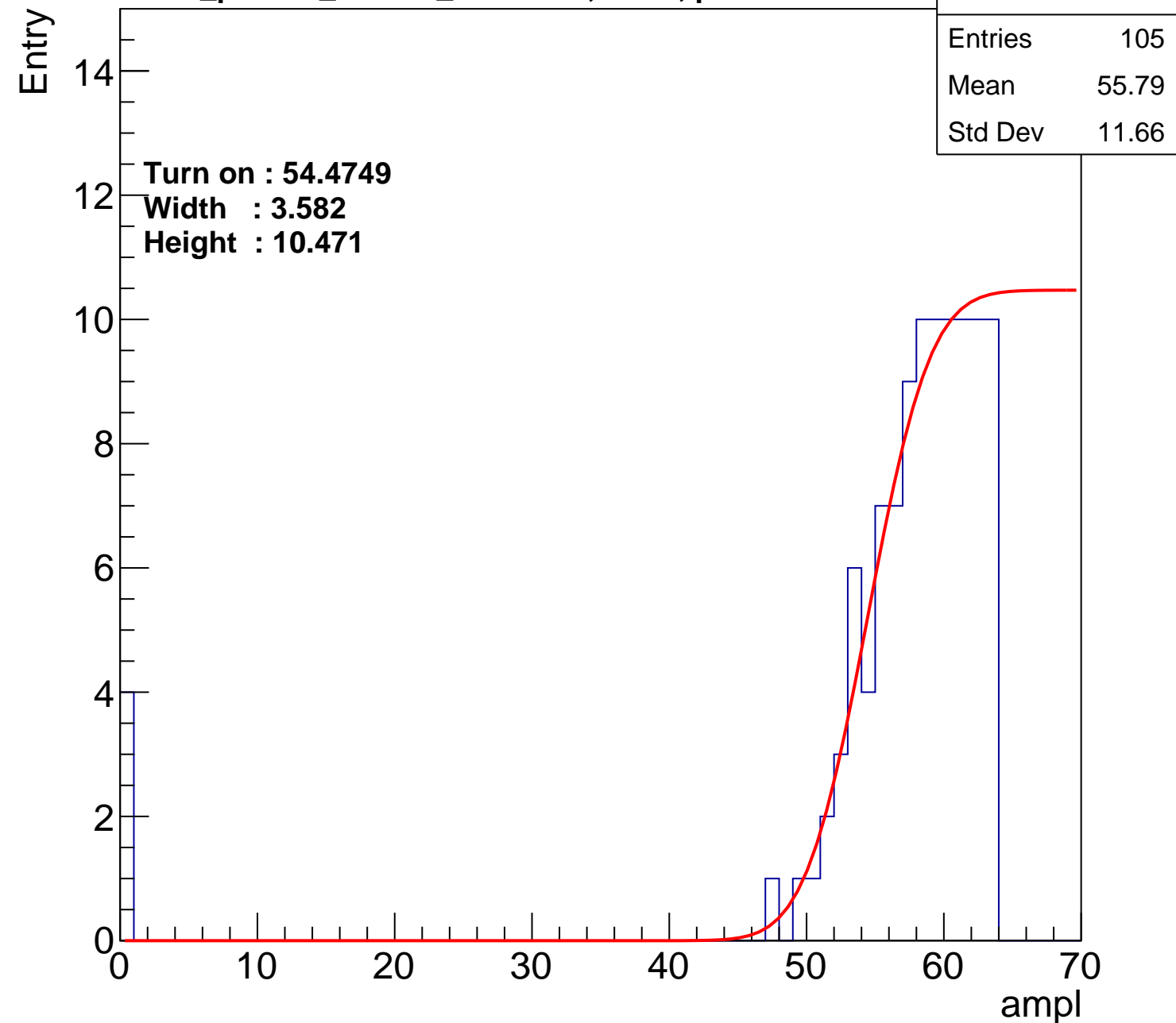
**Height : 10.471**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U26-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.23
Std Dev	10.69

Turn on : 52.2140

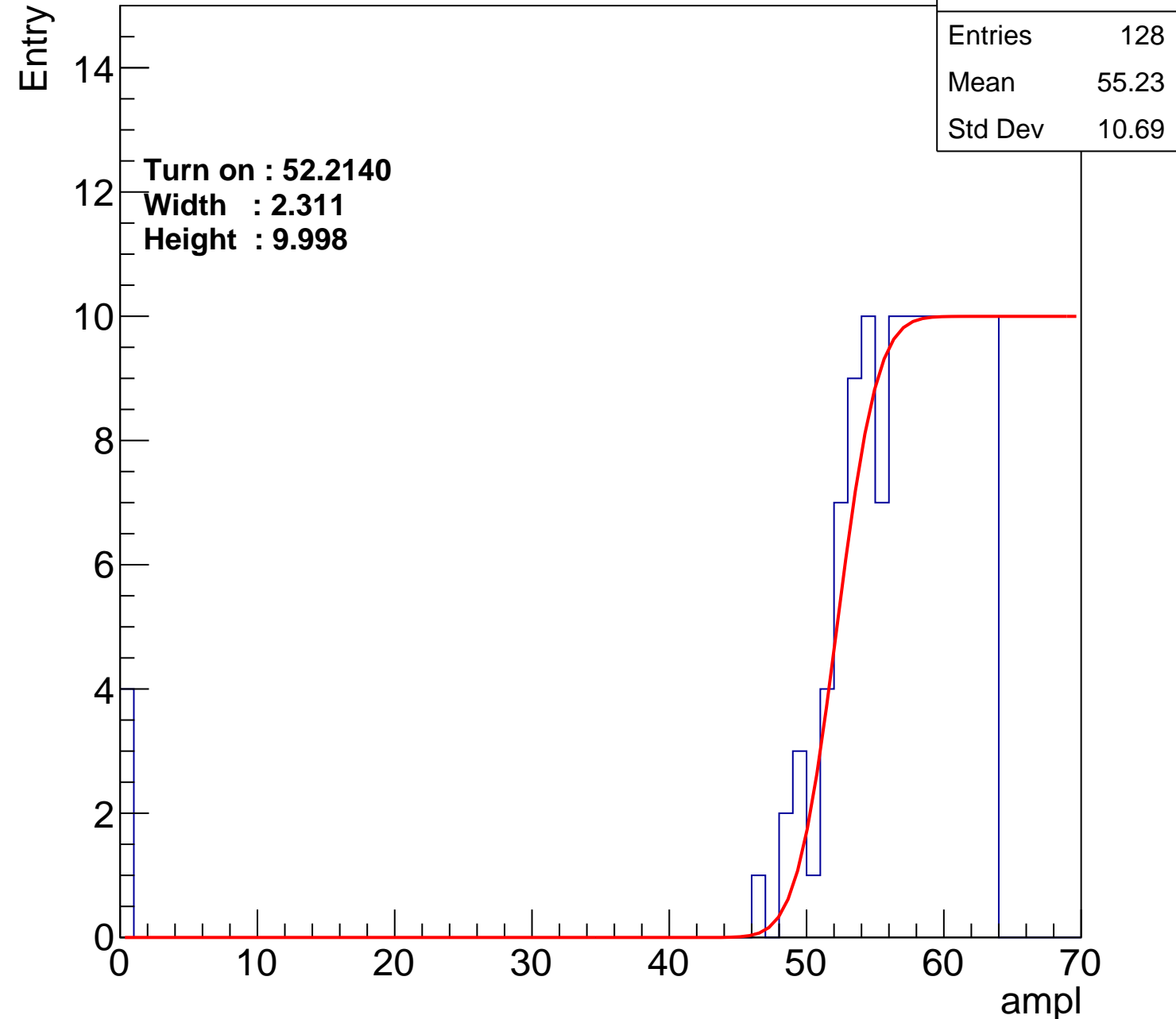
Width : 2.311

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.47
Std Dev	9.445

Turn on : 51.2681

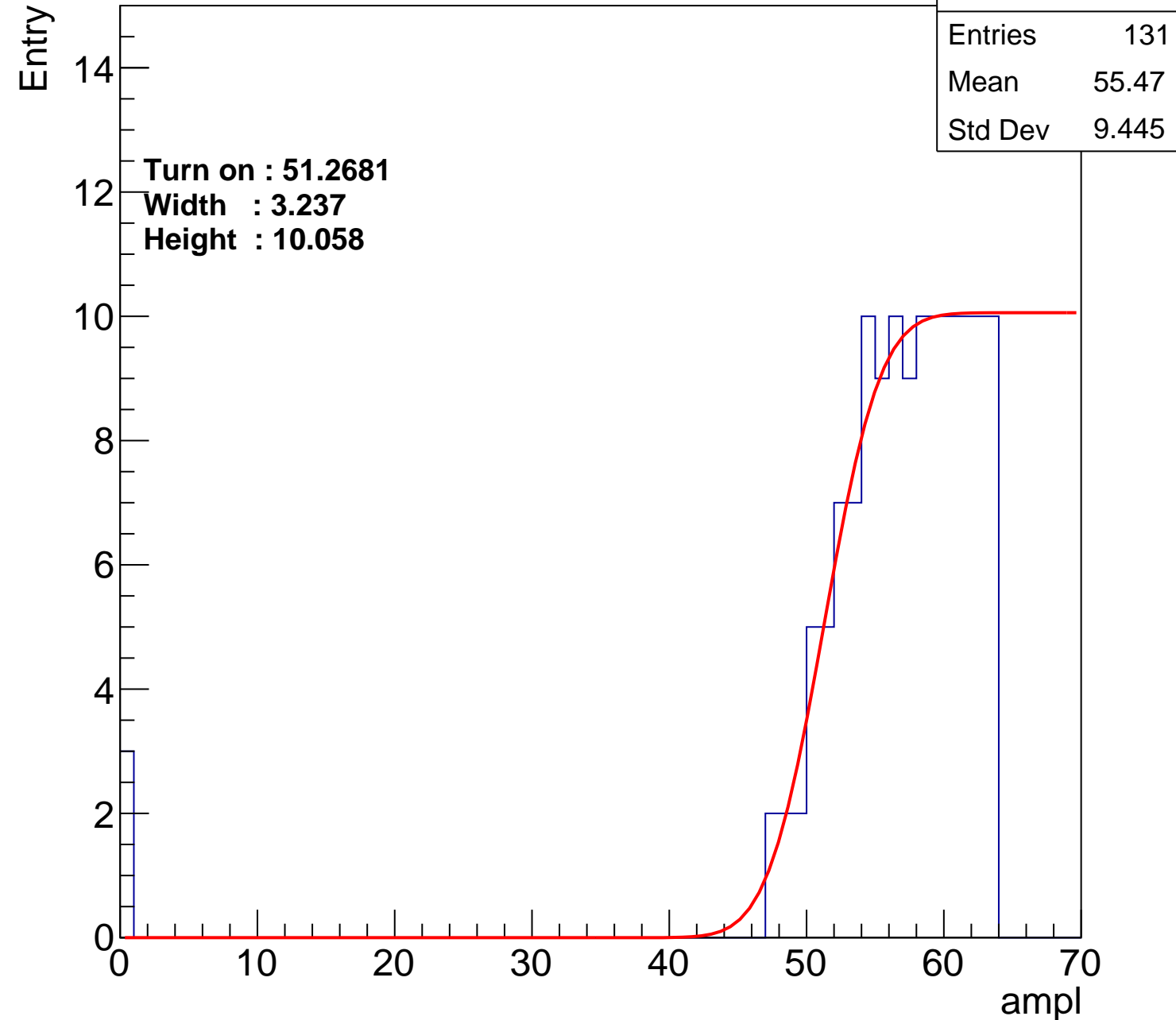
Width : 3.237

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	55.4
Std Dev	11.13

Turn on : 53.0984

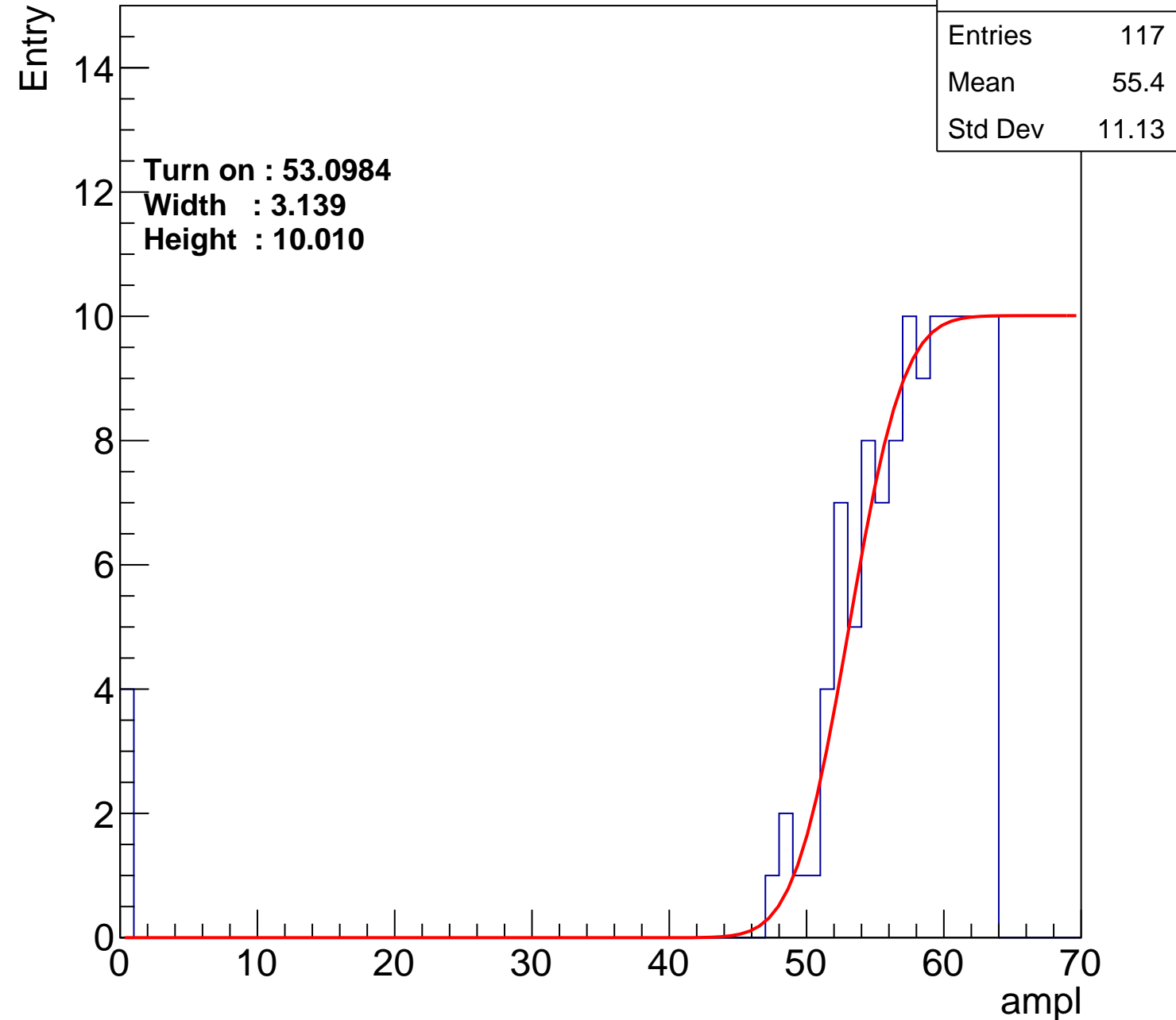
Width : 3.139

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch123

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	55.82
Std Dev	11.46

Turn on : 53.6812

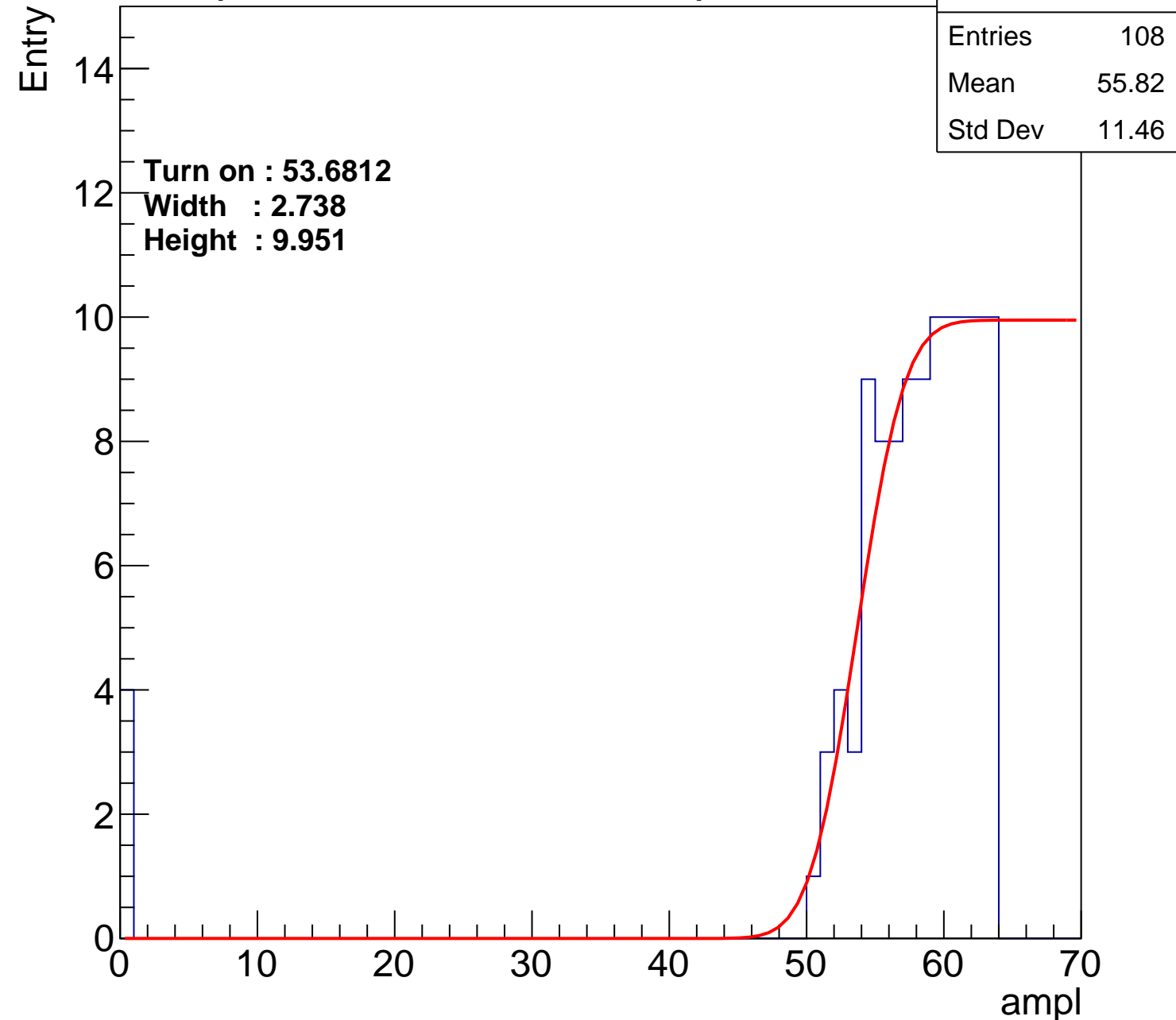
Width : 2.738

Height : 9.951

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	55.18
Std Dev	12.02

Turn on : 54.8056

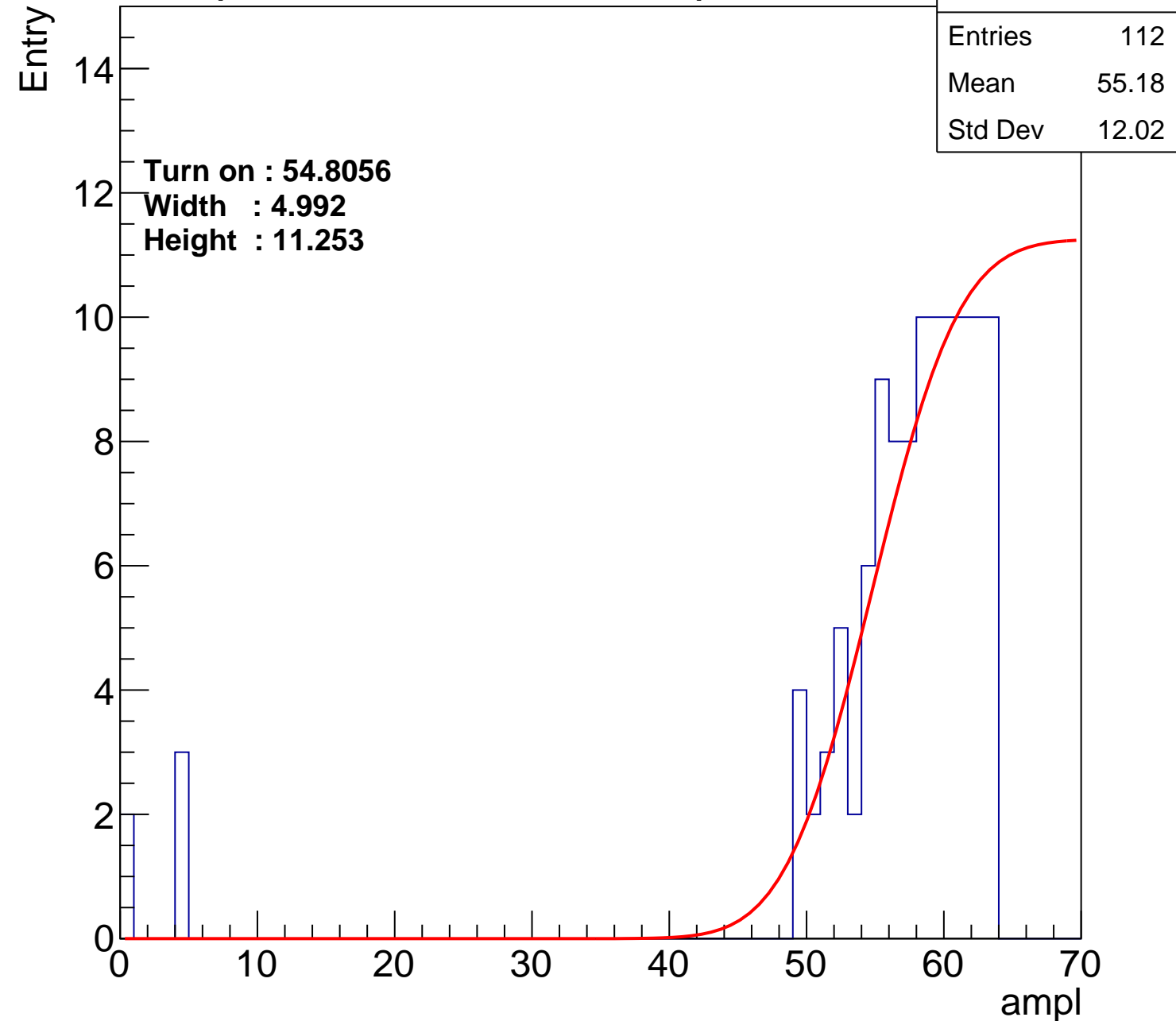
Width : 4.992

Height : 11.253

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch125

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.92
Std Dev	9.202

Turn on : 50.1683

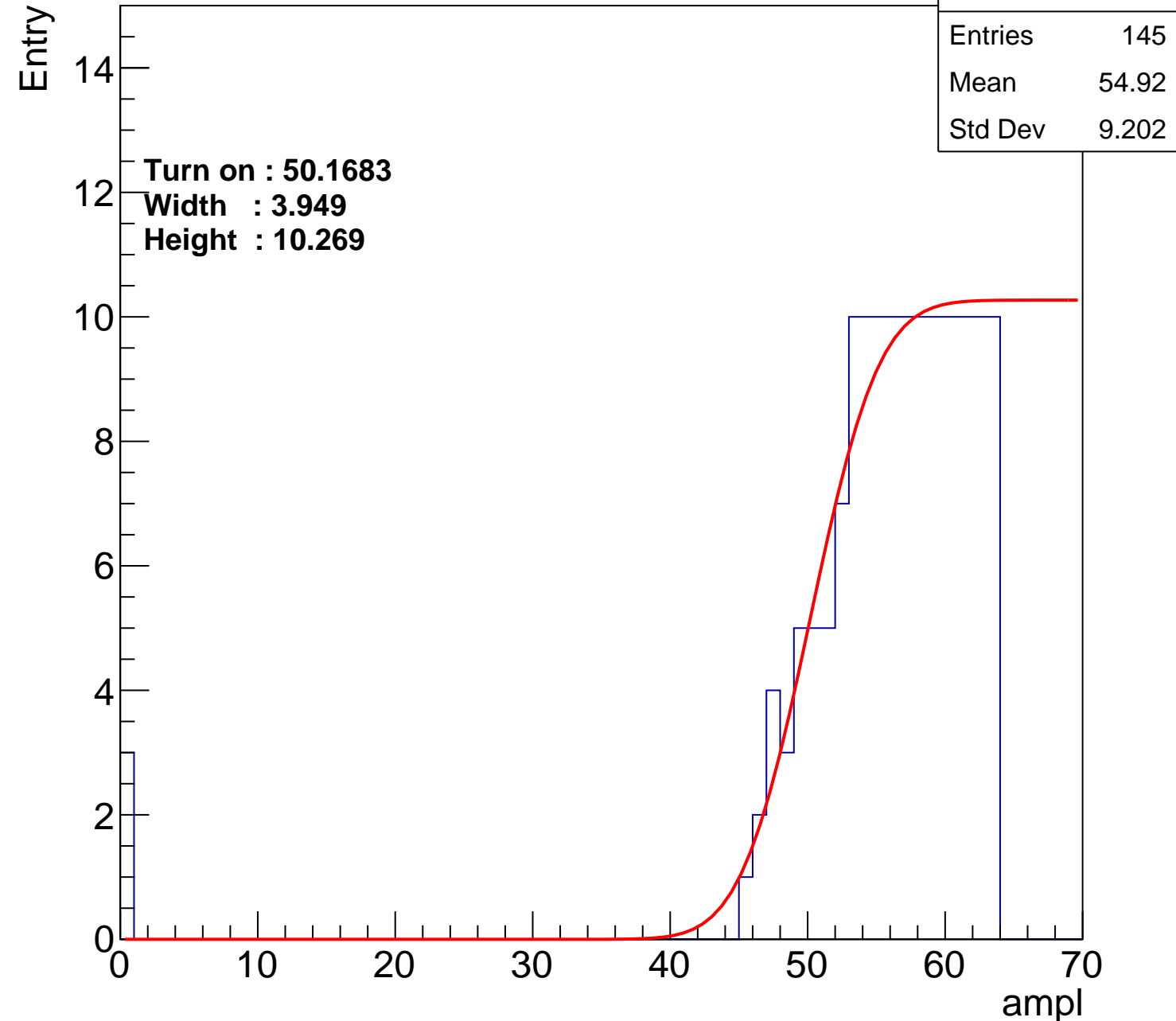
Width : 3.949

Height : 10.269

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U26-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.21
Std Dev	8.278

Turn on : 52.5582

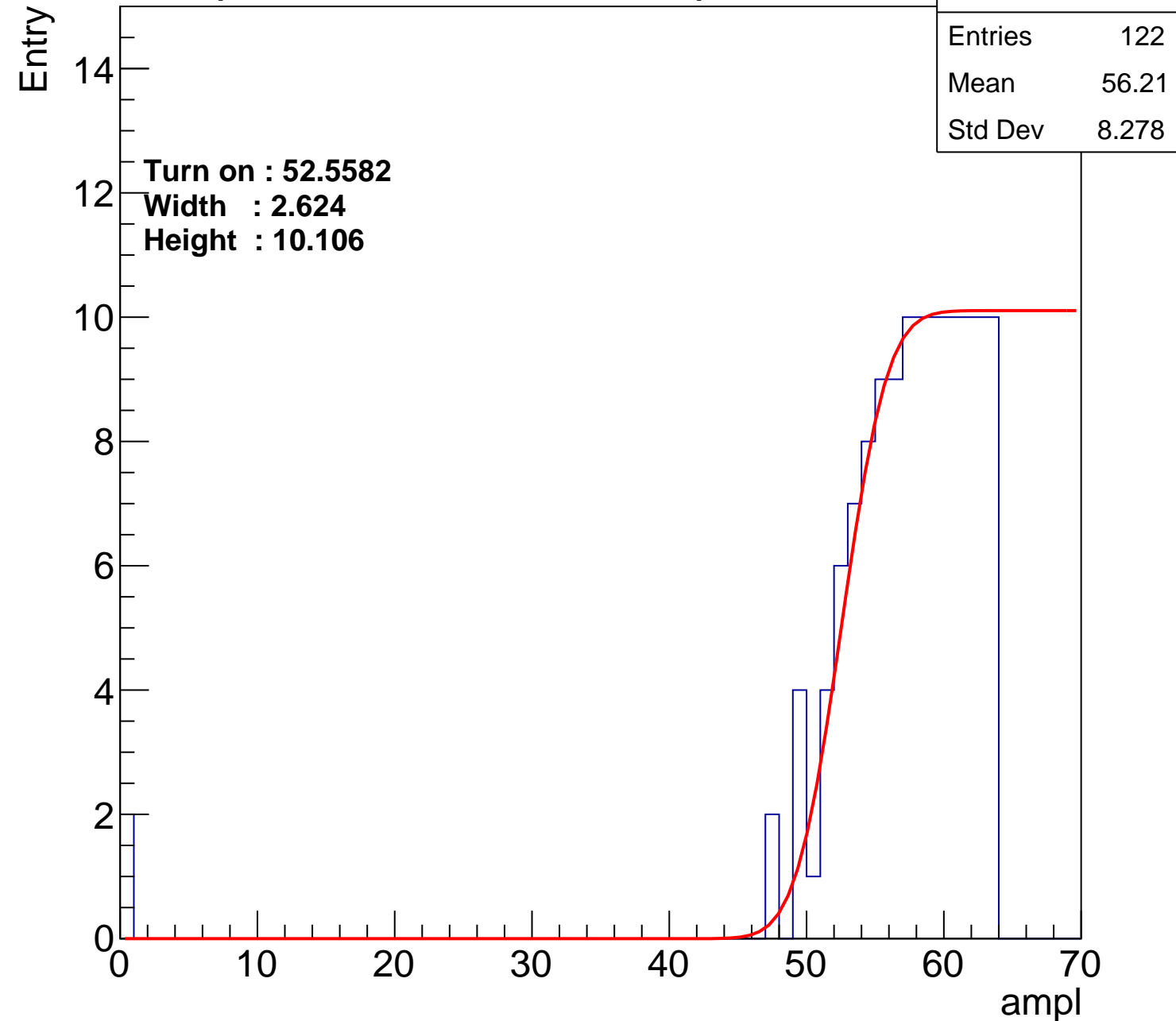
Width : 2.624

Height : 10.106

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U26-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

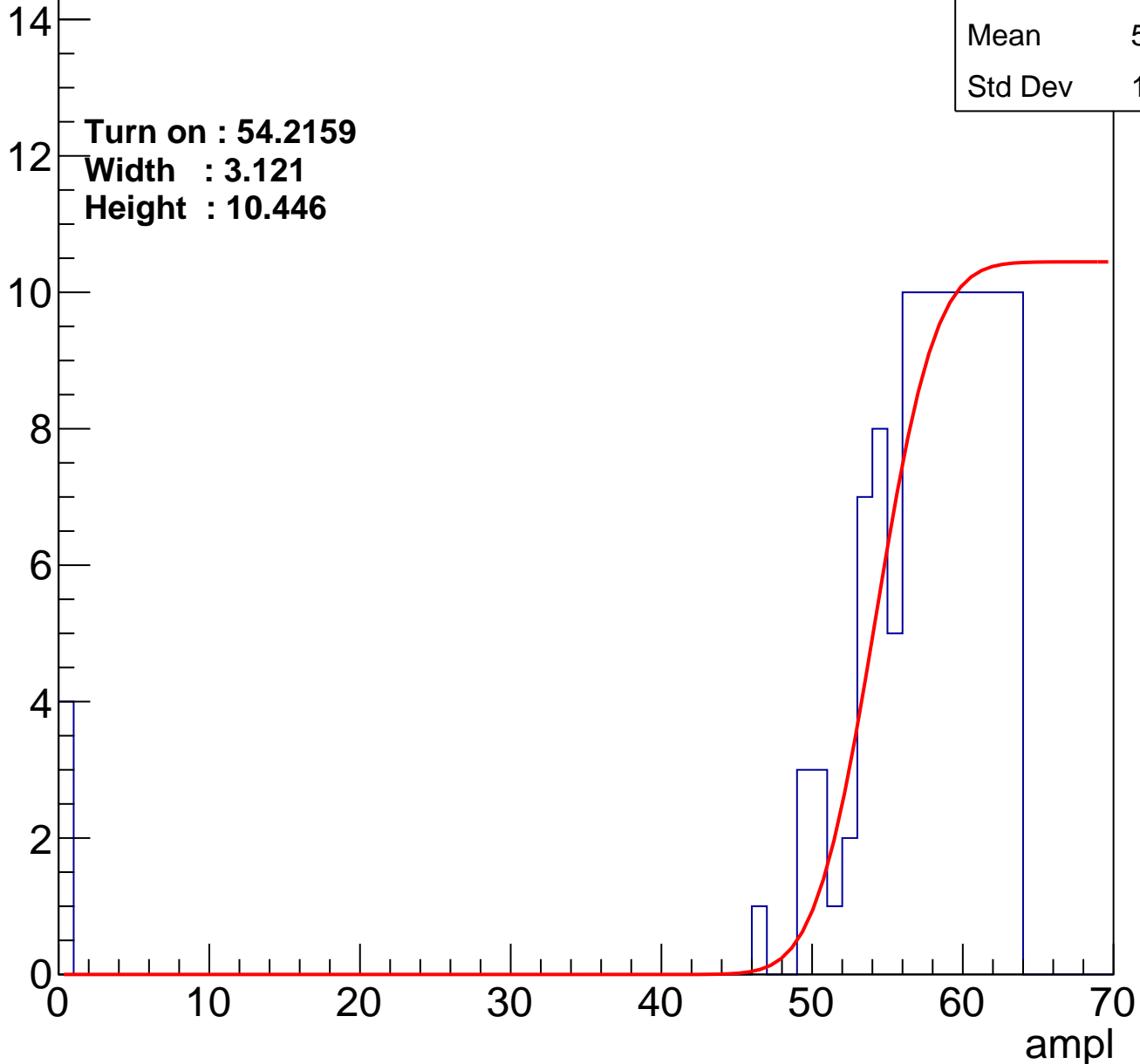
Entries	114
Mean	55.58
Std Dev	11.25

Turn on : 54.2159

Width : 3.121

Height : 10.446

Entry



# B0L103S, U26-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	114
Mean	55.58
Std Dev	11.25

Turn on : 54.2159

Width : 3.121

Height : 10.446

Entry

