

B1L003S, U14-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

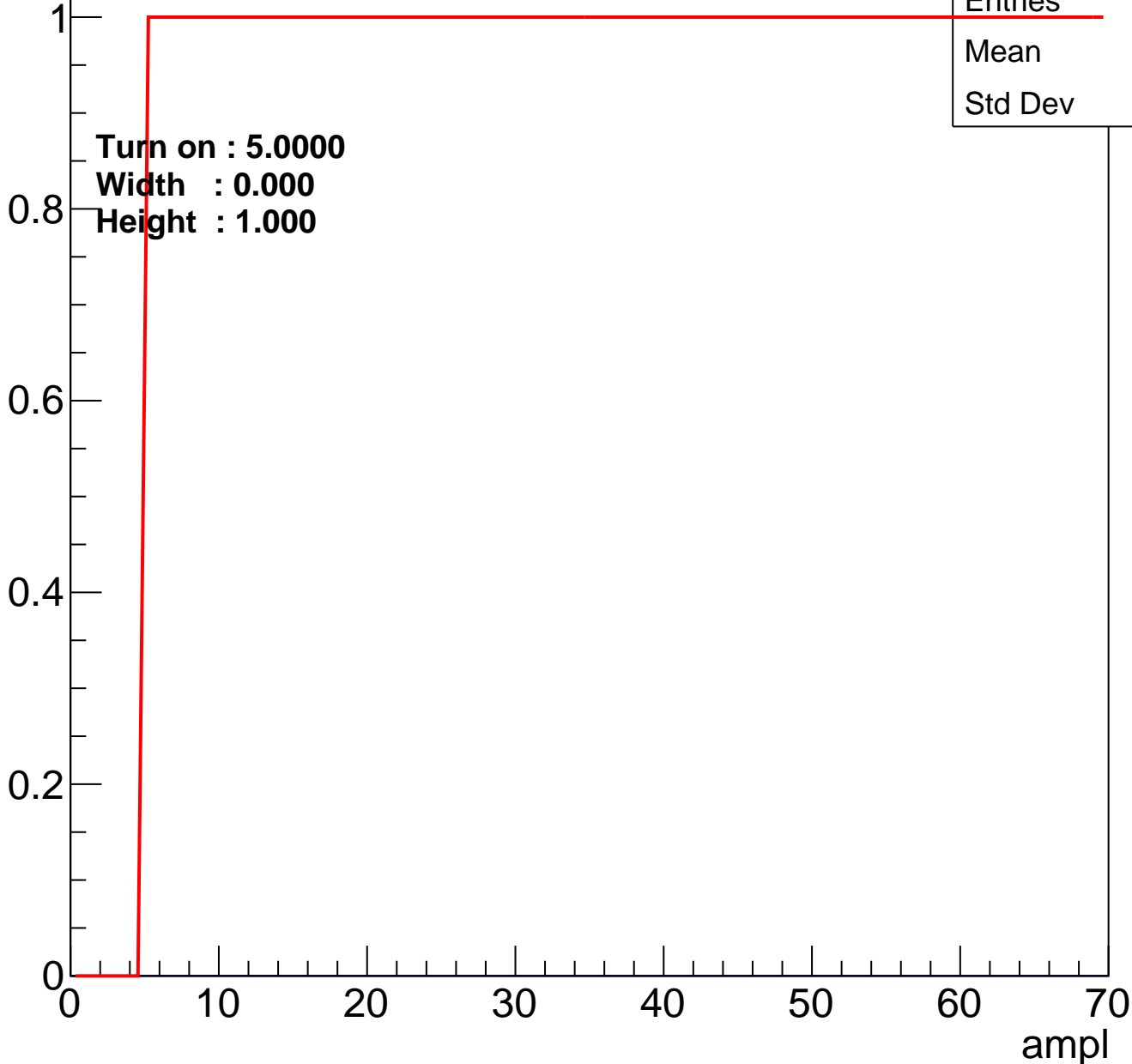


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entry

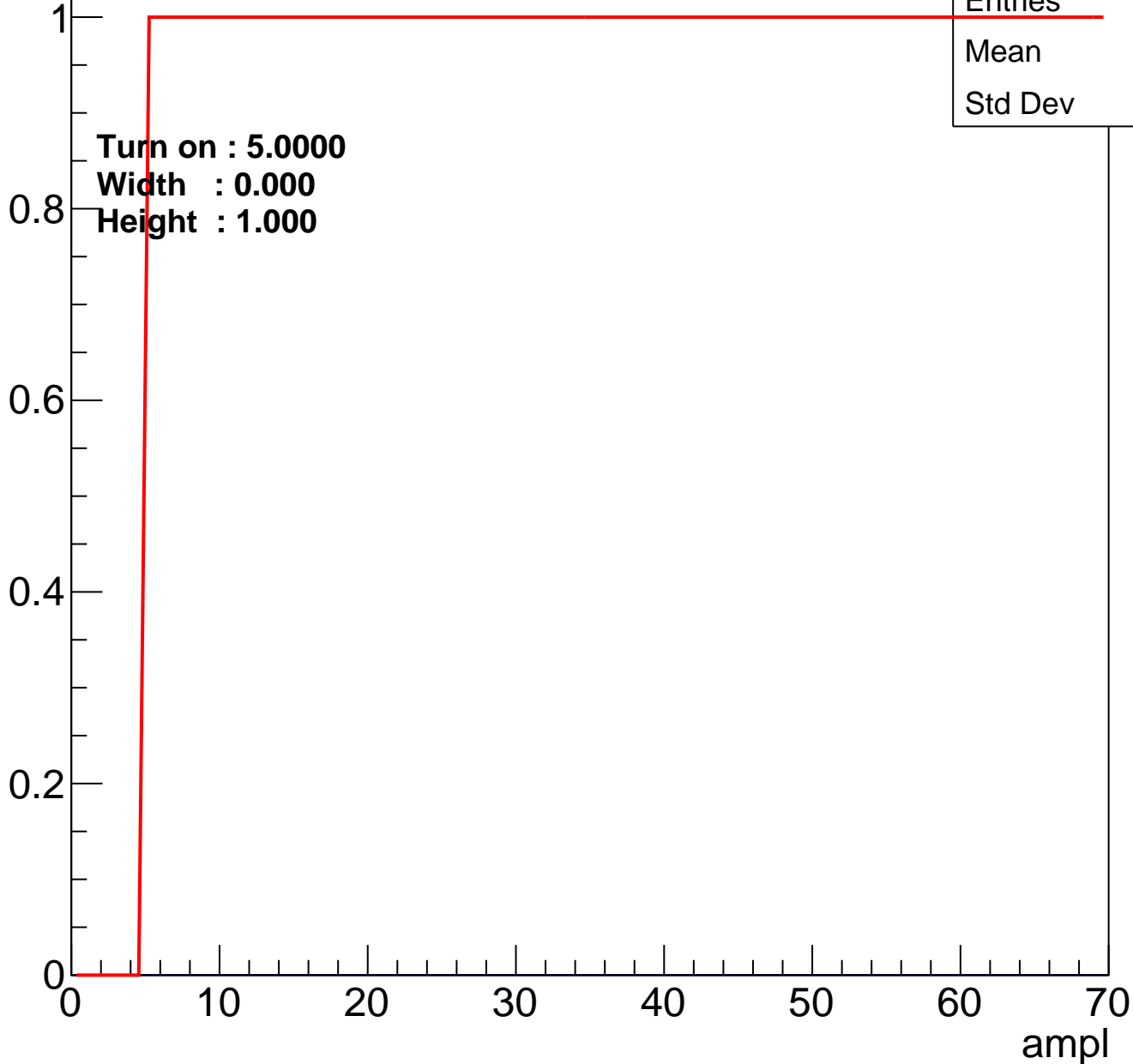


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch13

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch25

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U14-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.63
Std Dev	11.25

Turn on : 26.6520

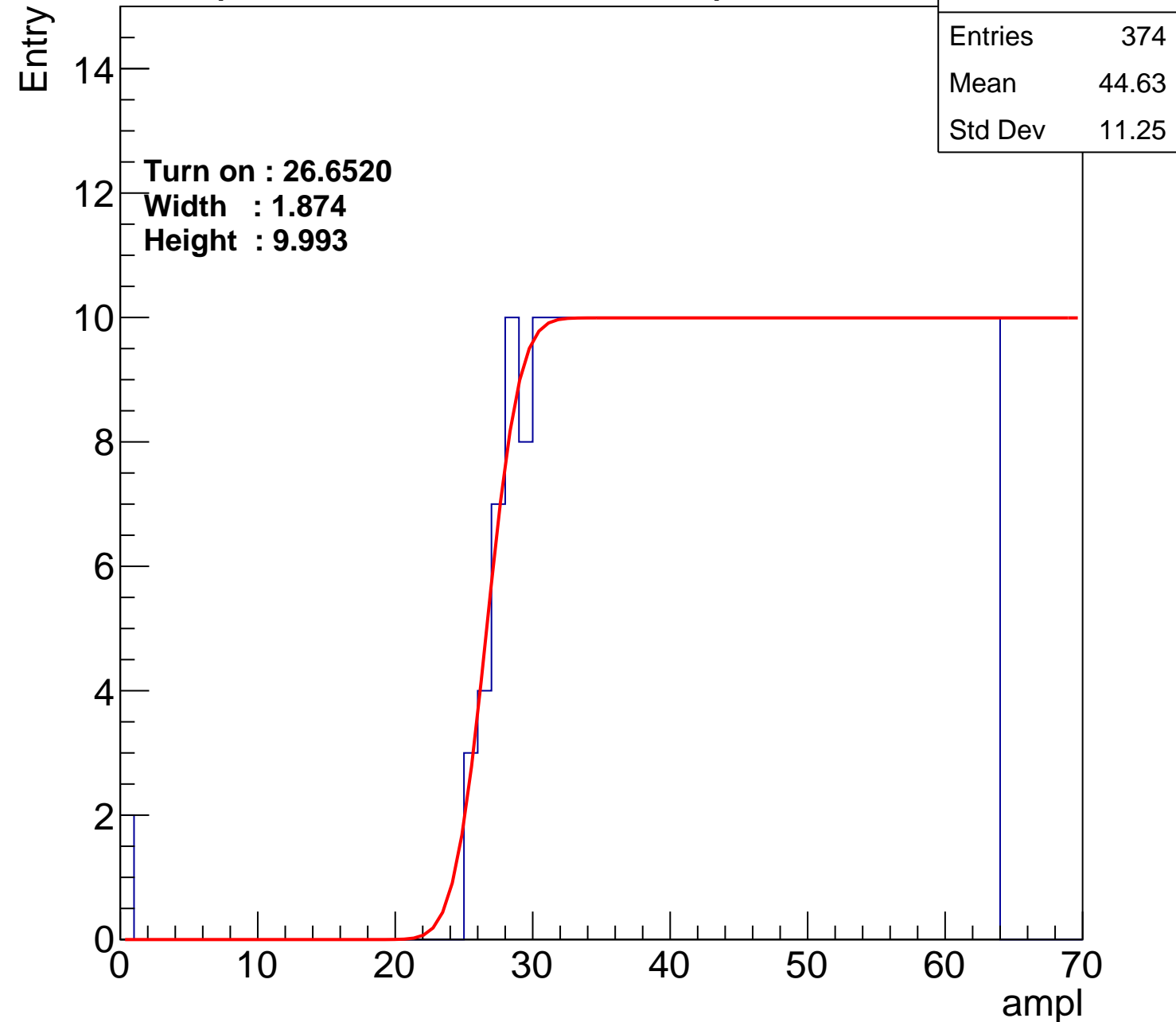
Width : 1.874

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch33

calib_packv5_042523_0143.root, FC#13, port D2

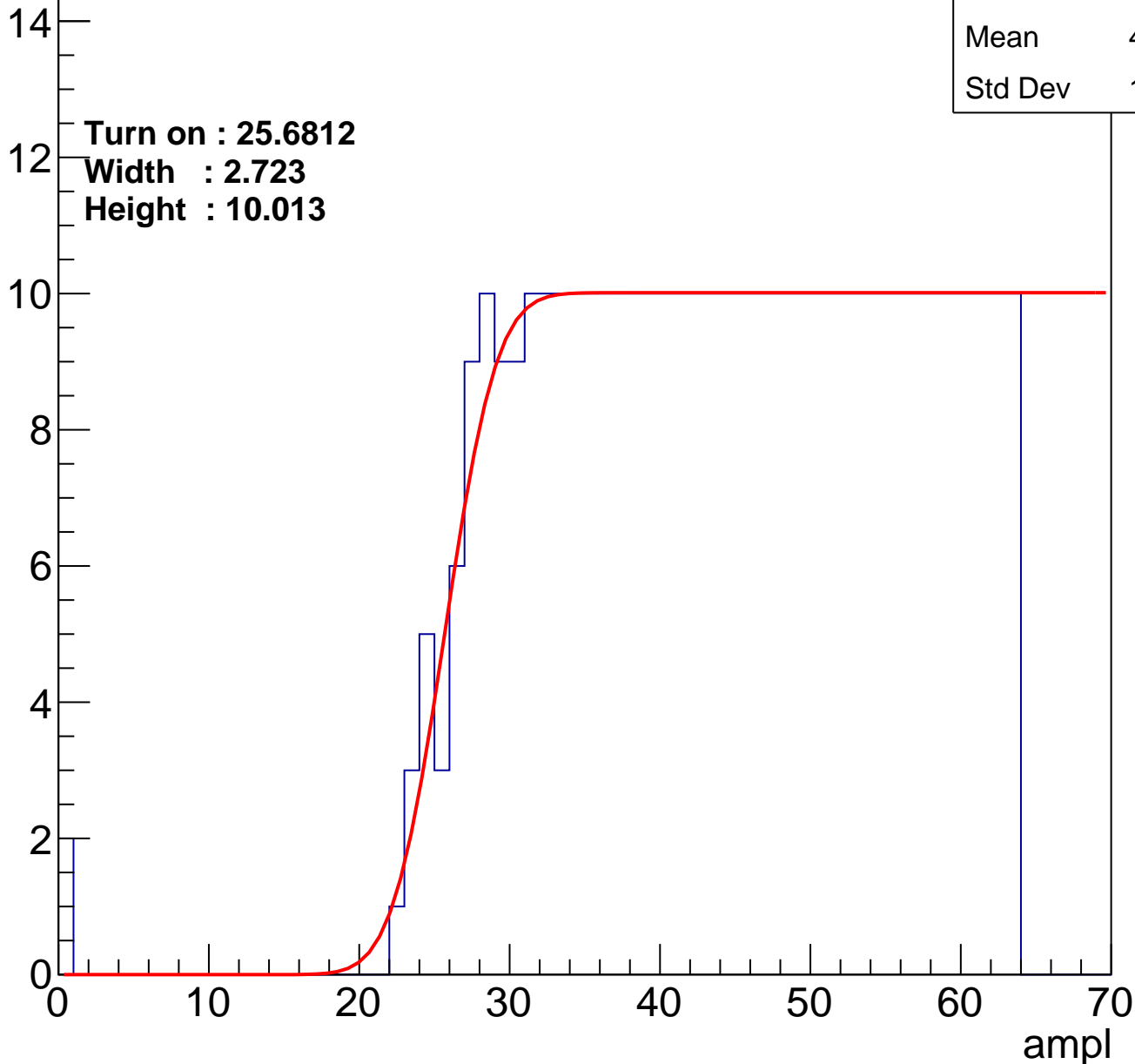
Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.6812

Width : 2.723

Height : 10.013

Entry



B1L003S, U14-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.5375

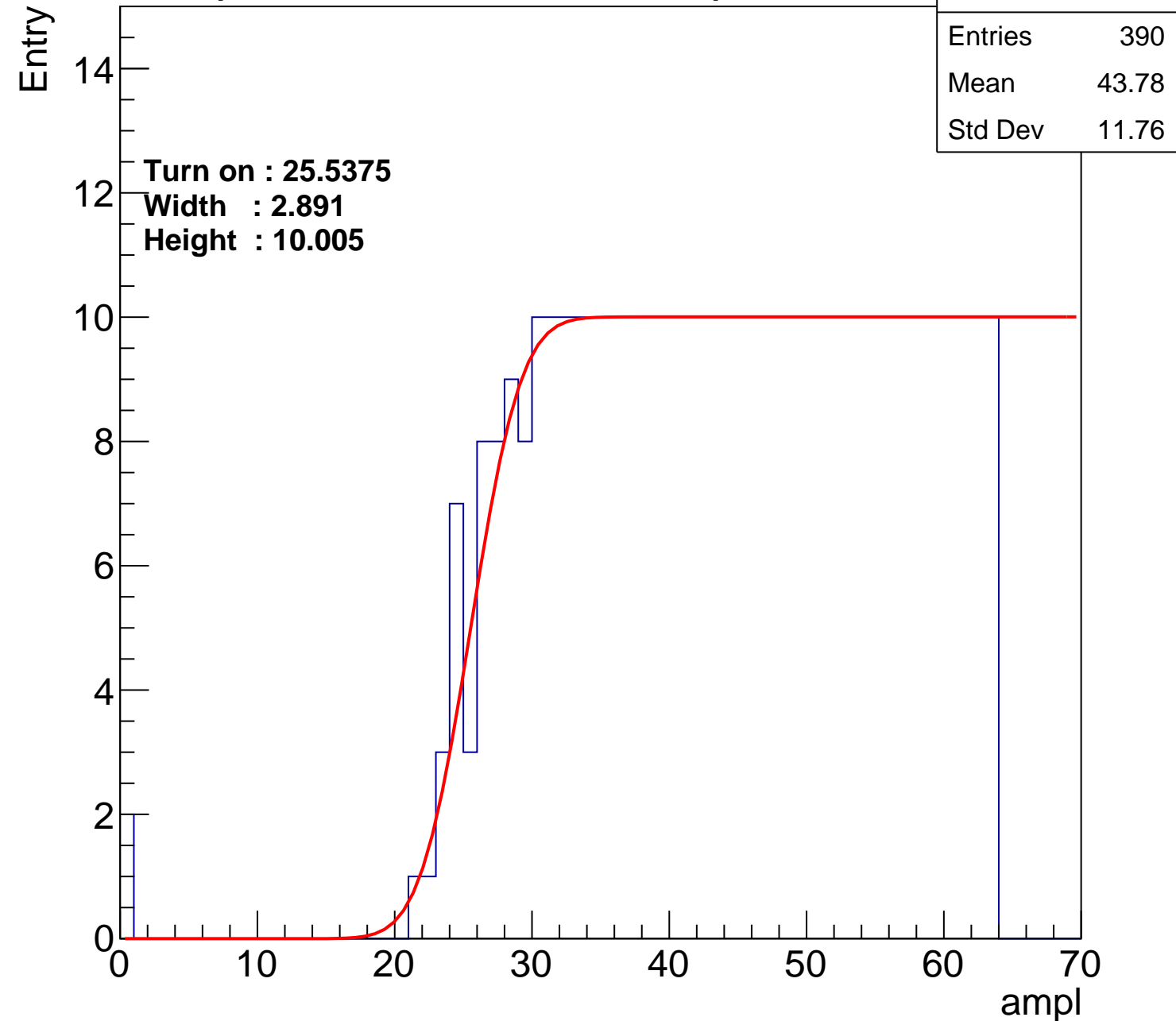
Width : 2.891

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.69
Std Dev	11.67

Turn on : 25.2195

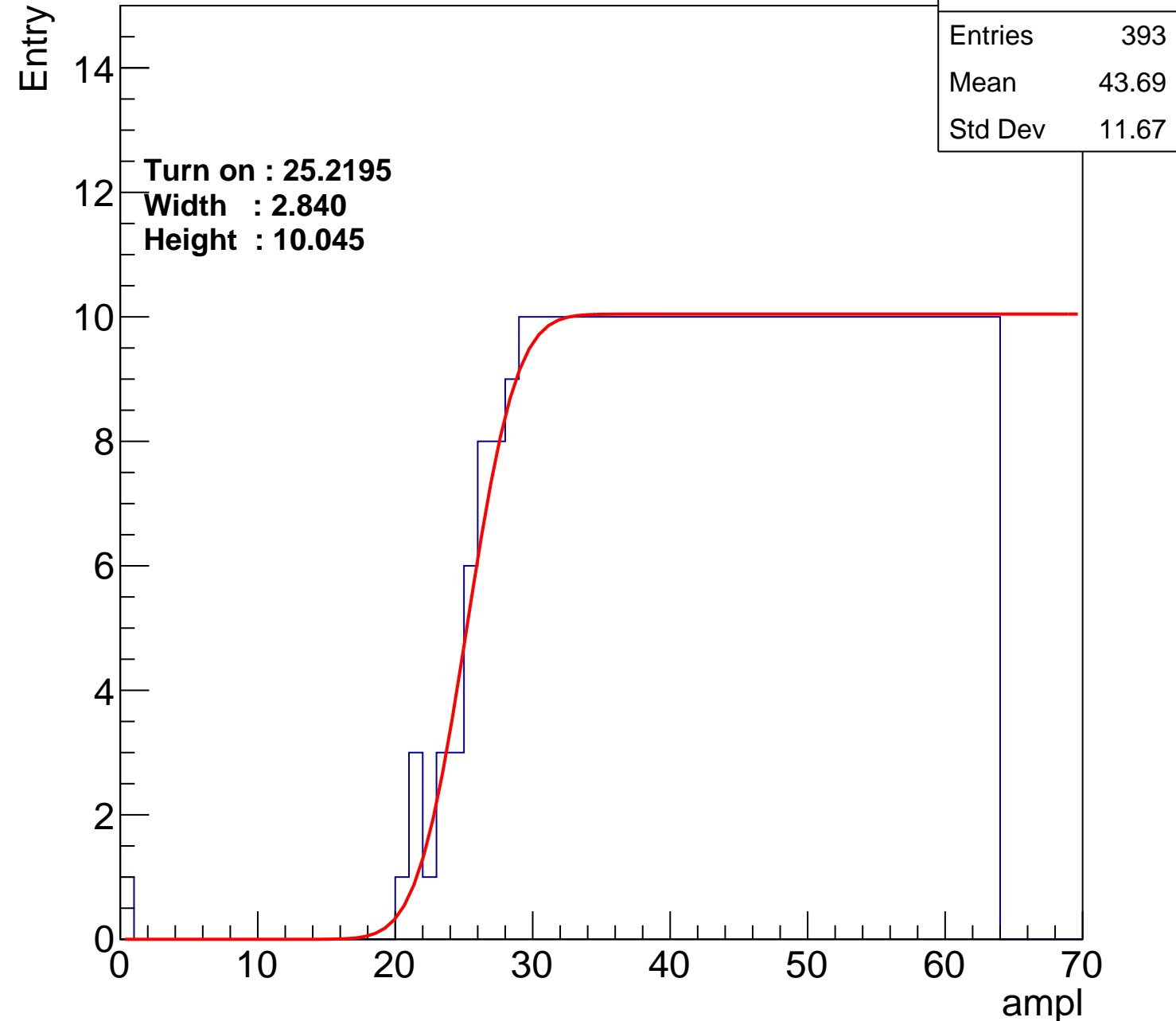
Width : 2.840

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.69
Std Dev	11.44

Turn on : 27.6867

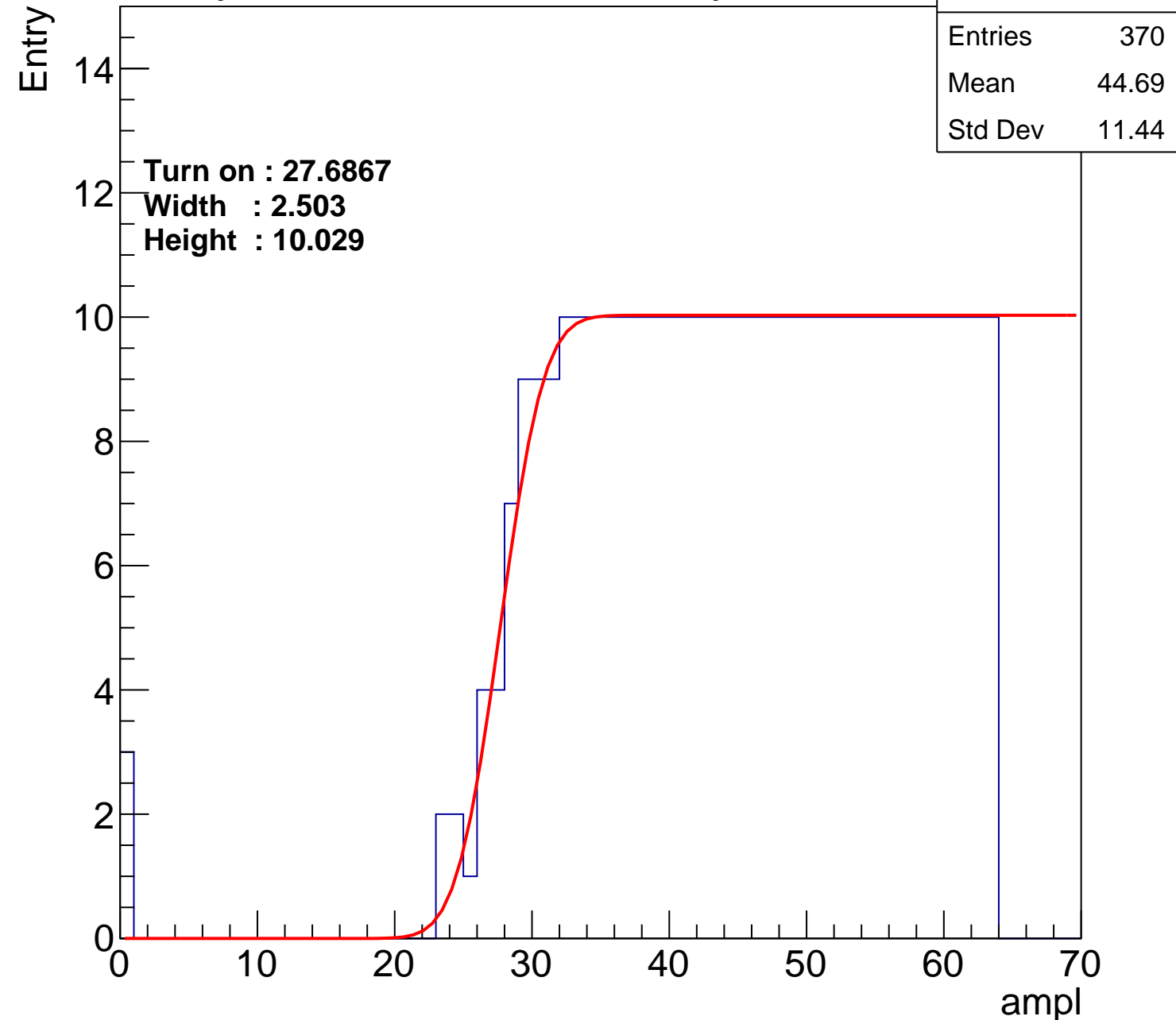
Width : 2.503

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.67
Std Dev	11.24

Turn on : 27.0273

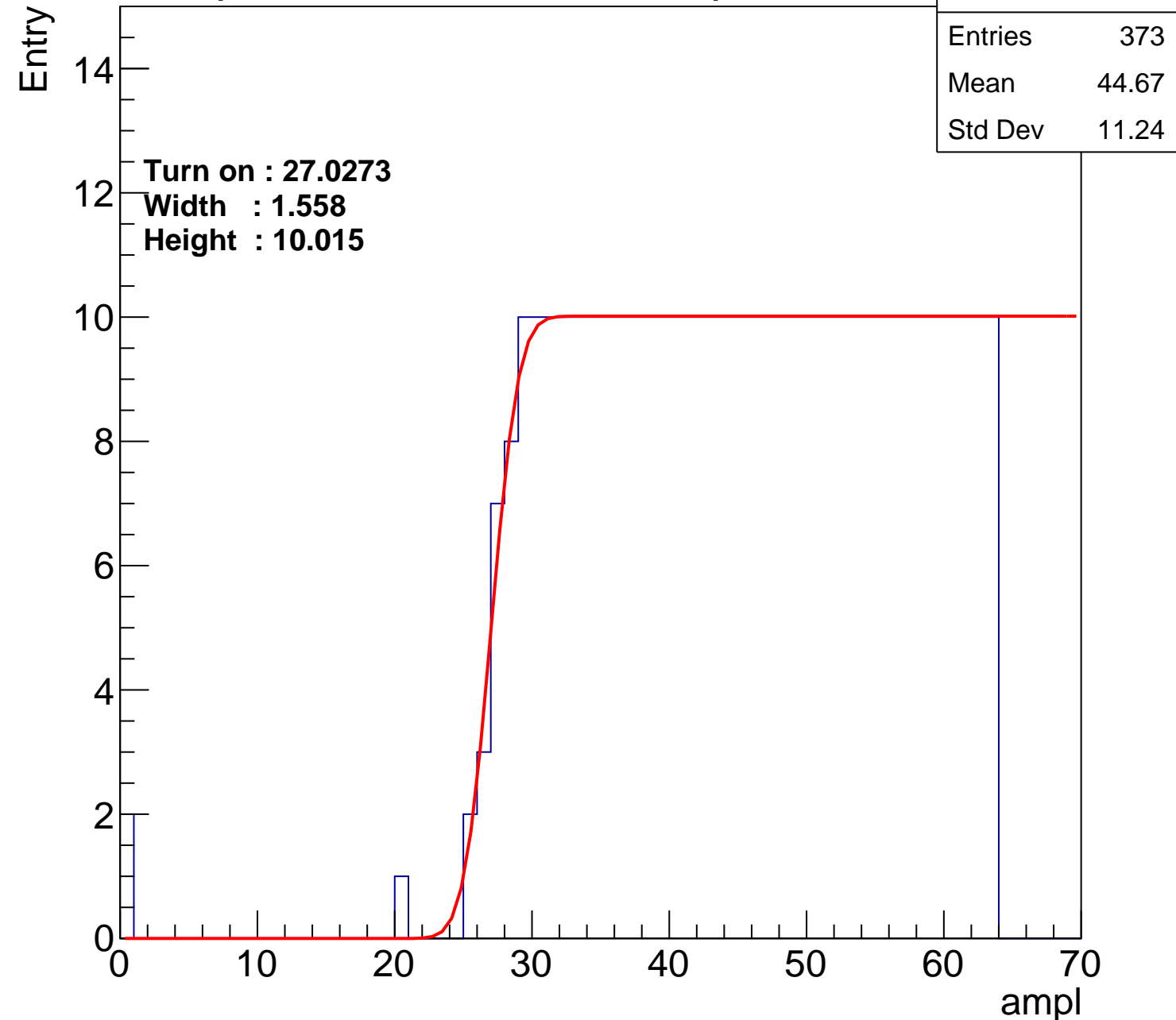
Width : 1.558

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch38

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.09
Std Dev	10.88

Turn on : 27.6581

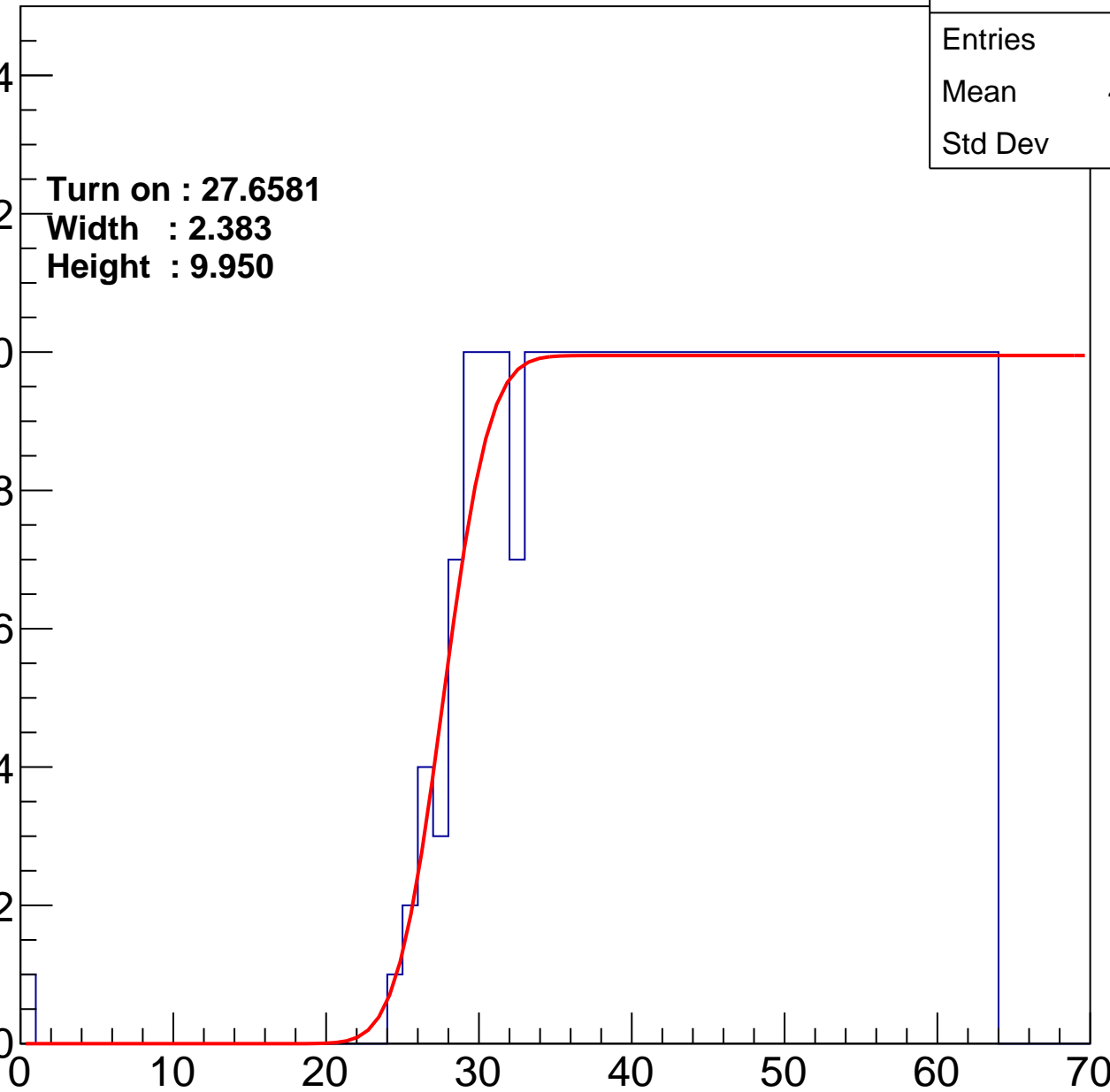
Width : 2.383

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch39

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.77
Std Dev	11.95

Turn on : 25.9356

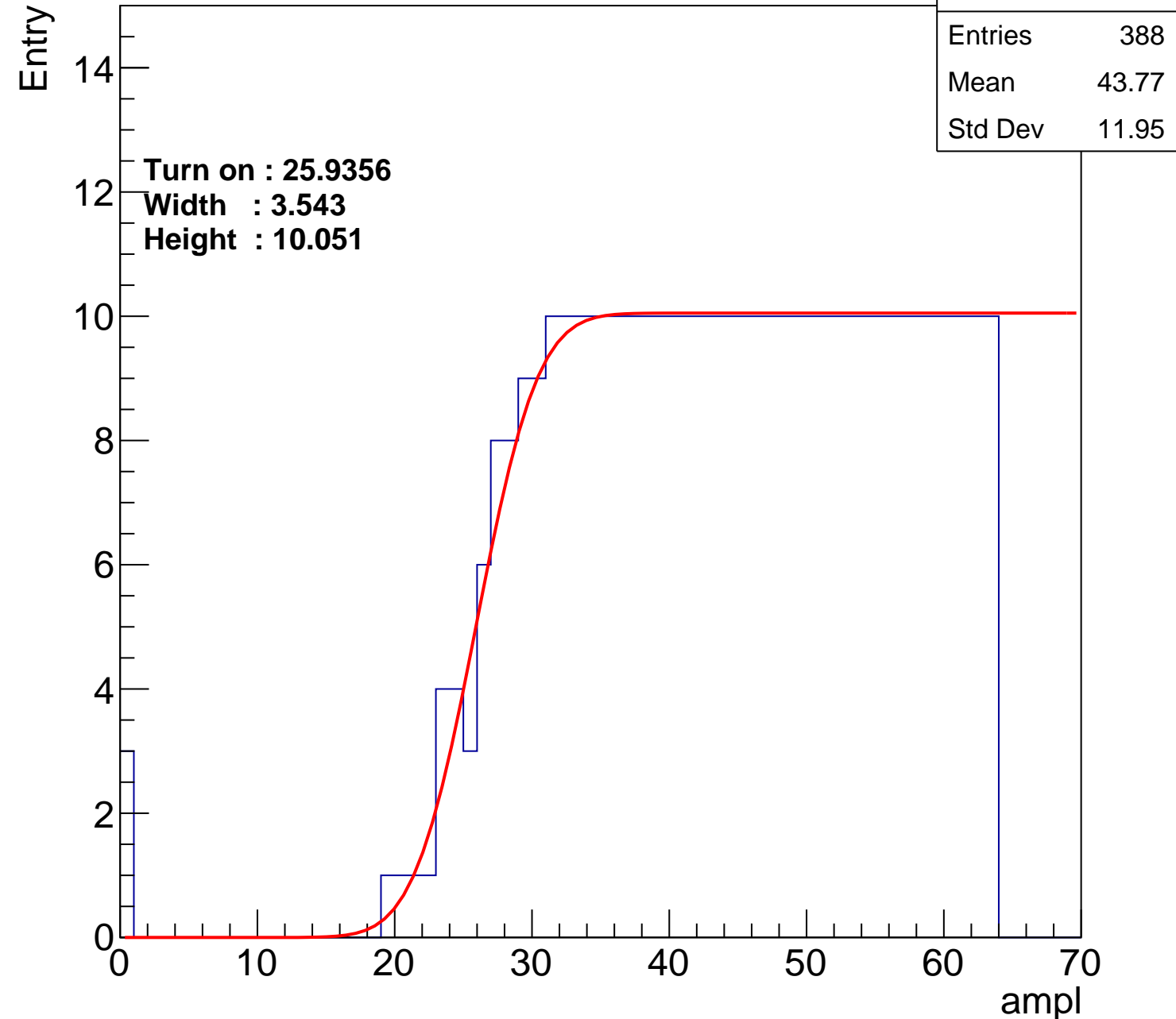
Width : 3.543

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch40

calib_packv5_042523_0143.root, FC#13, port D2

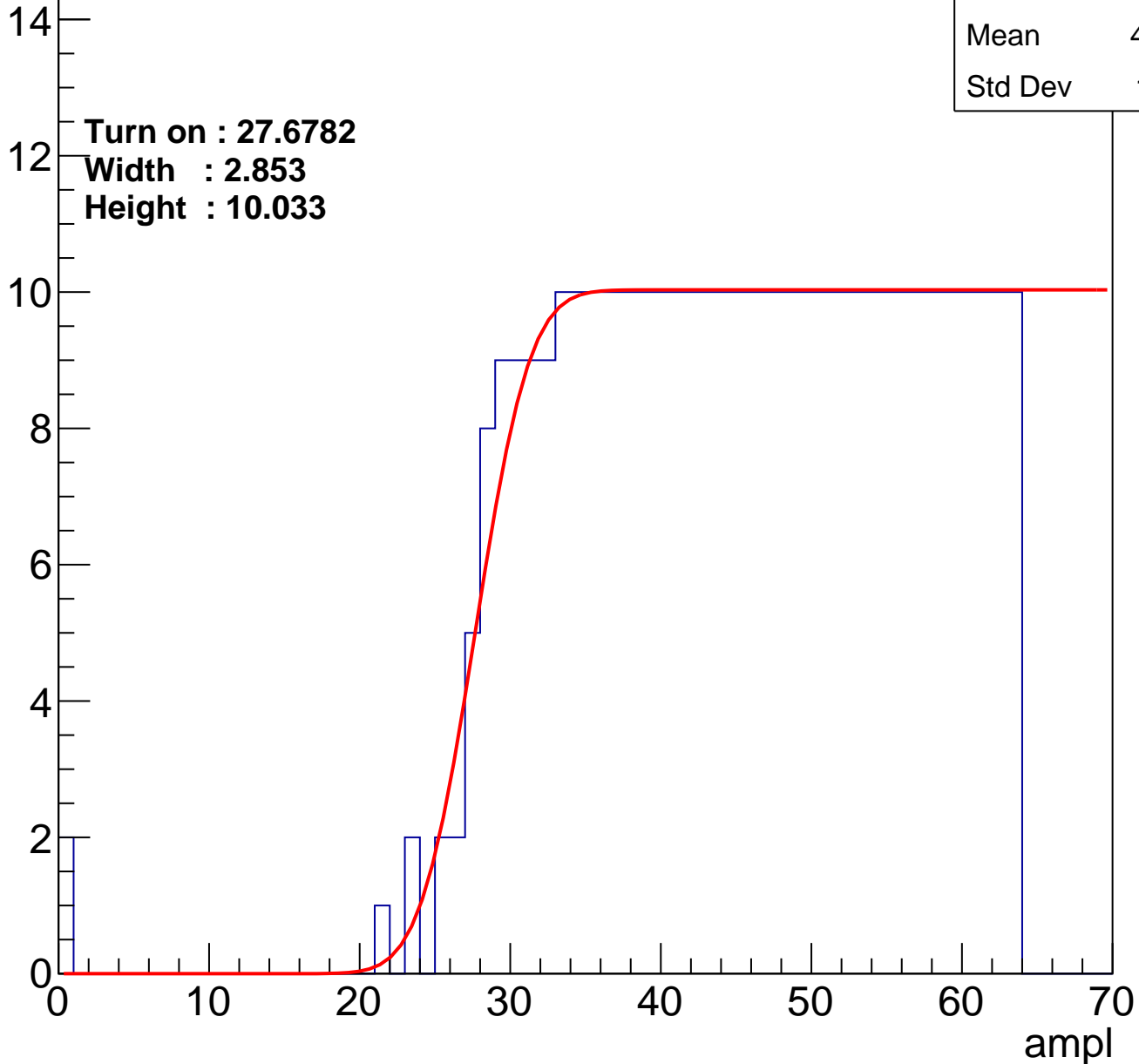
Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.6782

Width : 2.853

Height : 10.033

Entry



B1L003S, U14-ch41

calib_packv5_042523_0143.root, FC#13, port D2

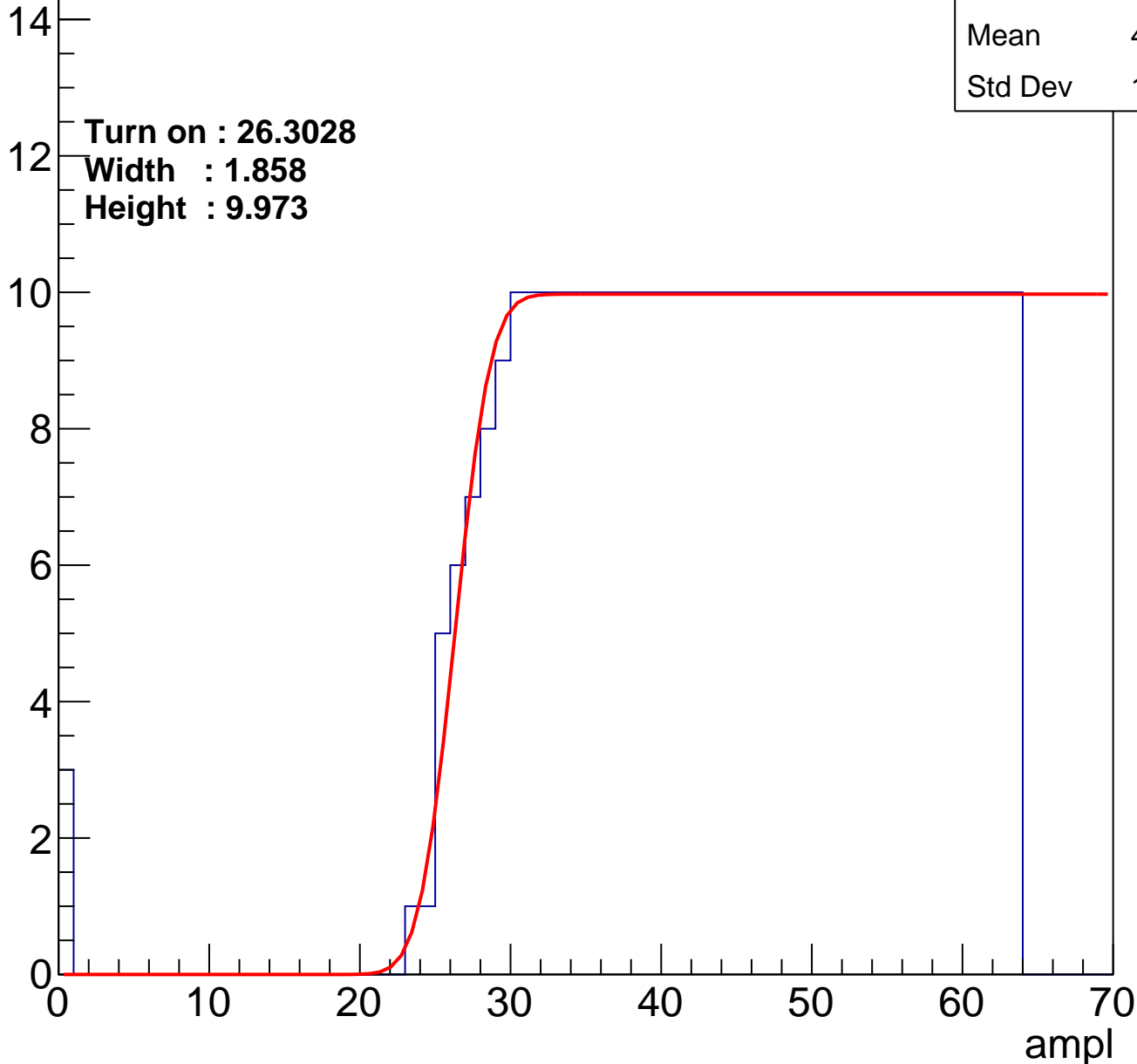
Entries	380
Mean	44.24
Std Dev	11.62

Turn on : 26.3028

Width : 1.858

Height : 9.973

Entry



B1L003S, U14-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.7664

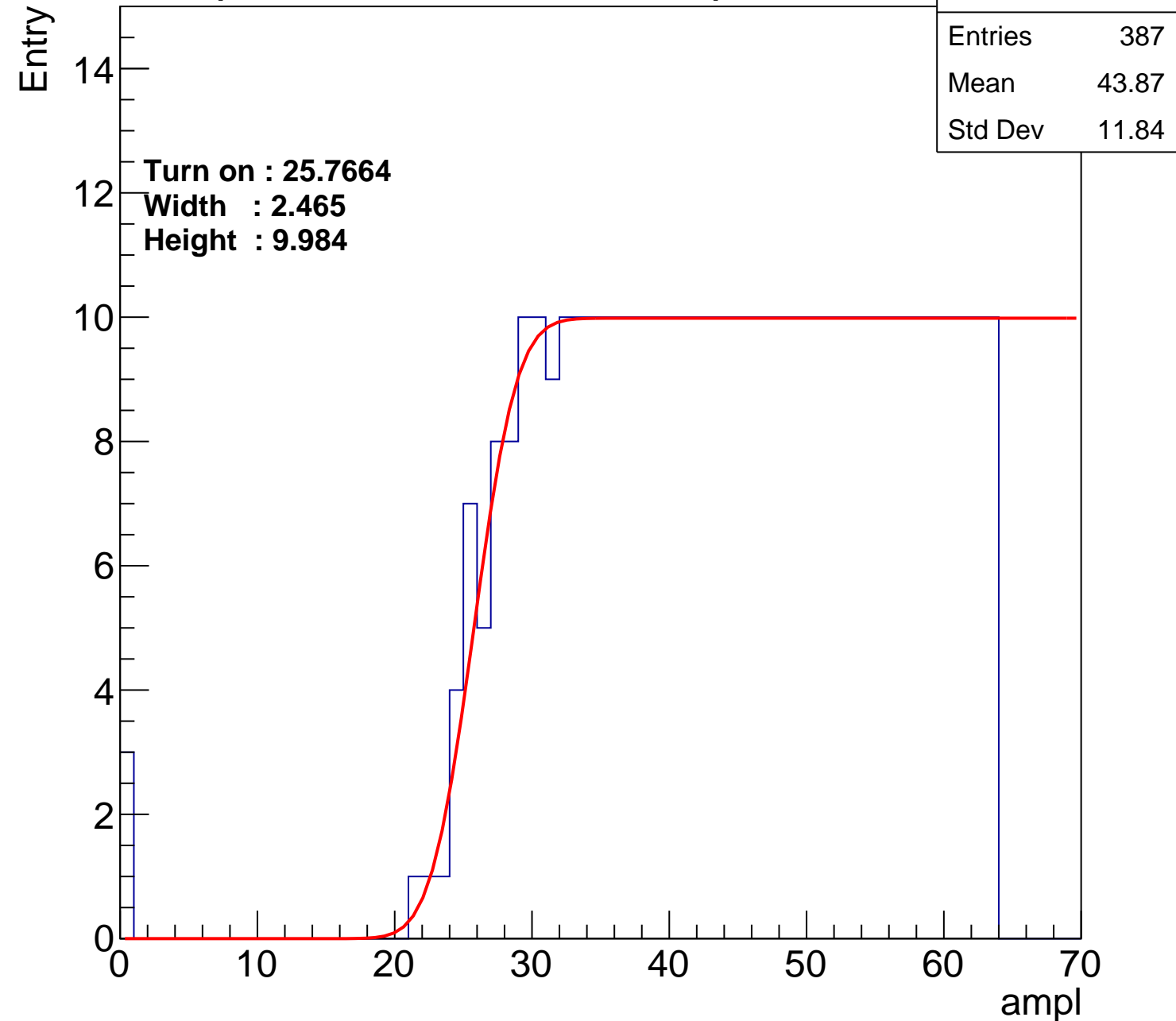
Width : 2.465

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.2195
Width : 1.933
Height : 10.014



B1L003S, U14-ch44

calib_packv5_042523_0143.root, FC#13, port D2

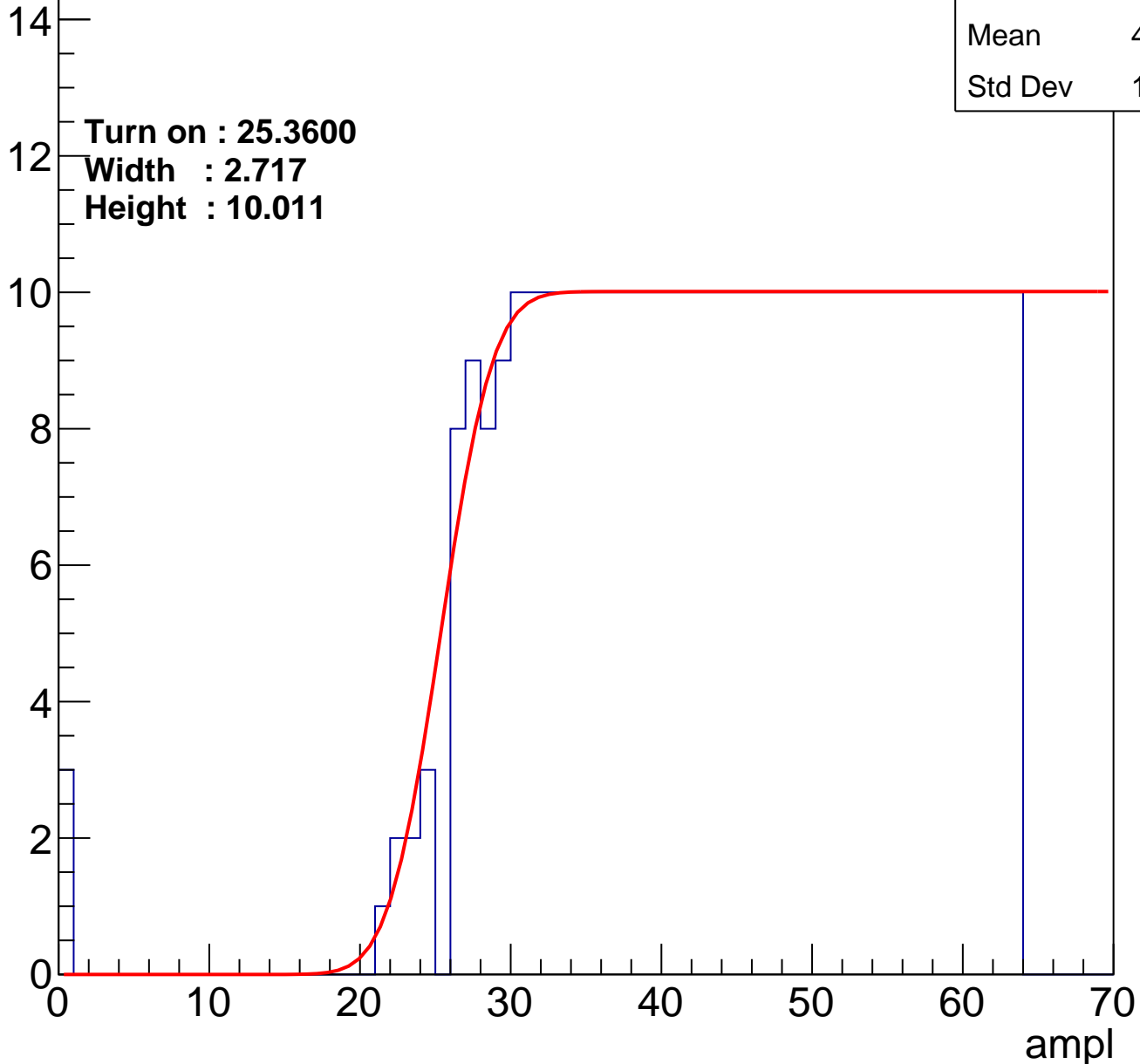
Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 25.3600

Width : 2.717

Height : 10.011

Entry



B1L003S, U14-ch45

calib_packv5_042523_0143.root, FC#13, port D2

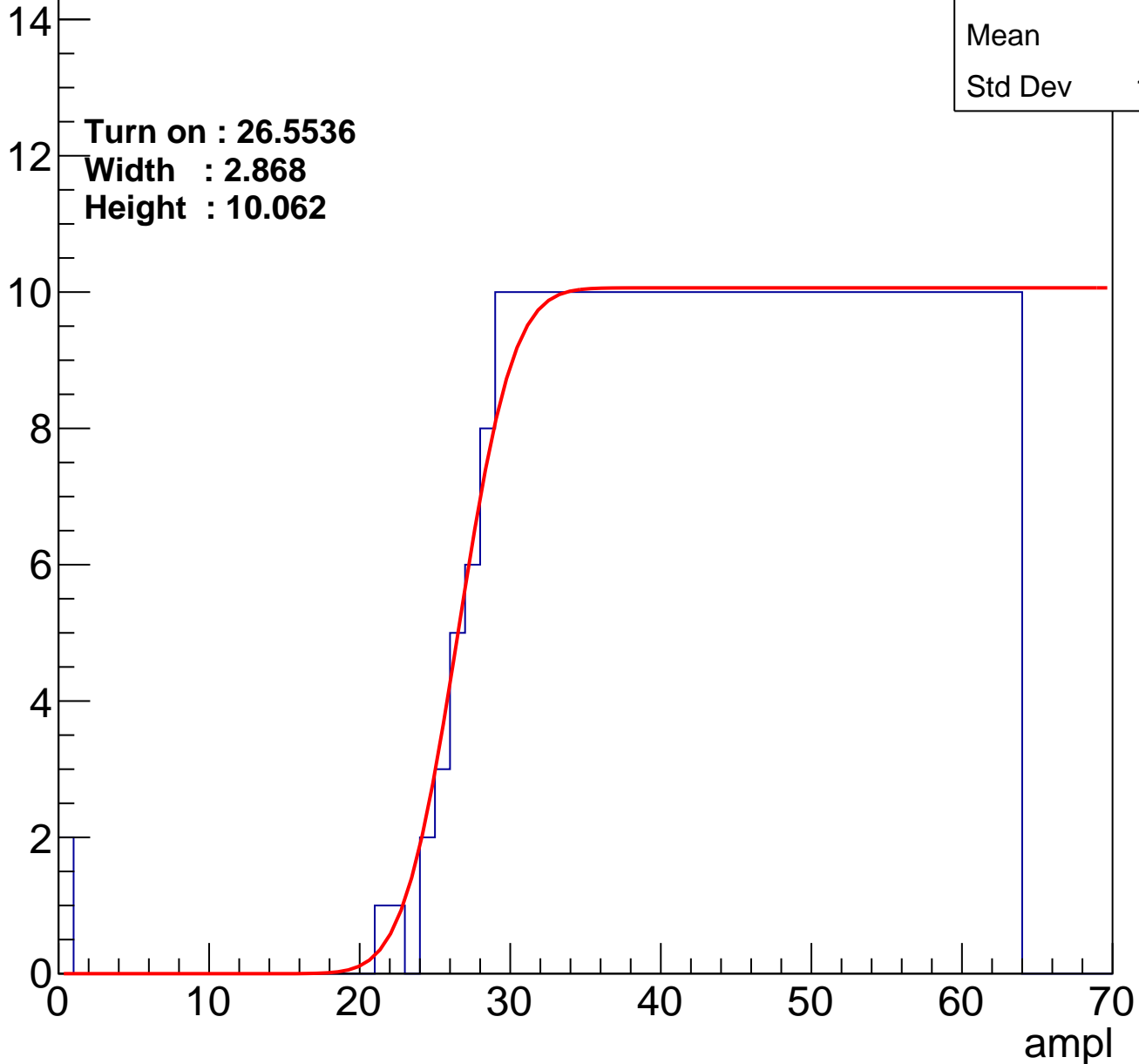
Entries	378
Mean	44.4
Std Dev	11.41

Turn on : 26.5536

Width : 2.868

Height : 10.062

Entry



B1L003S, U14-ch46

calib_packv5_042523_0143.root, FC#13, port D2

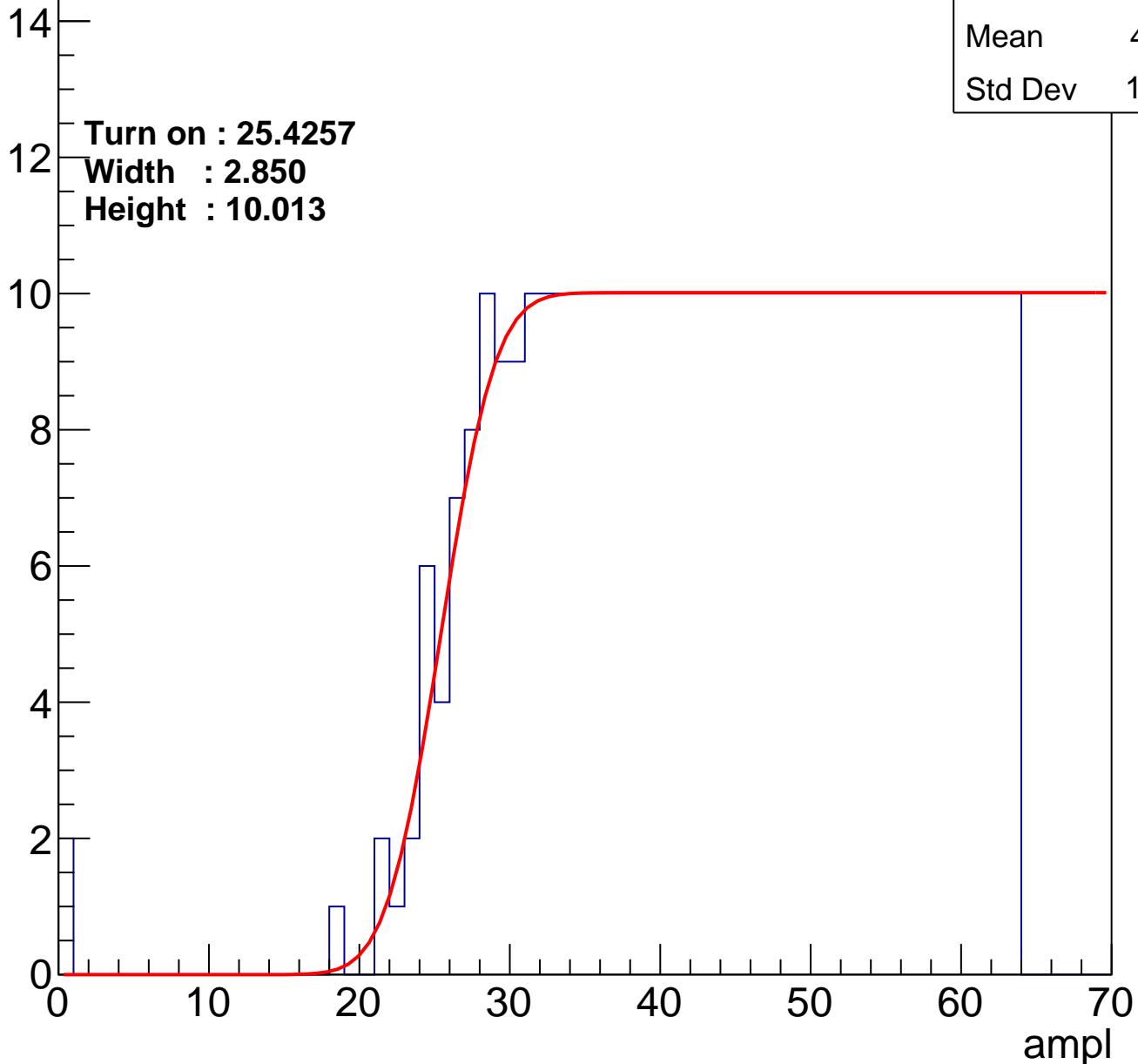
Entries	391
Mean	43.71
Std Dev	11.82

Turn on : 25.4257

Width : 2.850

Height : 10.013

Entry



B1L003S, U14-ch47

calib_packv5_042523_0143.root, FC#13, port D2

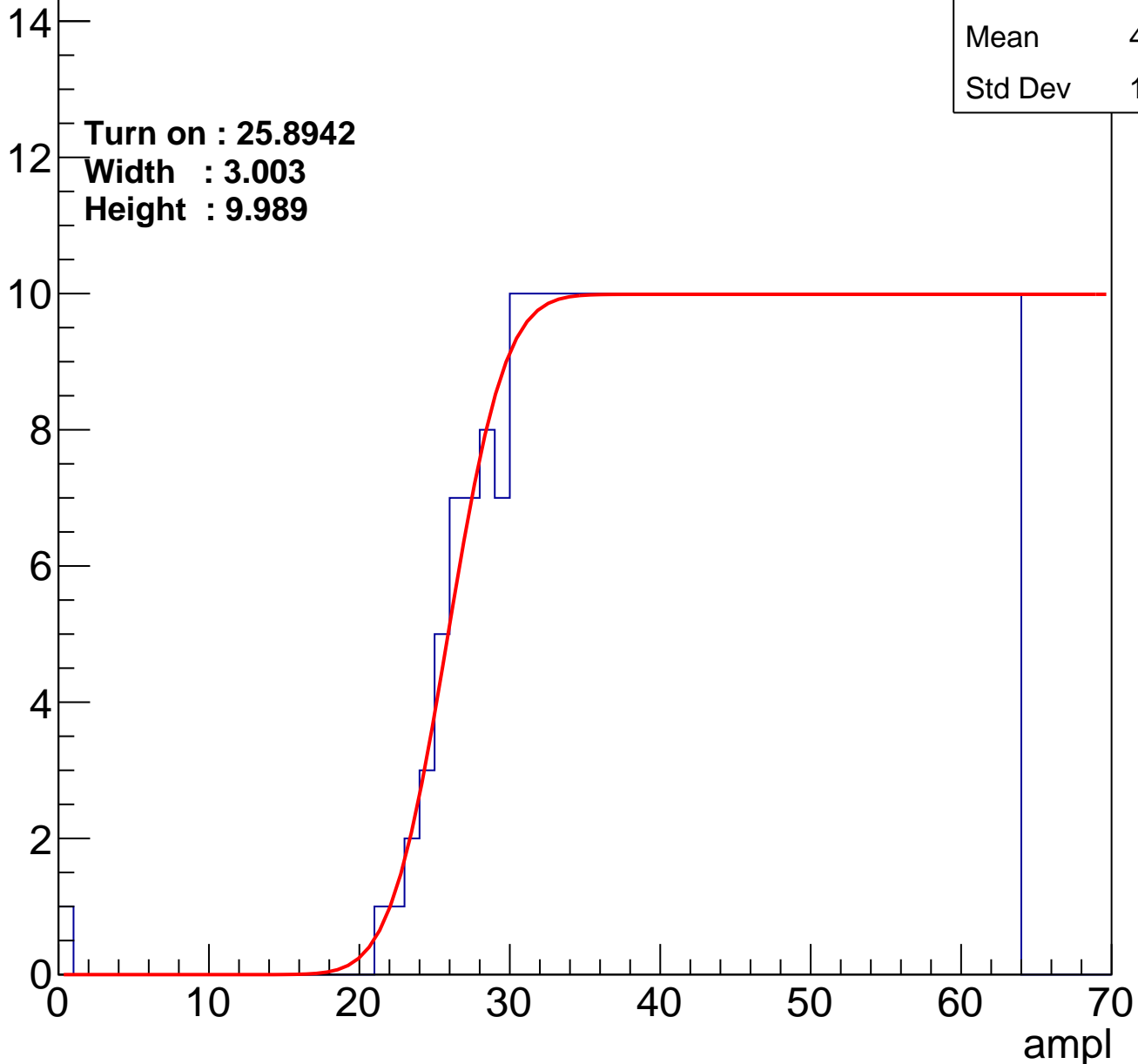
Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 25.8942

Width : 3.003

Height : 9.989

Entry



B1L003S, U14-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 26.5467

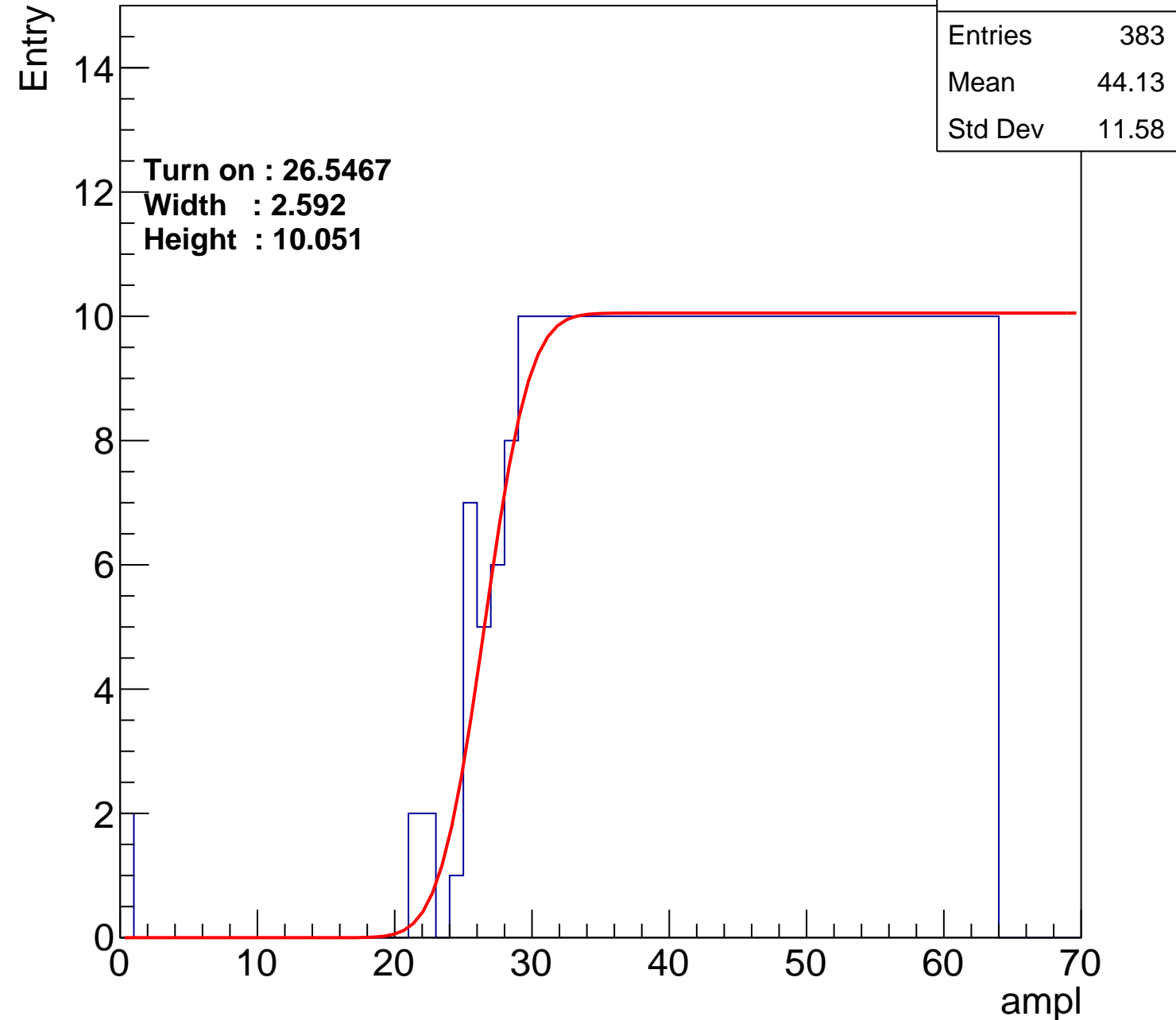
Width : 2.592

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch49

calib_packv5_042523_0143.root, FC#13, port D2

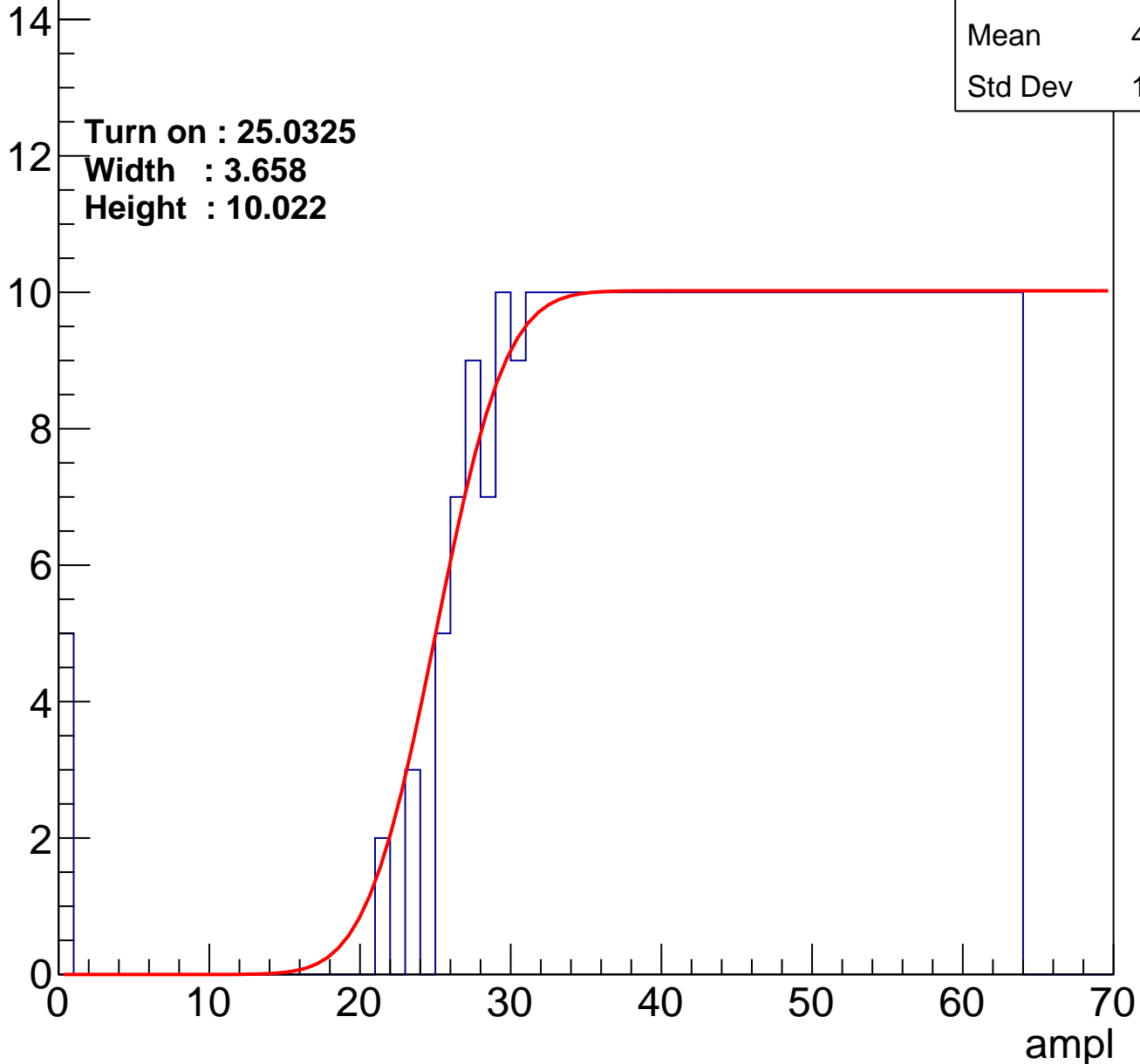
Entries	387
Mean	43.74
Std Dev	12.18

Turn on : 25.0325

Width : 3.658

Height : 10.022

Entry



B1L003S, U14-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.7973

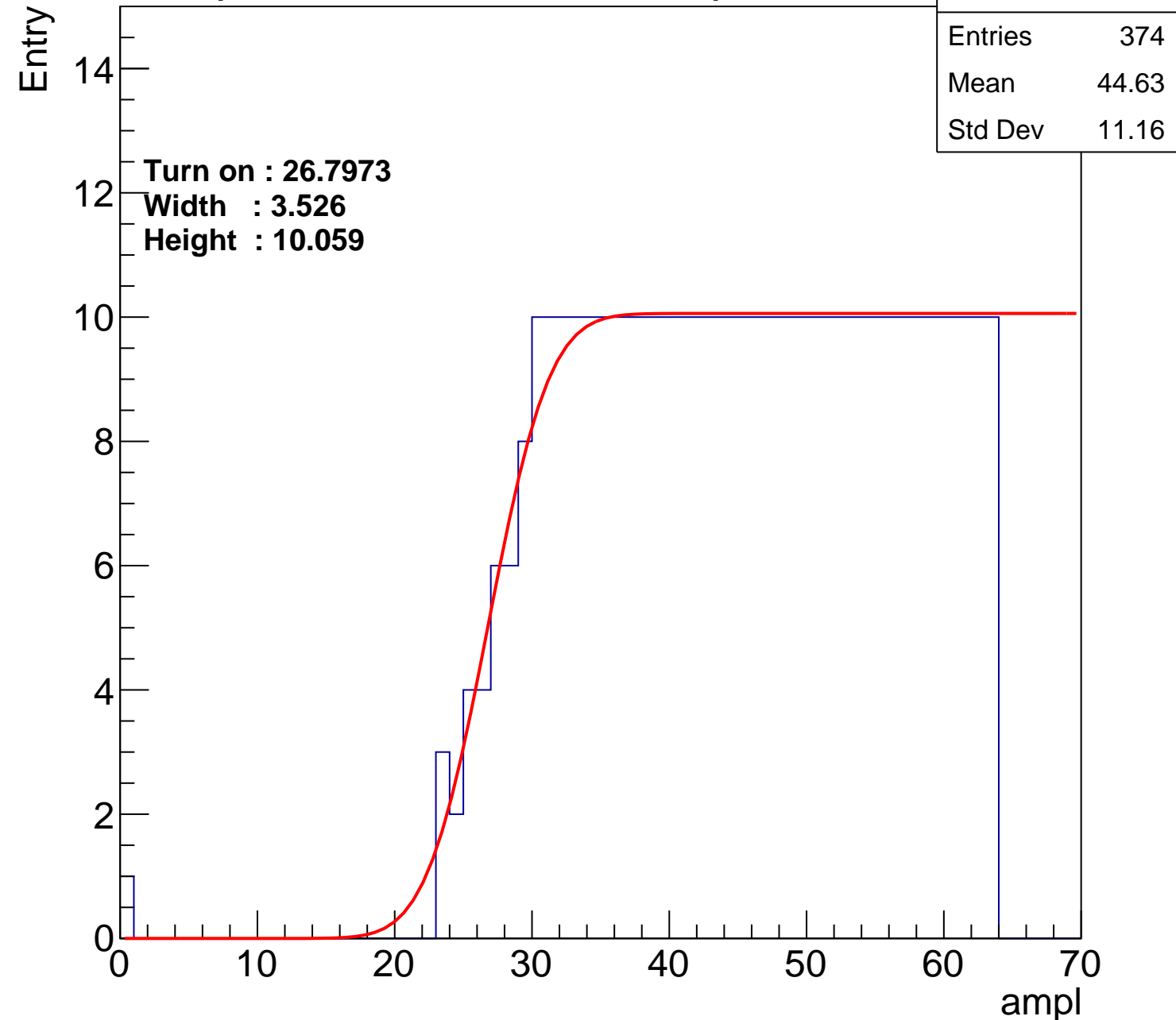
Width : 3.526

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch51

calib_packv5_042523_0143.root, FC#13, port D2

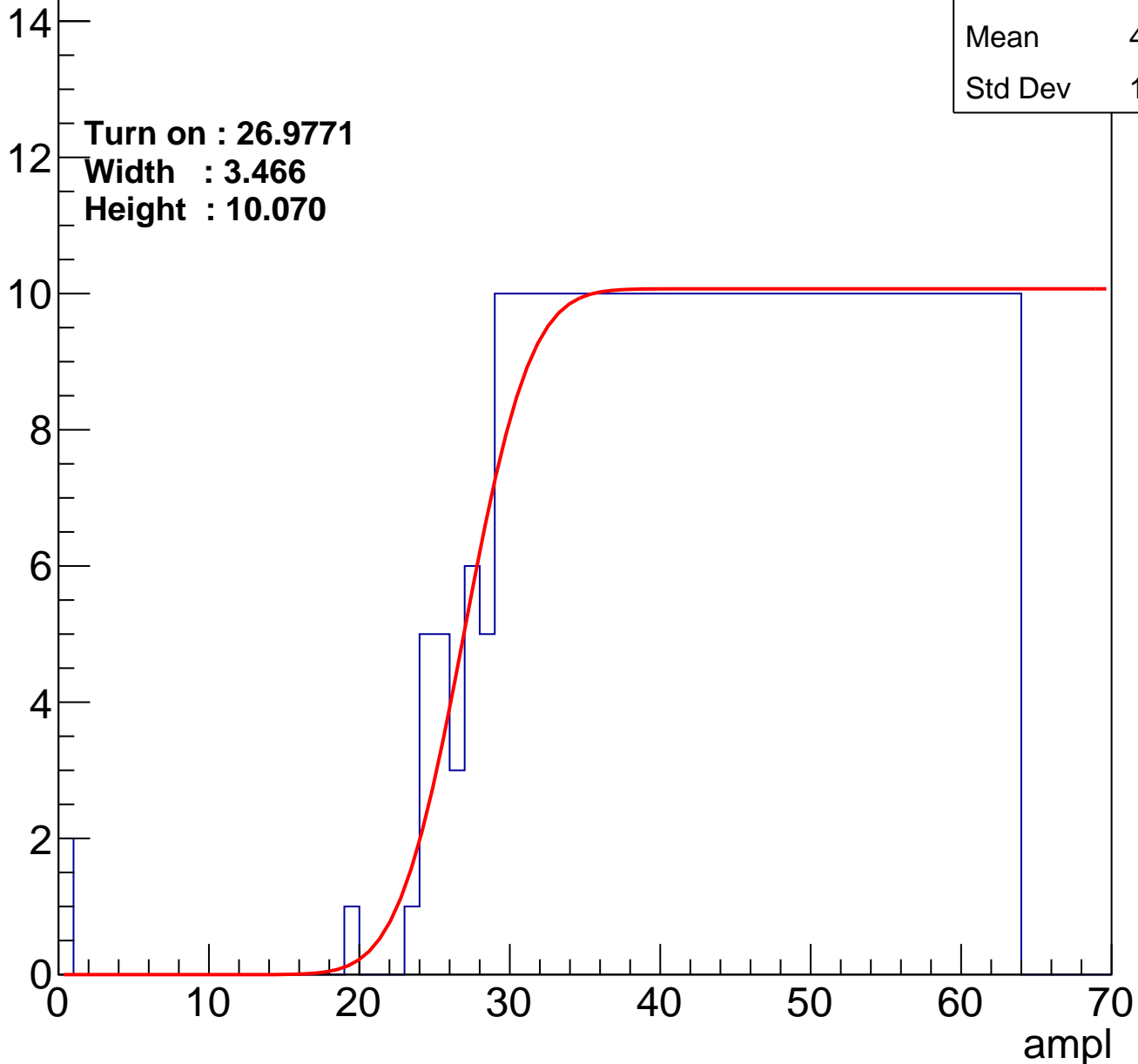
Entries	378
Mean	44.36
Std Dev	11.48

Turn on : 26.9771

Width : 3.466

Height : 10.070

Entry



B1L003S, U14-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	44.06
Std Dev	11.5

Turn on : 25.7569

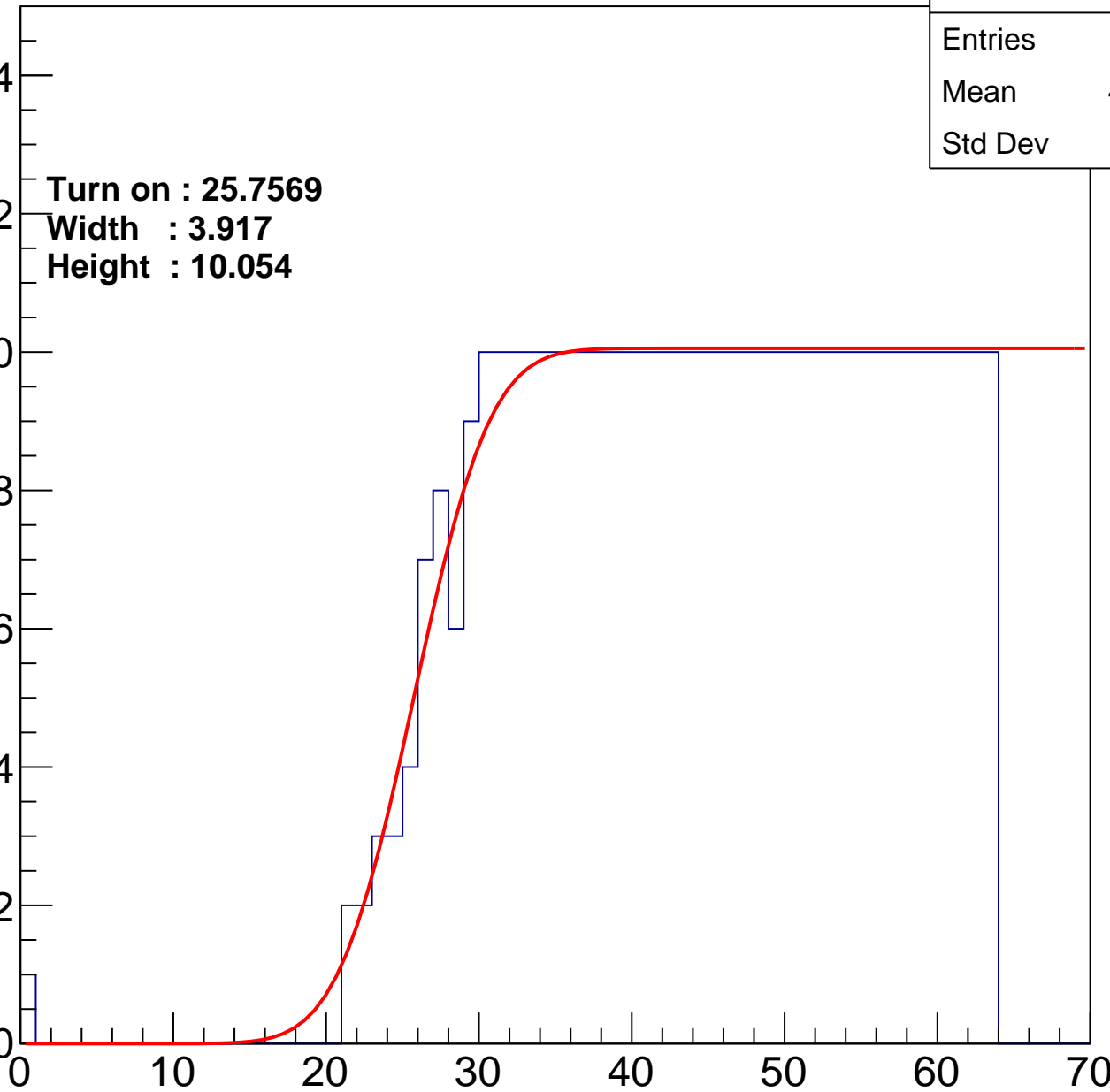
Width : 3.917

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.26
Std Dev	11.43

Turn on : 29.3679

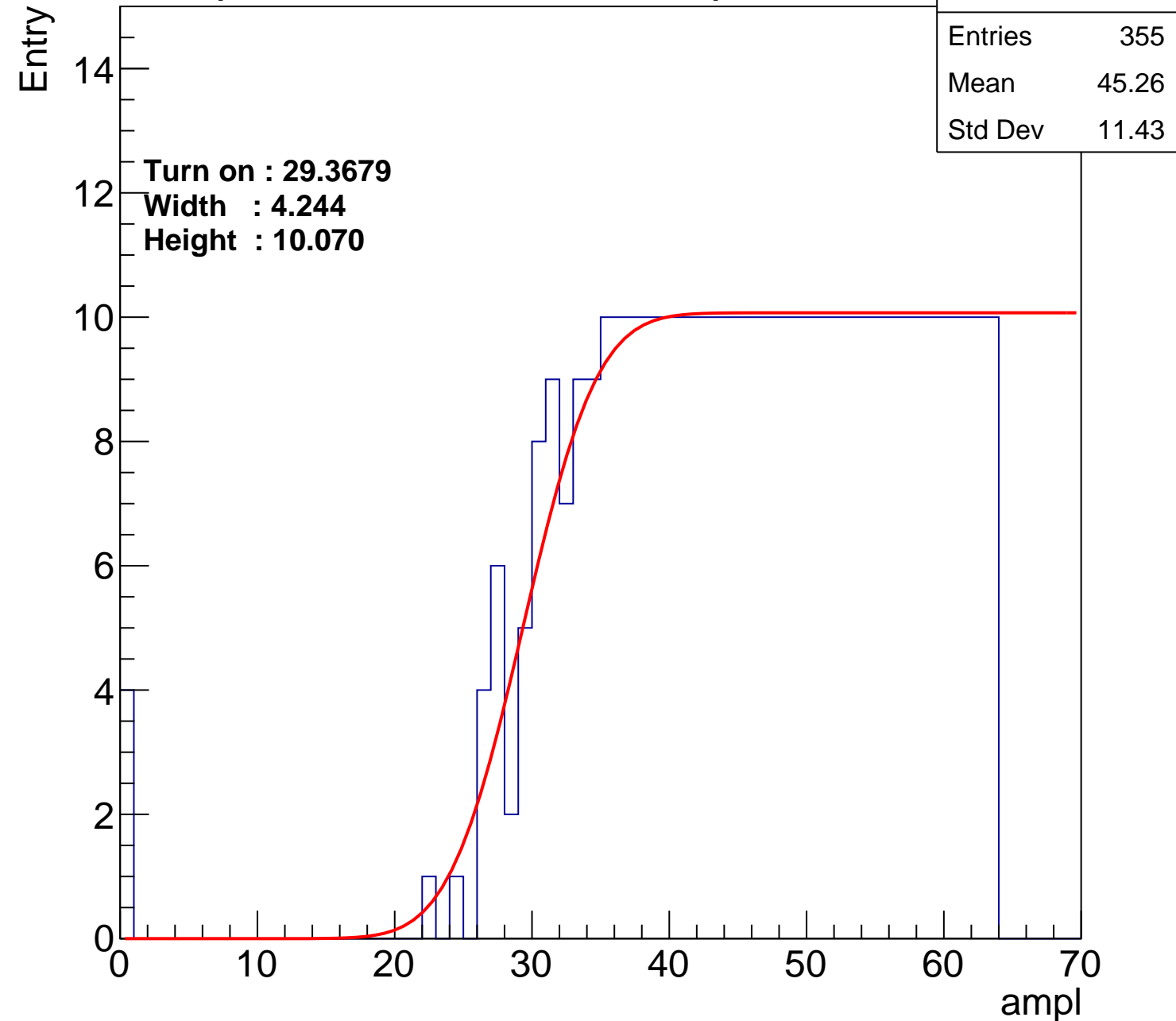
Width : 4.244

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.8
Std Dev	11.89

Turn on : 25.6762

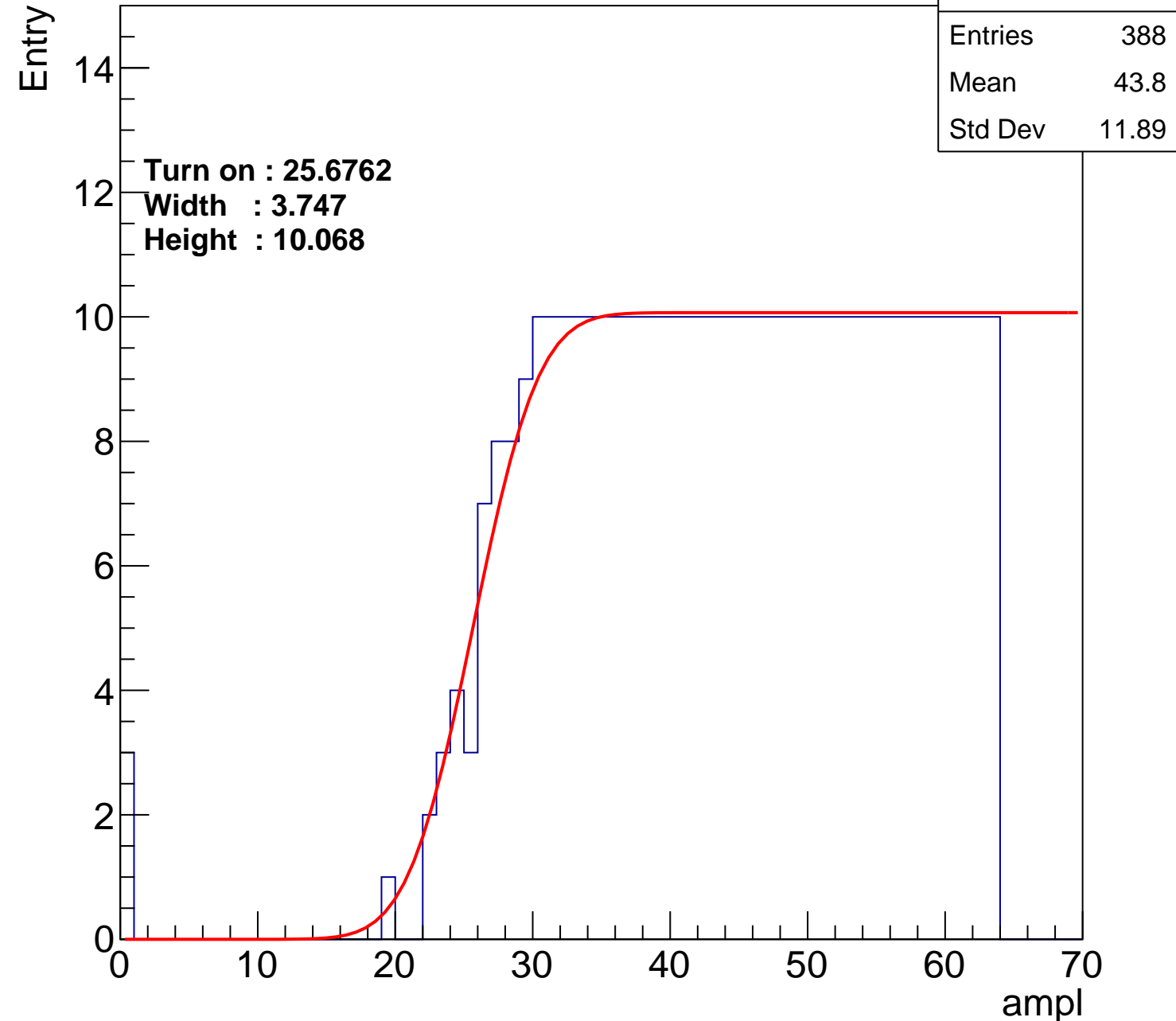
Width : 3.747

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.26
Std Dev	11.51

Turn on : 26.2387

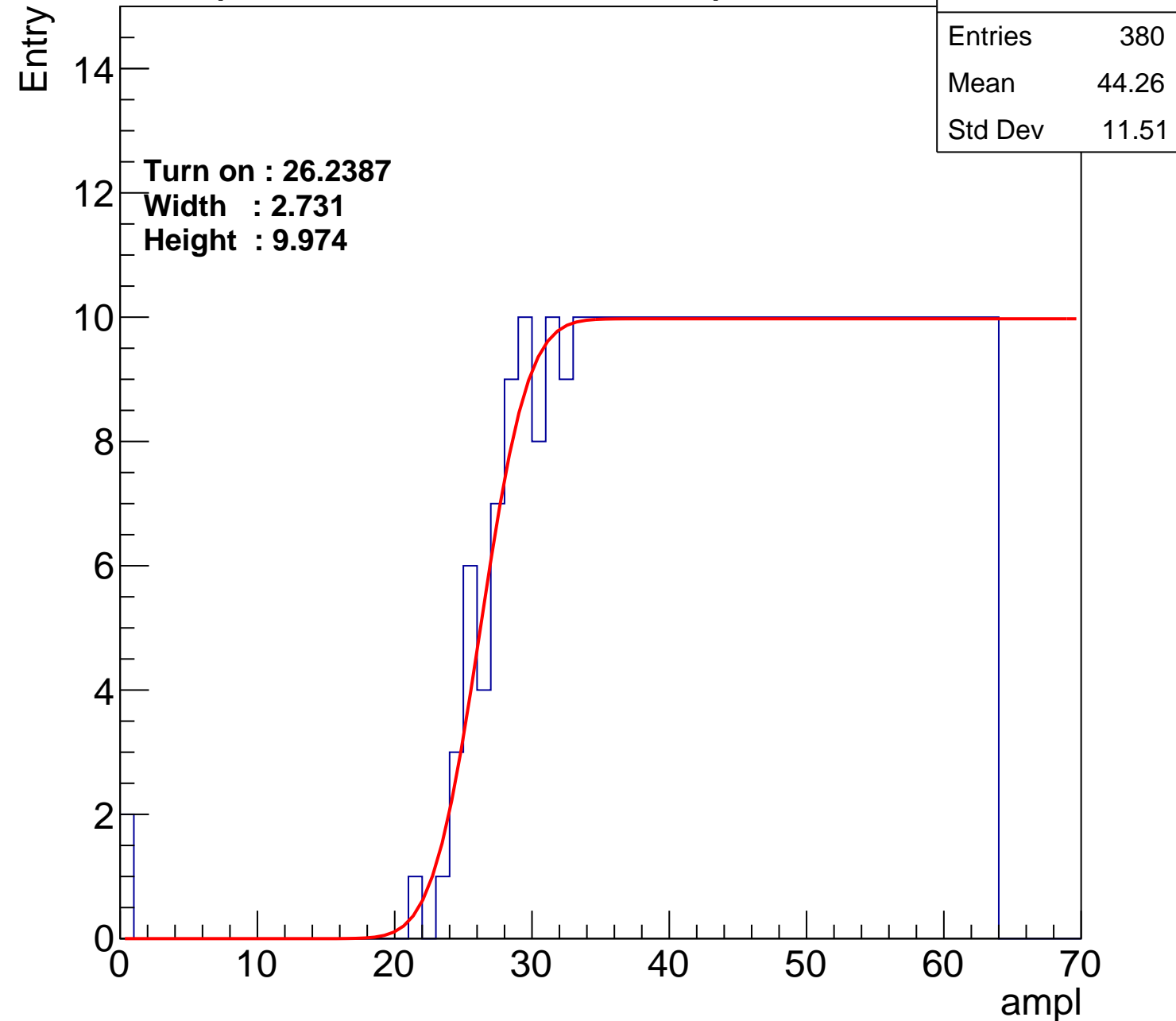
Width : 2.731

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.6878

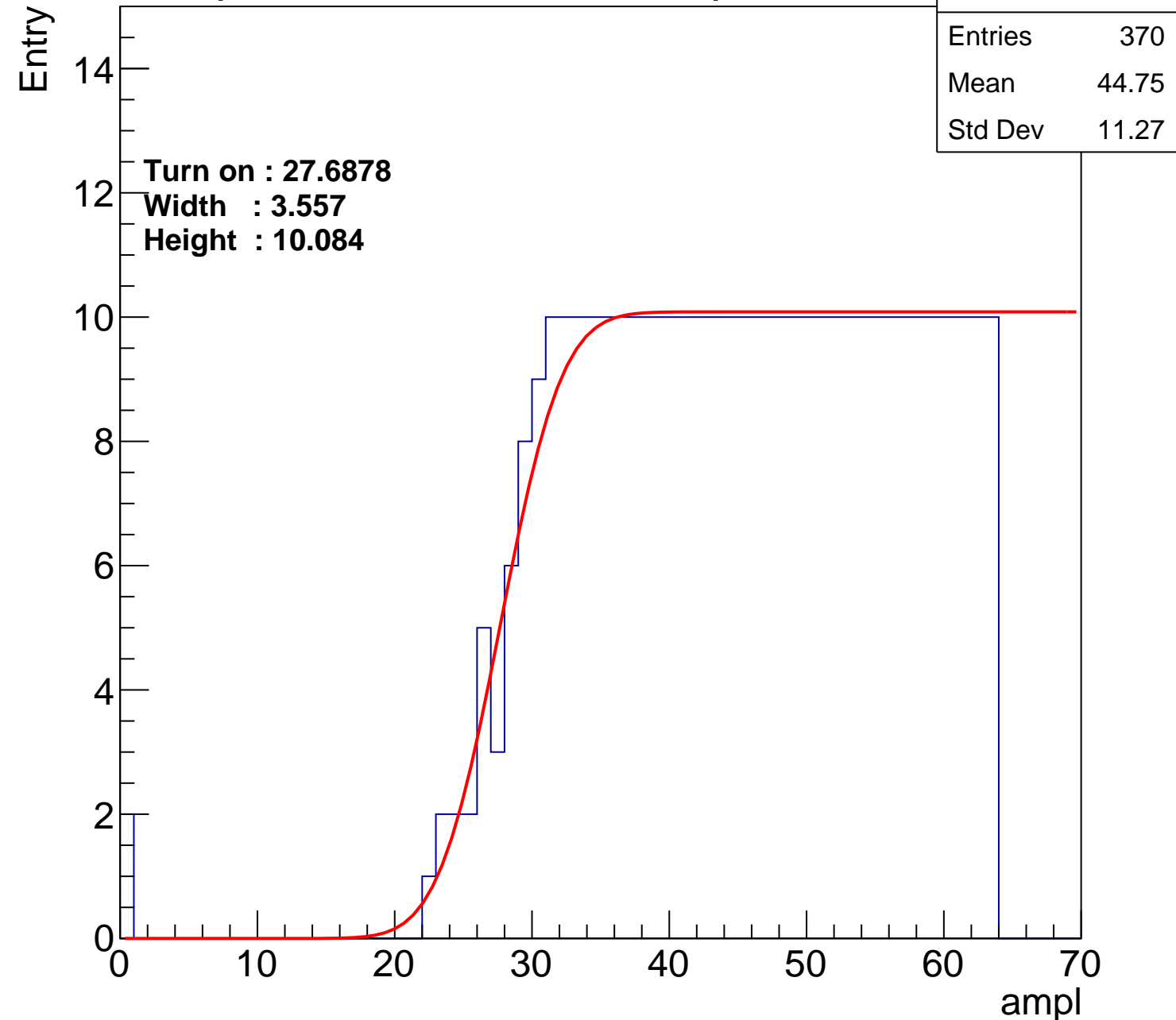
Width : 3.557

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.96
Std Dev	11.2

Turn on : 27.7779

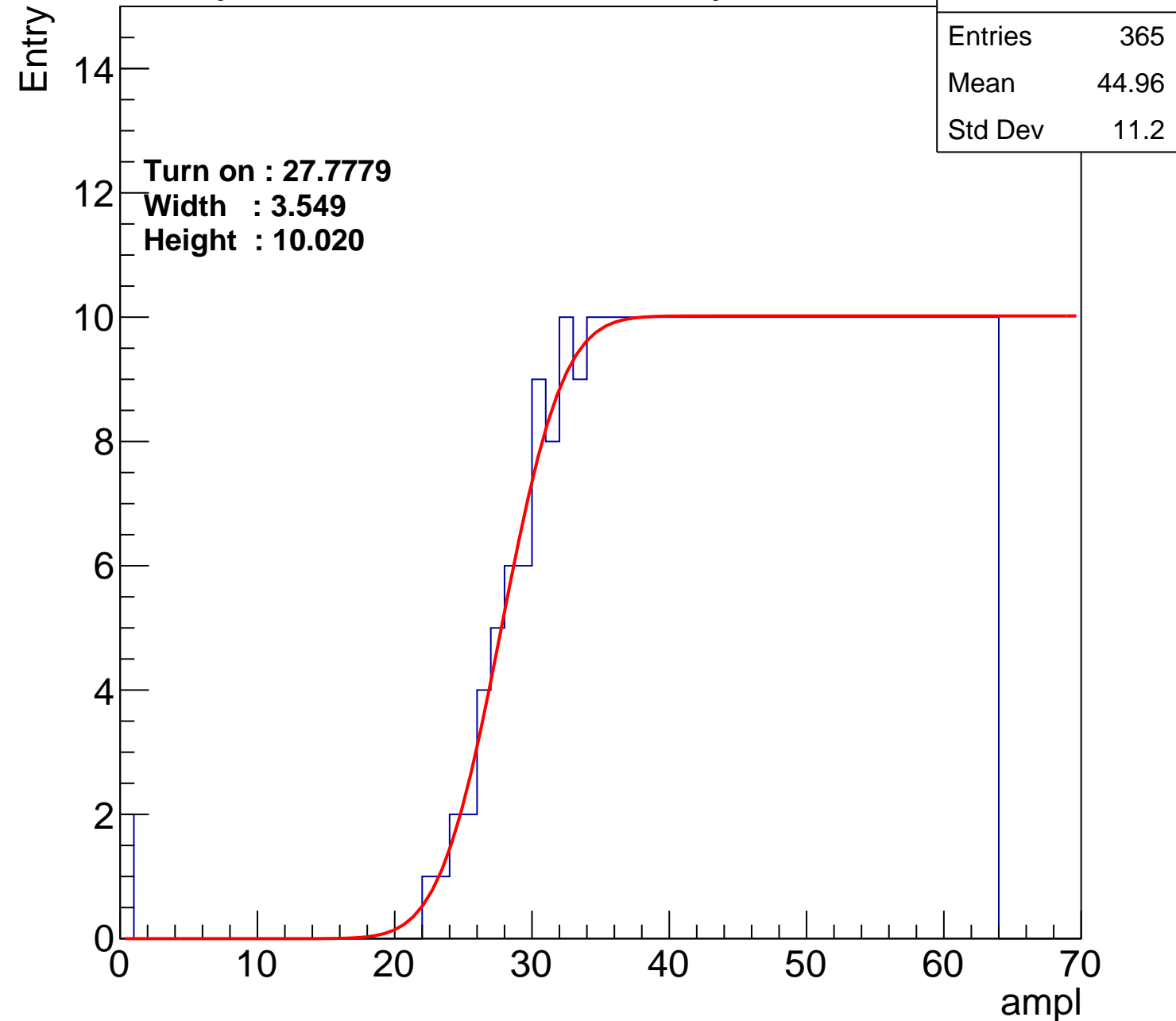
Width : 3.549

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.39
Std Dev	11.72

Turn on : 27.0852

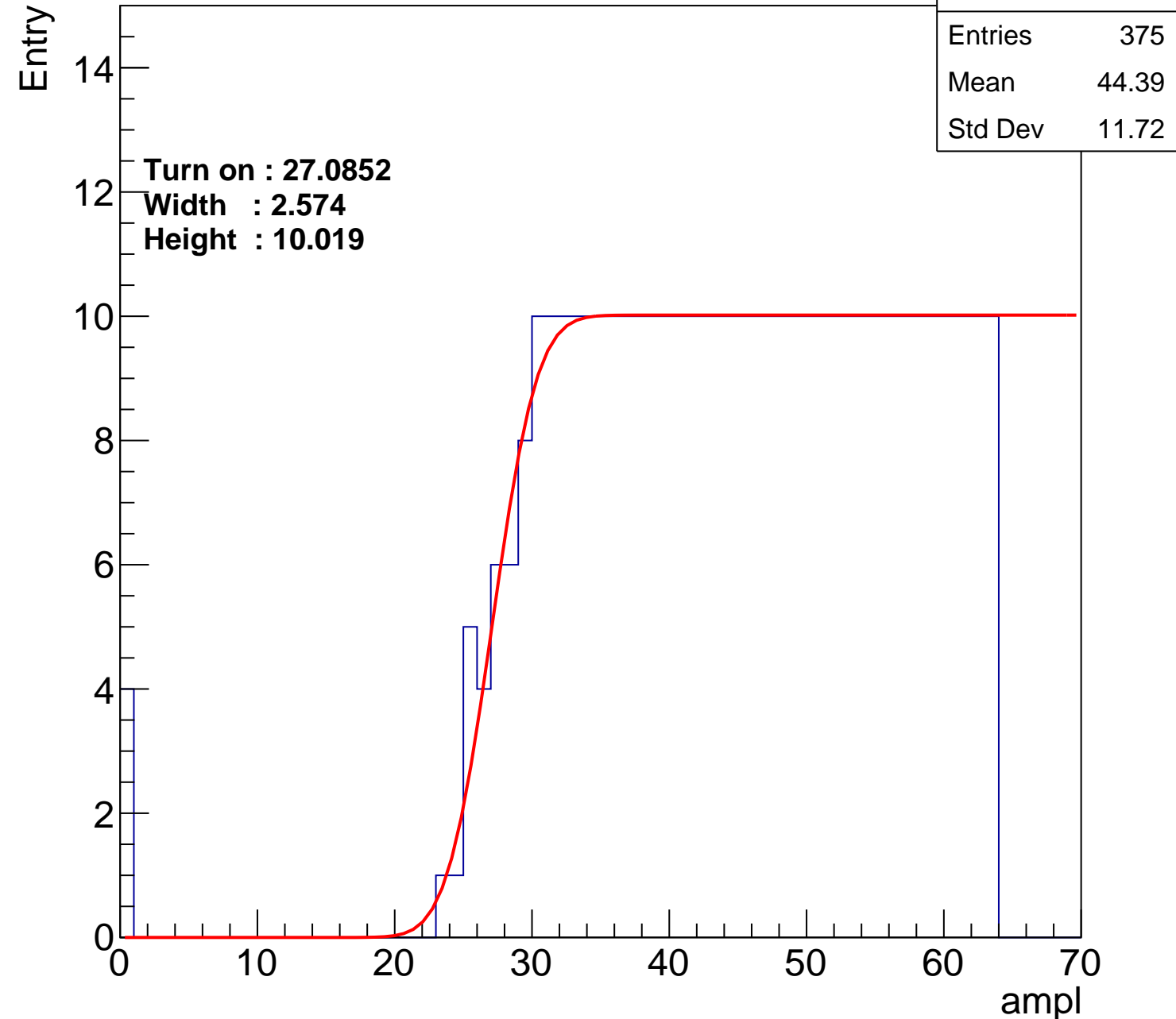
Width : 2.574

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch59

calib_packv5_042523_0143.root, FC#13, port D2

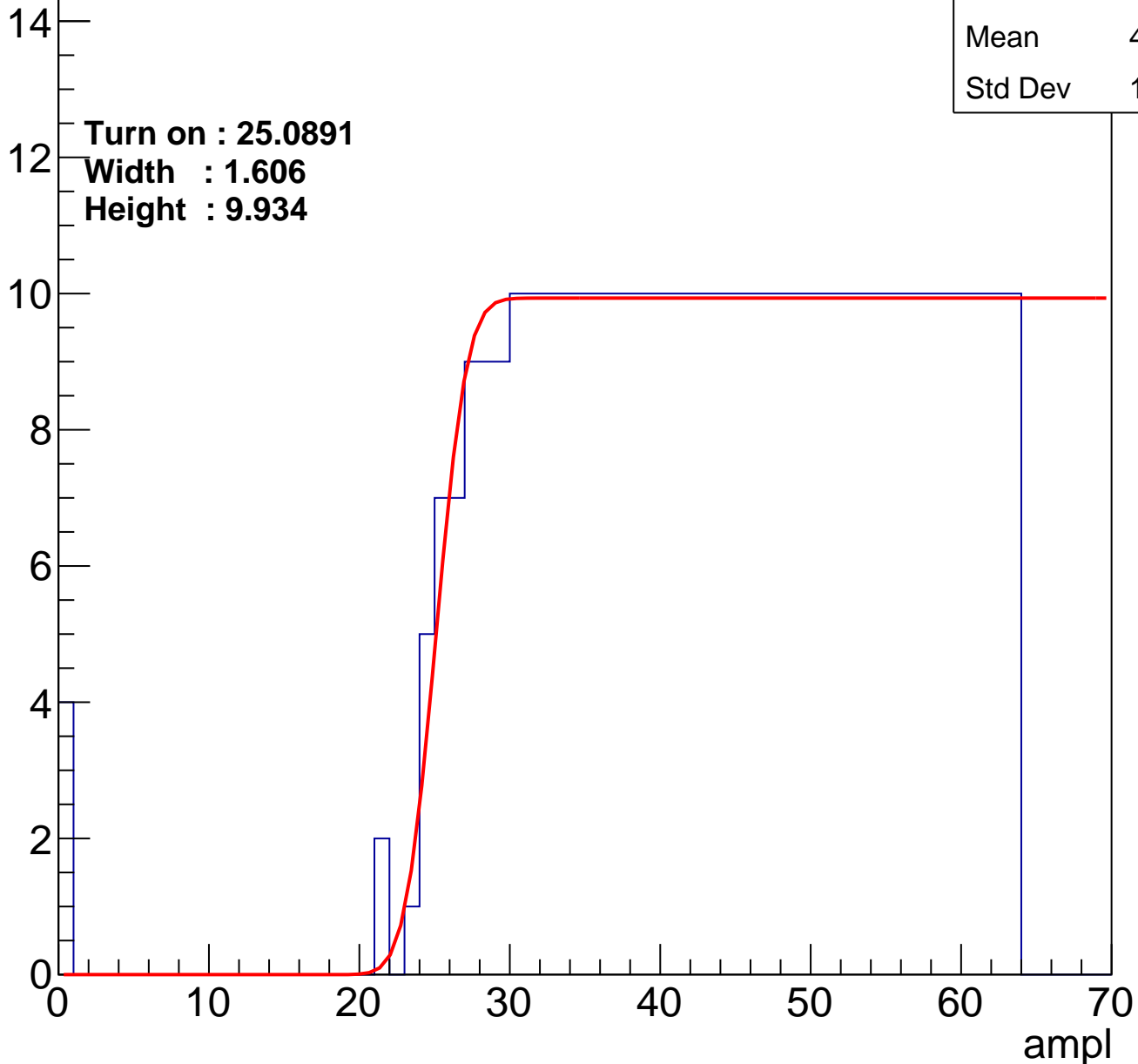
Entries	393
Mean	43.53
Std Dev	12.12

Turn on : 25.0891

Width : 1.606

Height : 9.934

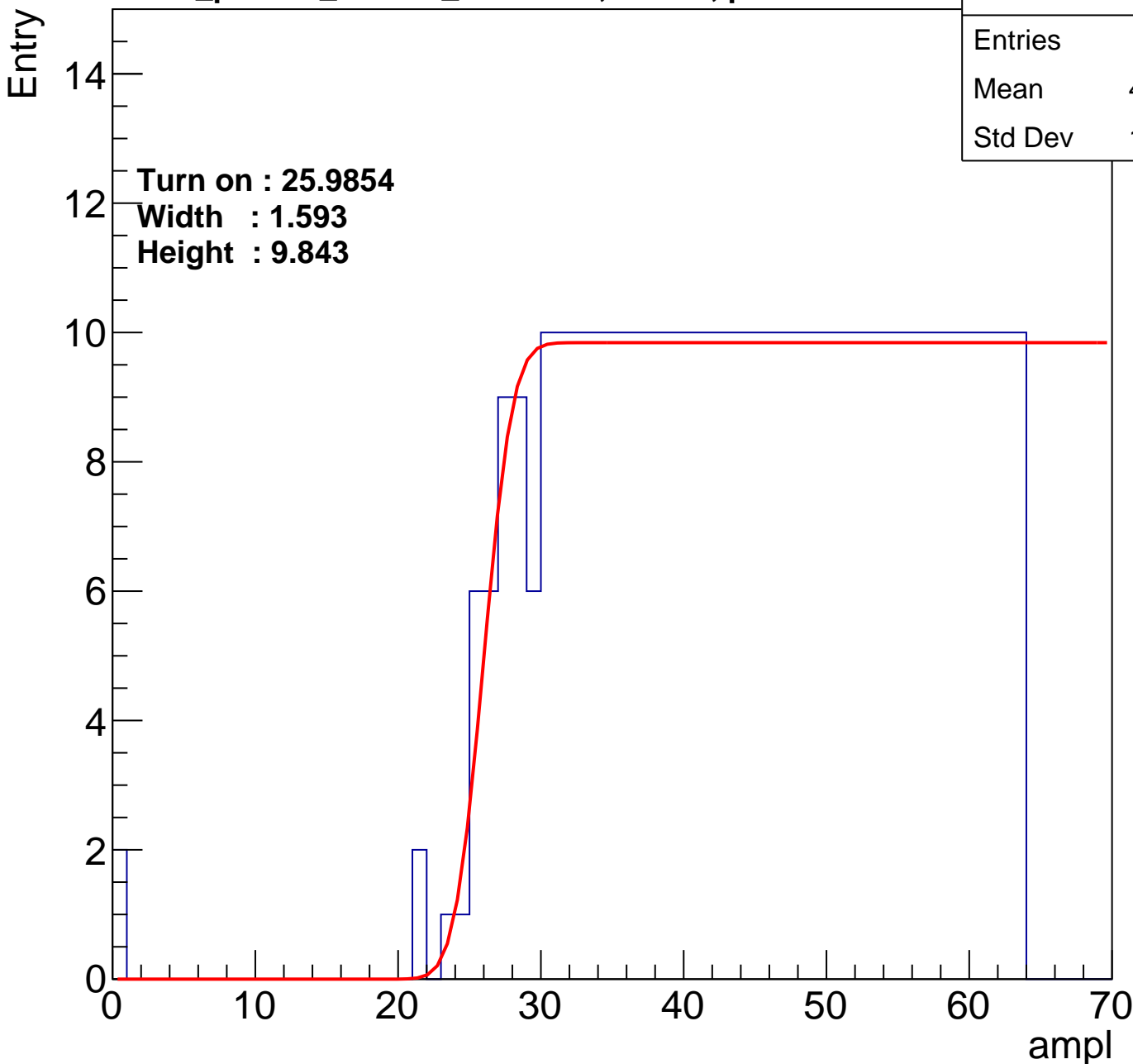
Entry



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 25.9854
Width : 1.593
Height : 9.843



B1L003S, U14-ch61

calib_packv5_042523_0143.root, FC#13, port D2

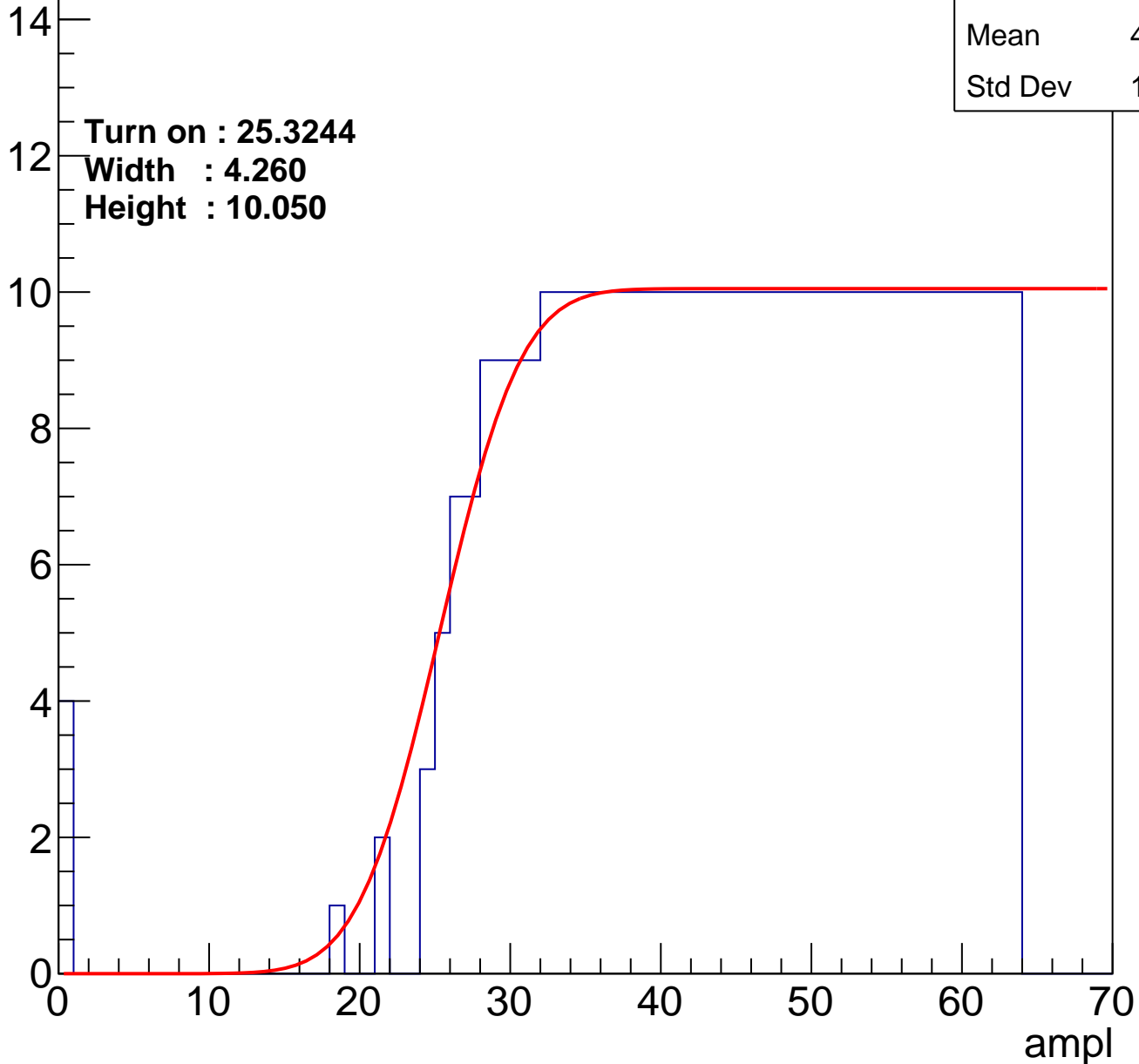
Entries	385
Mean	43.87
Std Dev	12.02

Turn on : 25.3244

Width : 4.260

Height : 10.050

Entry



B1L003S, U14-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.34
Std Dev	11.76

Turn on : 27.0947

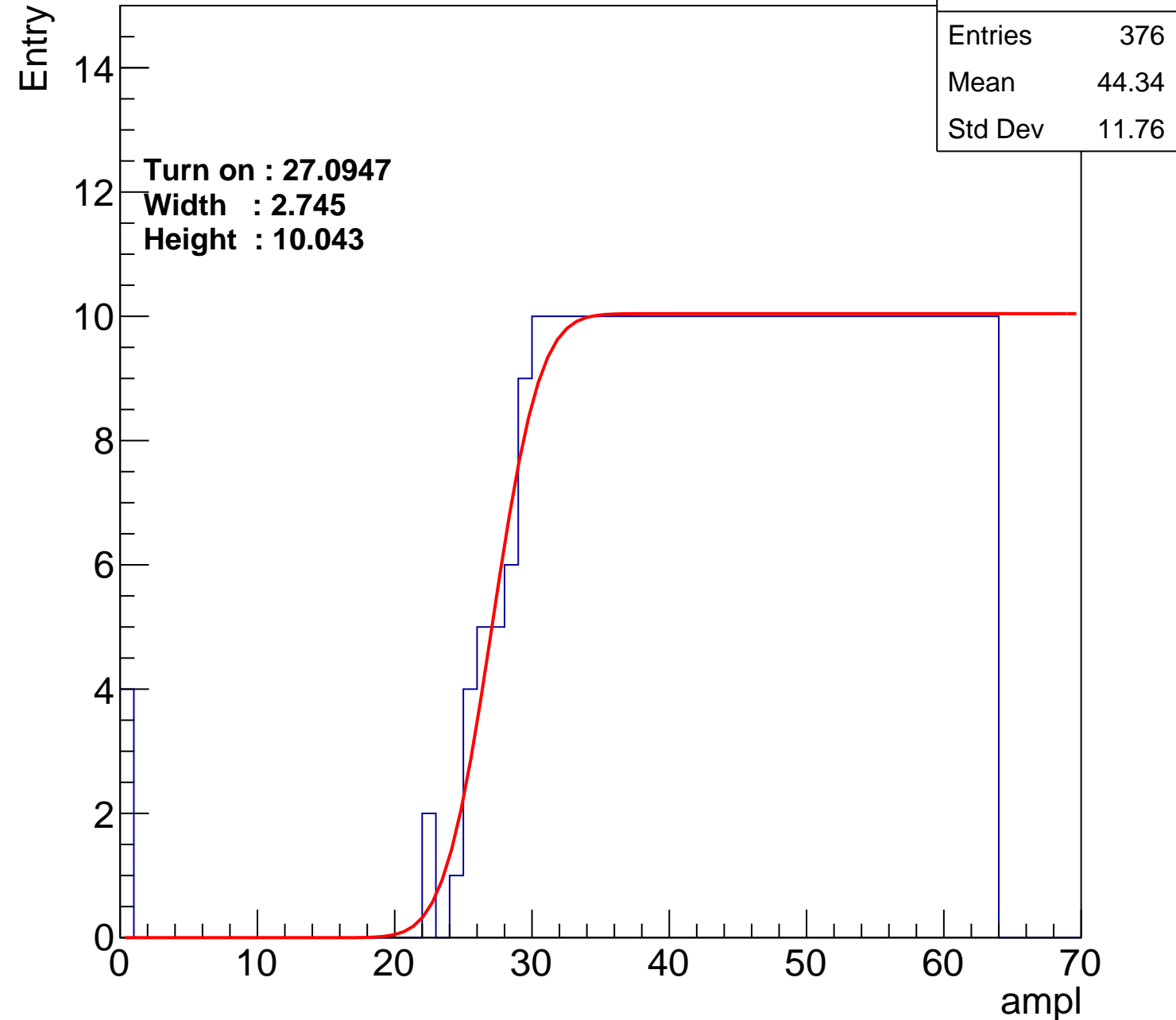
Width : 2.745

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch63

calib_packv5_042523_0143.root, FC#13, port D2

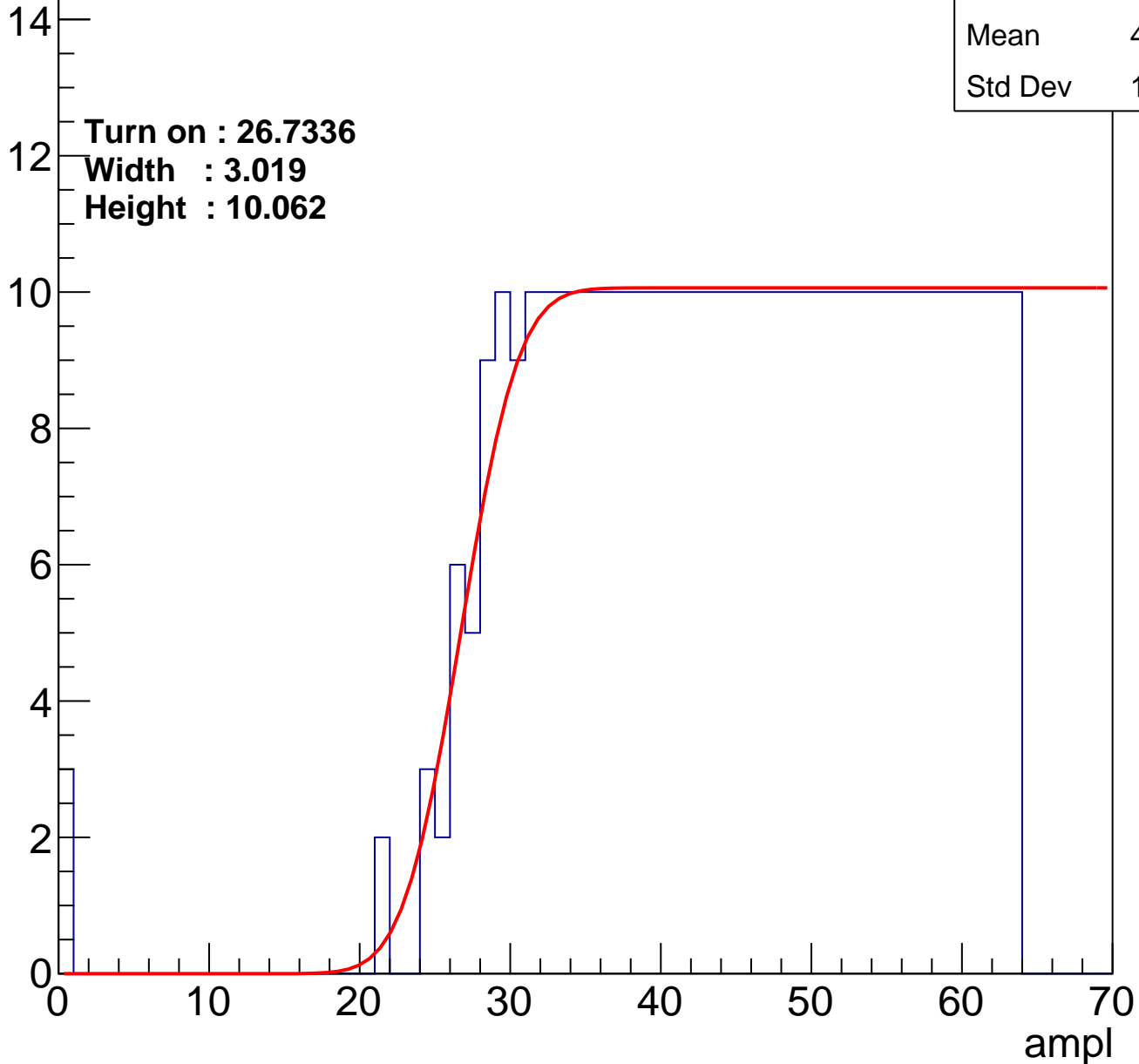
Entries	379
Mean	44.27
Std Dev	11.64

Turn on : 26.7336

Width : 3.019

Height : 10.062

Entry



B1L003S, U14-ch64

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.83
Std Dev	12.14

Turn on : 26.4630

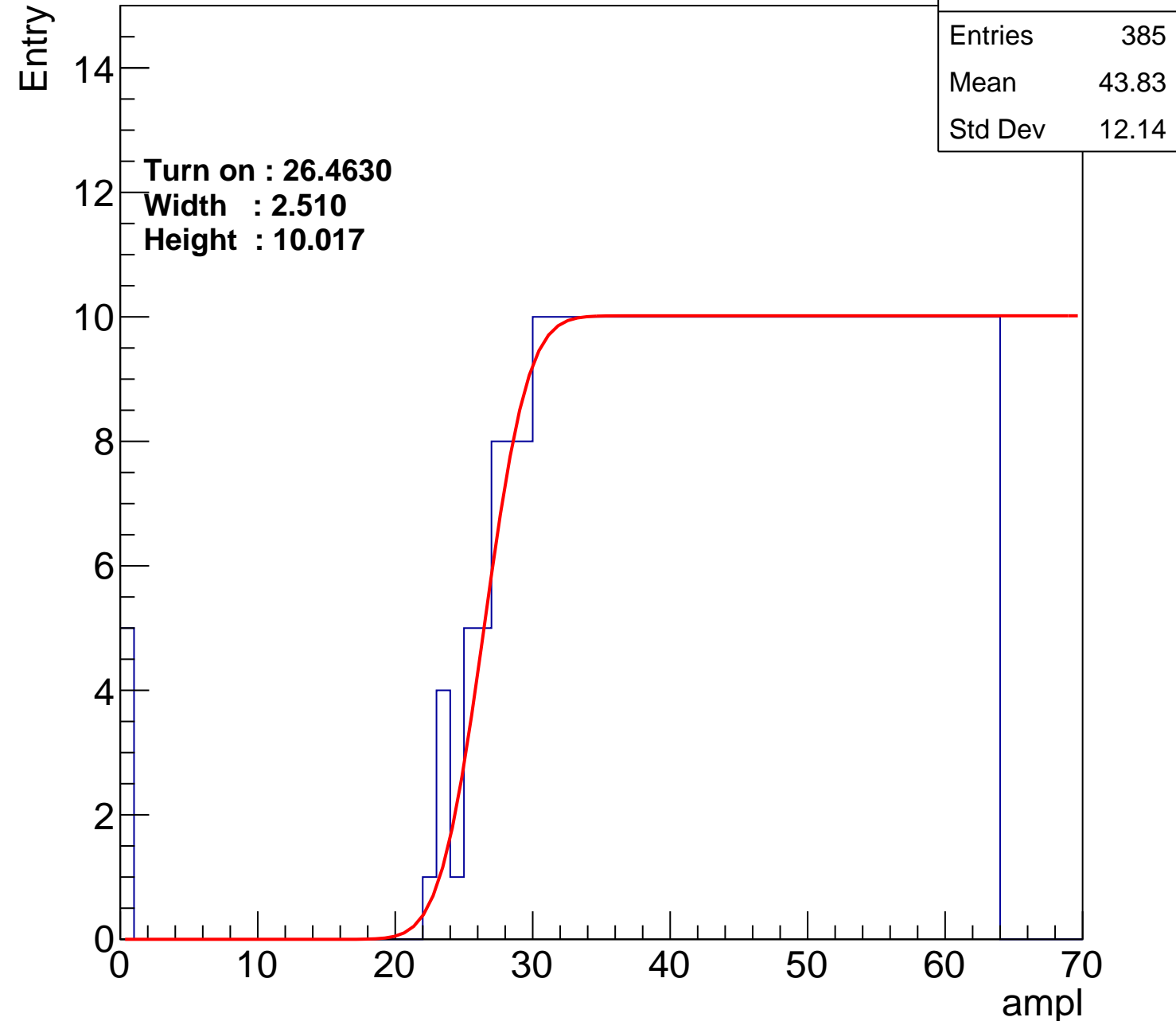
Width : 2.510

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.27
Std Dev	11.67

Turn on : 26.7086

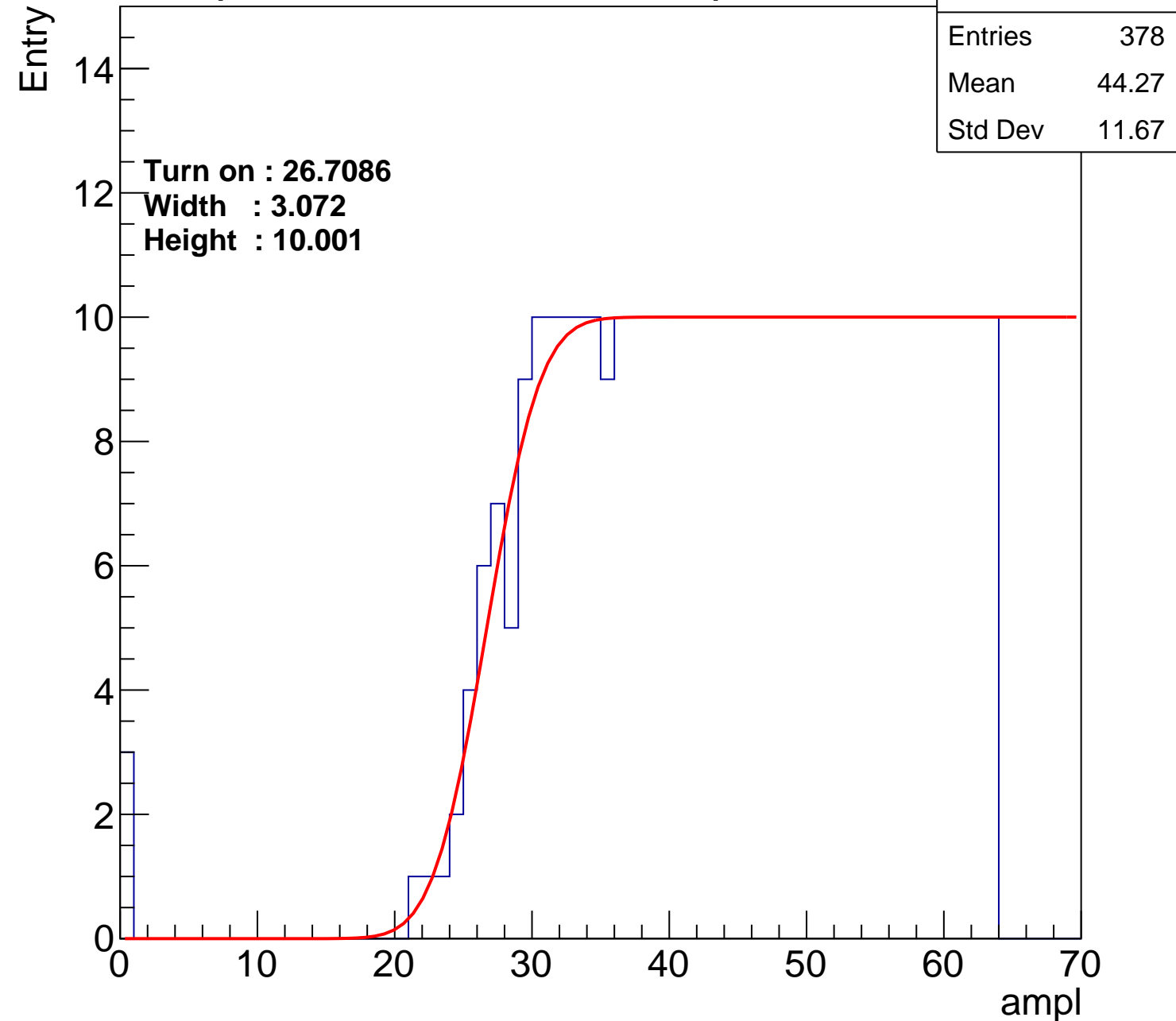
Width : 3.072

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch66

calib_packv5_042523_0143.root, FC#13, port D2

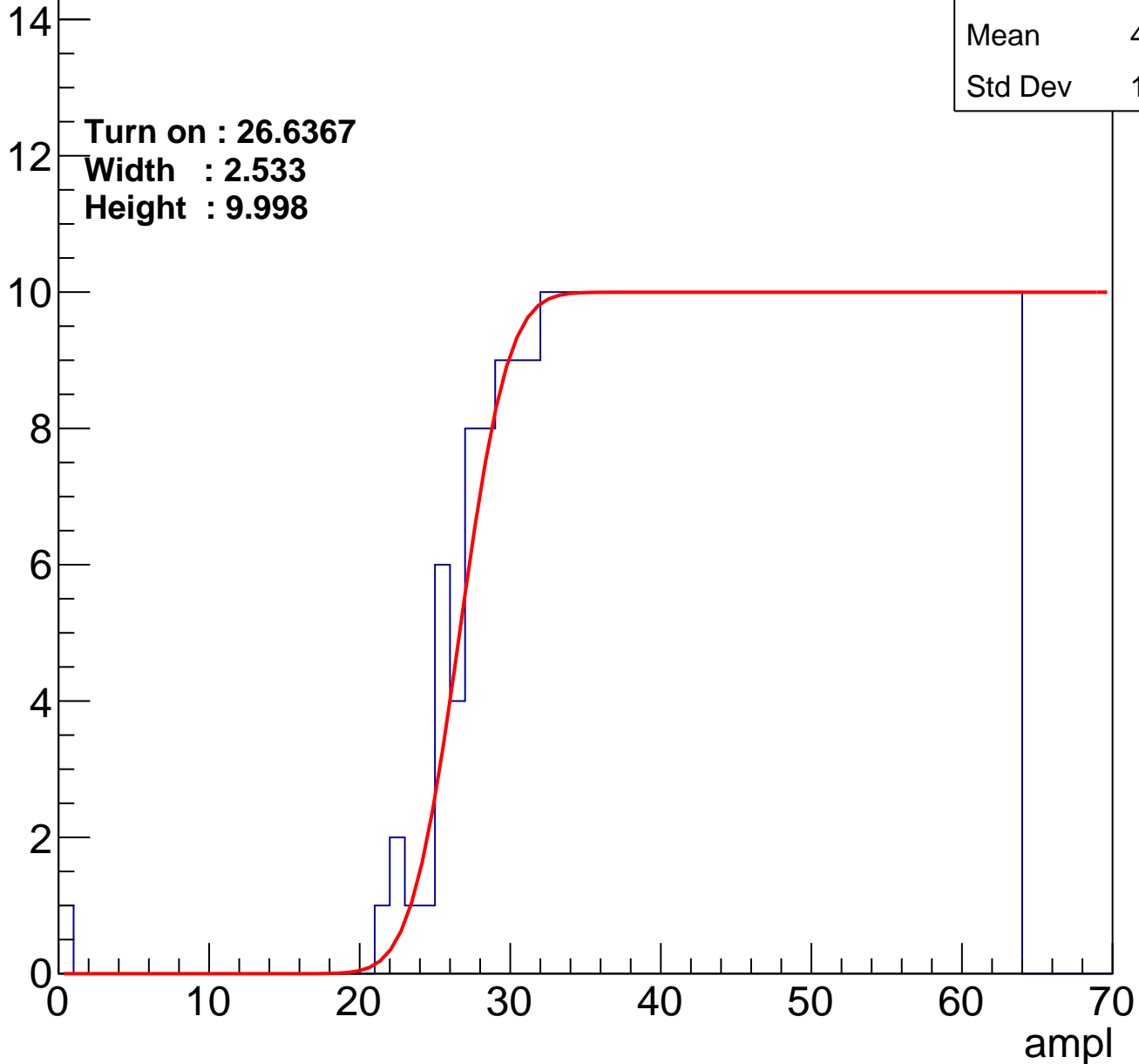
Entries	379
Mean	44.37
Std Dev	11.32

Turn on : 26.6367

Width : 2.533

Height : 9.998

Entry



B1L003S, U14-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.29
Std Dev	11.52

Turn on : 26.5666

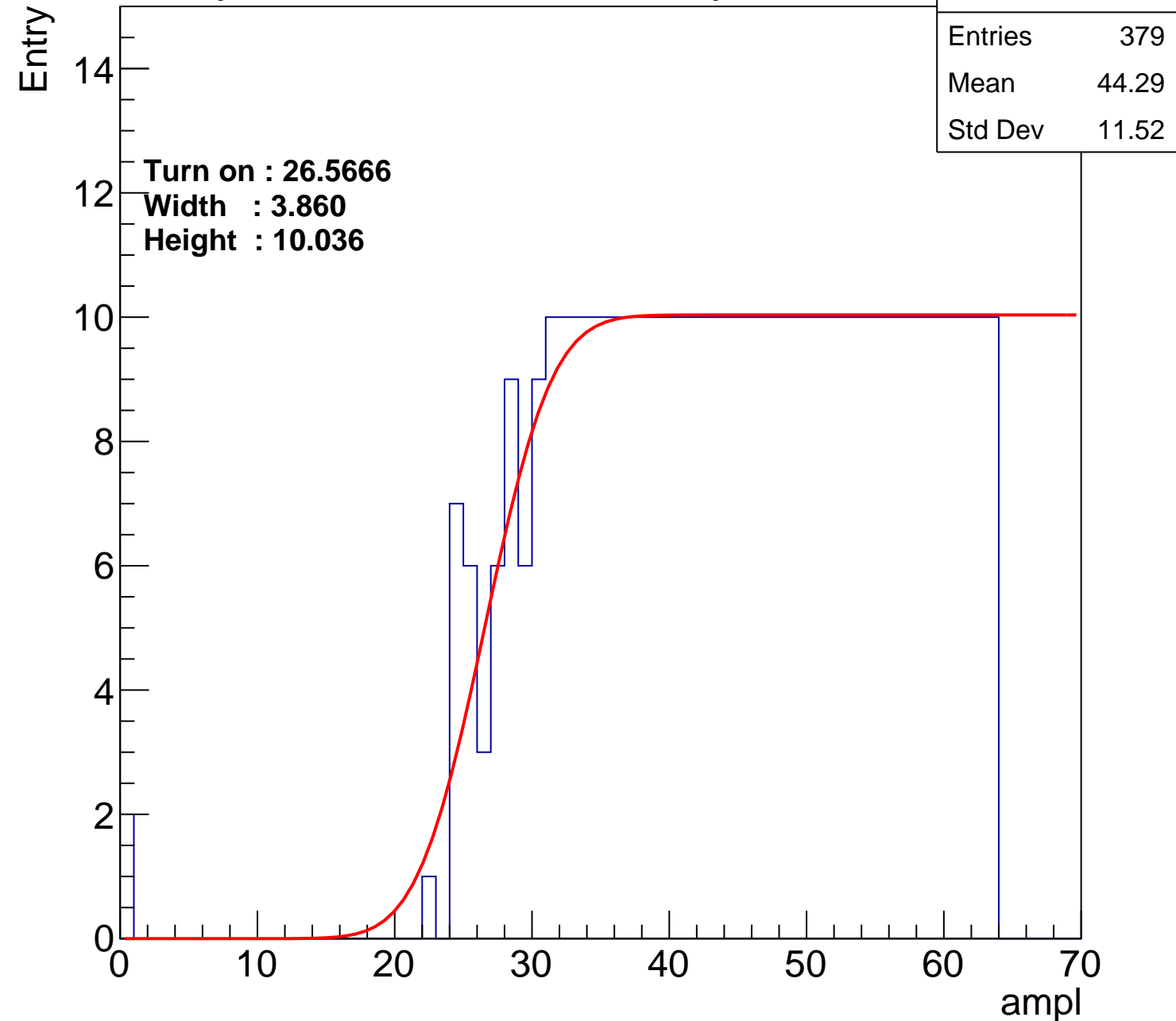
Width : 3.860

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.1665

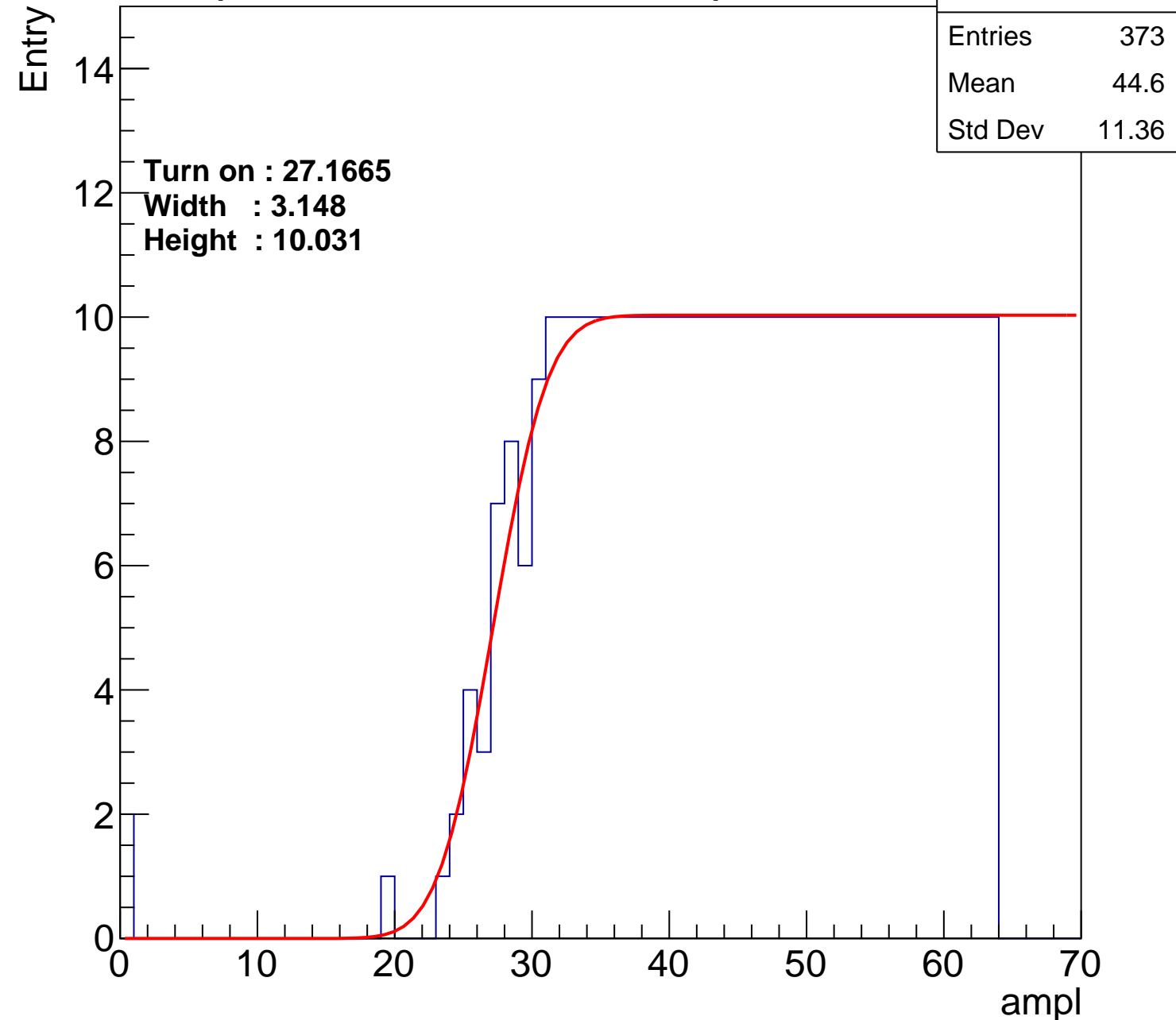
Width : 3.148

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.77
Std Dev	12.18

Turn on : 26.1492

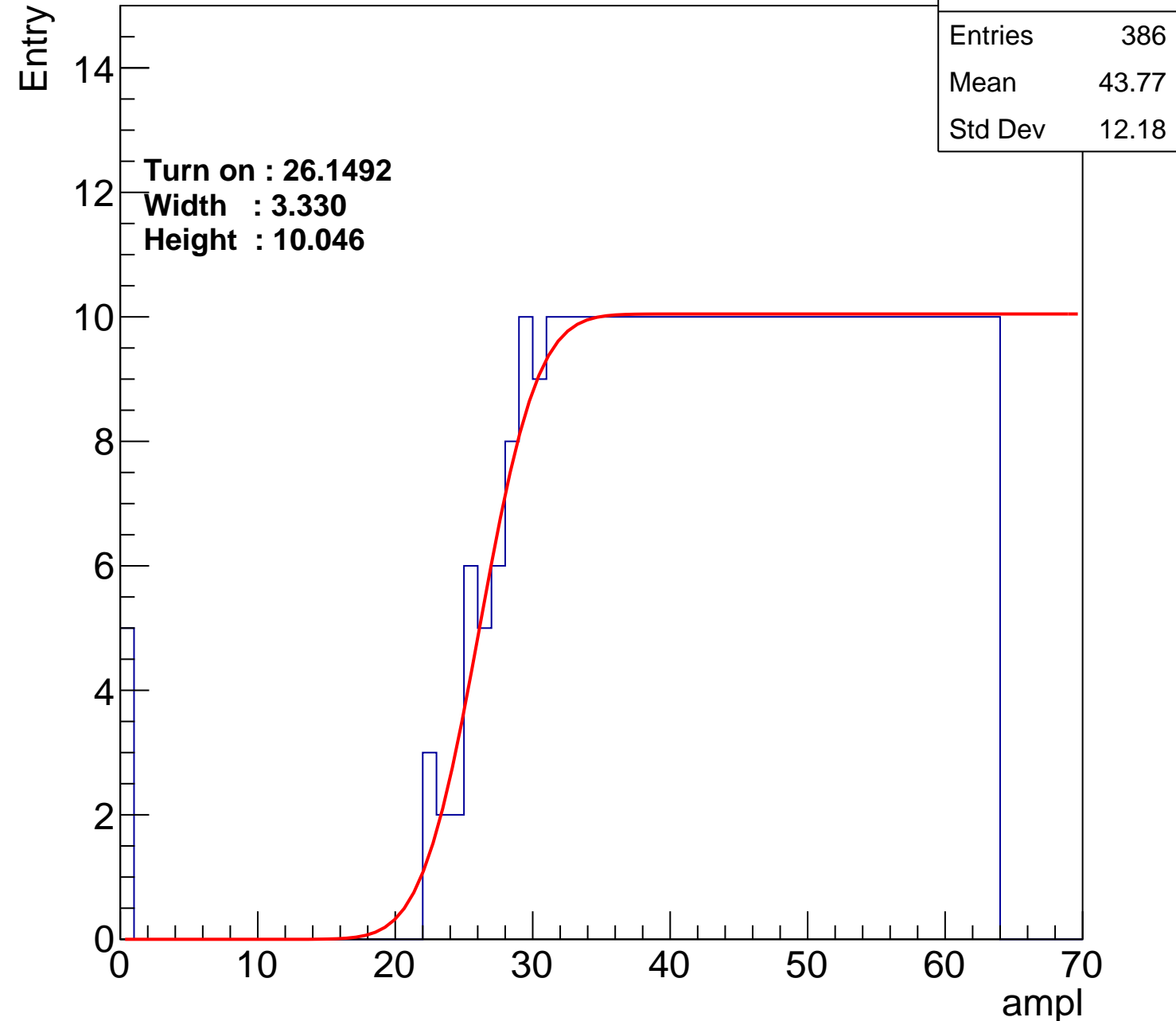
Width : 3.330

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch70

calib_packv5_042523_0143.root, FC#13, port D2

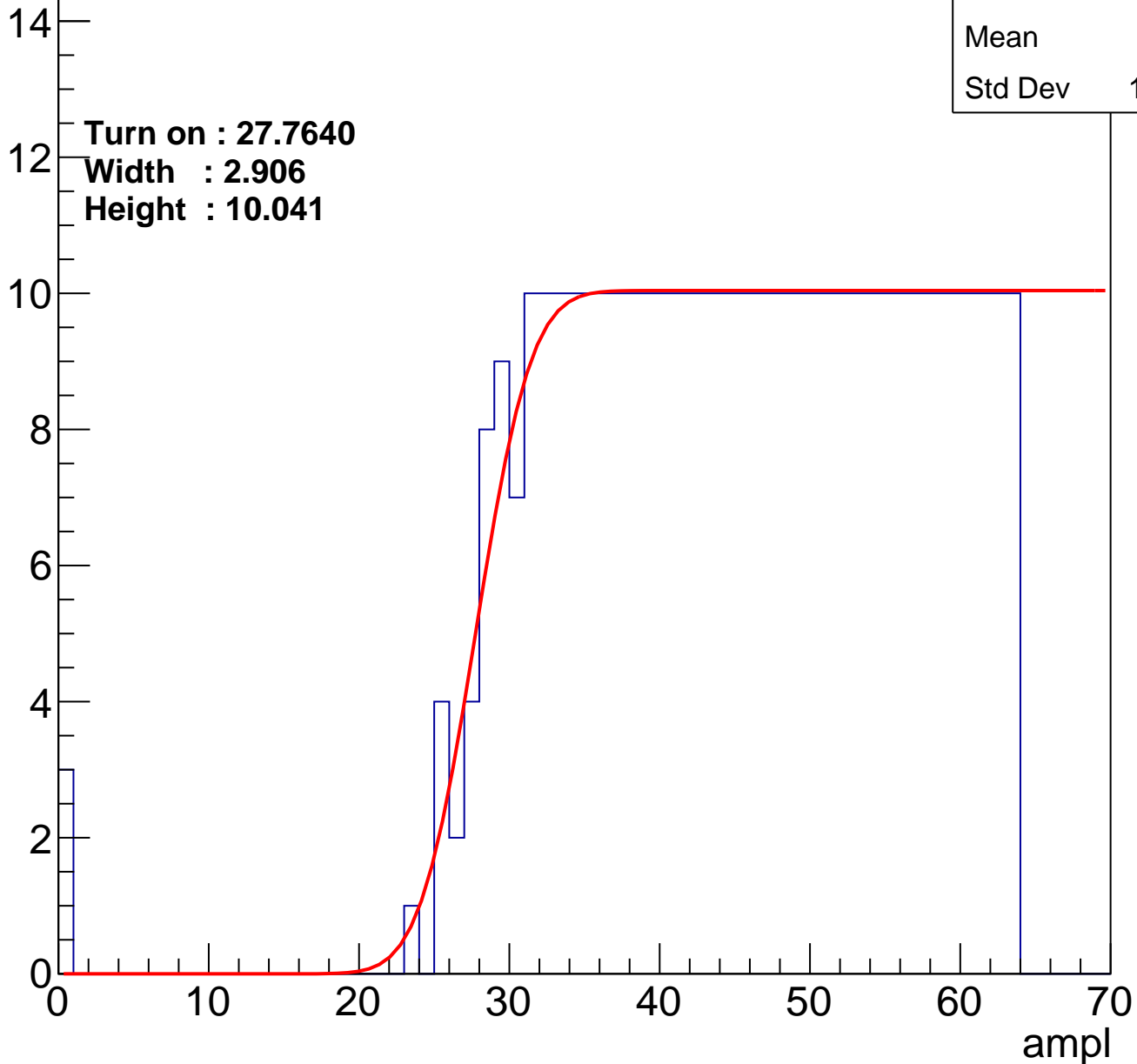
Entry

Entries	368
Mean	44.8
Std Dev	11.37

Turn on : 27.7640

Width : 2.906

Height : 10.041



B1L003S, U14-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.93
Std Dev	11.98

Turn on : 26.4021

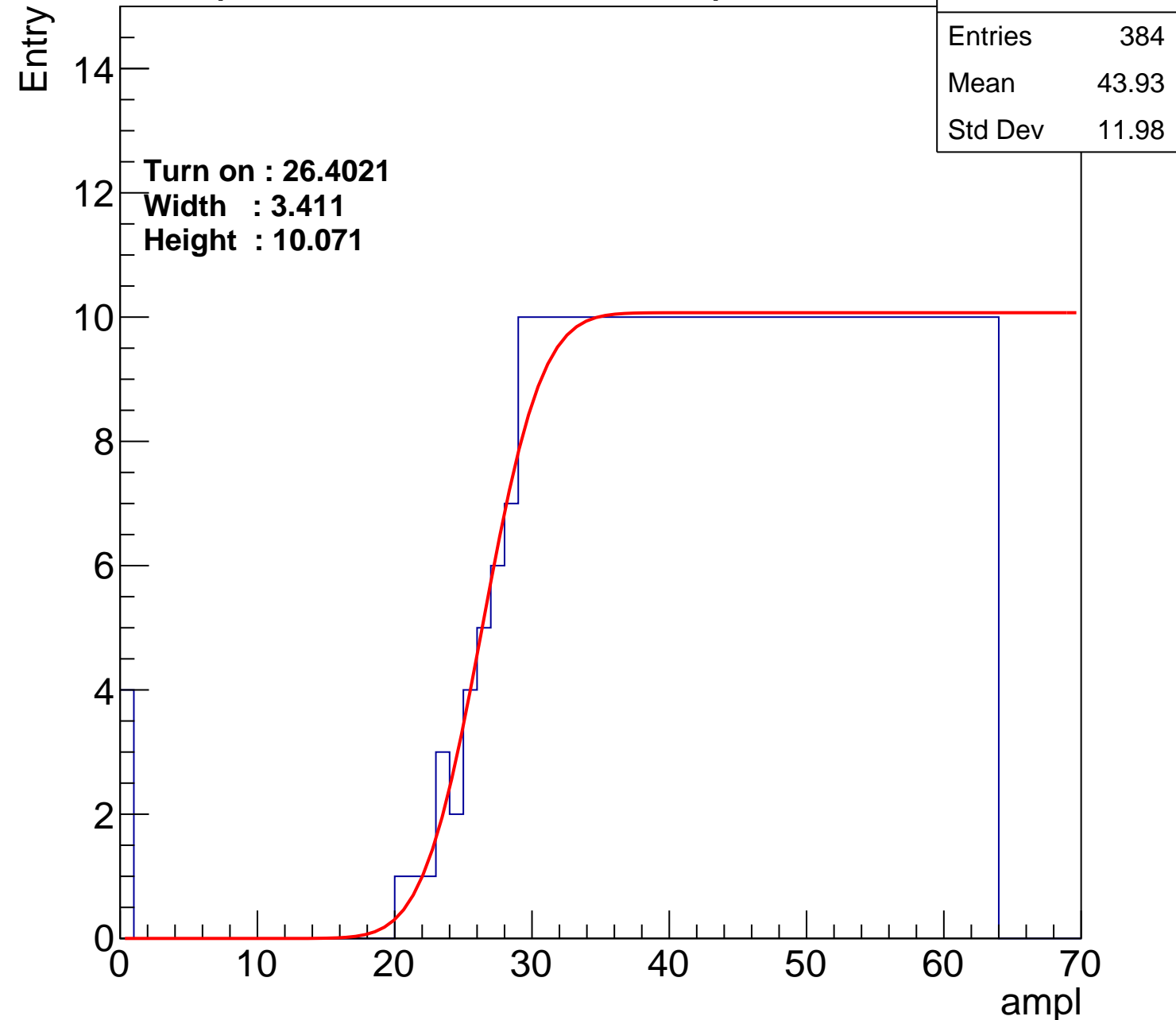
Width : 3.411

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.66
Std Dev	12.01

Turn on : 25.4917

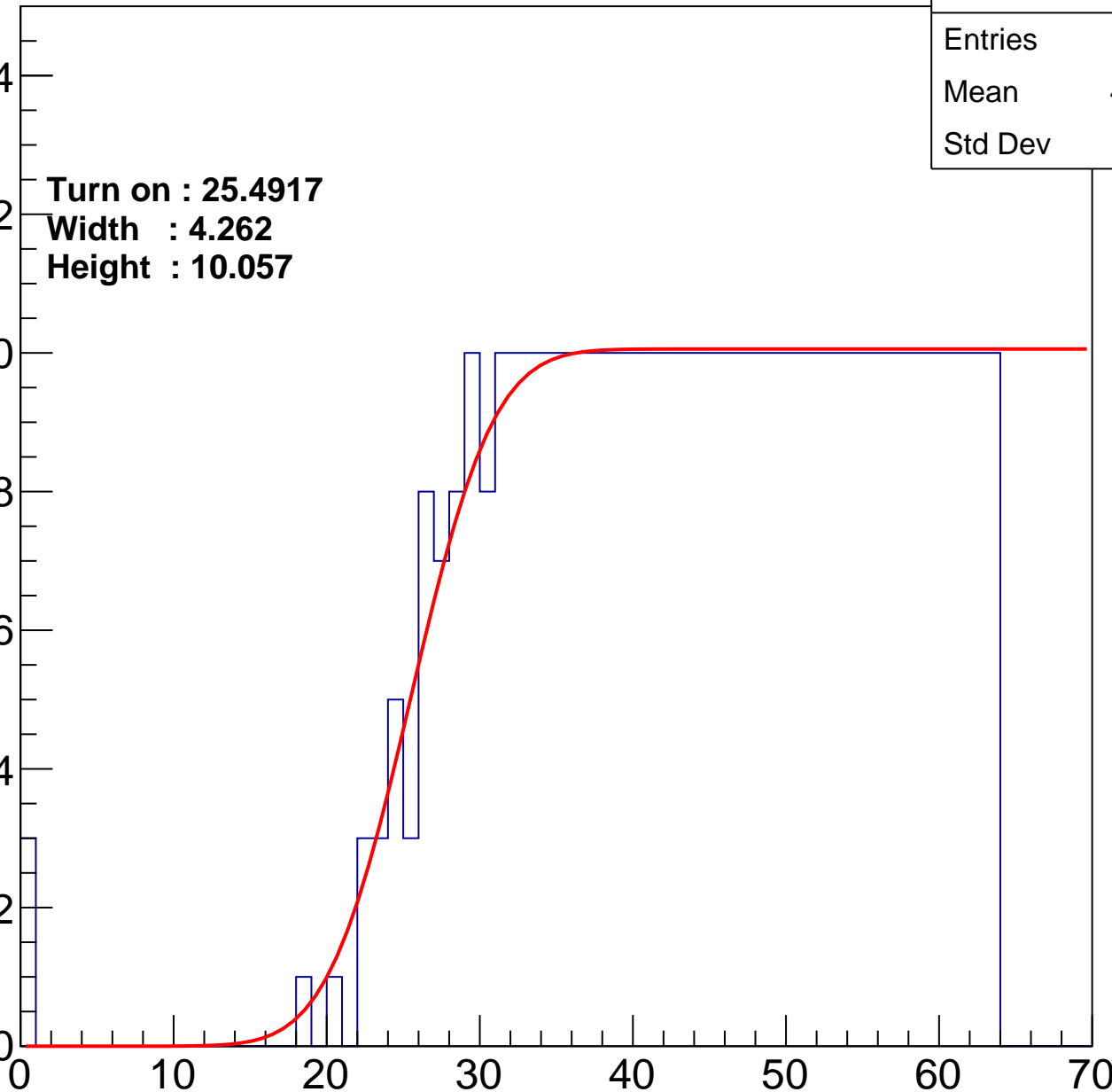
Width : 4.262

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch73

calib_packv5_042523_0143.root, FC#13, port D2

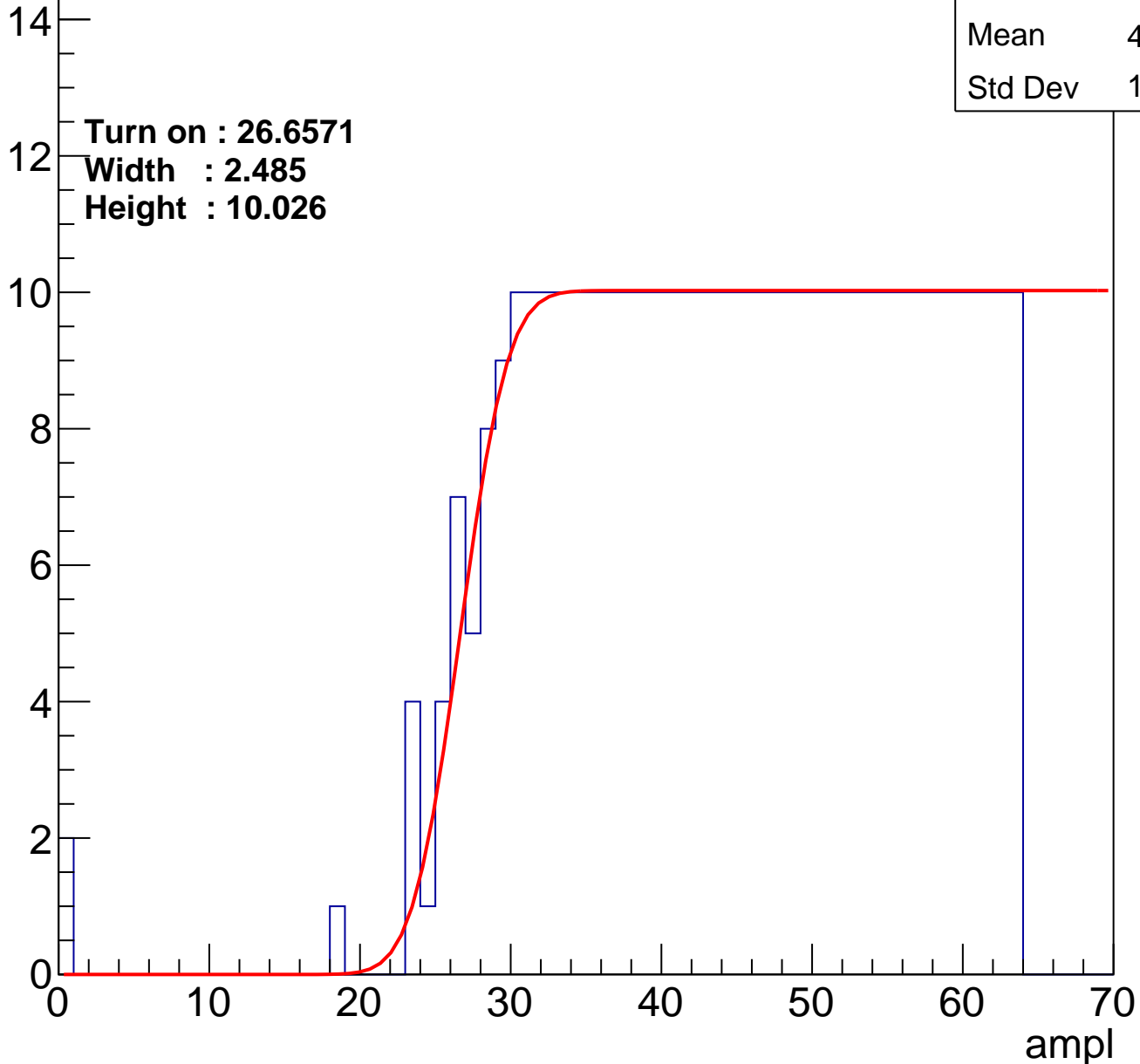
Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.6571

Width : 2.485

Height : 10.026

Entry



B1L003S, U14-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.76
Std Dev	11.86

Turn on : 25.1579

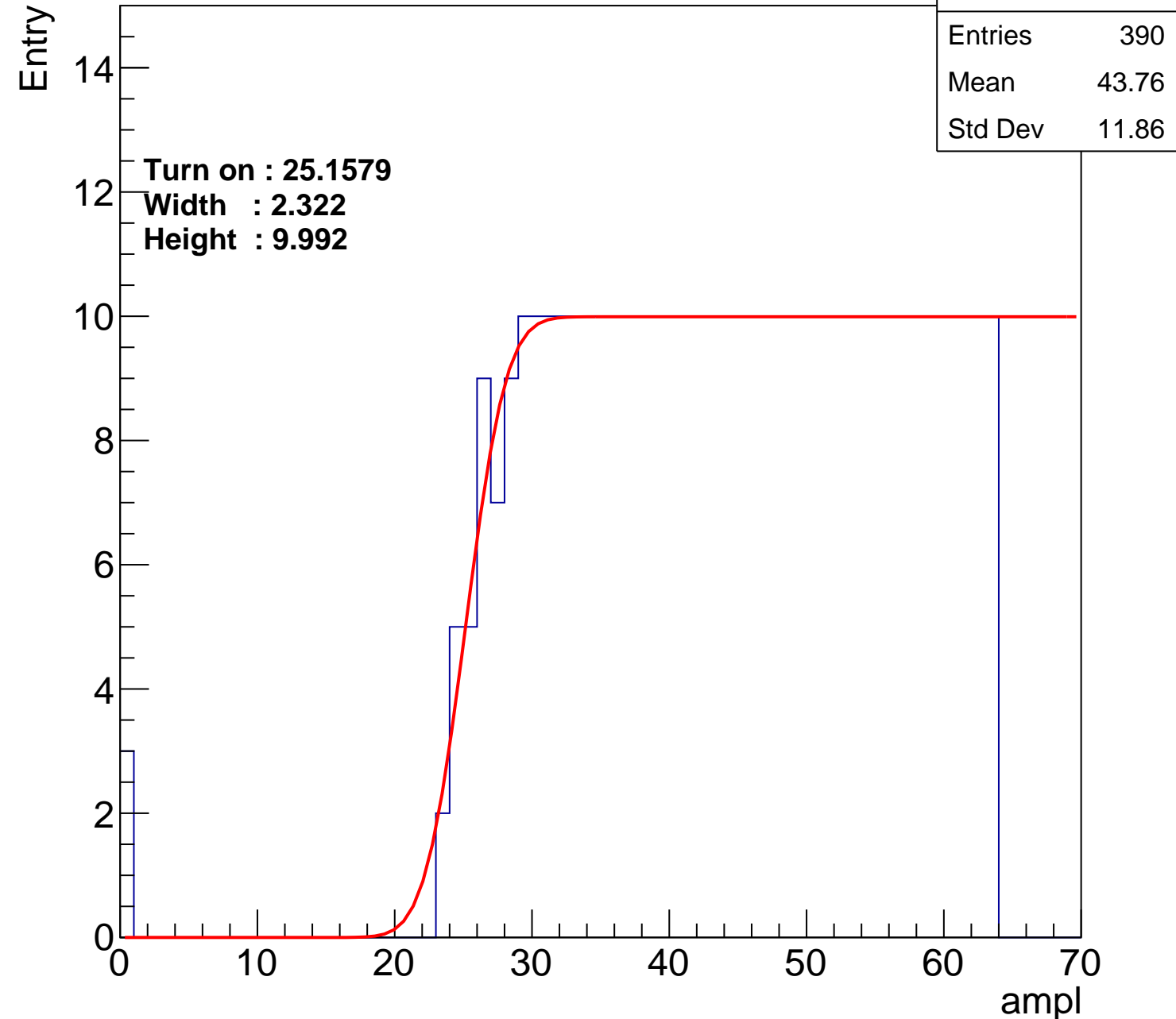
Width : 2.322

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.2720

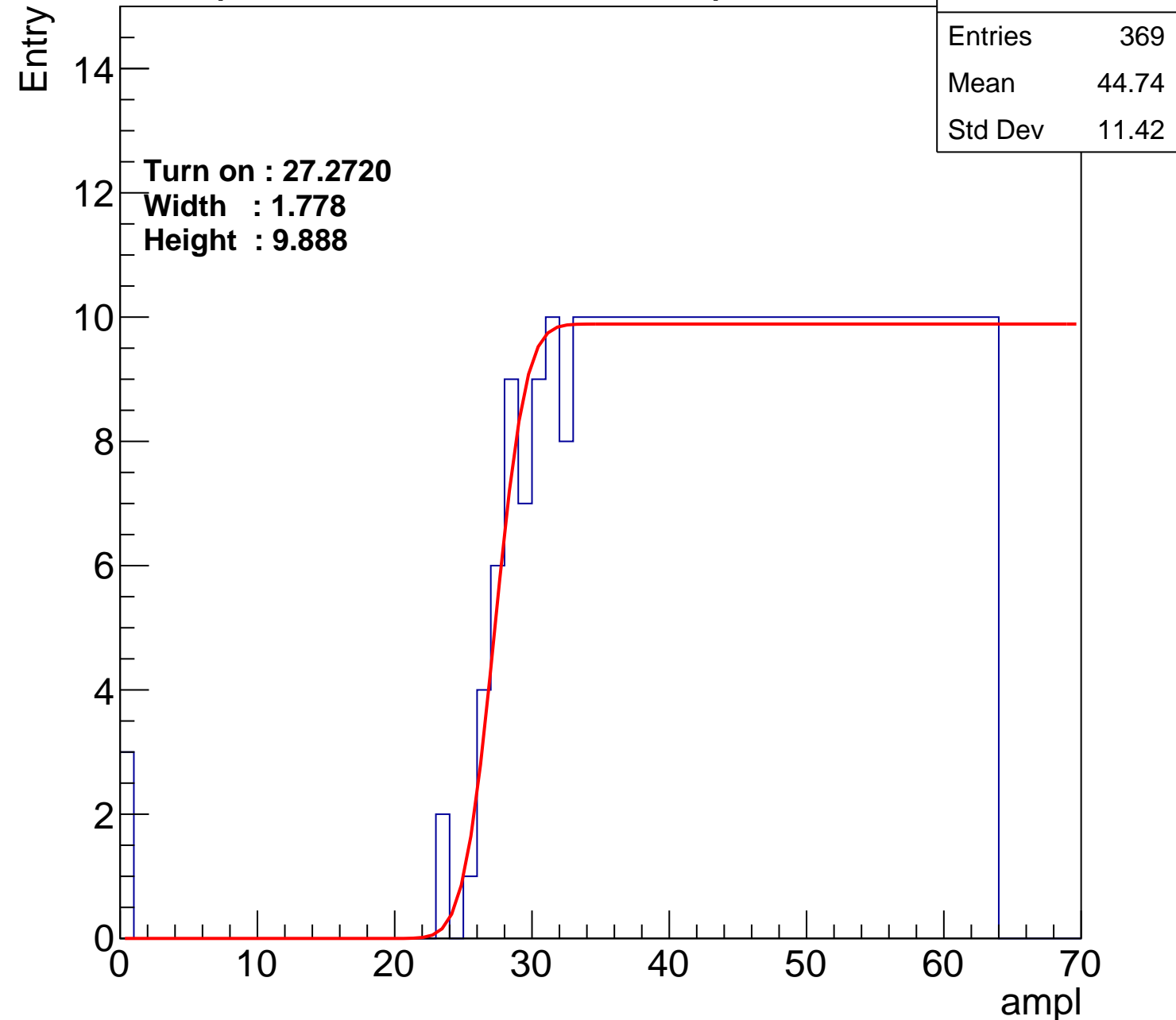
Width : 1.778

Height : 9.888

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch76

calib_packv5_042523_0143.root, FC#13, port D2

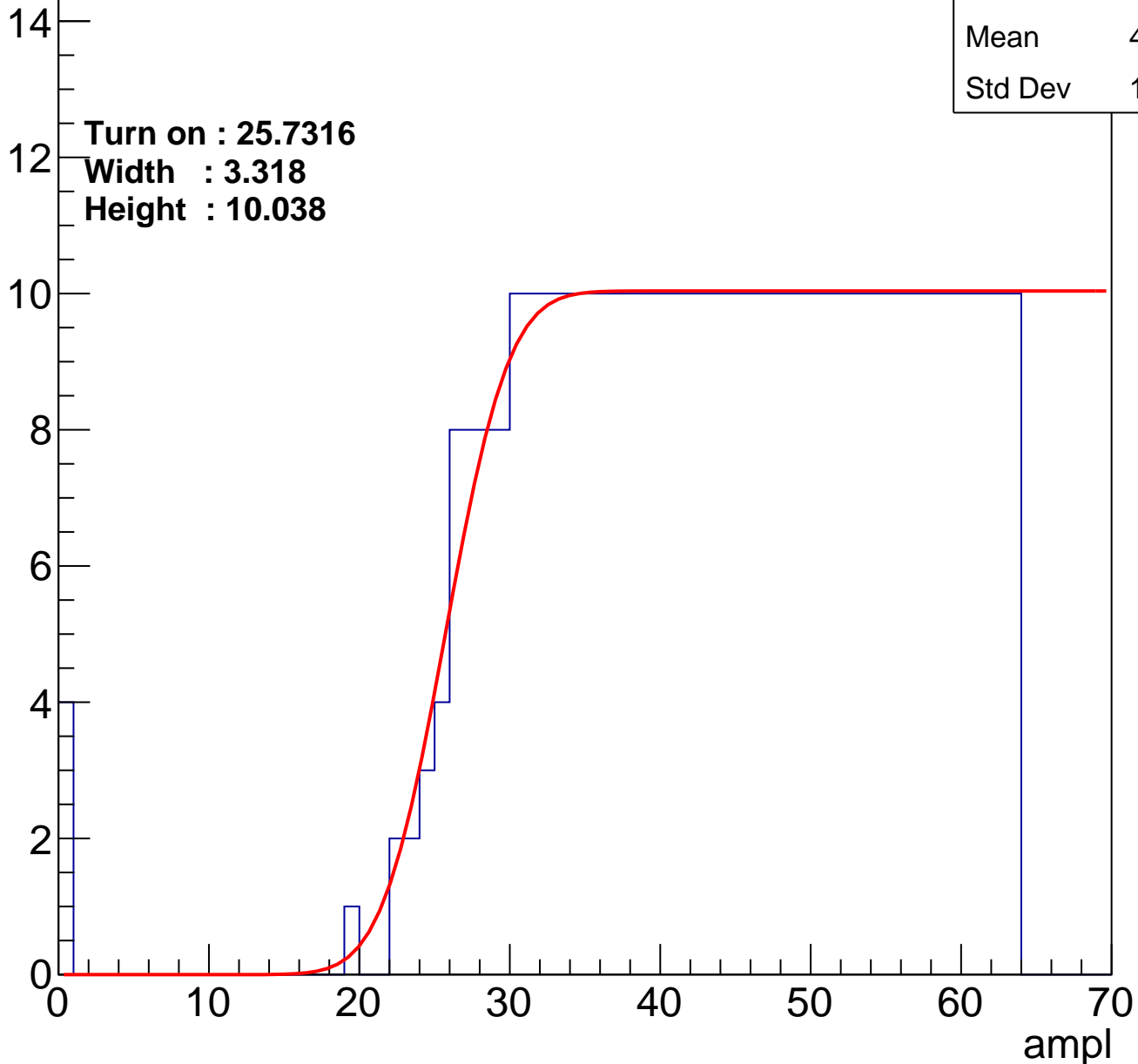
Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.7316

Width : 3.318

Height : 10.038

Entry



B1L003S, U14-ch77

calib_packv5_042523_0143.root, FC#13, port D2

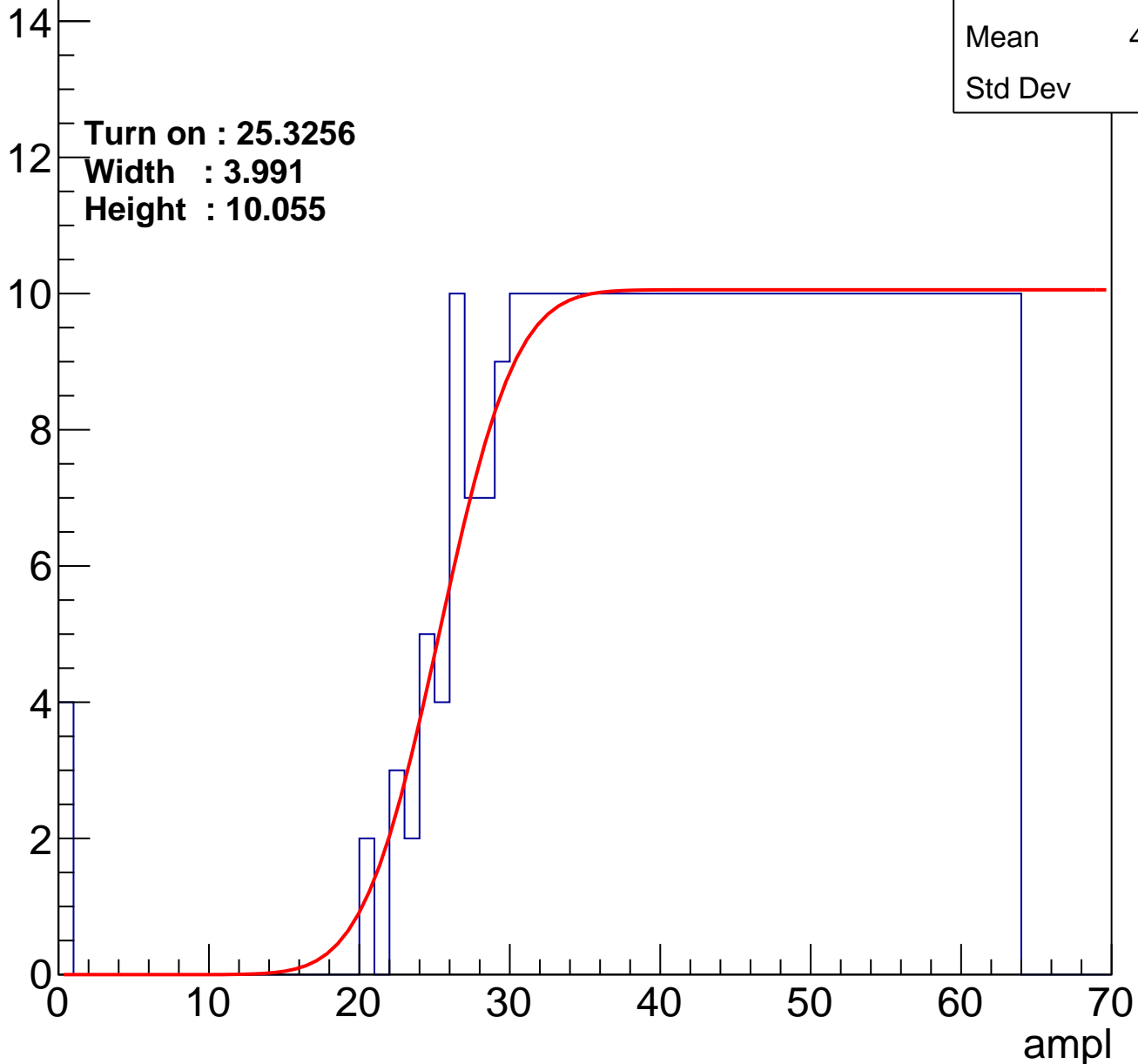
Entries	393
Mean	43.48
Std Dev	12.2

Turn on : 25.3256

Width : 3.991

Height : 10.055

Entry



B1L003S, U14-ch78

calib_packv5_042523_0143.root, FC#13, port D2

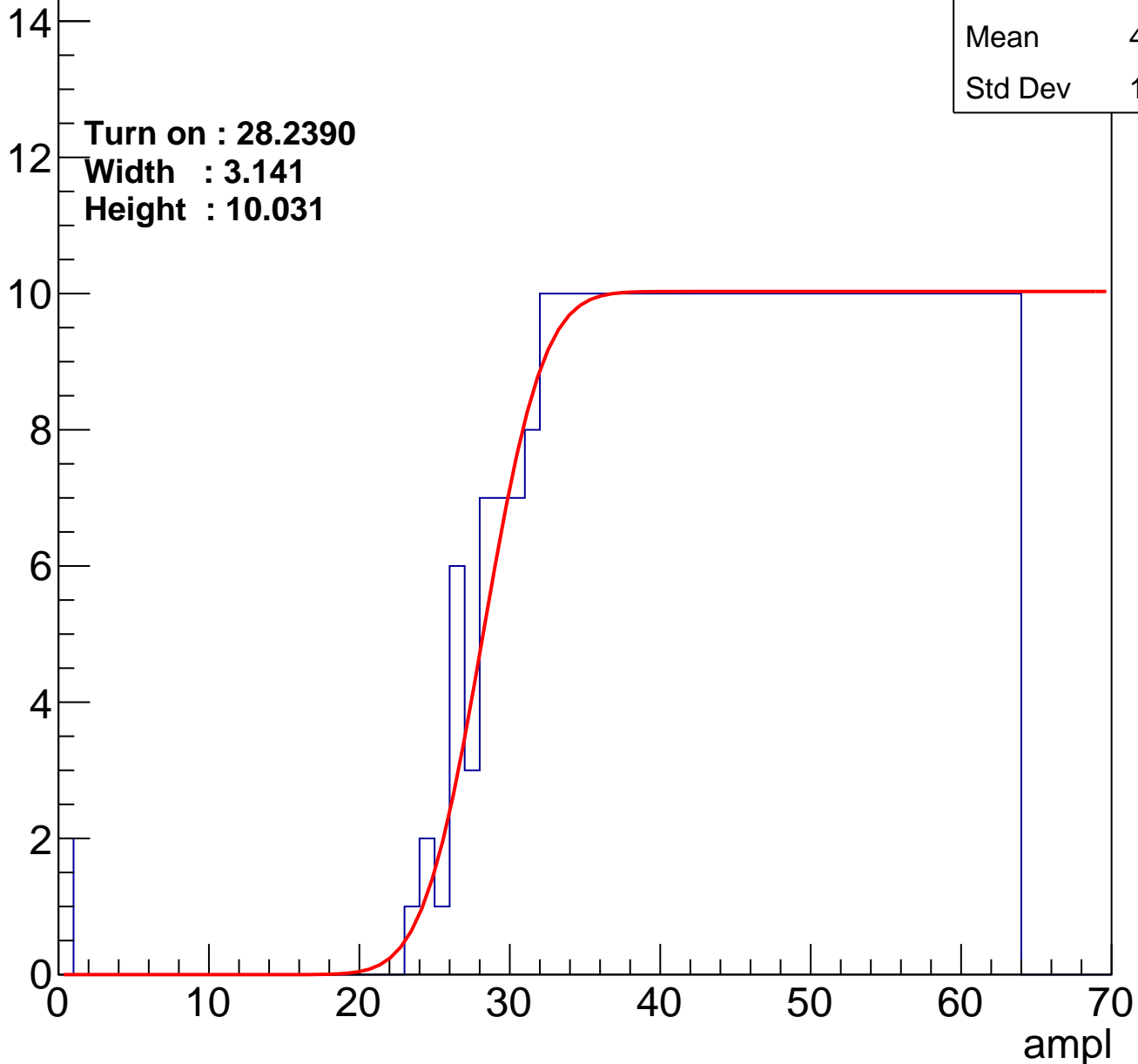
Entries	364
Mean	45.03
Std Dev	11.14

Turn on : 28.2390

Width : 3.141

Height : 10.031

Entry



B1L003S, U14-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.1364

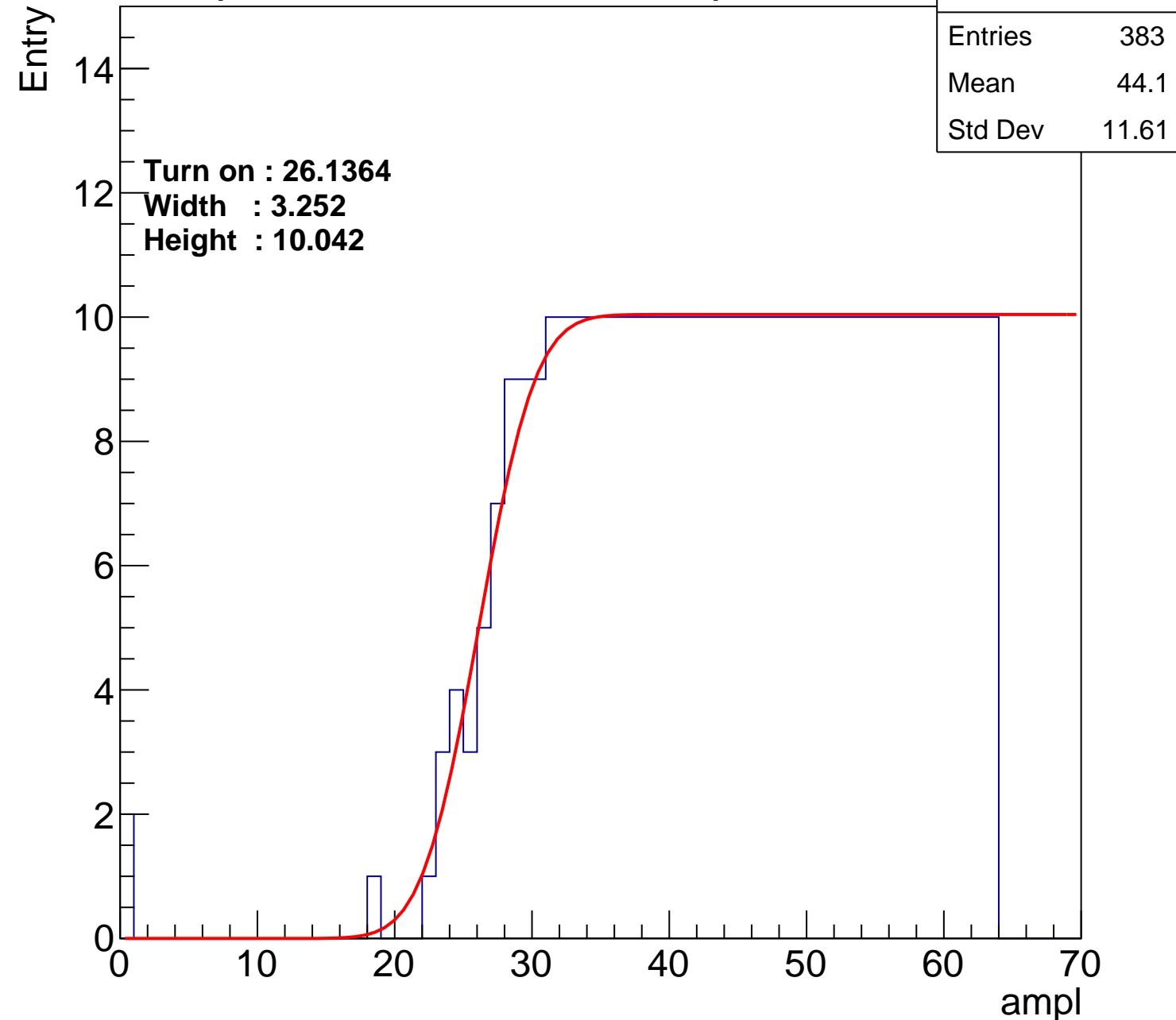
Width : 3.252

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.22
Std Dev	11.4

Turn on : 29.4757

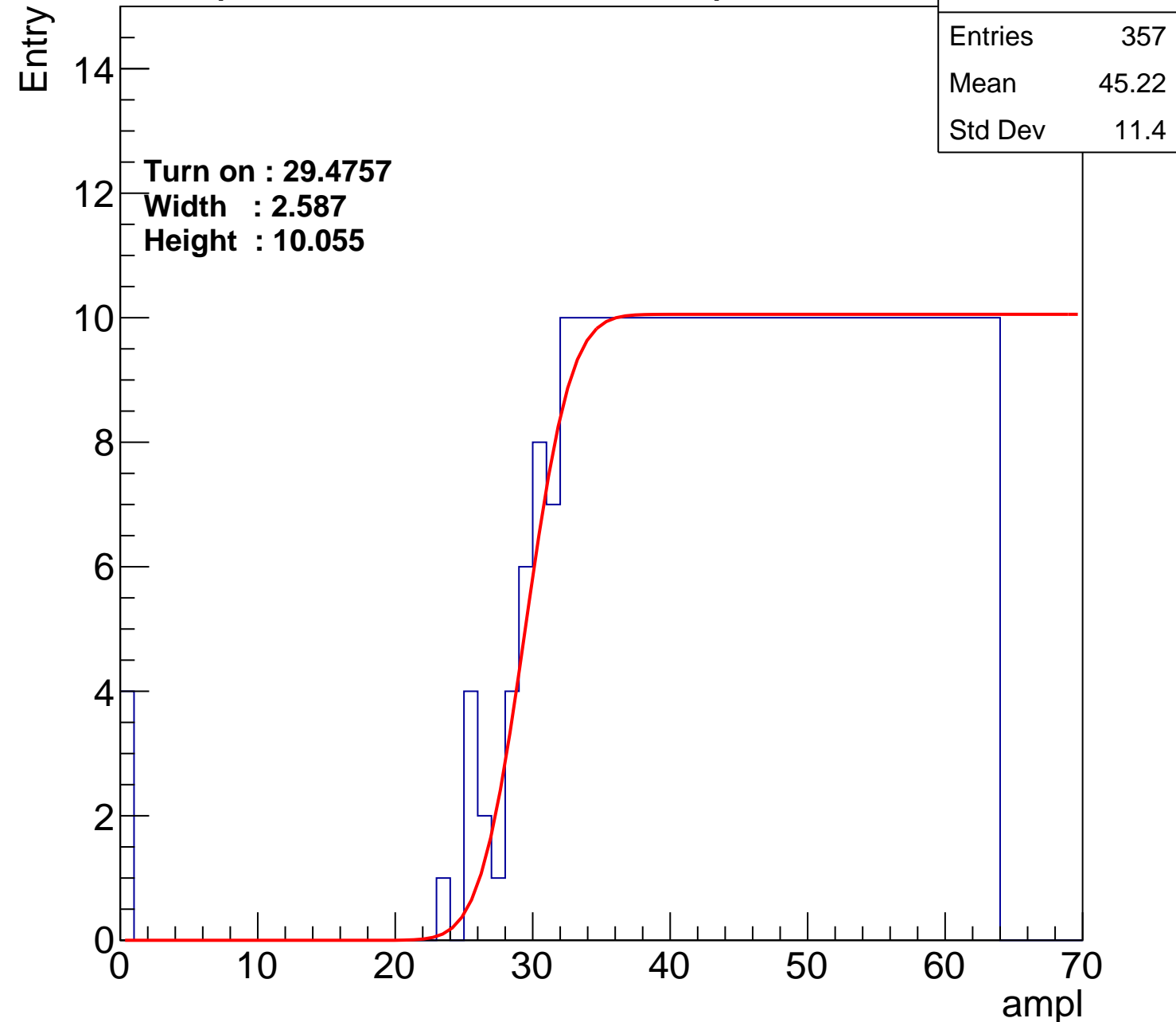
Width : 2.587

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch81

calib_packv5_042523_0143.root, FC#13, port D2

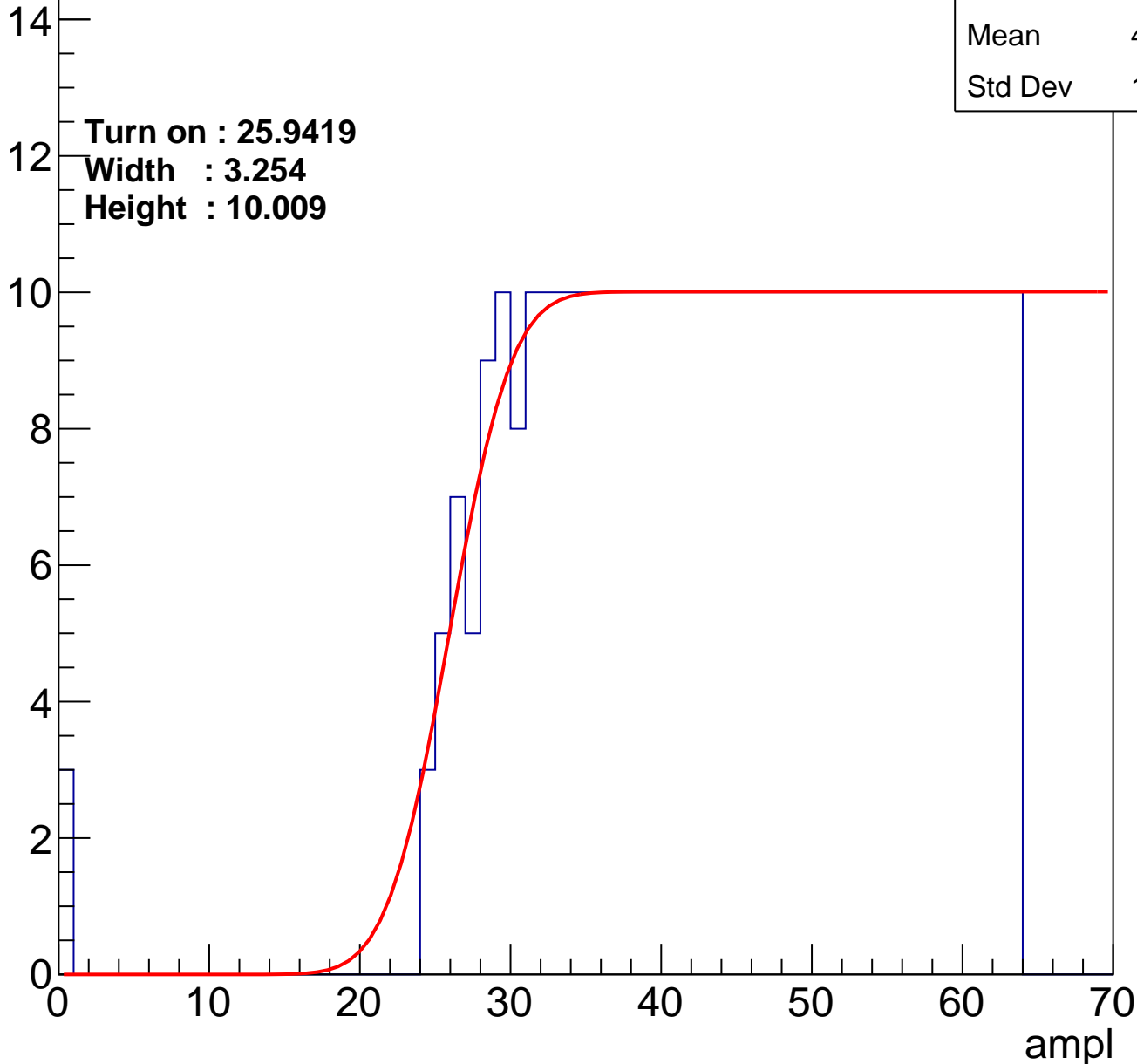
Entries	380
Mean	44.23
Std Dev	11.64

Turn on : 25.9419

Width : 3.254

Height : 10.009

Entry



B1L003S, U14-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 28.1926

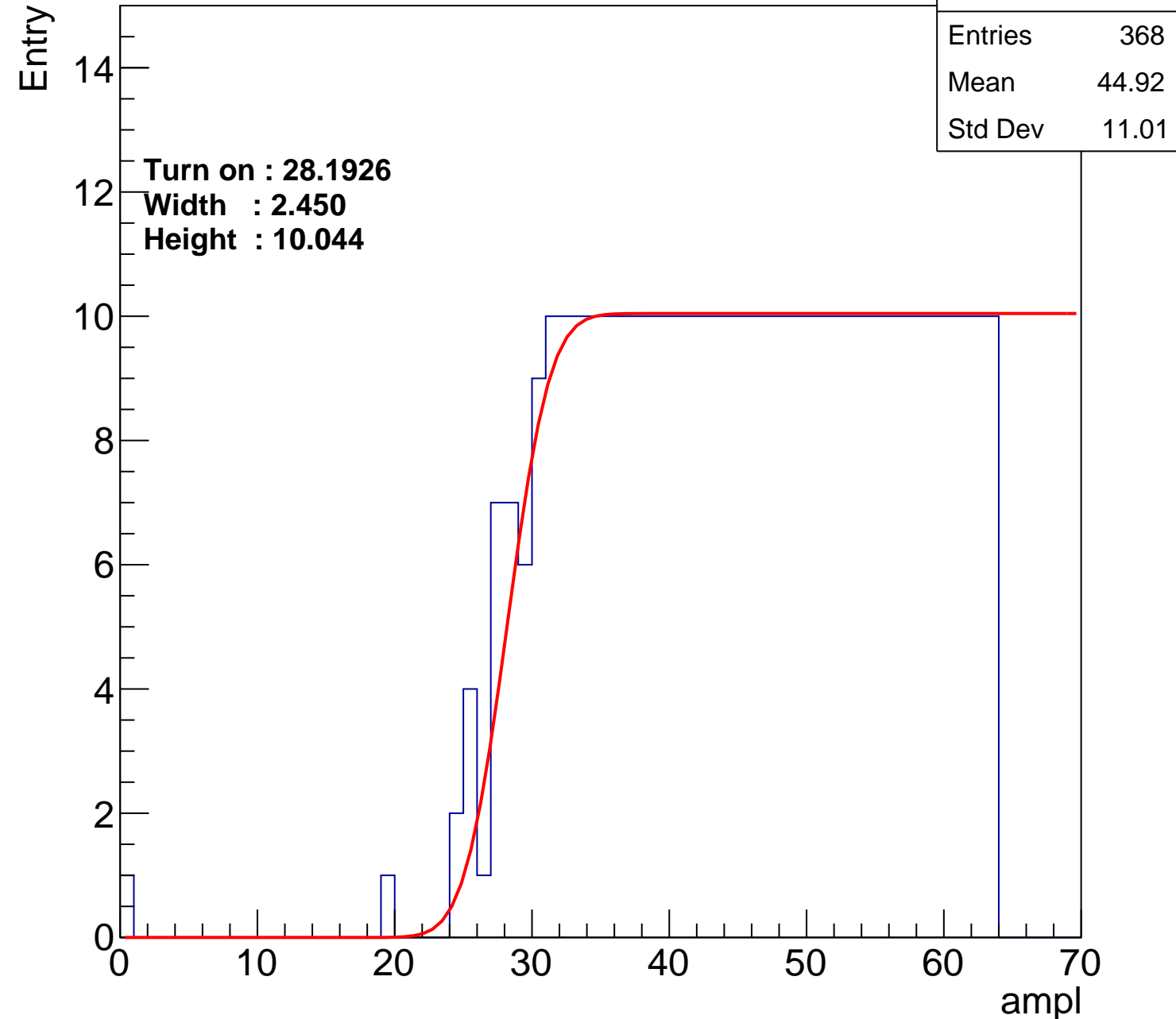
Width : 2.450

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 27.1994

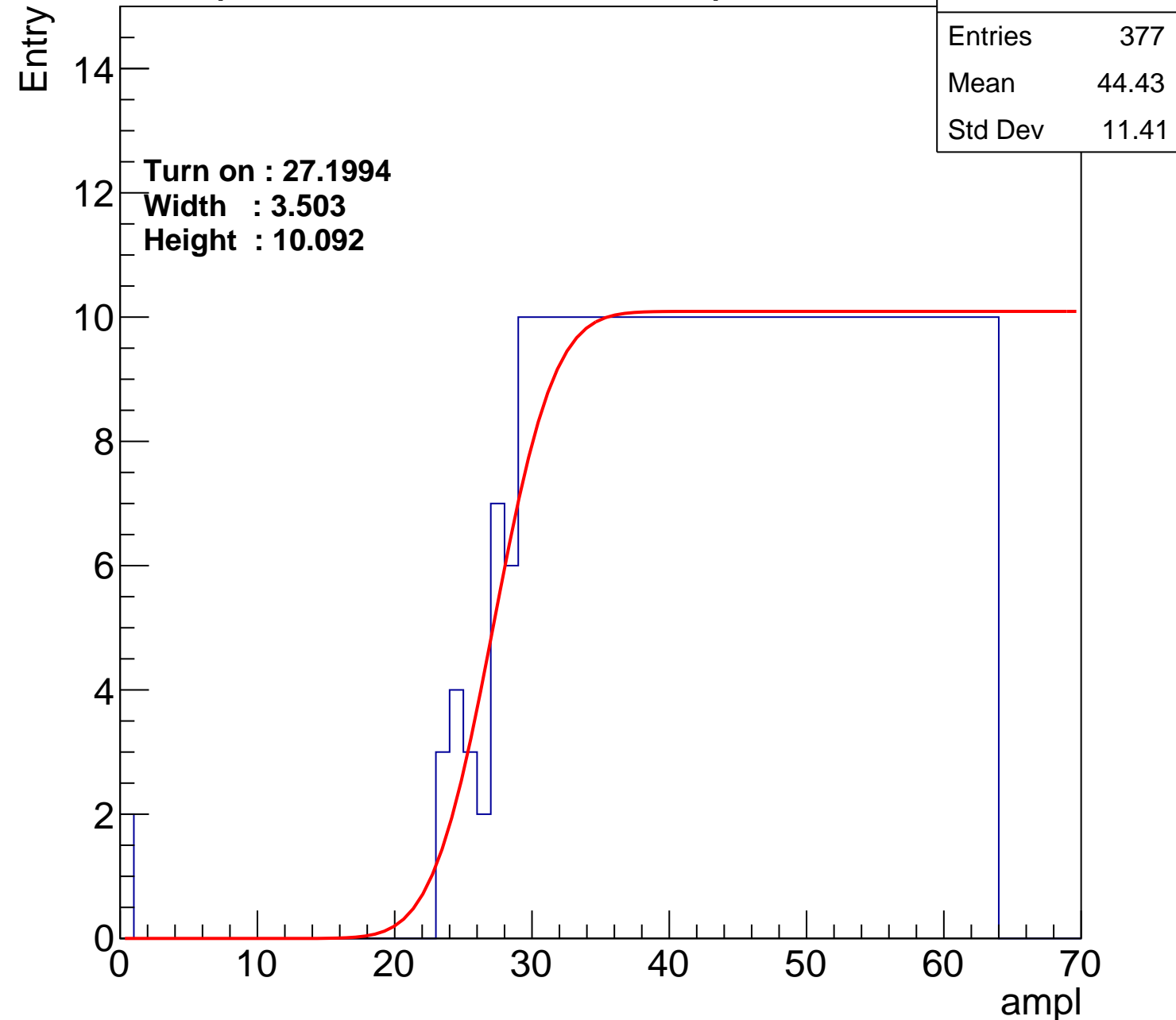
Width : 3.503

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 26.4418

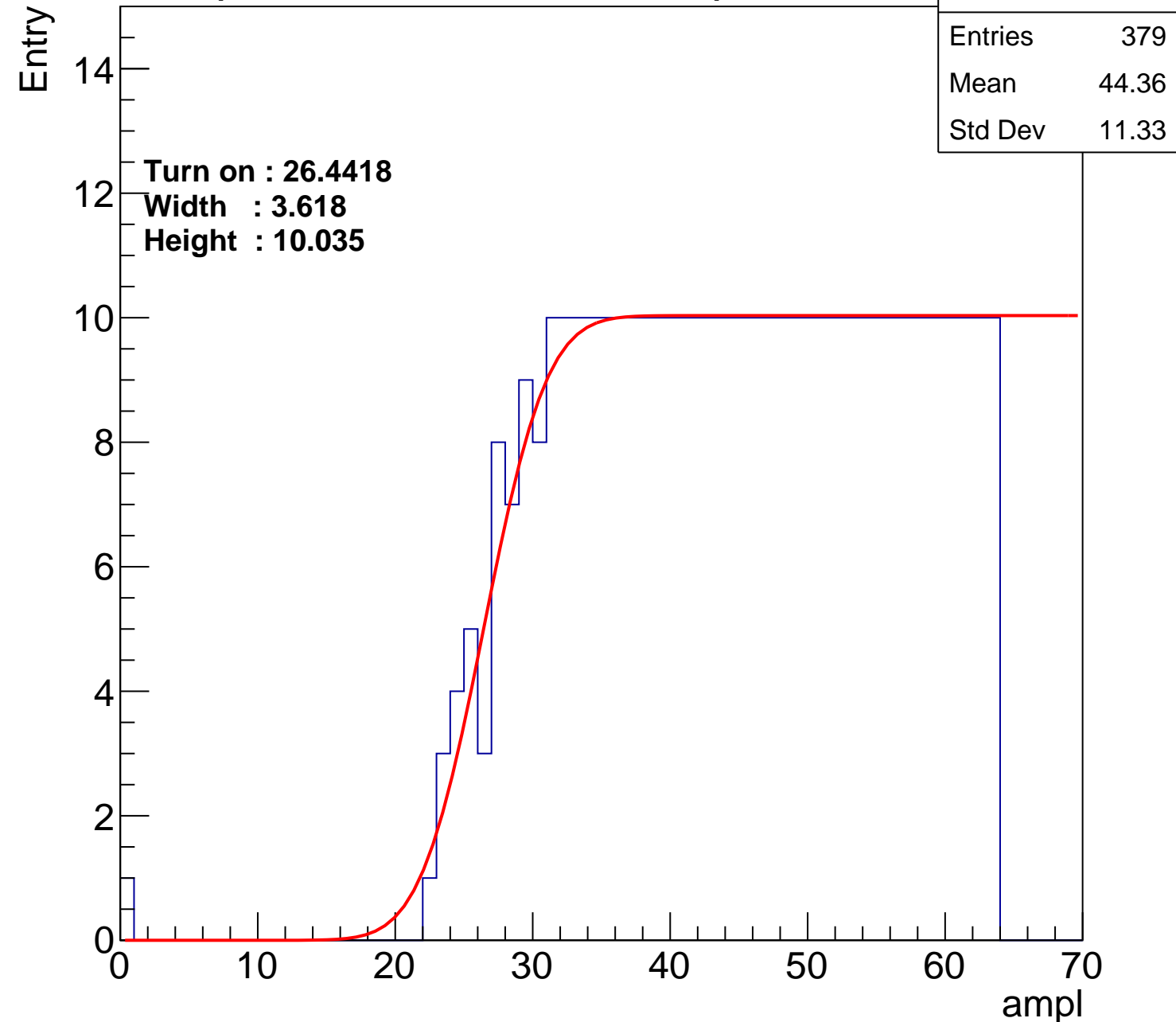
Width : 3.618

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.62
Std Dev	11.45

Turn on : 27.3858

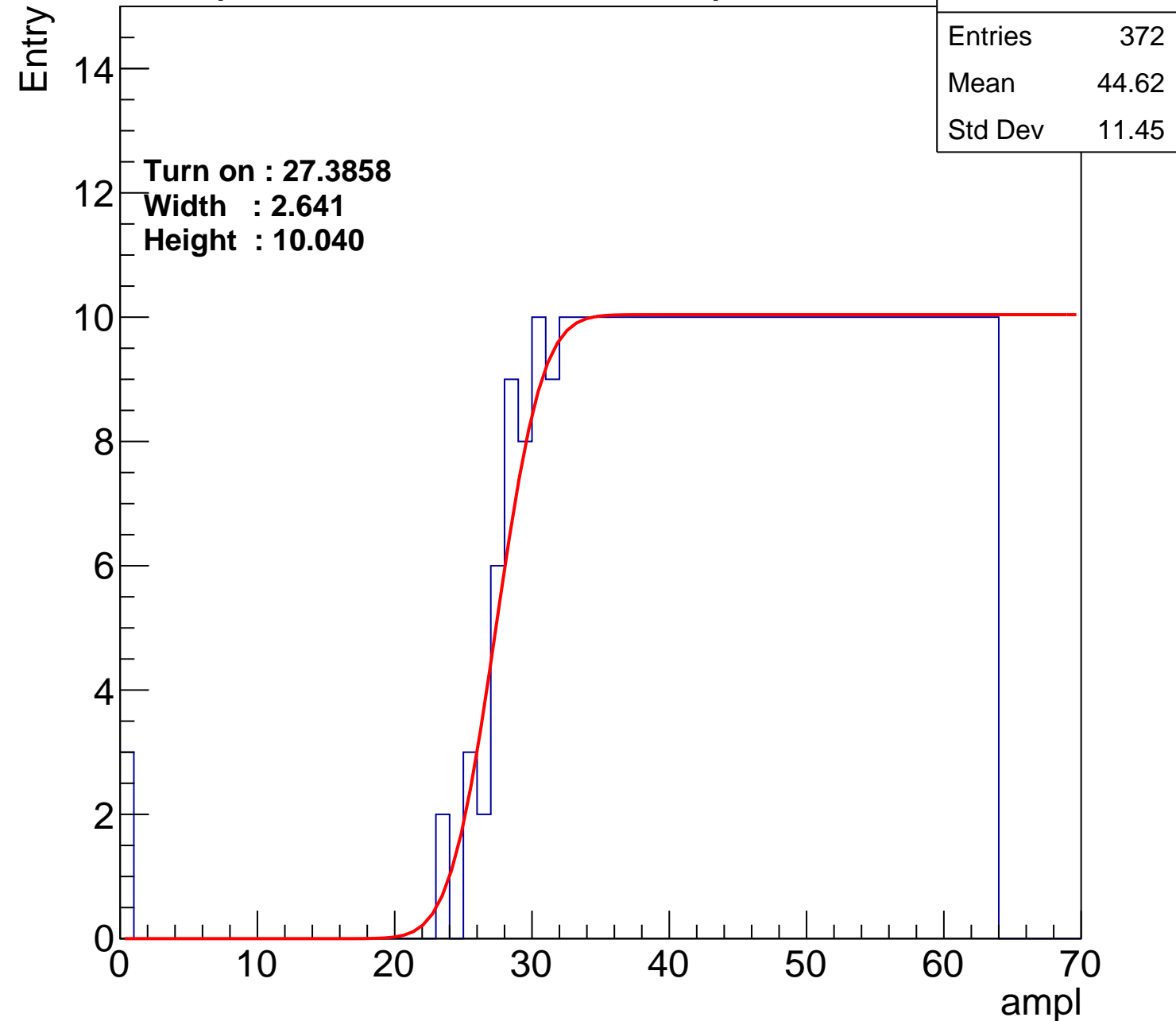
Width : 2.641

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.84
Std Dev	11.38

Turn on : 28.2655

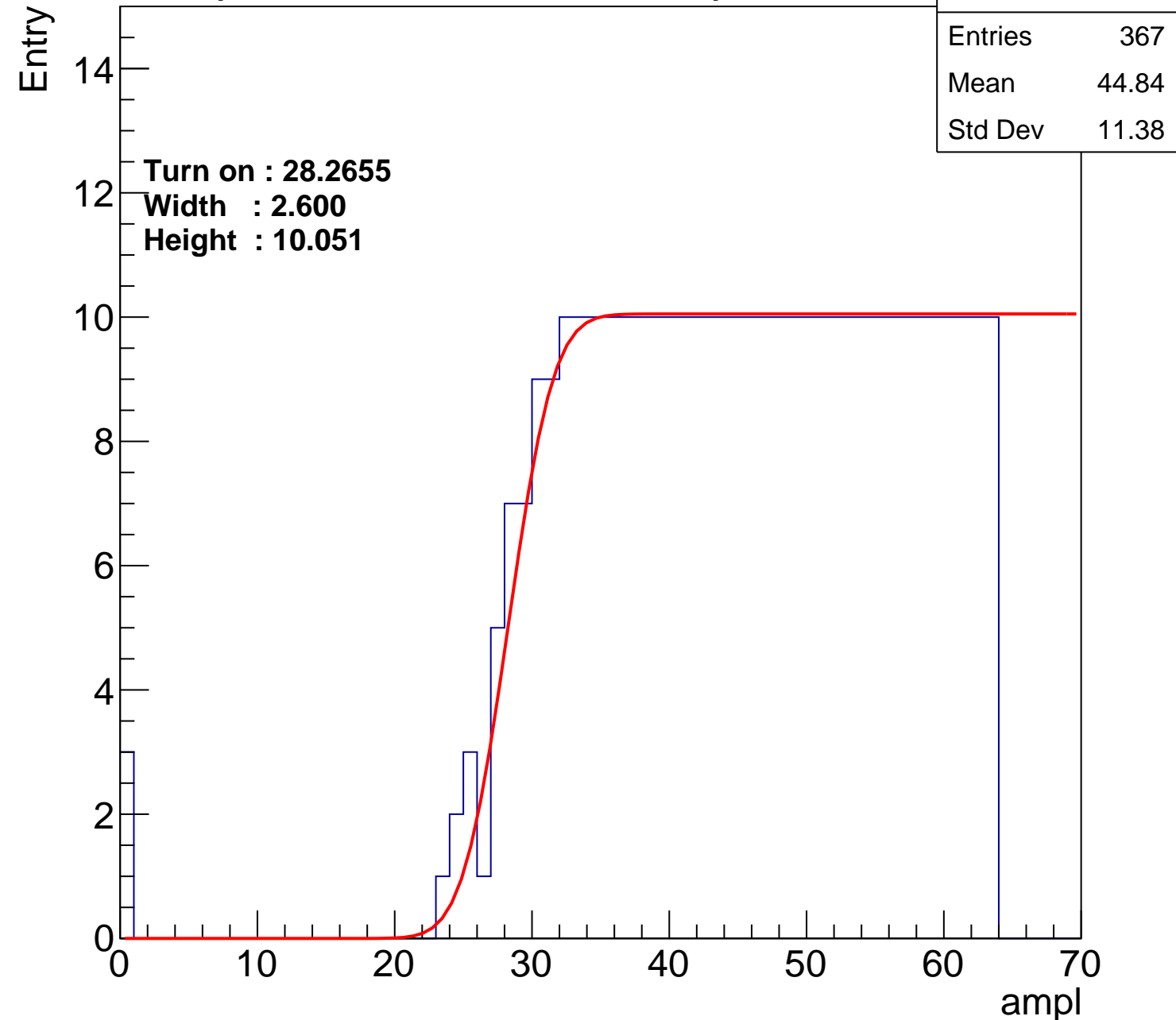
Width : 2.600

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.51
Std Dev	11.66

Turn on : 26.9941

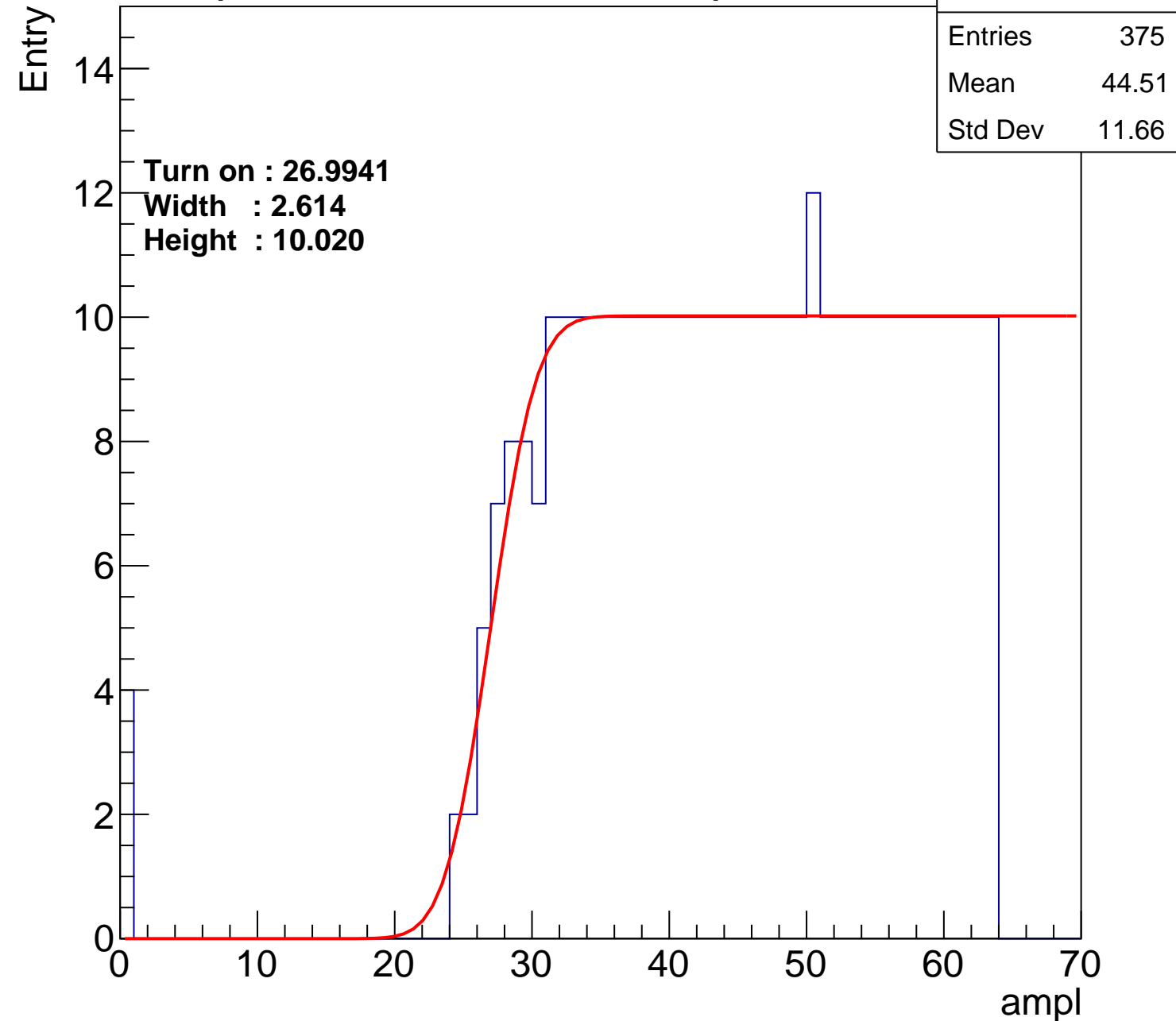
Width : 2.614

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.58
Std Dev	11.83

Turn on : 27.7991

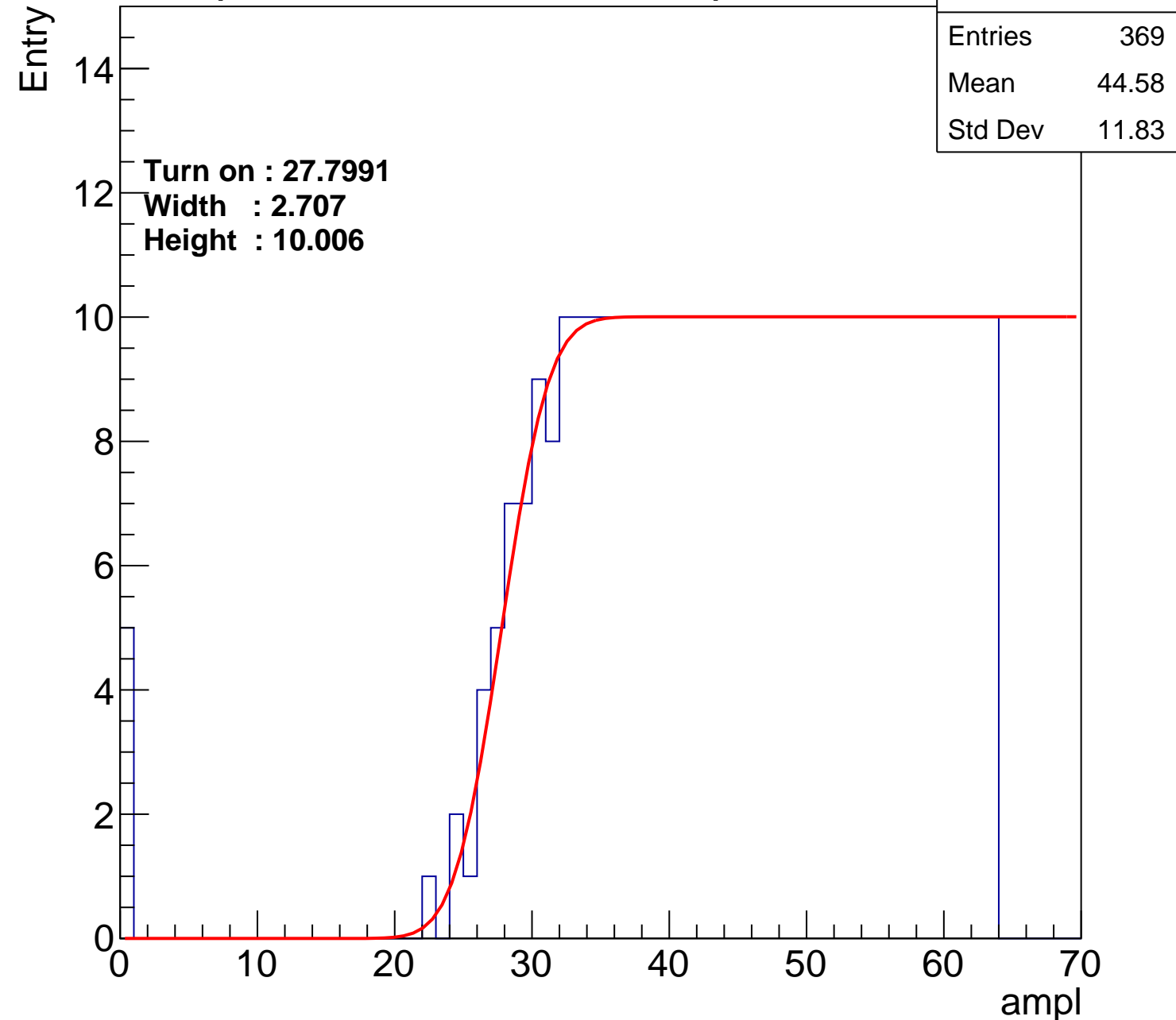
Width : 2.707

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch89

calib_packv5_042523_0143.root, FC#13, port D2

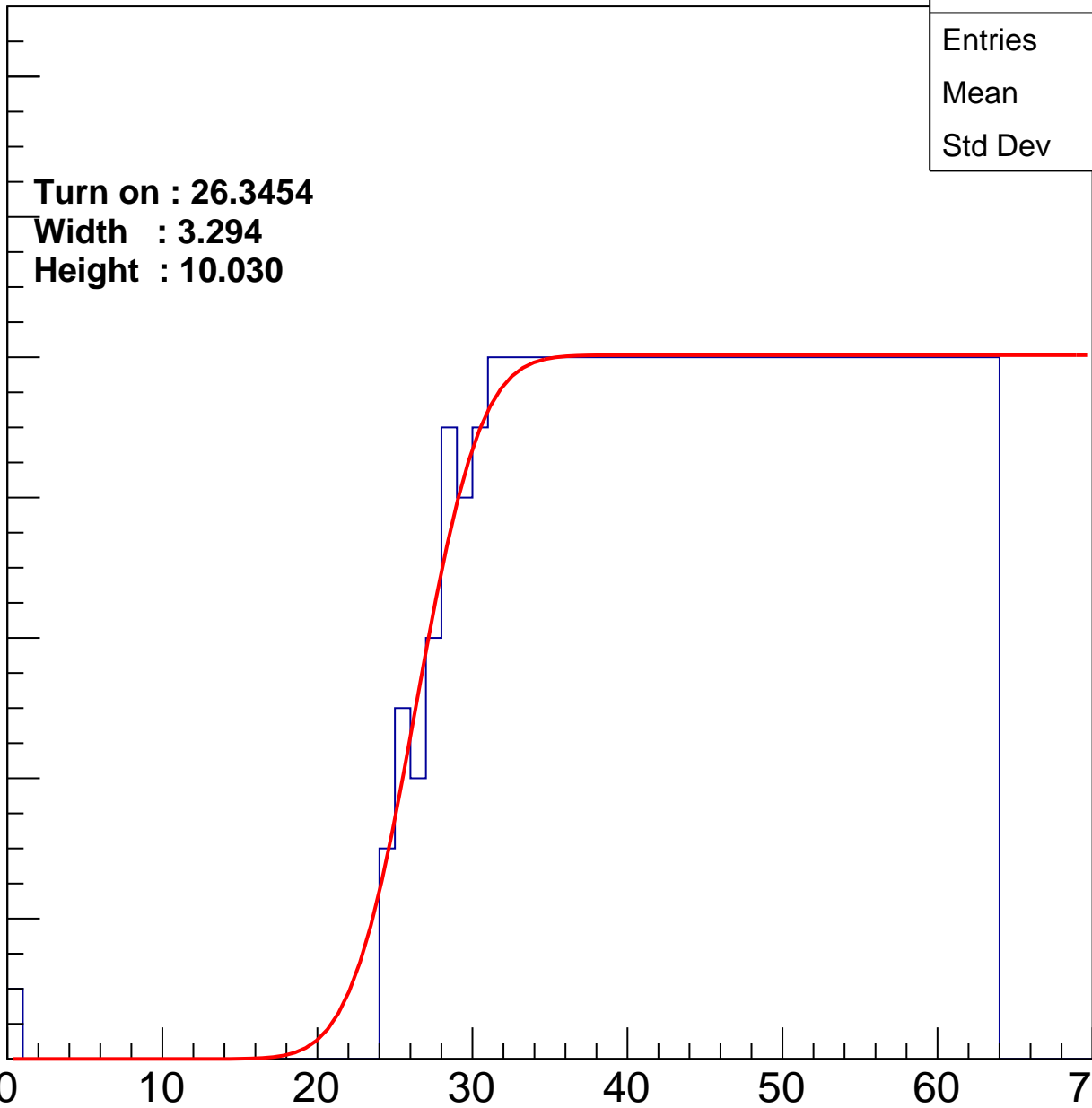
Entry

14
12
10
8
6
4
2
0

Turn on : 26.3454
Width : 3.294
Height : 10.030

Entries	375
Mean	44.61
Std Dev	11.15

ampl



B1L003S, U14-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.35
Std Dev	11.53

Turn on : 26.9498

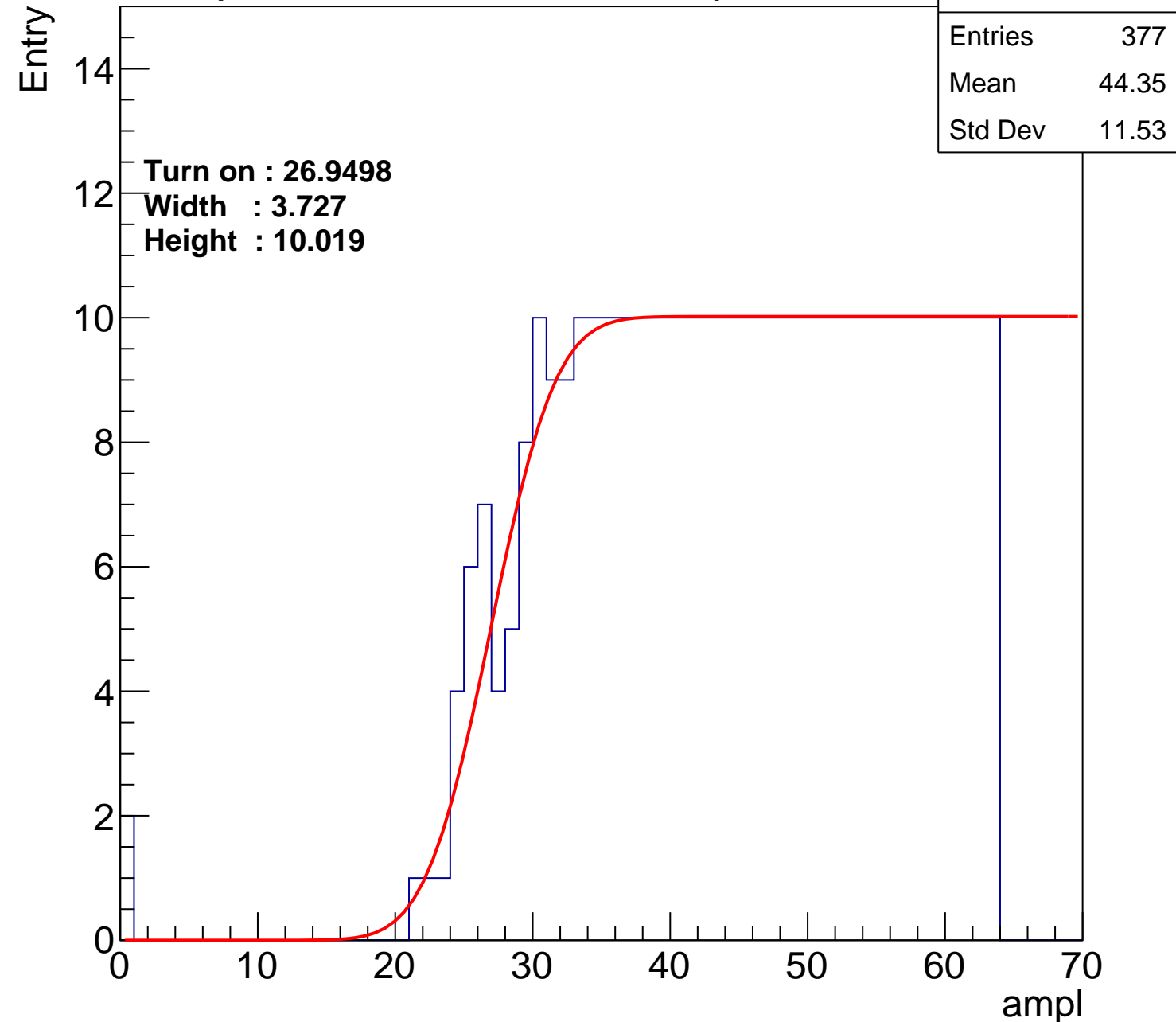
Width : 3.727

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch91

calib_packv5_042523_0143.root, FC#13, port D2

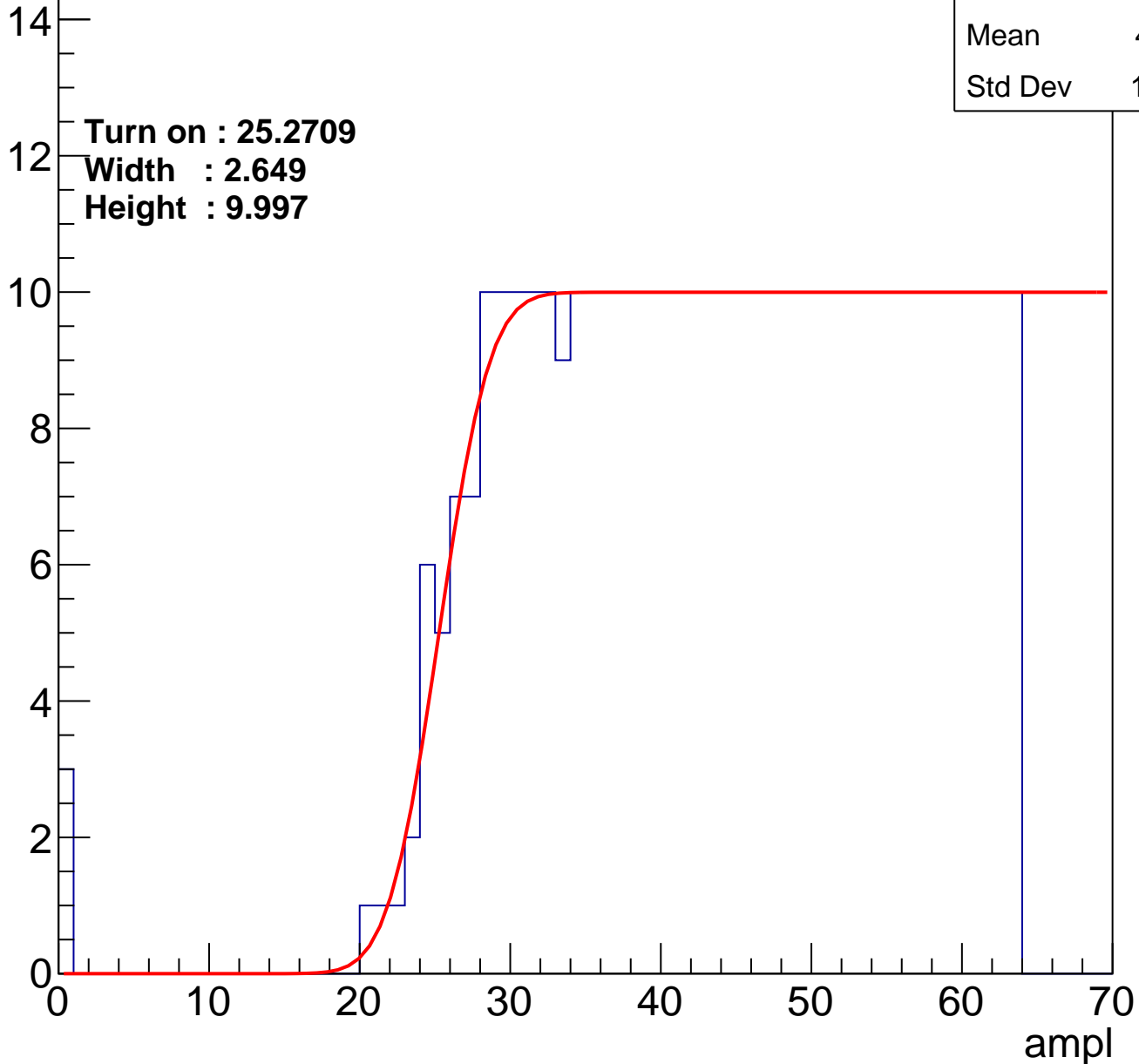
Entries	392
Mean	43.61
Std Dev	11.98

Turn on : 25.2709

Width : 2.649

Height : 9.997

Entry



B1L003S, U14-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.14
Std Dev	11.64

Turn on : 29.2117

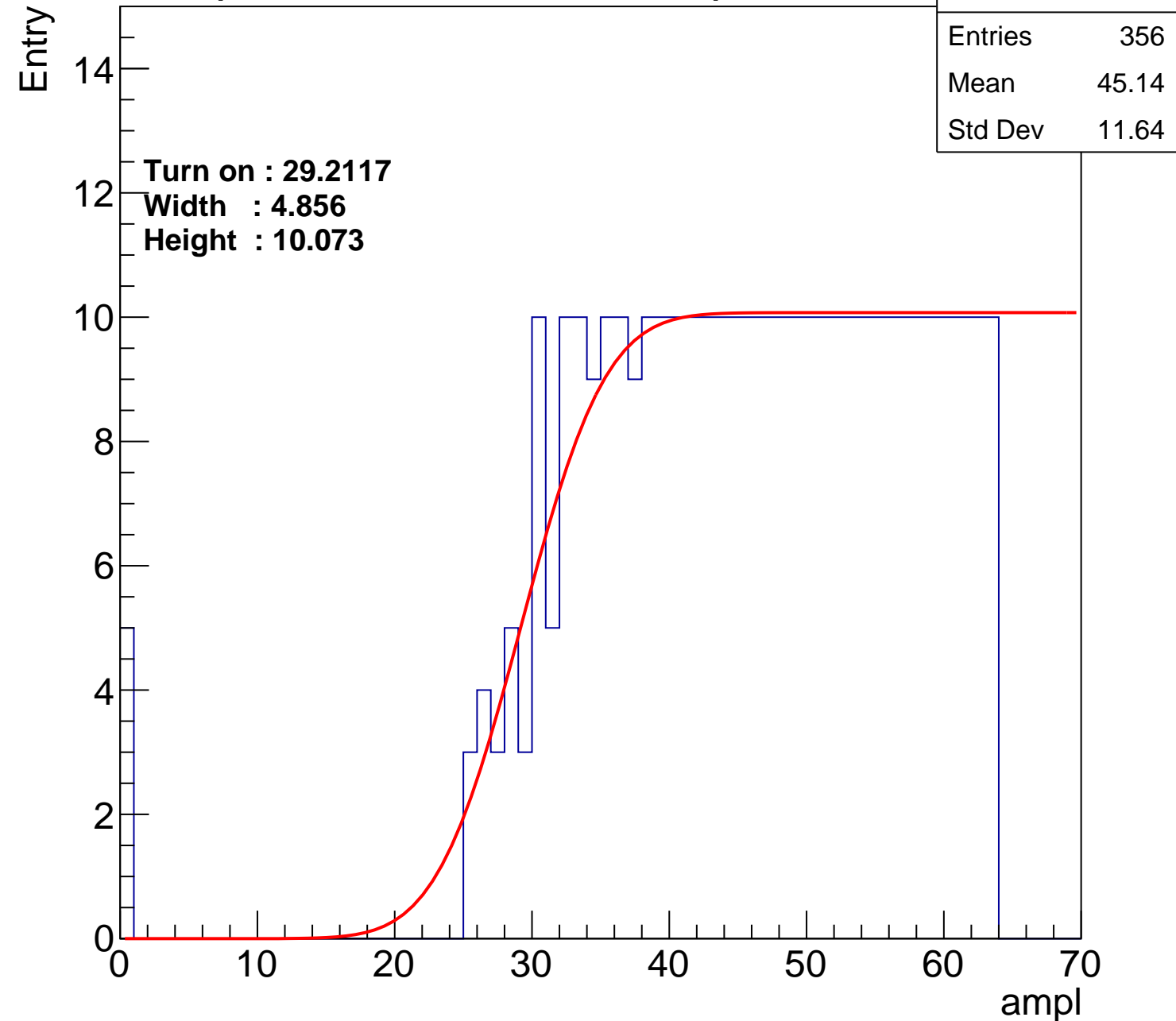
Width : 4.856

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.97
Std Dev	11.33

Turn on : 28.3781

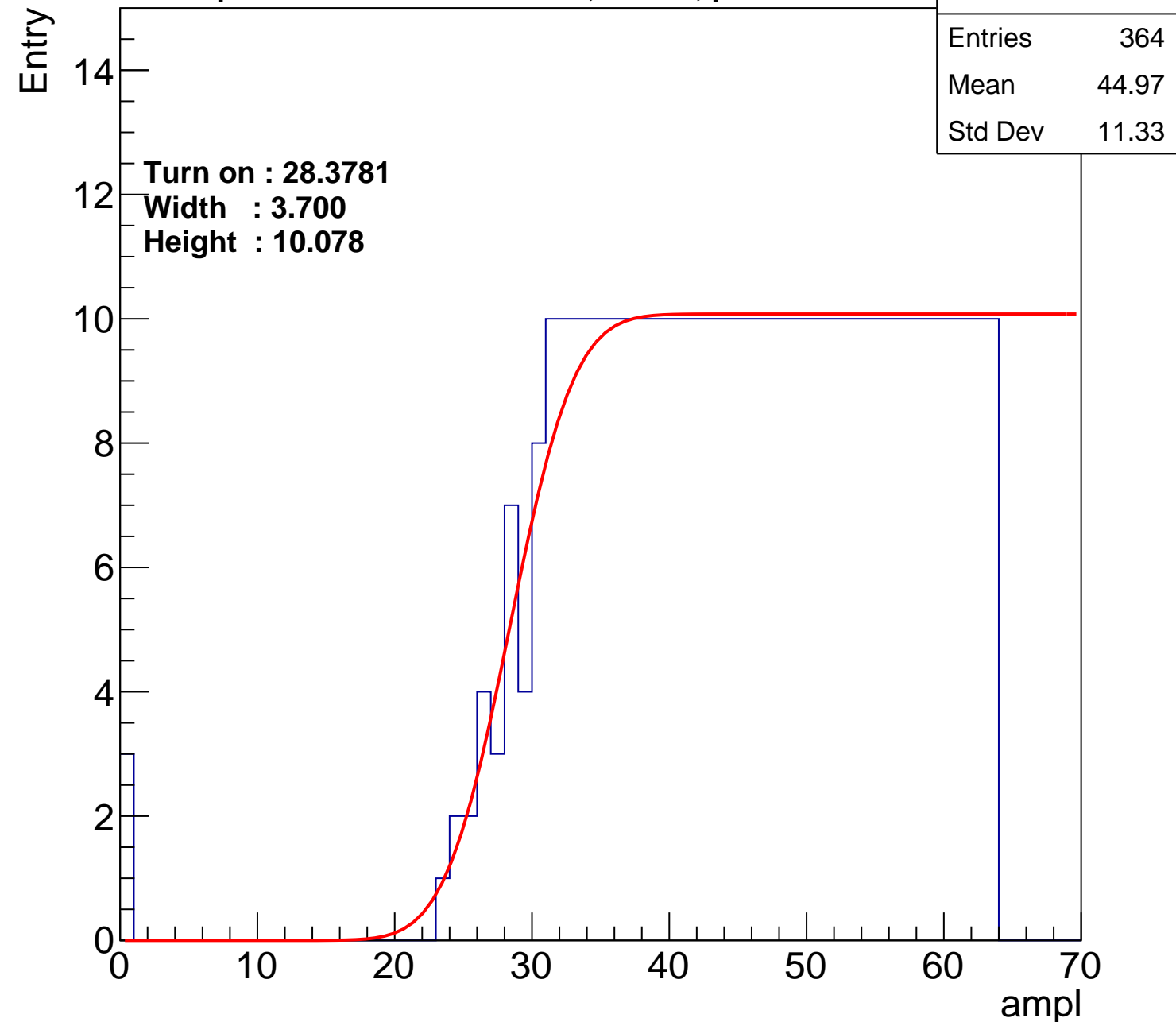
Width : 3.700

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch94

calib_packv5_042523_0143.root, FC#13, port D2

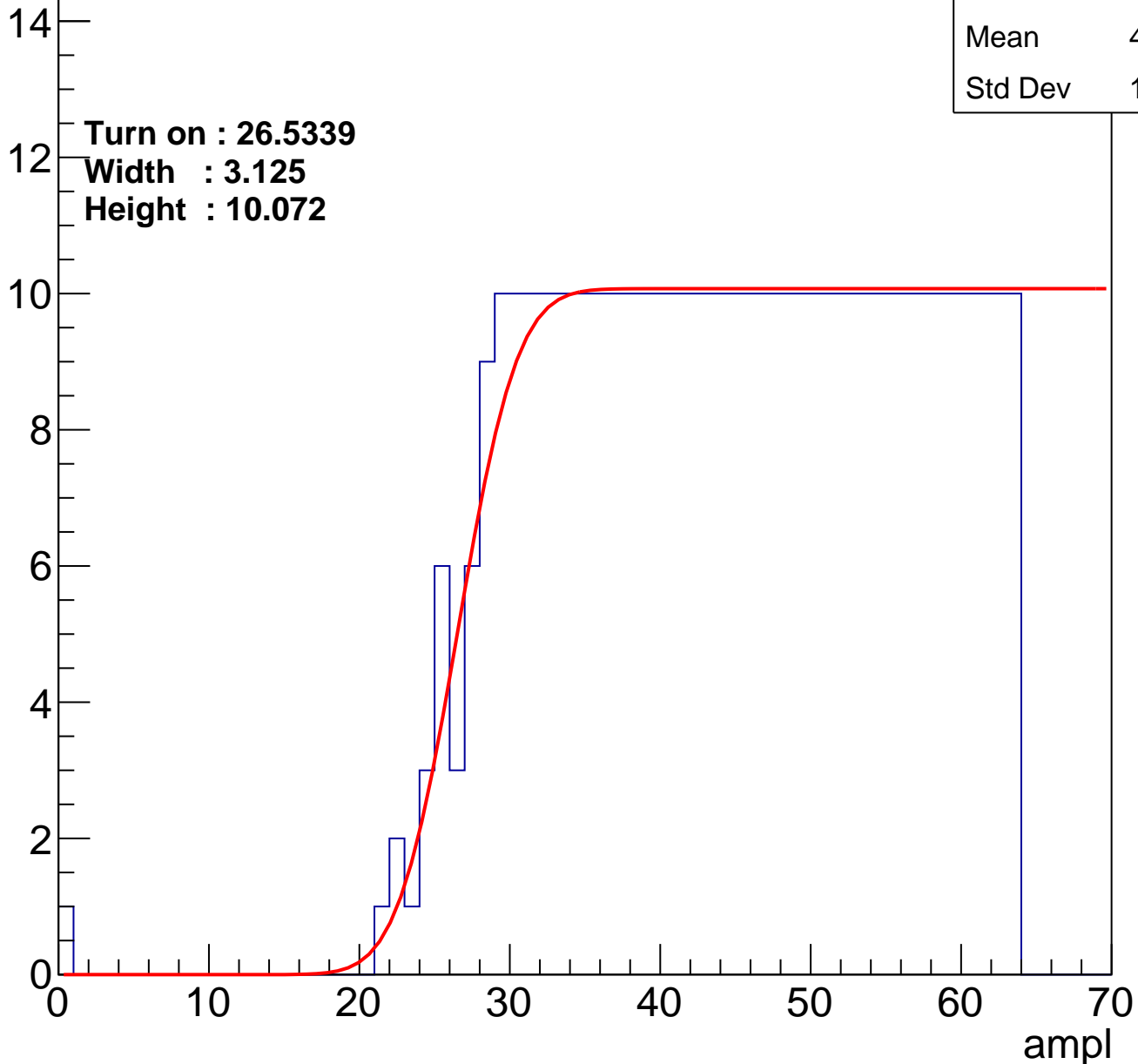
Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.5339

Width : 3.125

Height : 10.072

Entry



B1L003S, U14-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	43.68
Std Dev	12.66

Turn on : 26.3321

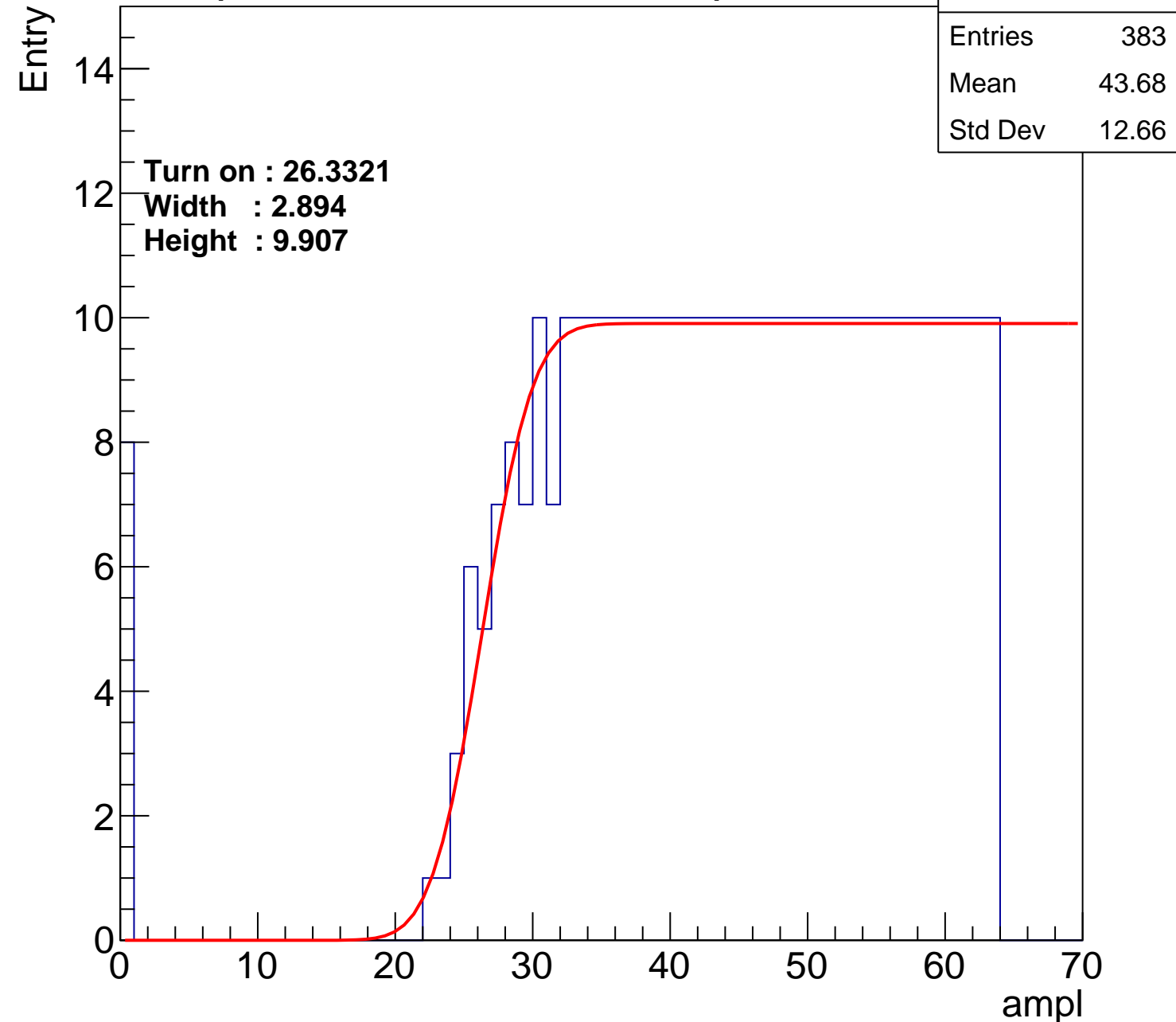
Width : 2.894

Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.6992
Width : 2.902
Height : 10.049



B1L003S, U14-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.6065

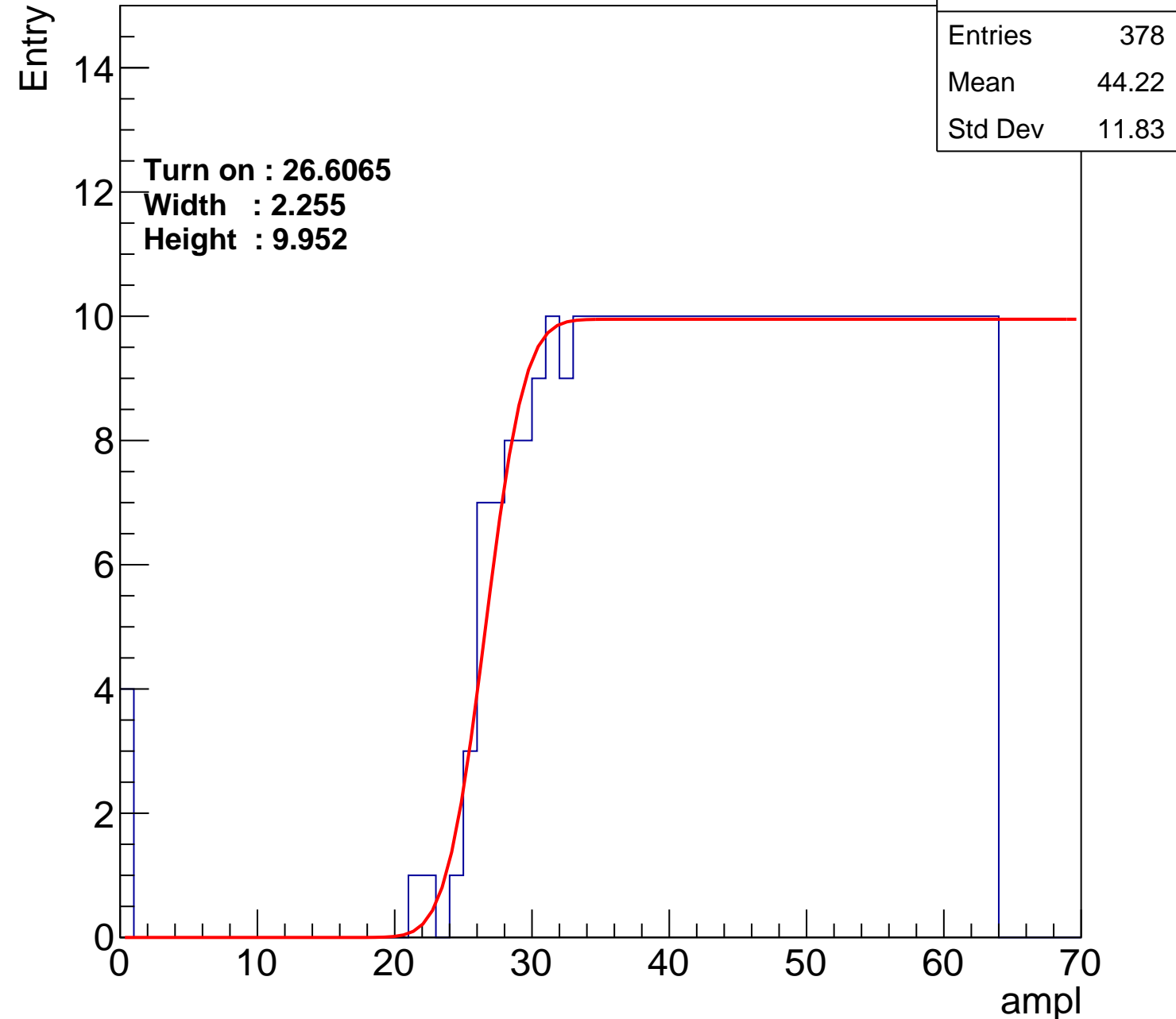
Width : 2.255

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.38
Std Dev	11.31

Turn on : 29.2284

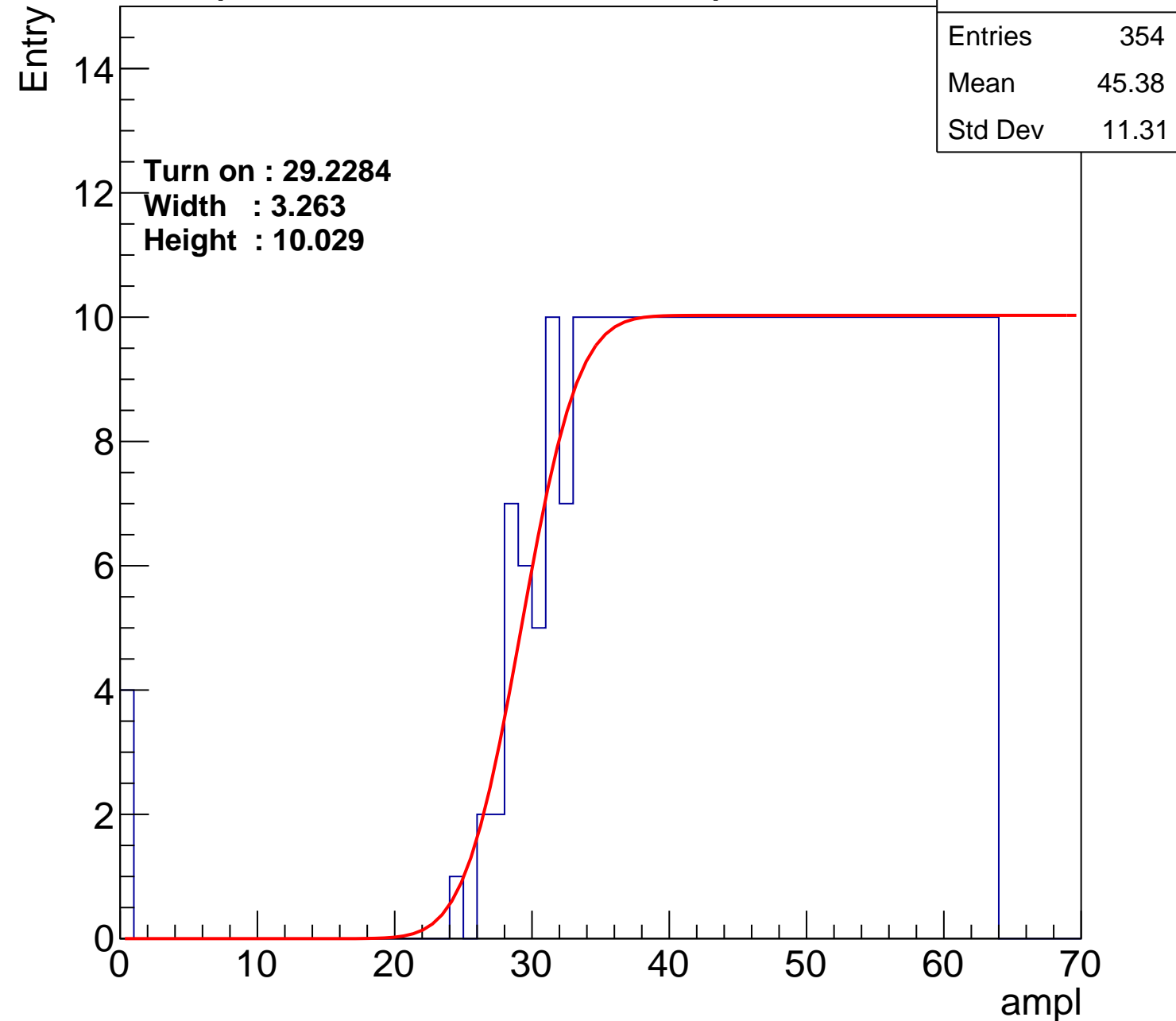
Width : 3.263

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch99

calib_packv5_042523_0143.root, FC#13, port D2

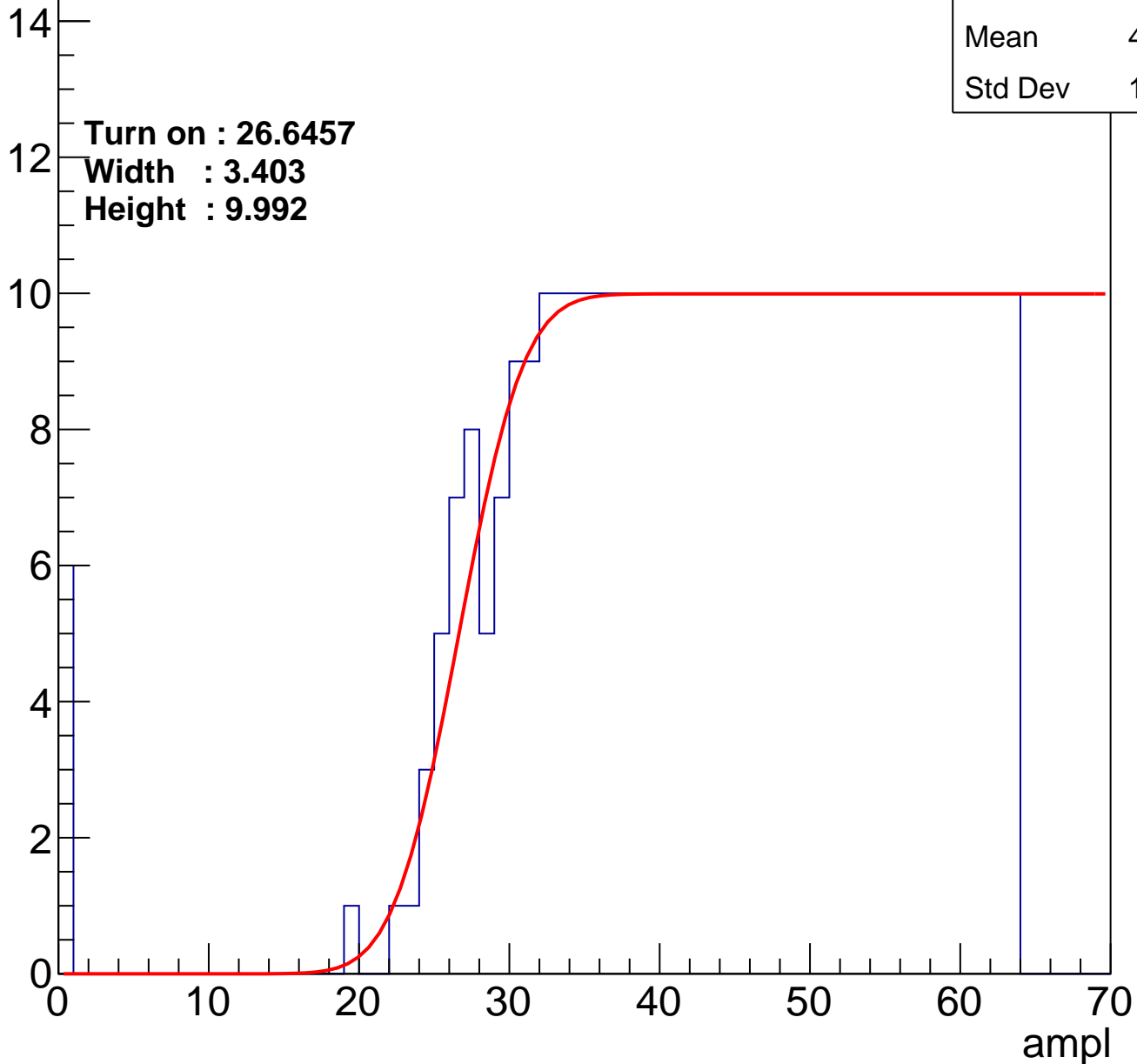
Entries	382
Mean	43.85
Std Dev	12.33

Turn on : 26.6457

Width : 3.403

Height : 9.992

Entry



B1L003S, U14-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.06
Std Dev	12.11

Turn on : 26.3606

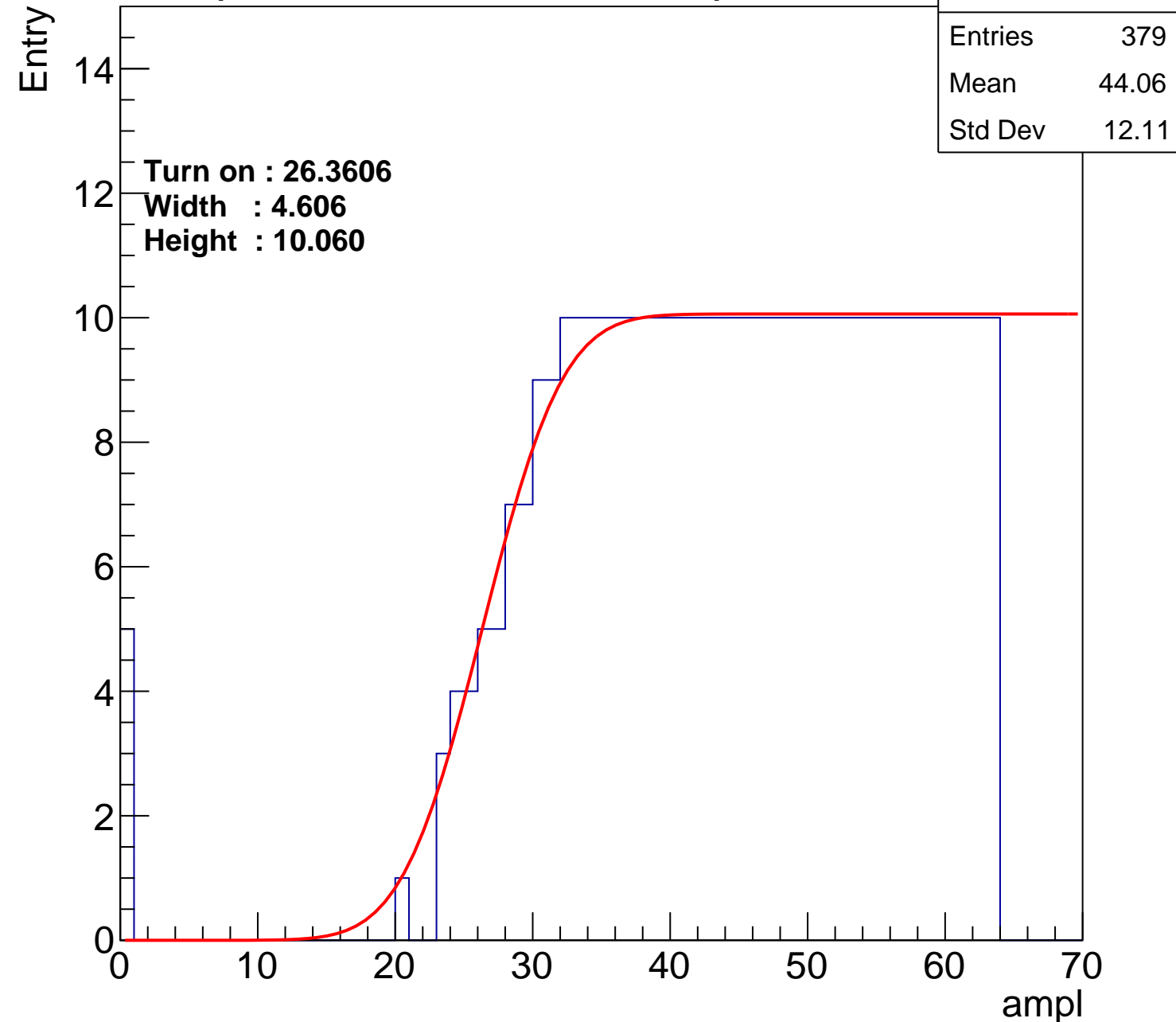
Width : 4.606

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch101

calib_packv5_042523_0143.root, FC#13, port D2

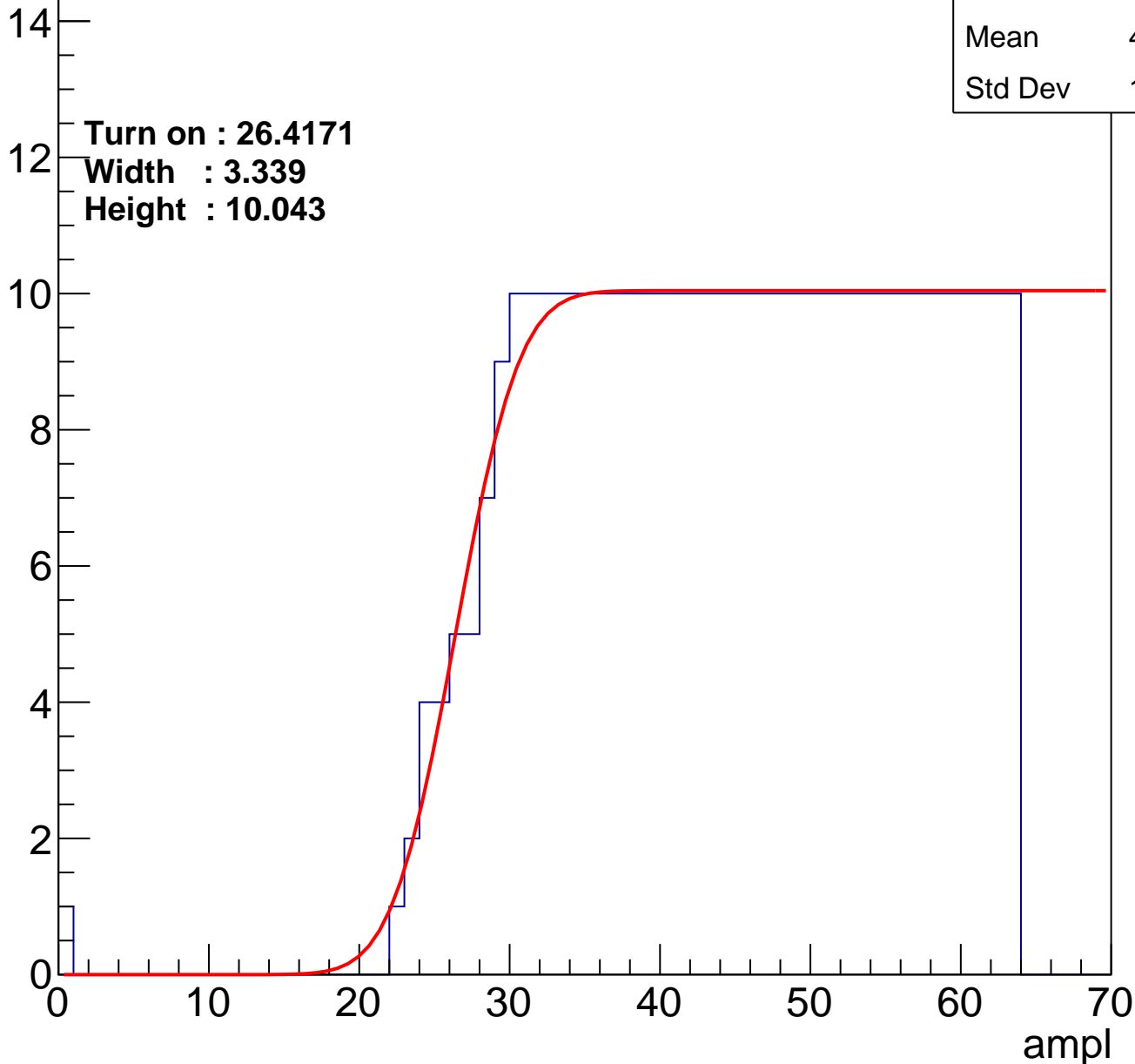
Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.4171

Width : 3.339

Height : 10.043

Entry



B1L003S, U14-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.68
Std Dev	11.97

Turn on : 28.3082

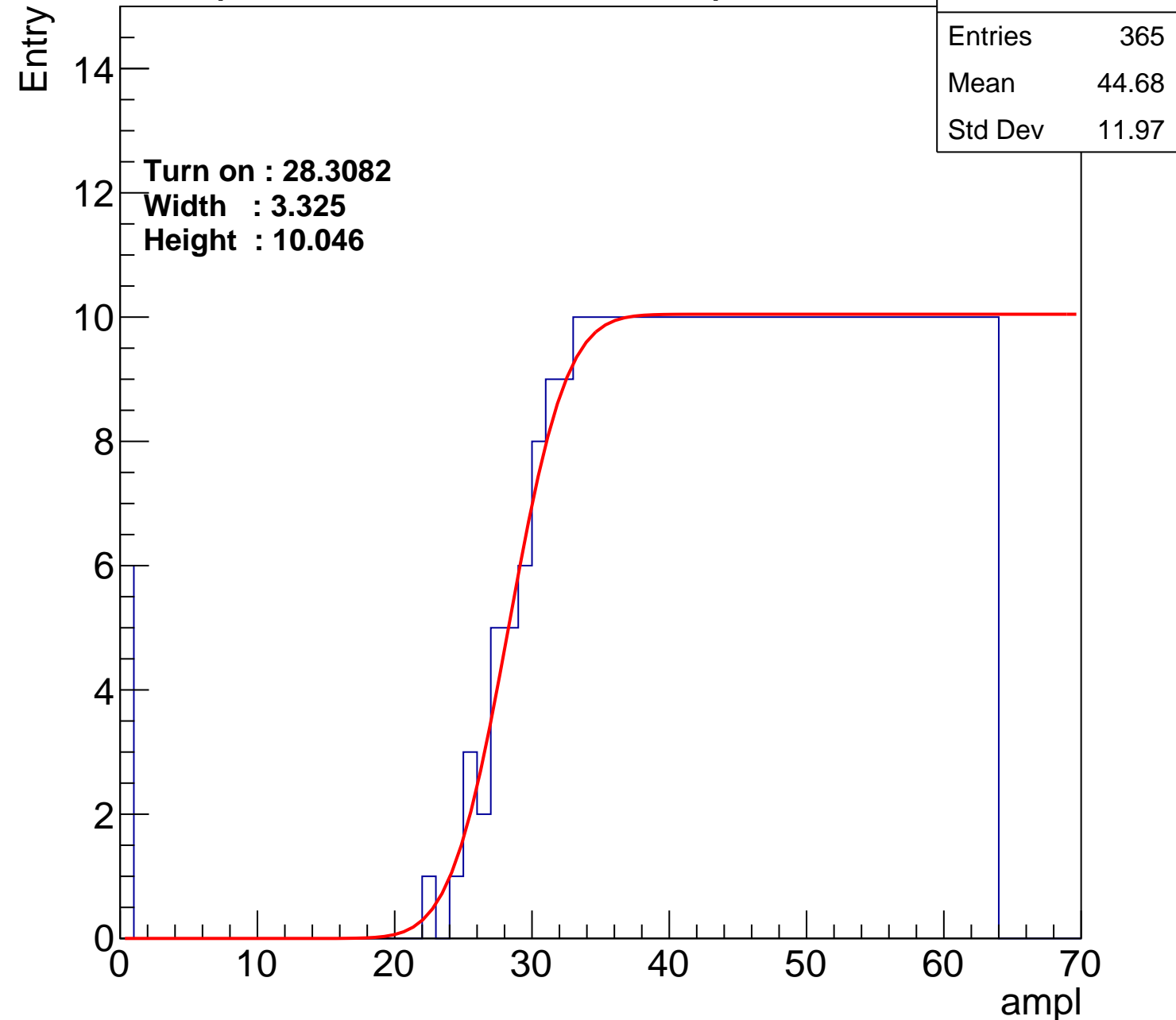
Width : 3.325

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch103

calib_packv5_042523_0143.root, FC#13, port D2

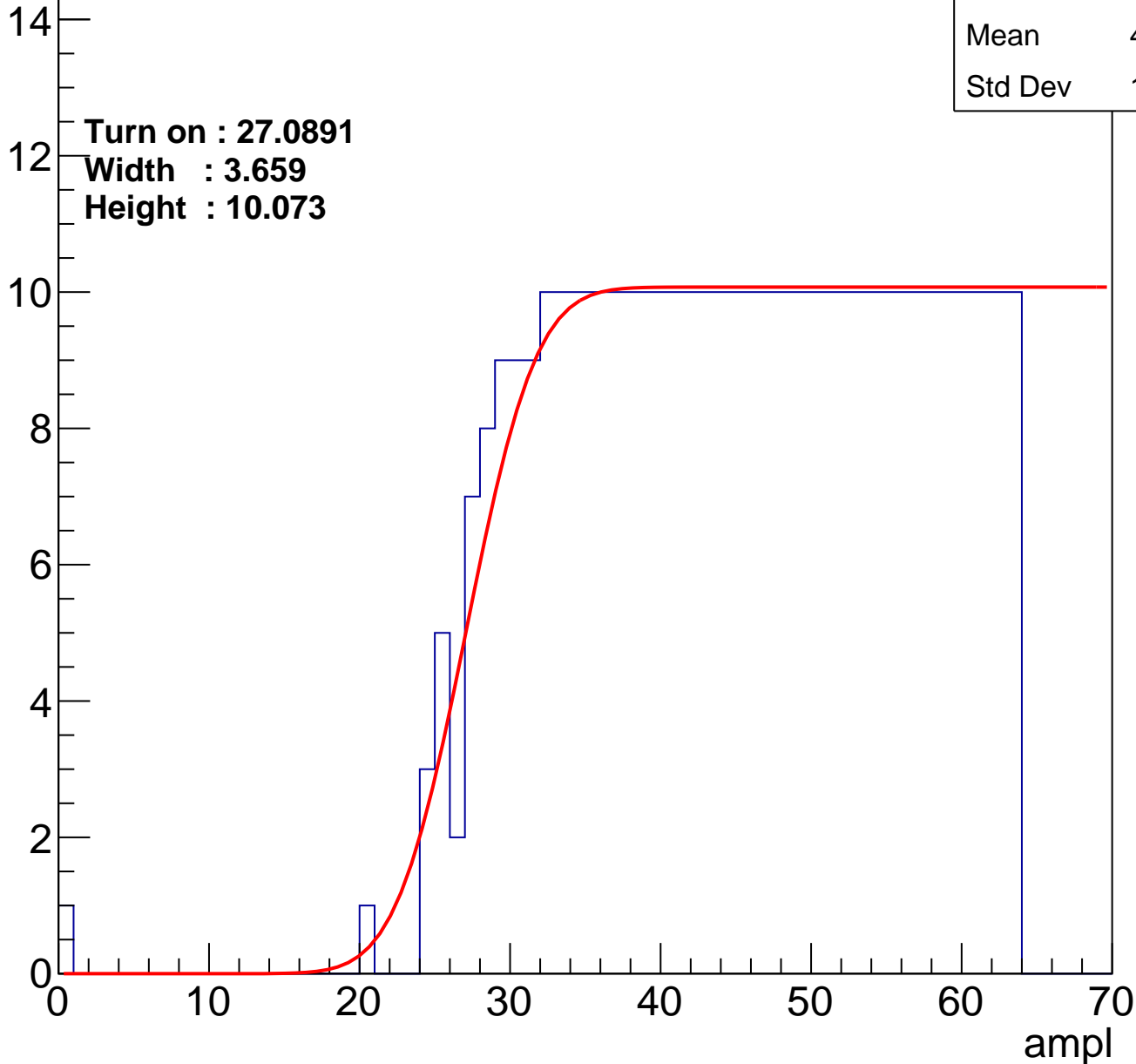
Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 27.0891

Width : 3.659

Height : 10.073

Entry



B1L003S, U14-ch104

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 29.0503

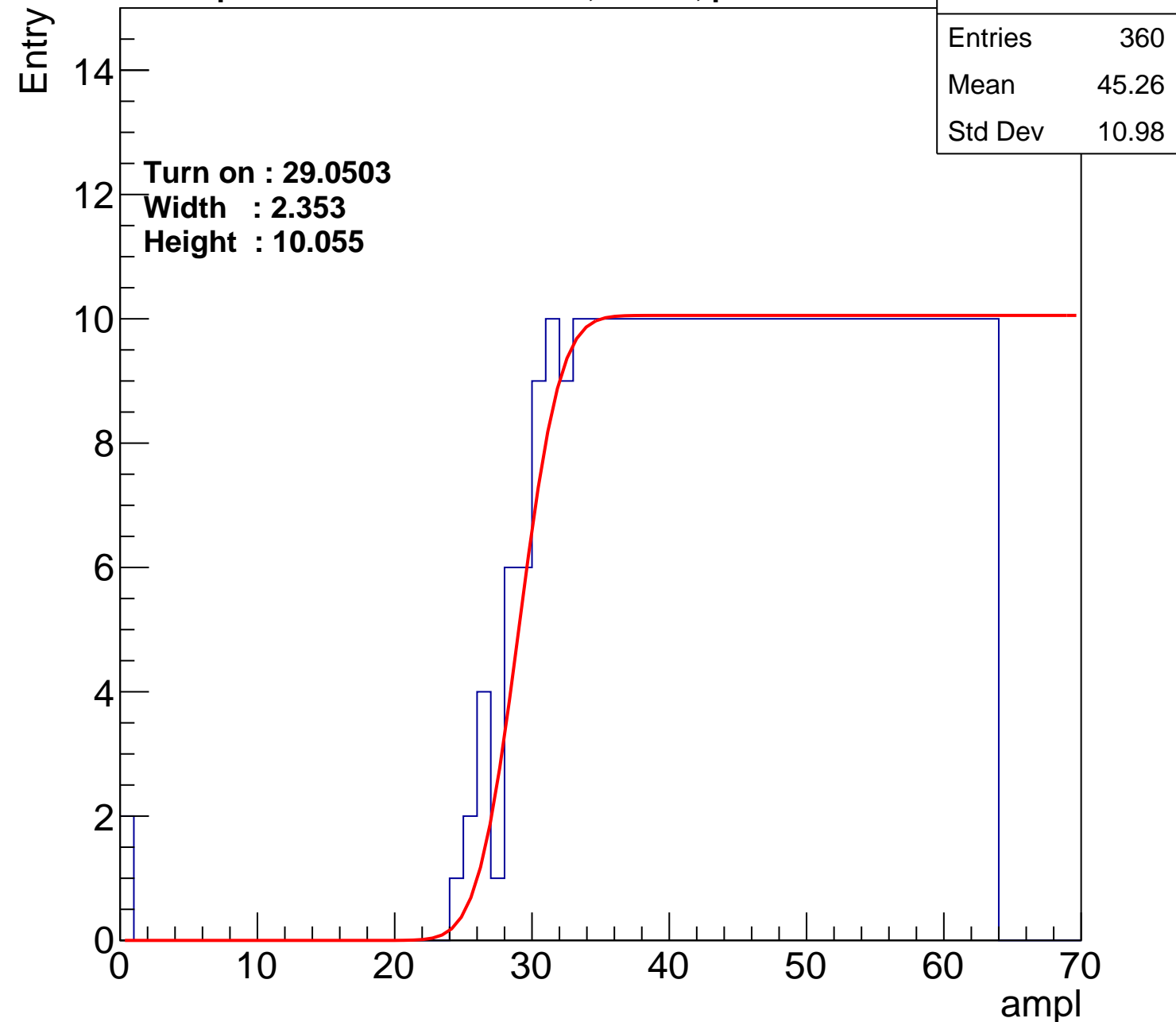
Width : 2.353

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch105

calib_packv5_042523_0143.root, FC#13, port D2

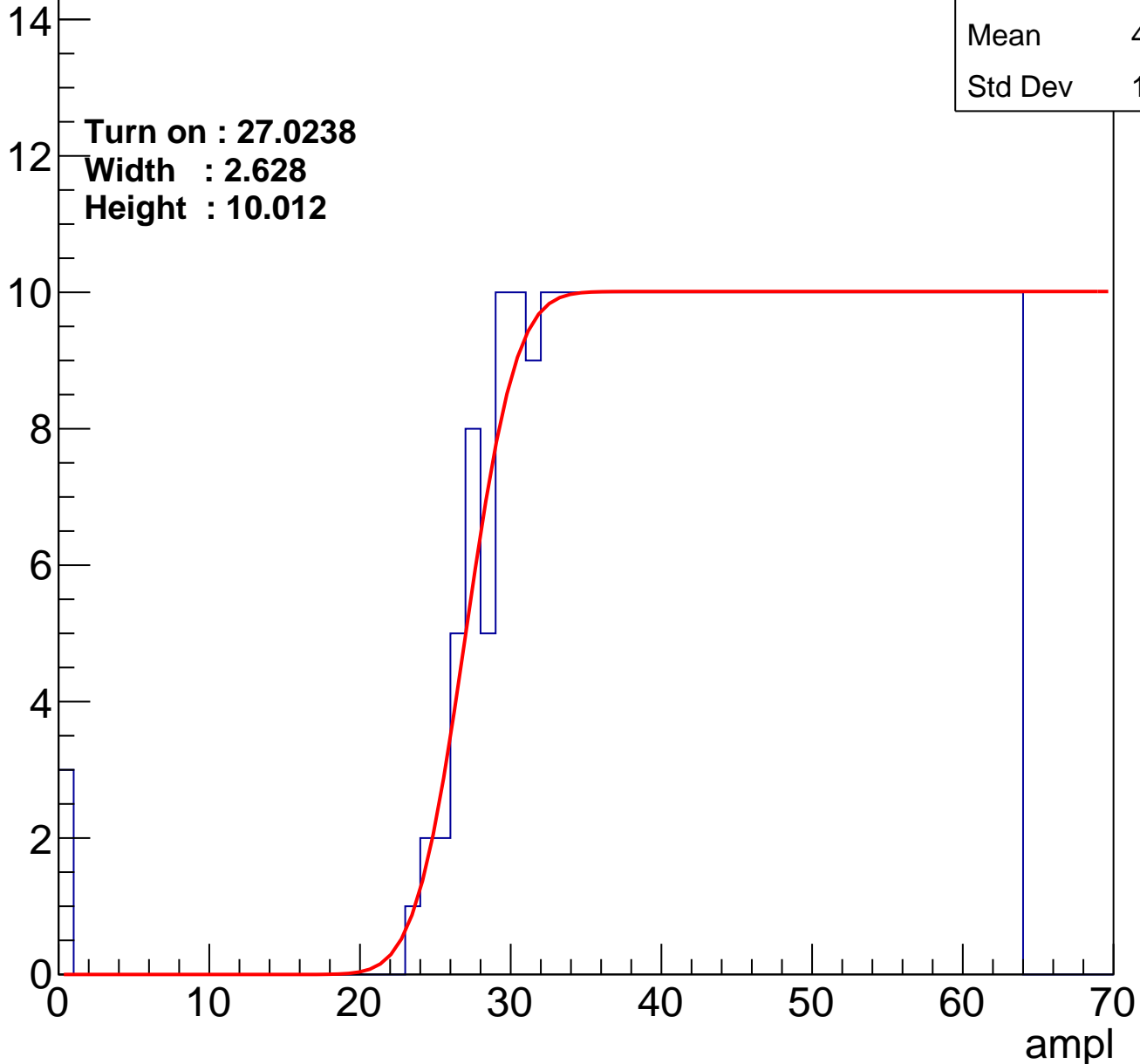
Entries	375
Mean	44.47
Std Dev	11.53

Turn on : 27.0238

Width : 2.628

Height : 10.012

Entry



B1L003S, U14-ch106

calib_packv5_042523_0143.root, FC#13, port D2

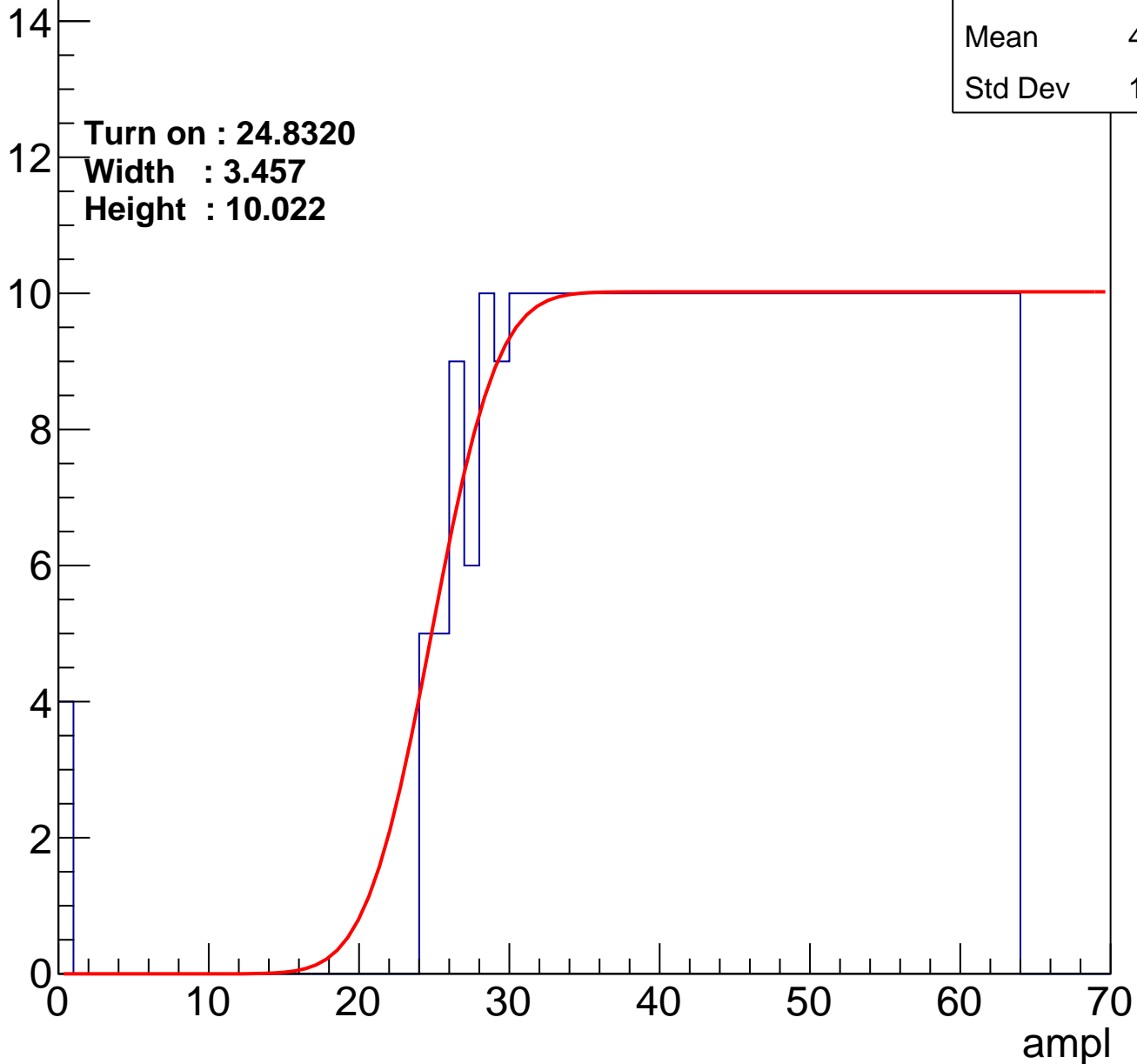
Entries	388
Mean	43.79
Std Dev	11.97

Turn on : 24.8320

Width : 3.457

Height : 10.022

Entry



B1L003S, U14-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.98
Std Dev	10.9

Turn on : 26.3457

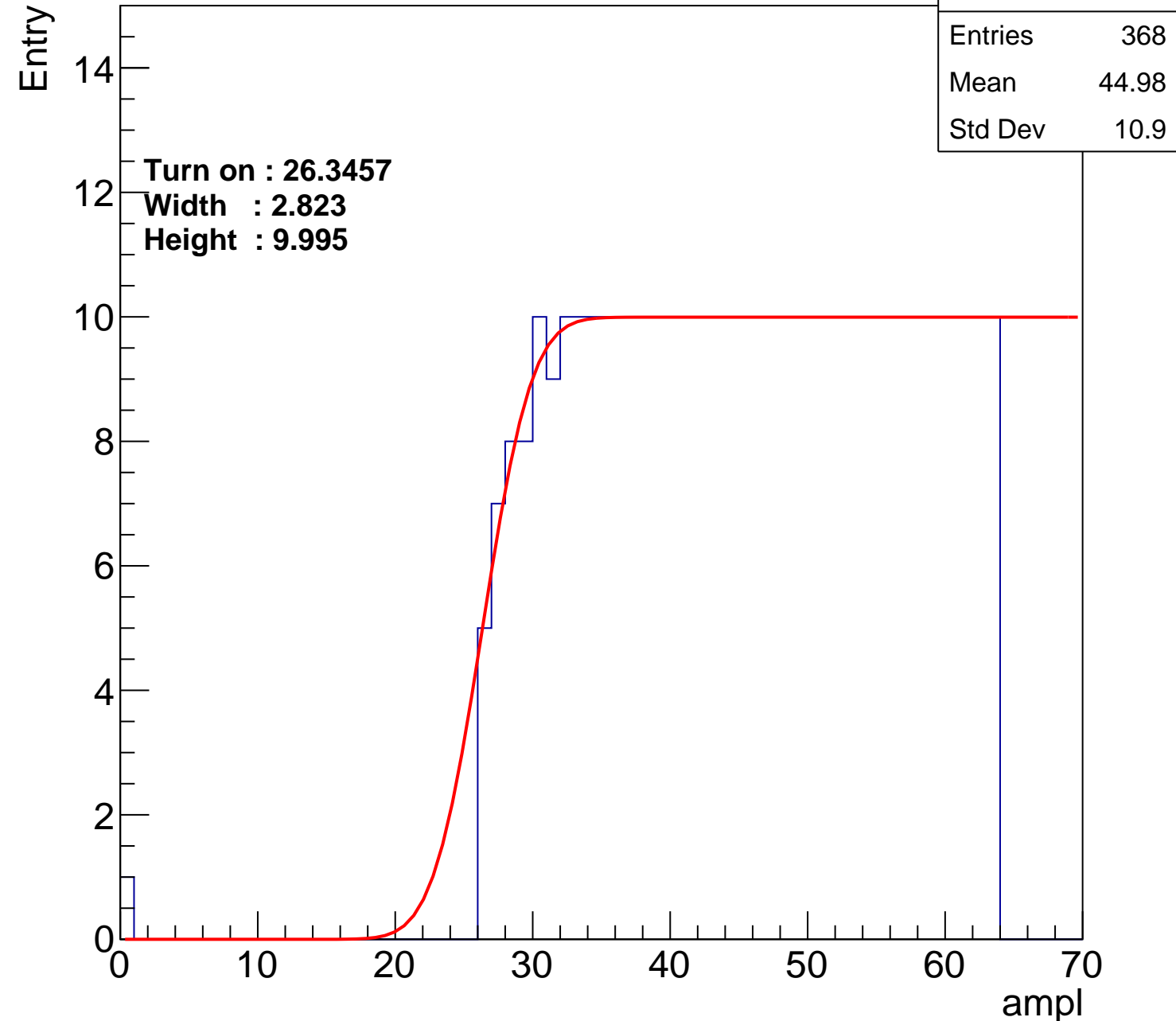
Width : 2.823

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 26.7667

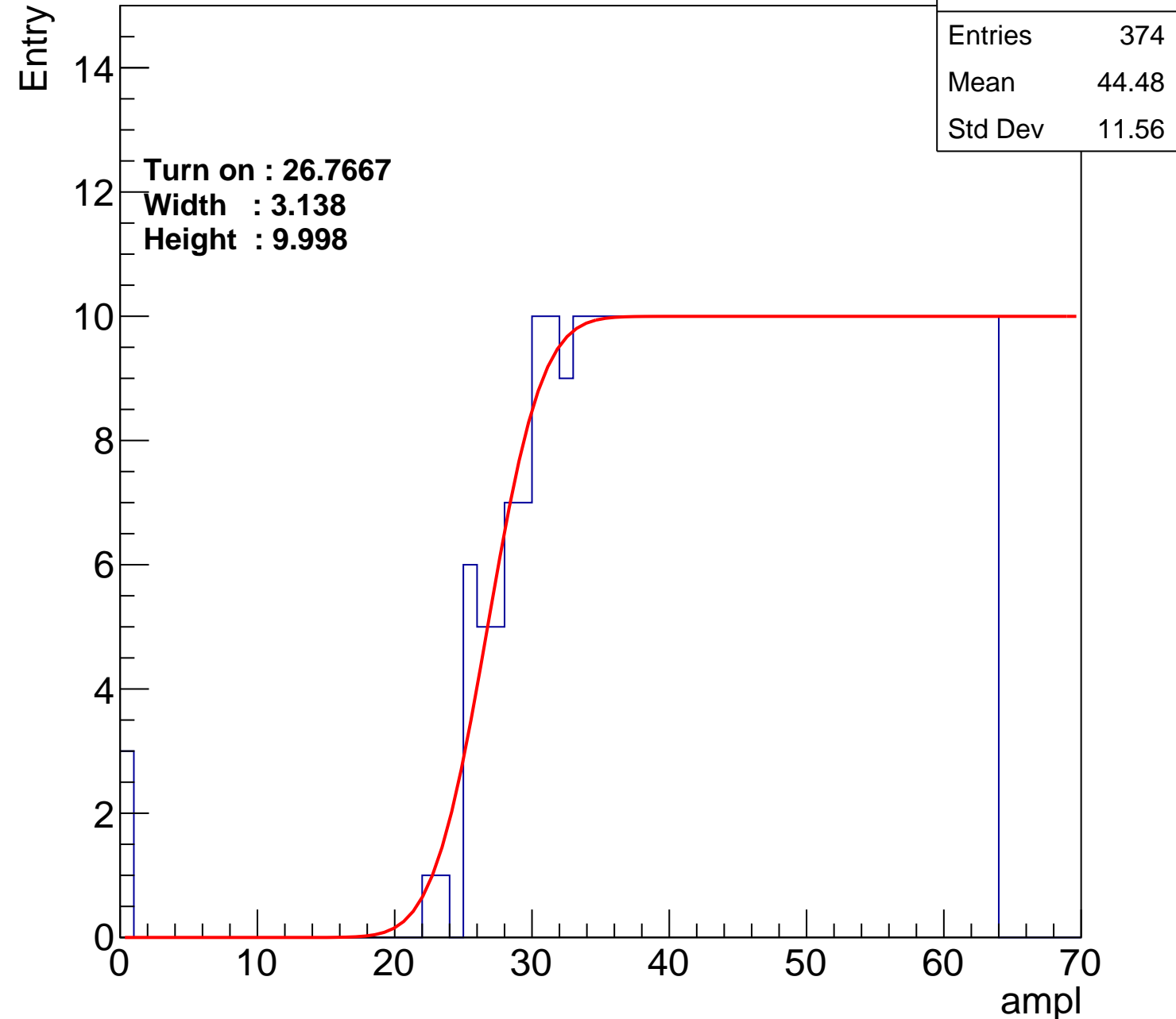
Width : 3.138

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch109

calib_packv5_042523_0143.root, FC#13, port D2

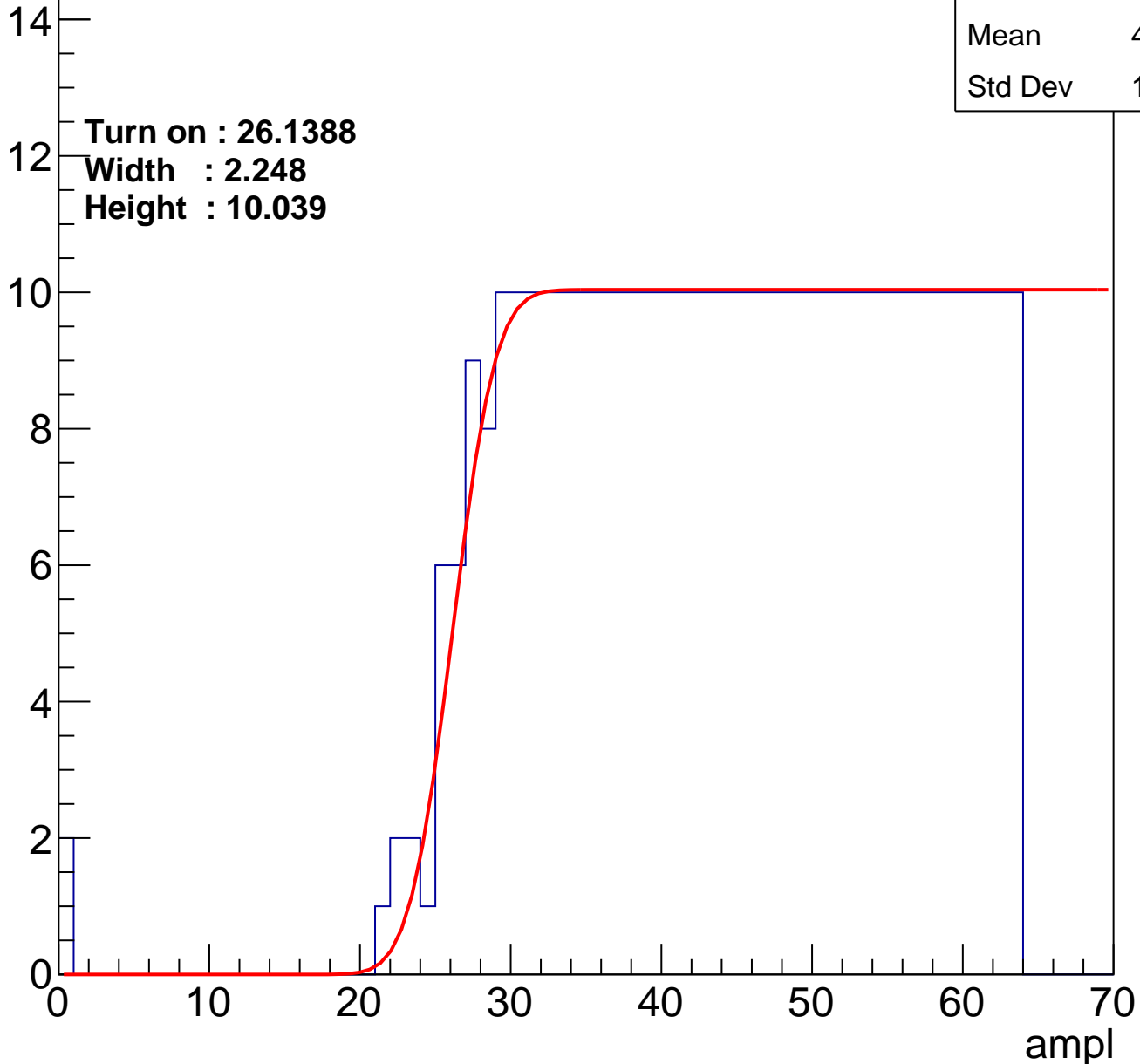
Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 26.1388

Width : 2.248

Height : 10.039

Entry



B1L003S, U14-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.43
Std Dev	12.61

Turn on : 25.7913

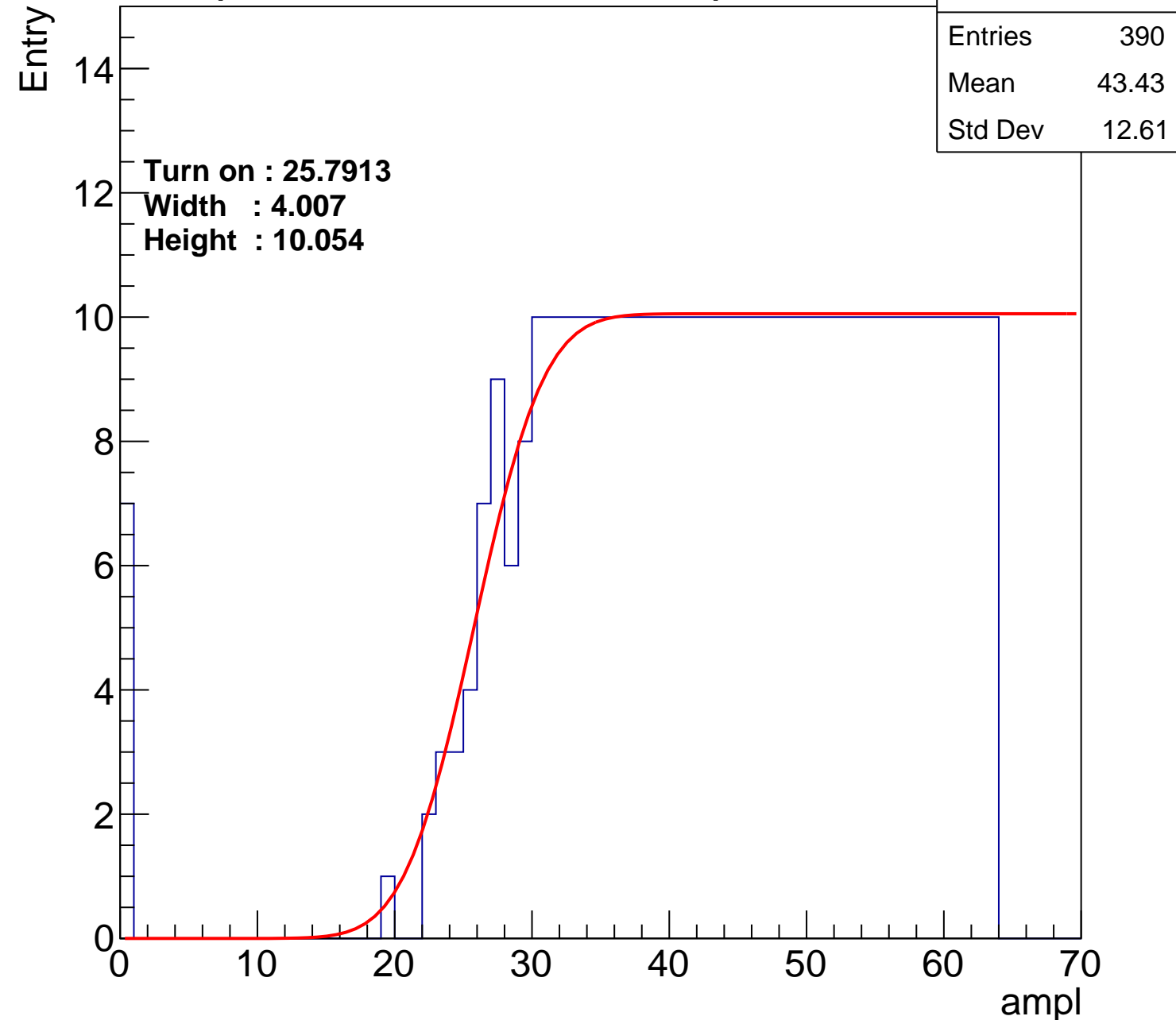
Width : 4.007

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.42
Std Dev	11.97

Turn on : 27.4314

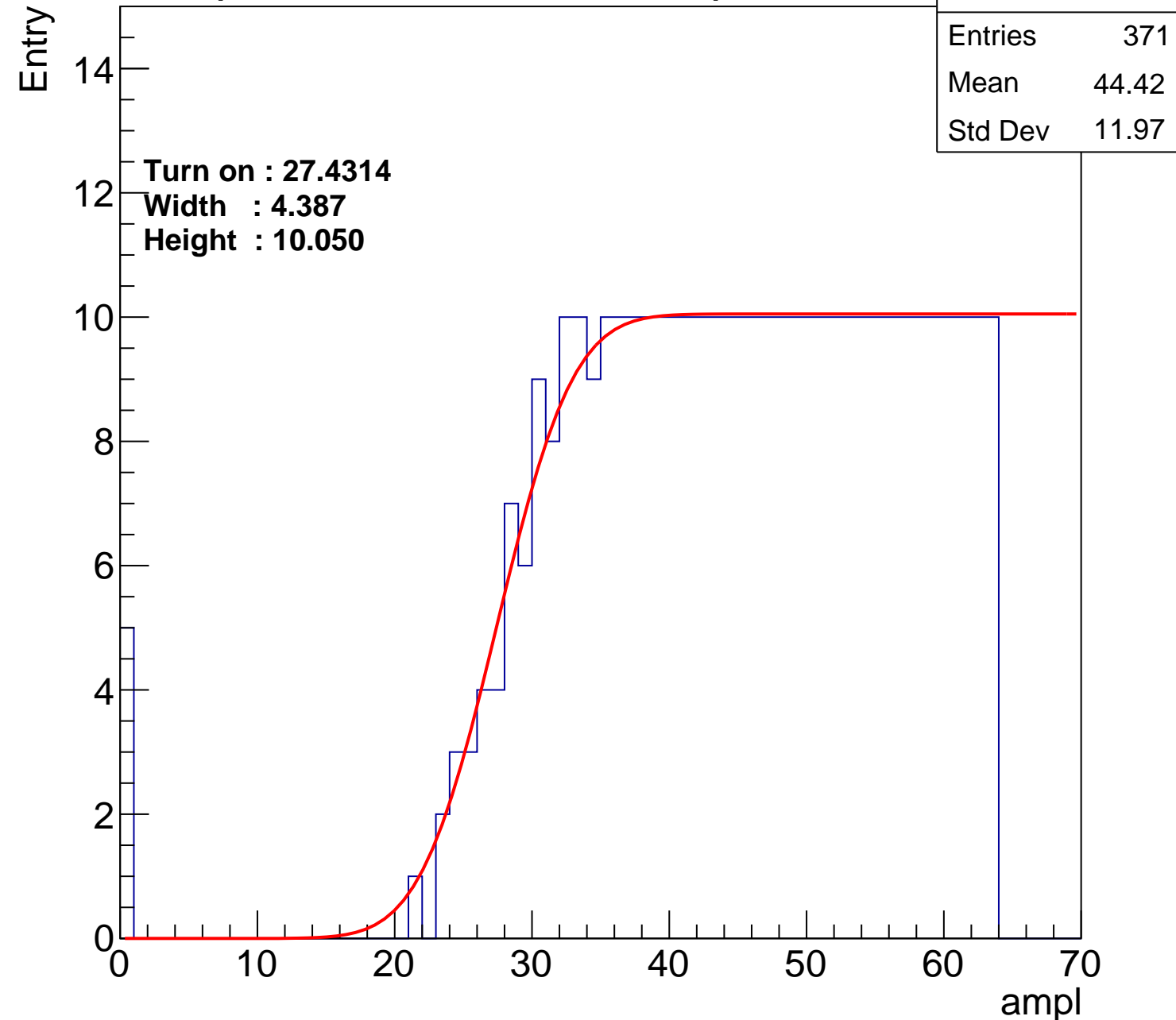
Width : 4.387

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.39
Std Dev	11.65

Turn on : 27.1287

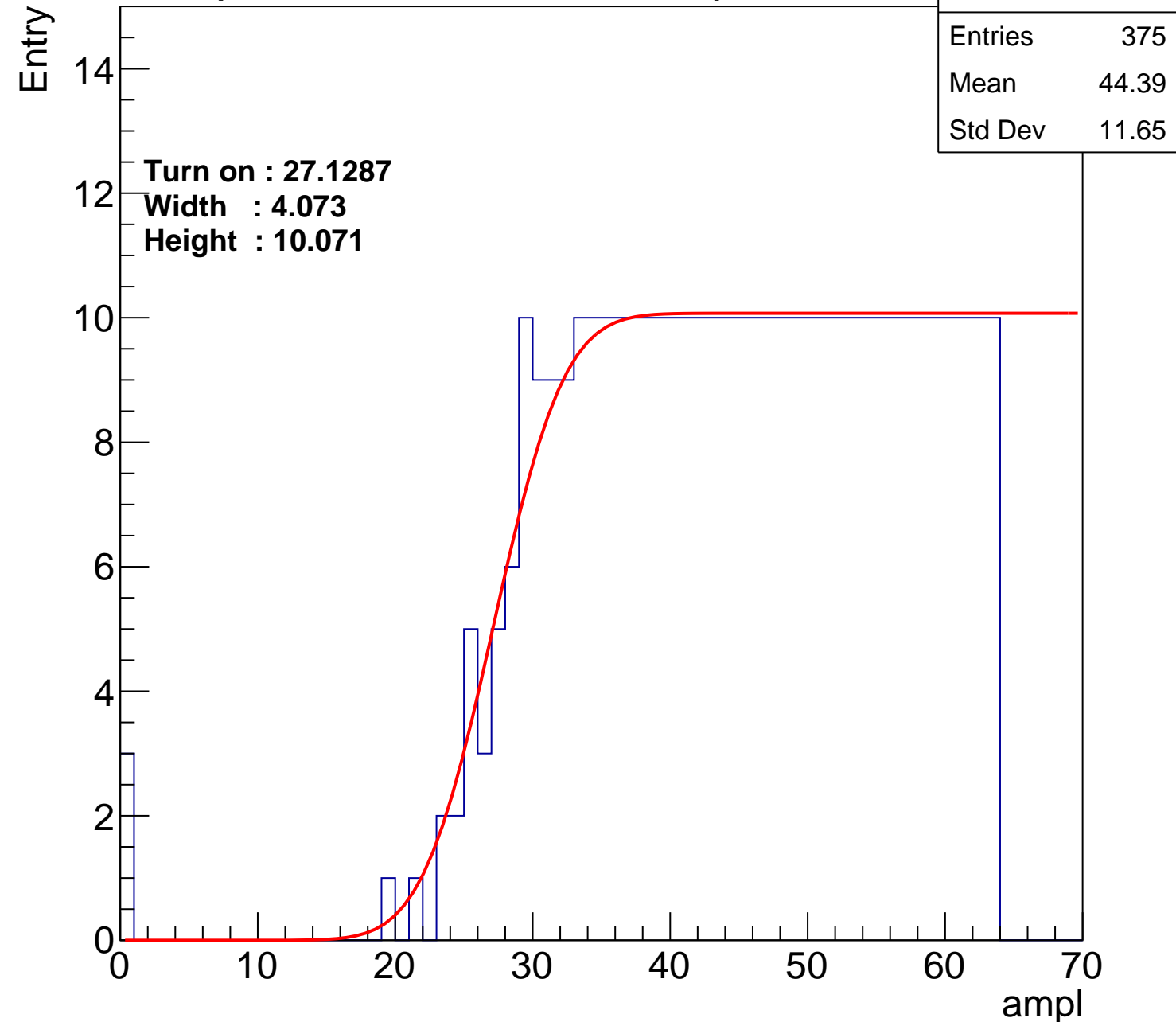
Width : 4.073

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch113

calib_packv5_042523_0143.root, FC#13, port D2

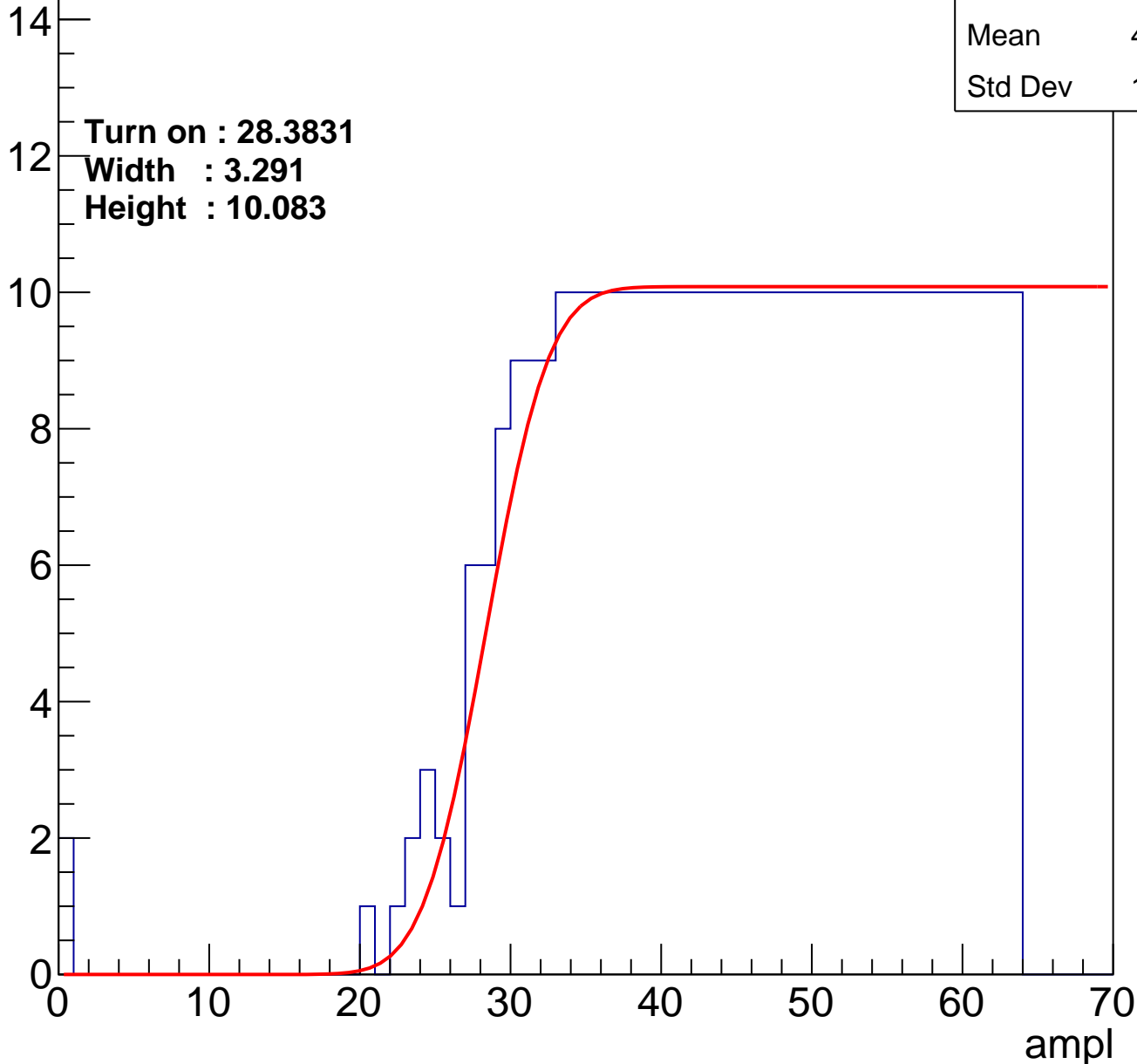
Entries	369
Mean	44.76
Std Dev	11.32

Turn on : 28.3831

Width : 3.291

Height : 10.083

Entry



B1L003S, U14-ch114

calib_packv5_042523_0143.root, FC#13, port D2

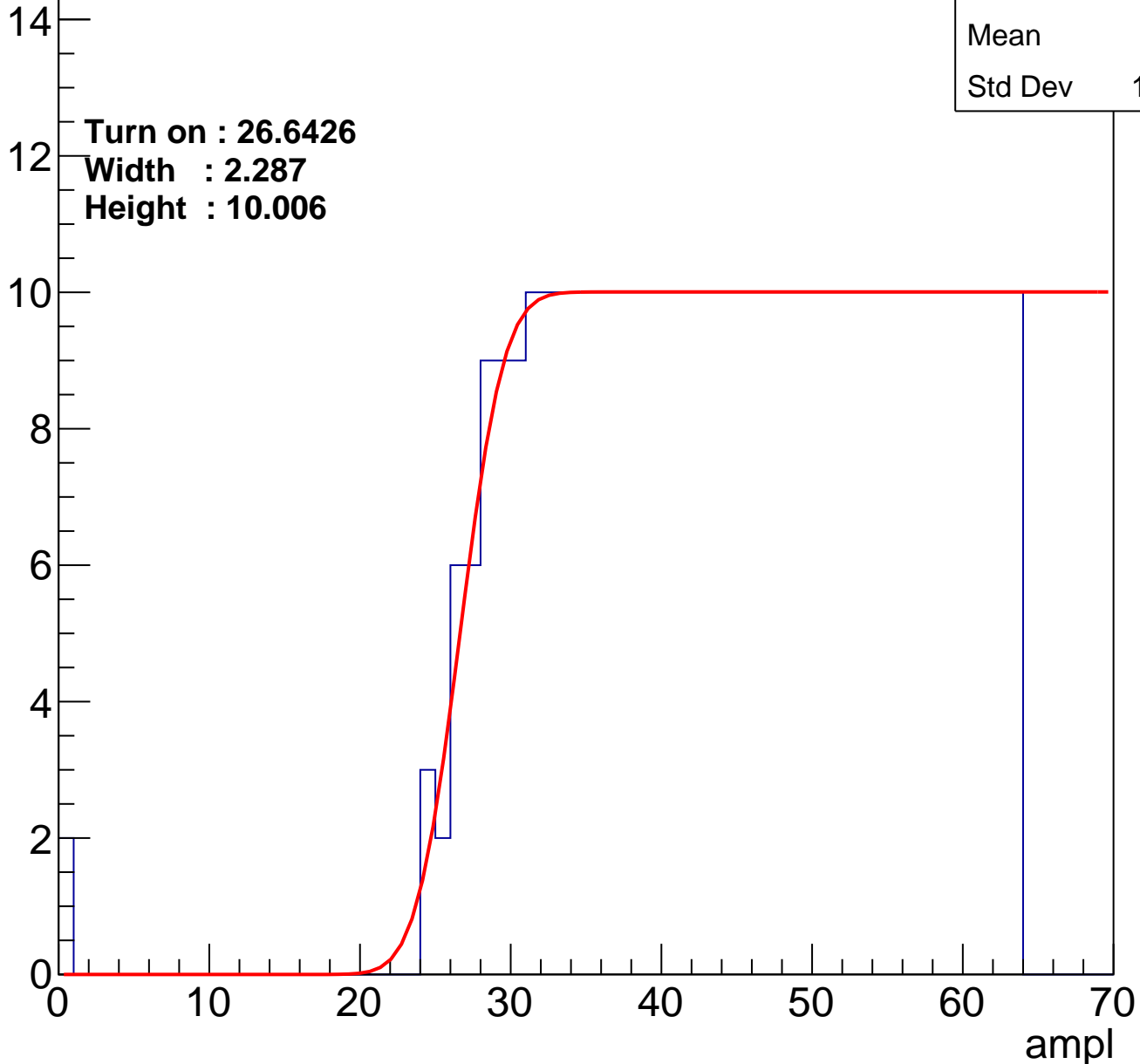
Entries	376
Mean	44.5
Std Dev	11.34

Turn on : 26.6426

Width : 2.287

Height : 10.006

Entry



B1L003S, U14-ch115

calib_packv5_042523_0143.root, FC#13, port D2

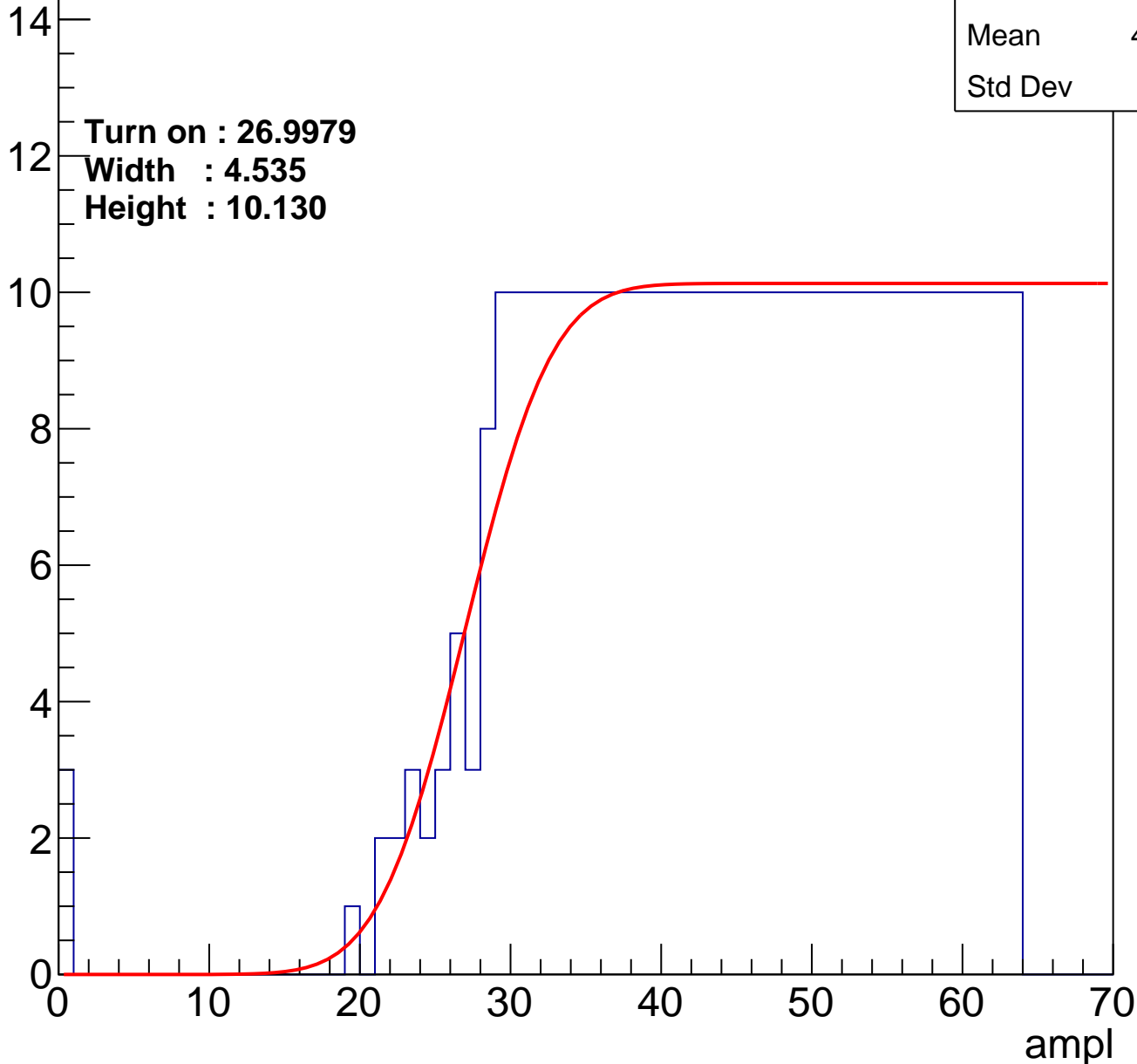
Entries	382
Mean	44.06
Std Dev	11.81

Turn on : 26.9979

Width : 4.535

Height : 10.130

Entry



B1L003S, U14-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.64
Std Dev	11.8

Turn on : 27.9647

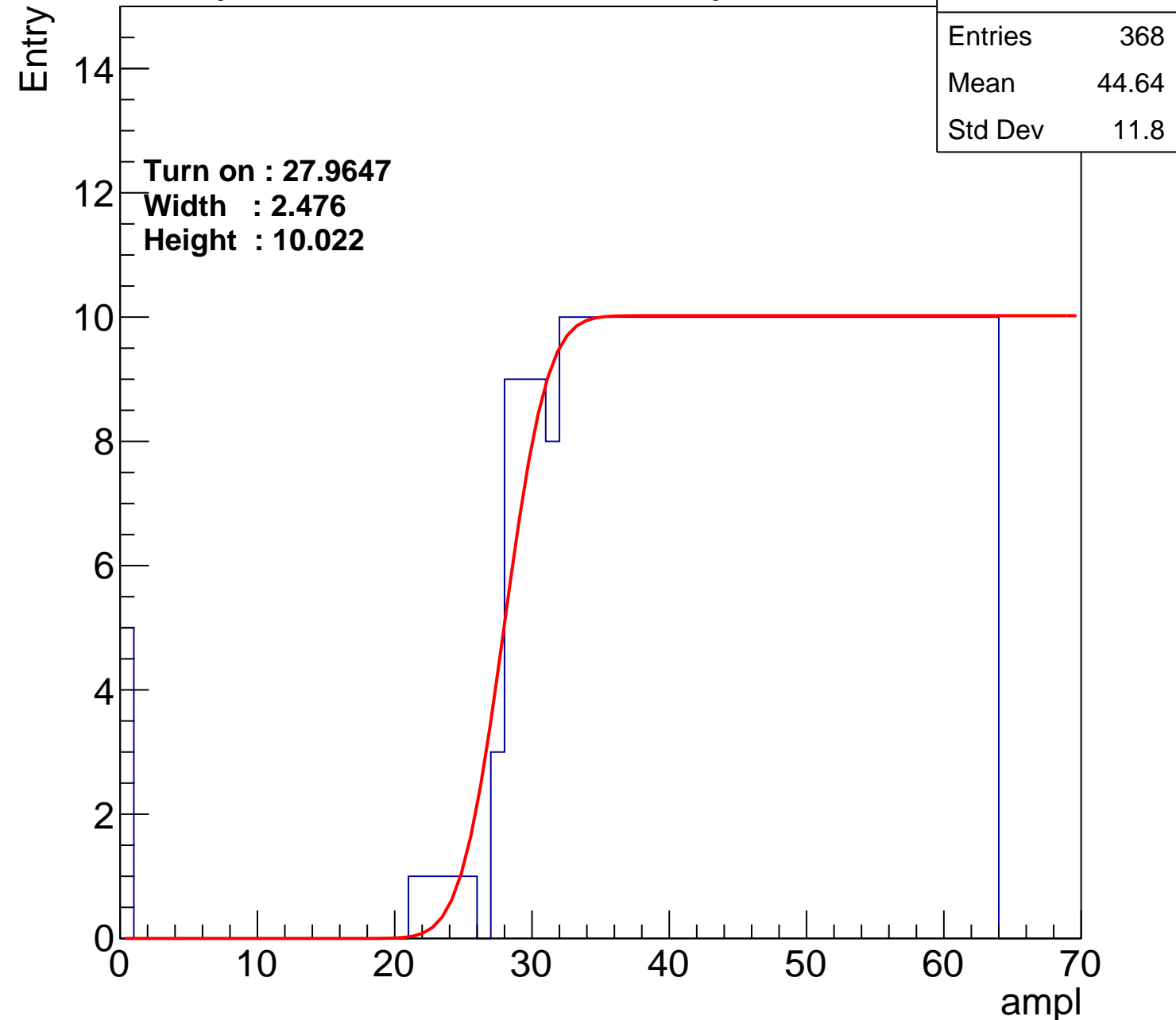
Width : 2.476

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.49
Std Dev	11.72

Turn on : 27.1372

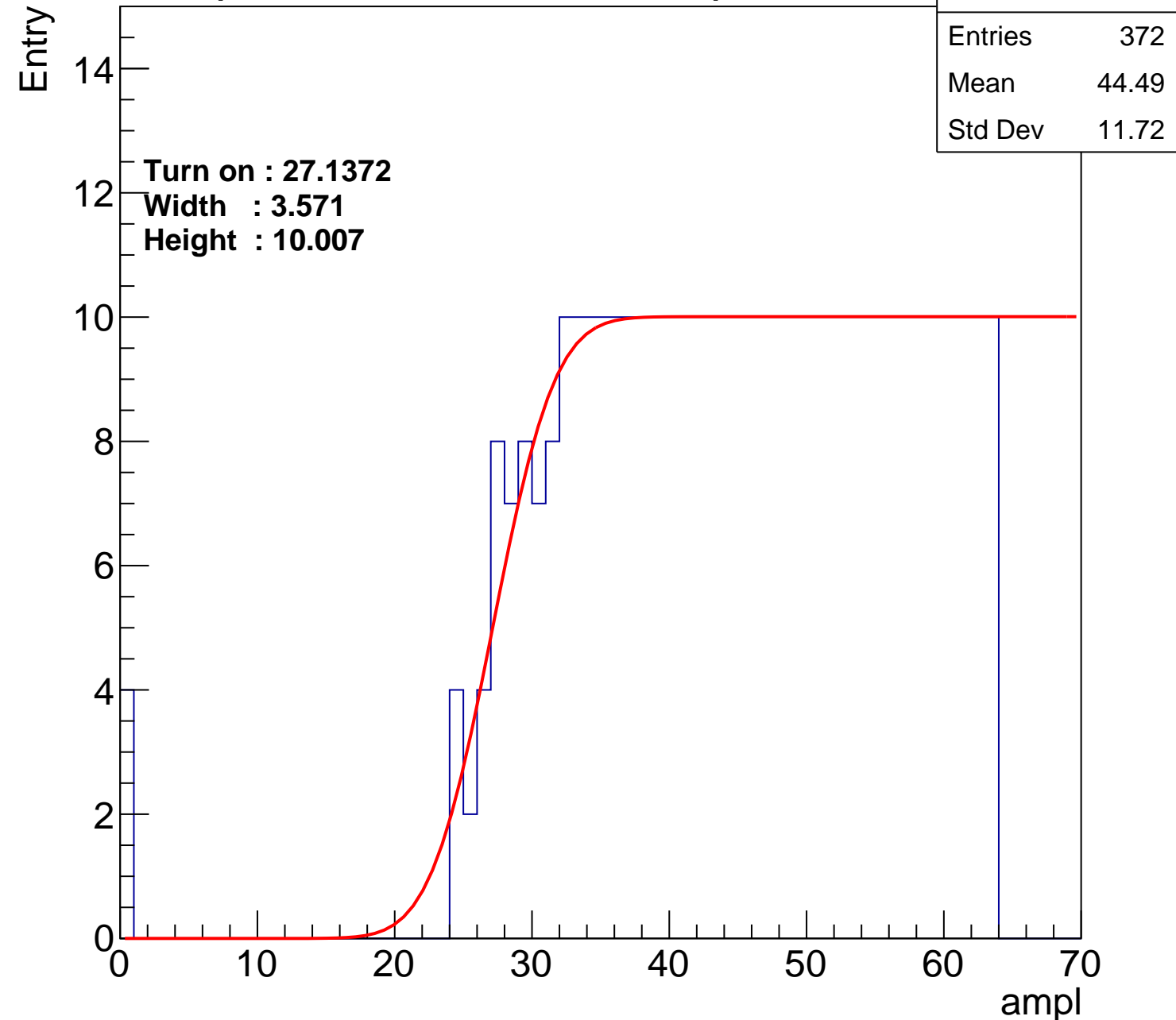
Width : 3.571

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 25.8600

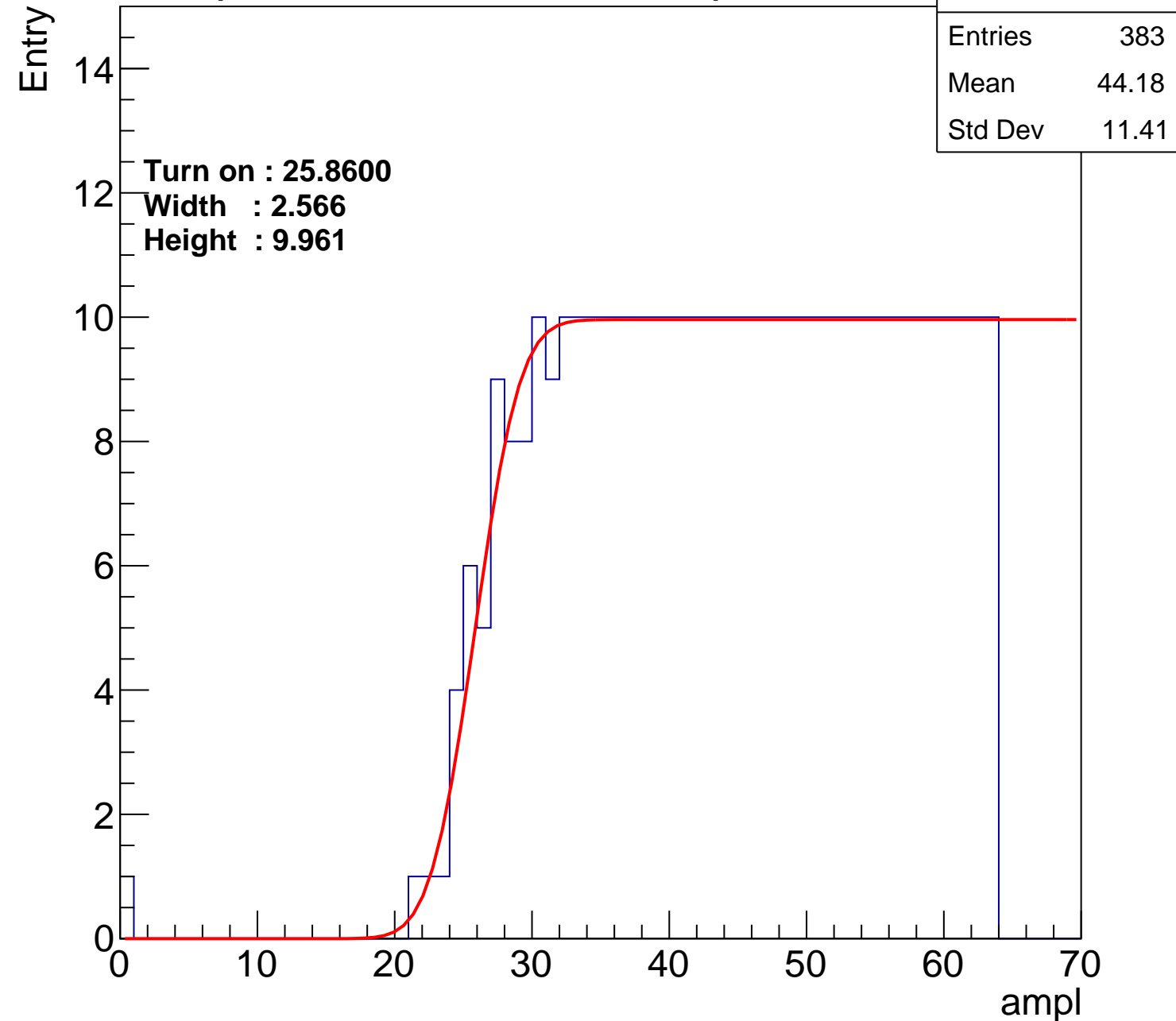
Width : 2.566

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch119

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 26.5330

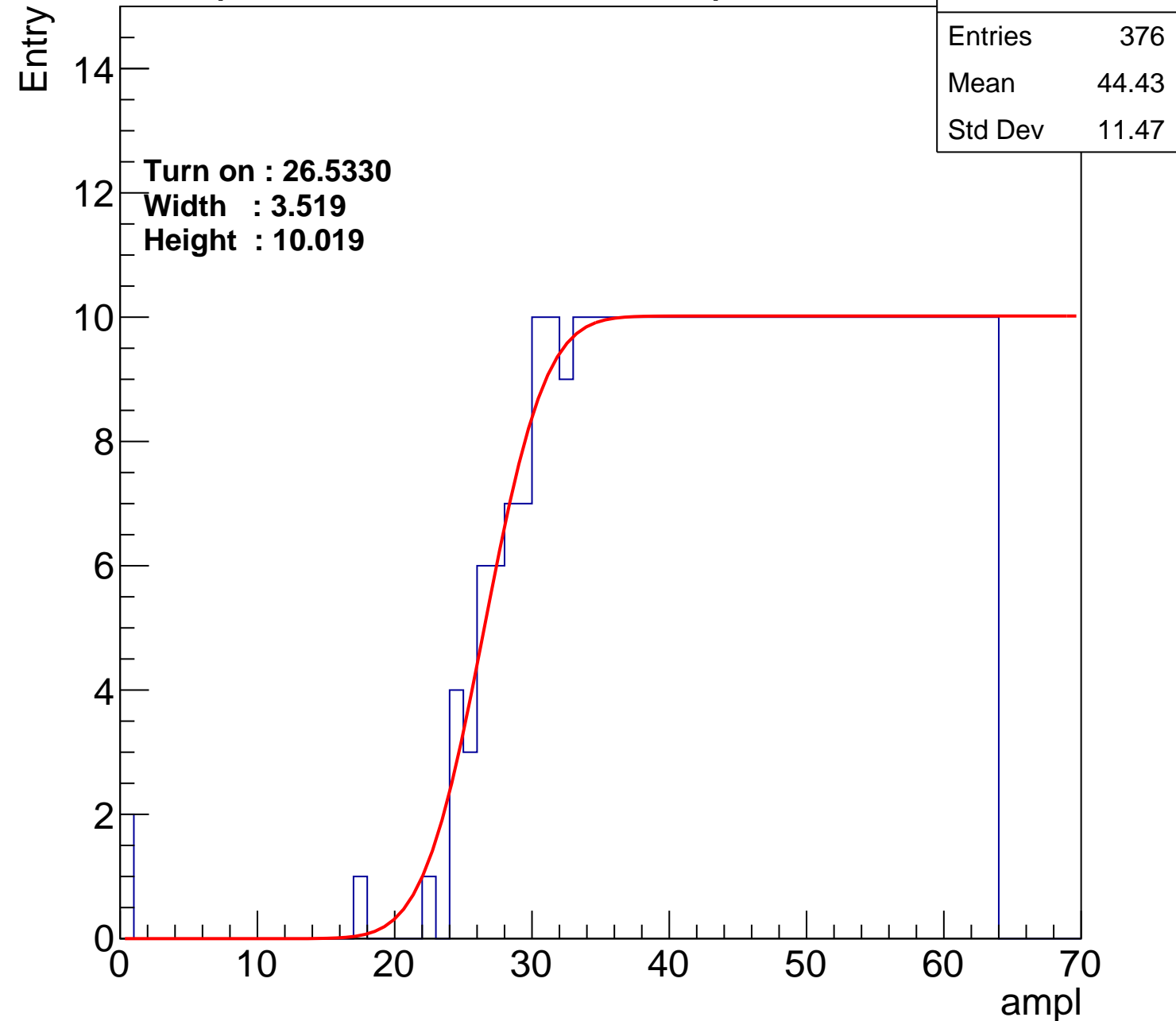
Width : 3.519

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 25.6076

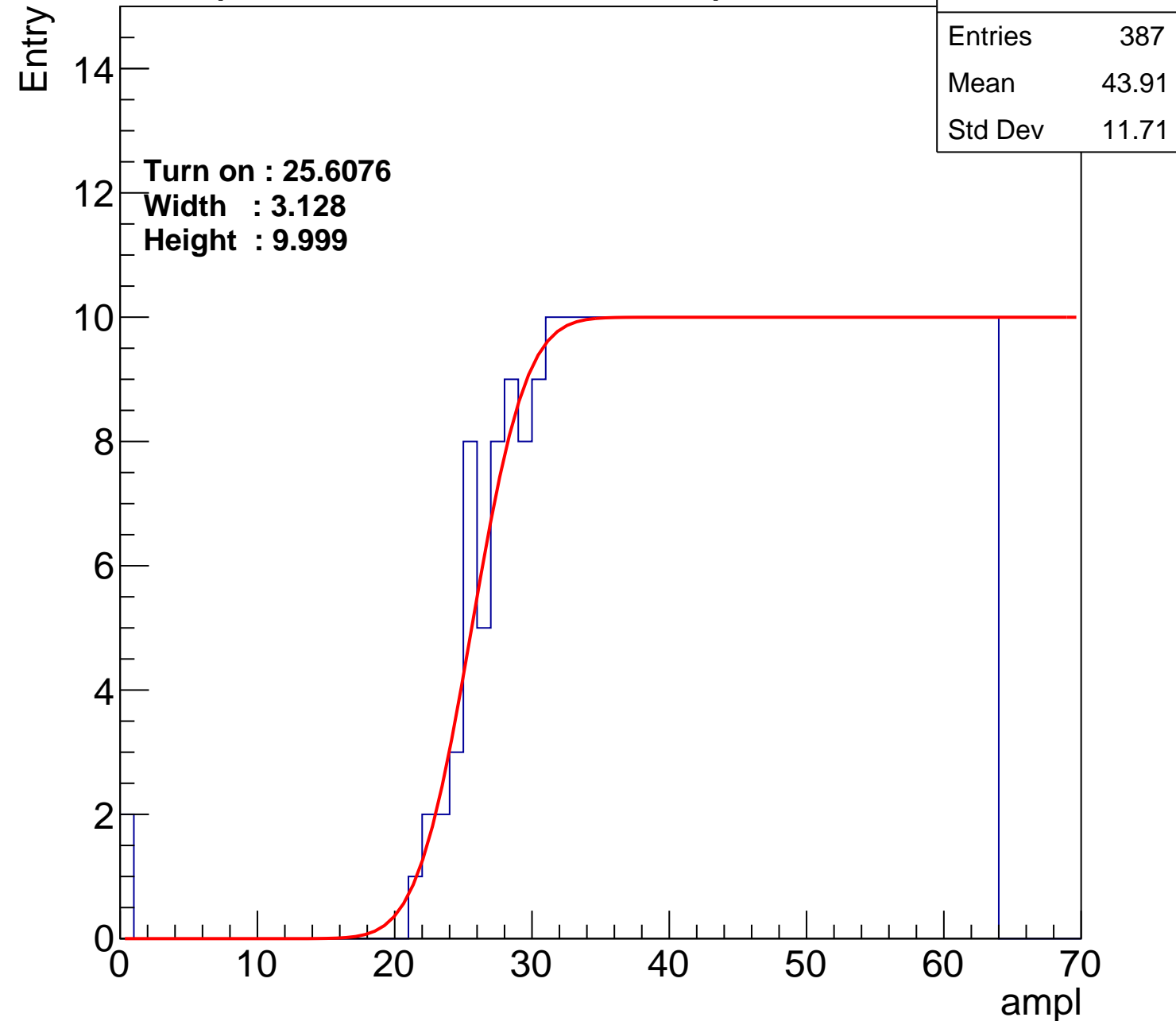
Width : 3.128

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.38
Std Dev	11.55

Turn on : 26.5531

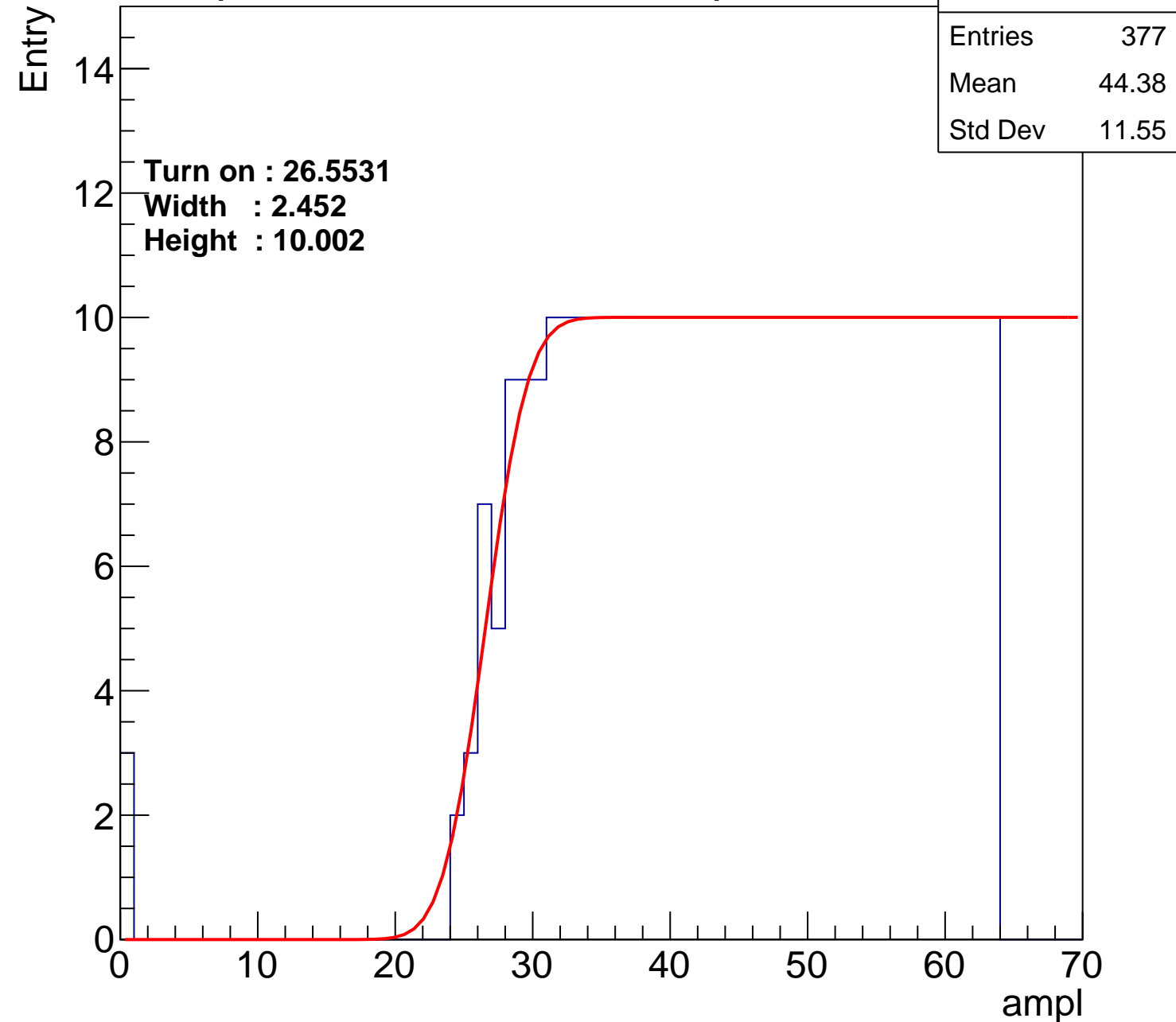
Width : 2.452

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.14
Std Dev	11.12

Turn on : 27.8176

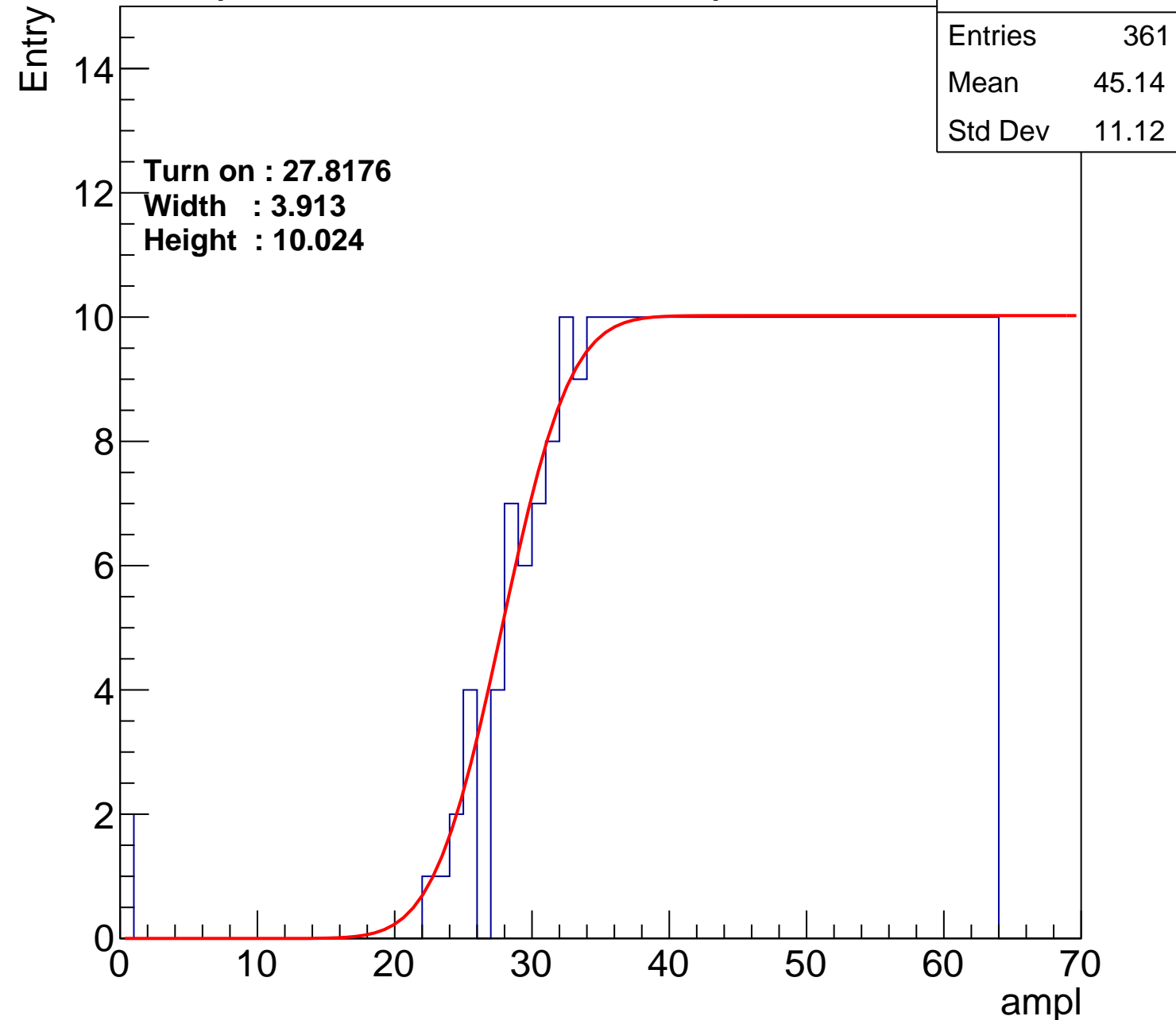
Width : 3.913

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.18
Std Dev	11.02

Turn on : 28.3700

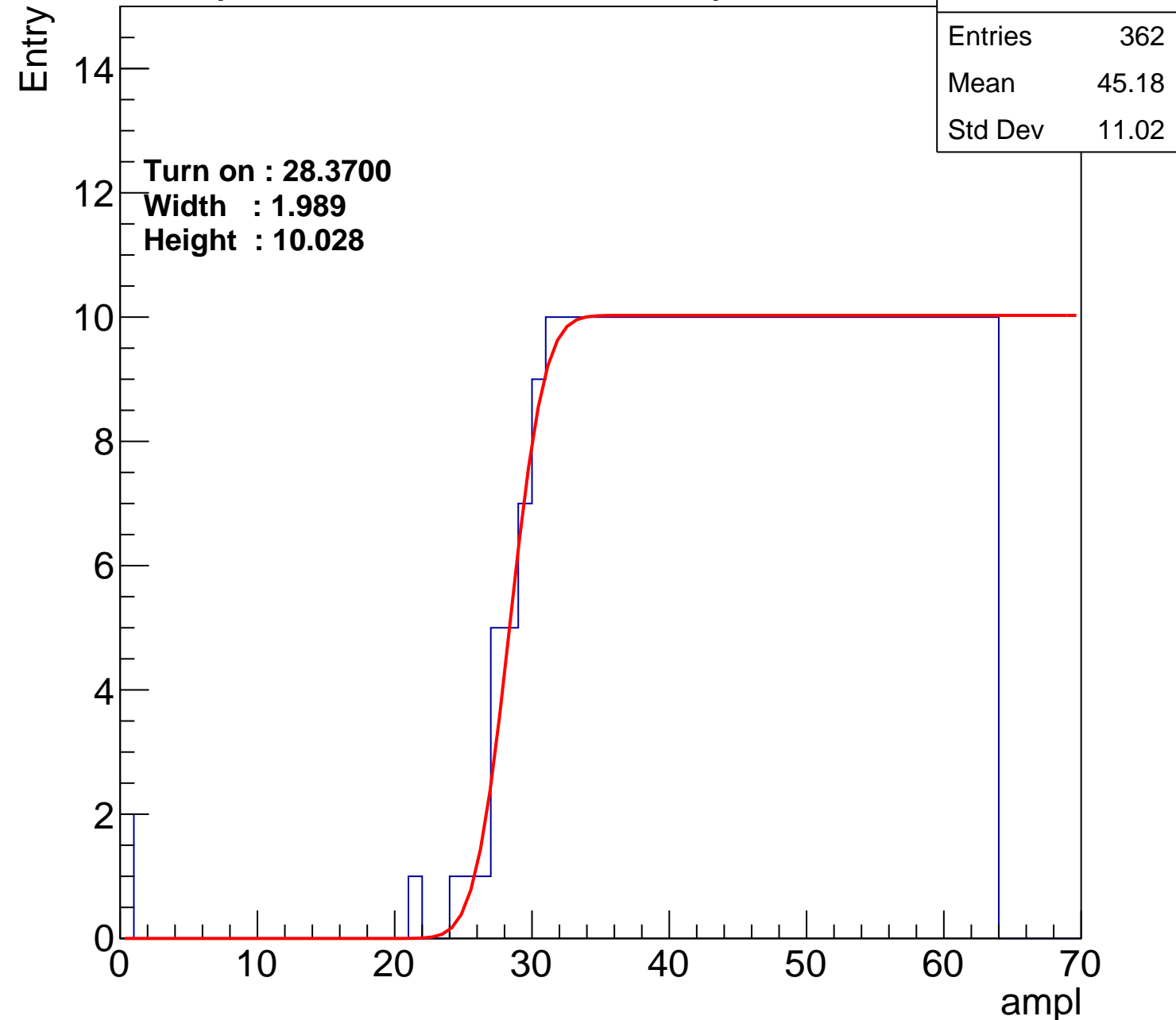
Width : 1.989

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.6322

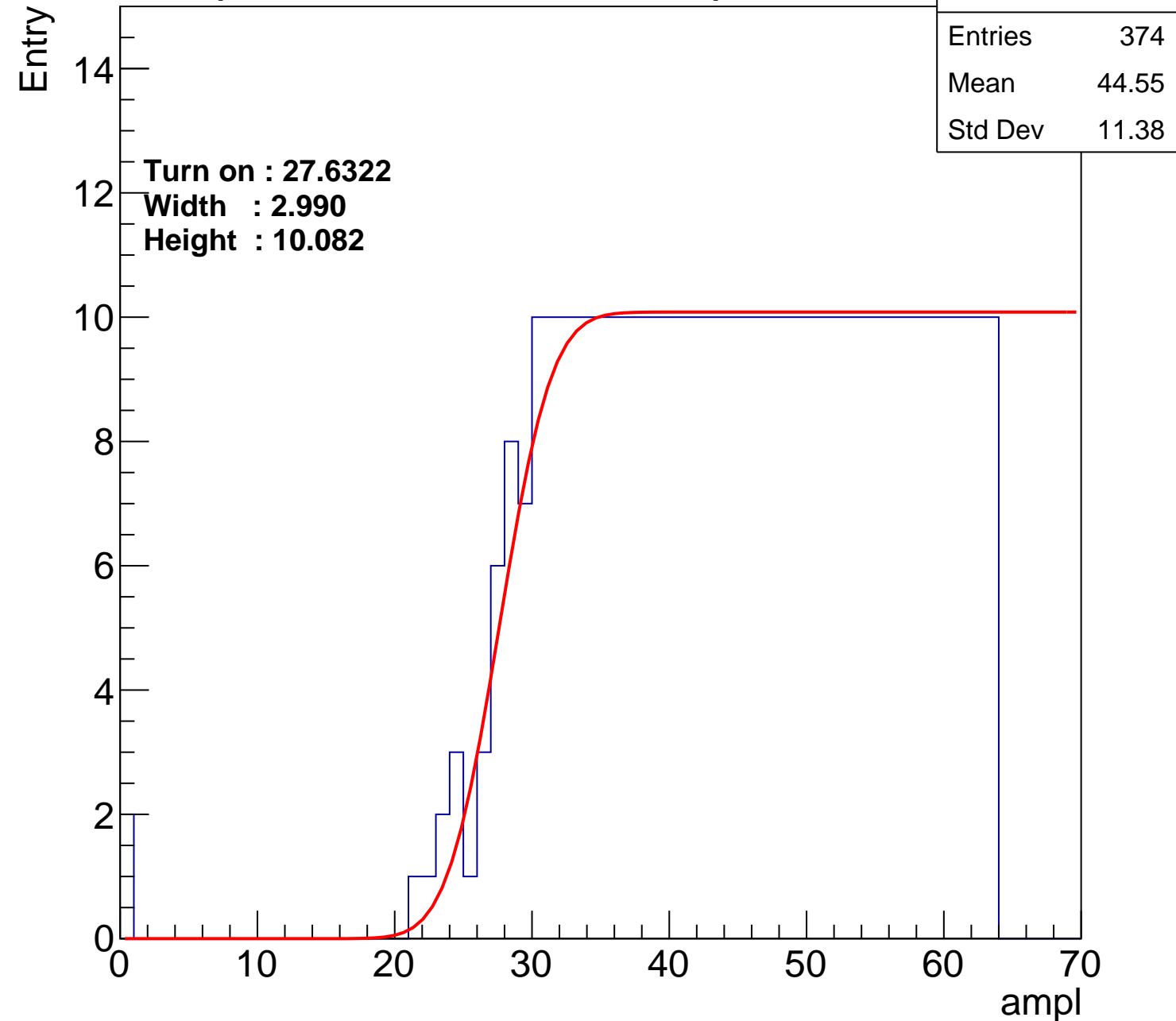
Width : 2.990

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch125

calib_packv5_042523_0143.root, FC#13, port D2

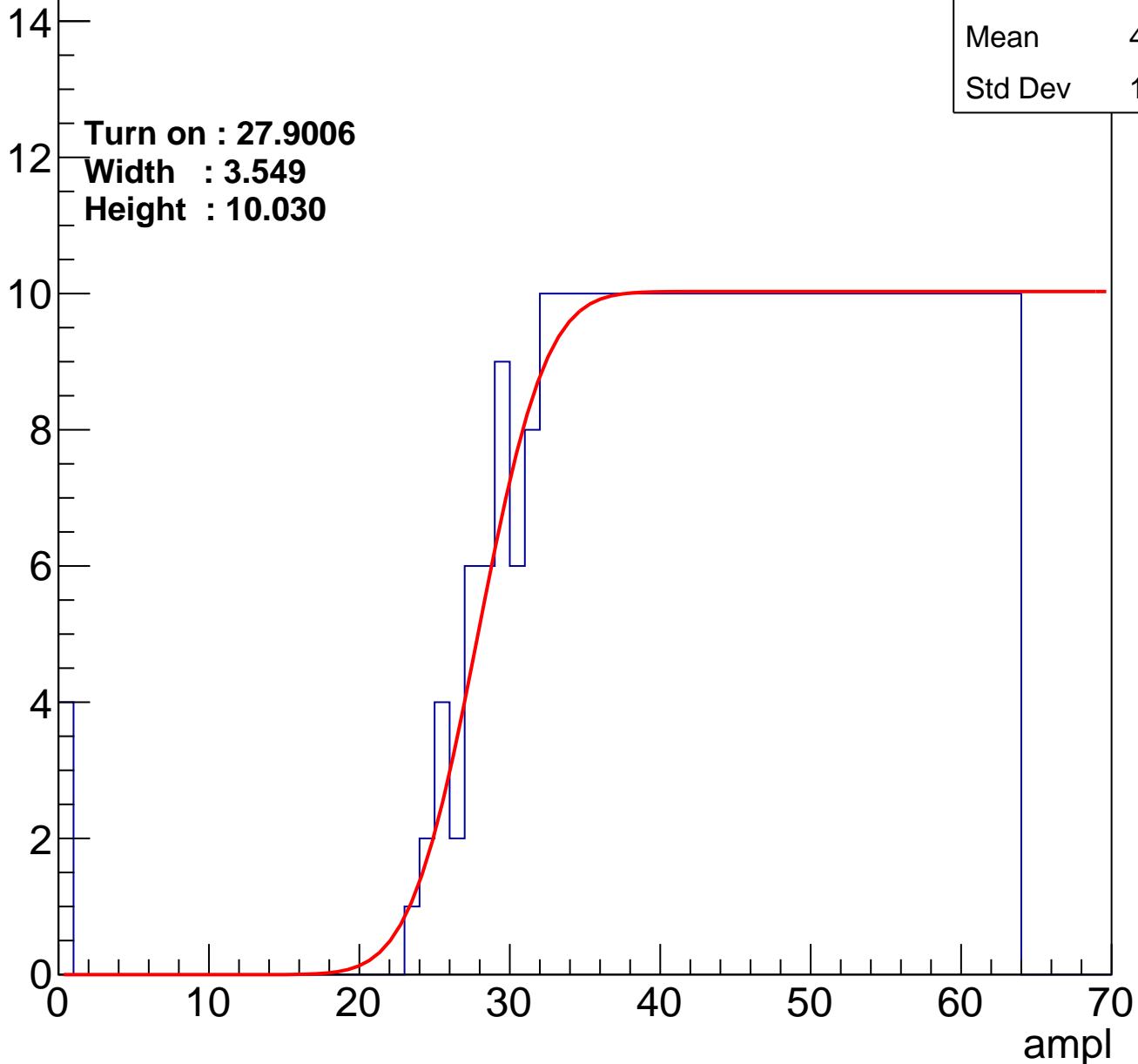
Entries	368
Mean	44.68
Std Dev	11.65

Turn on : 27.9006

Width : 3.549

Height : 10.030

Entry



B1L003S, U14-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	43.92
Std Dev	12.27

Turn on : 26.9224

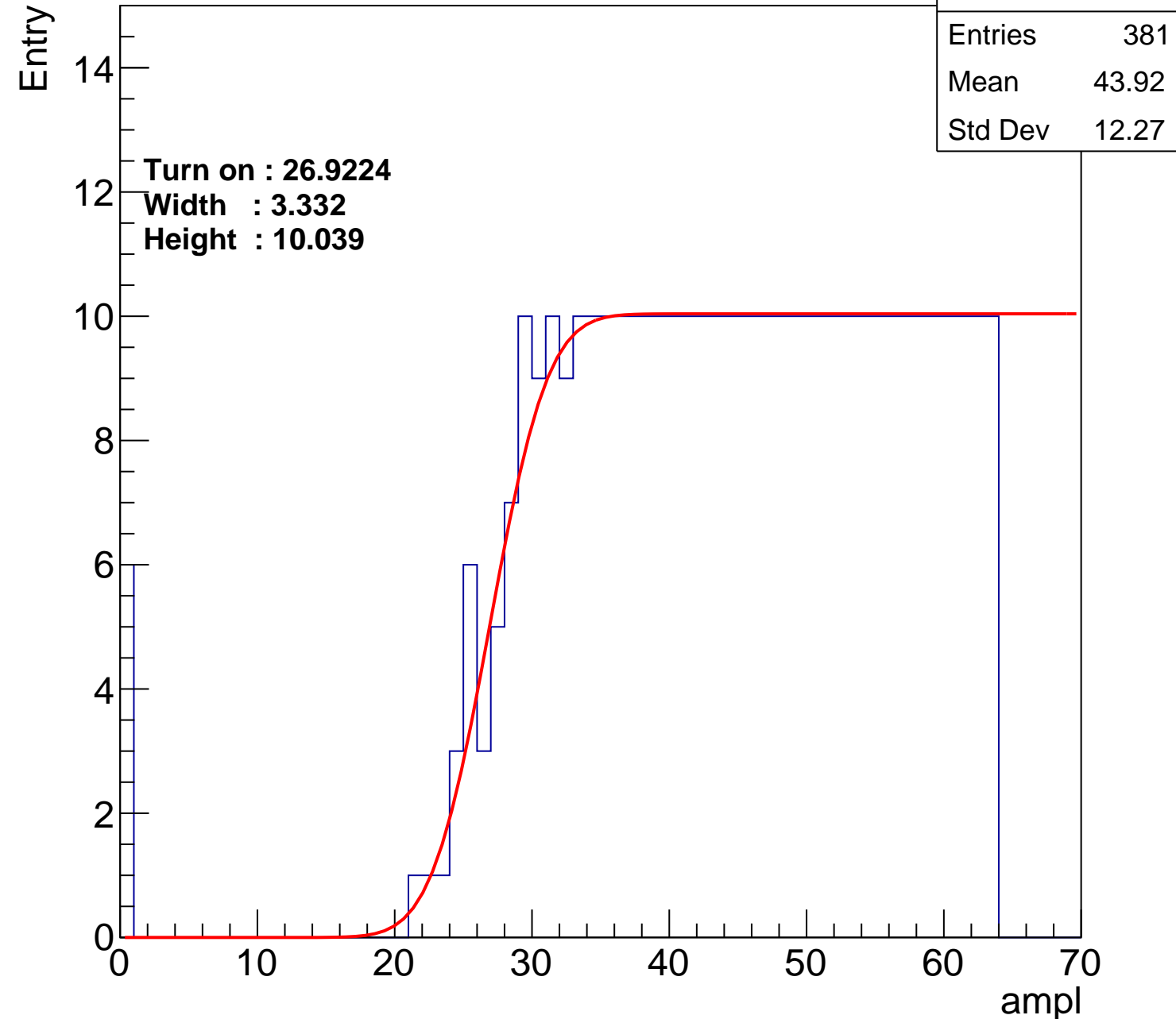
Width : 3.332

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.22
Std Dev	12.13

Turn on : 27.1867

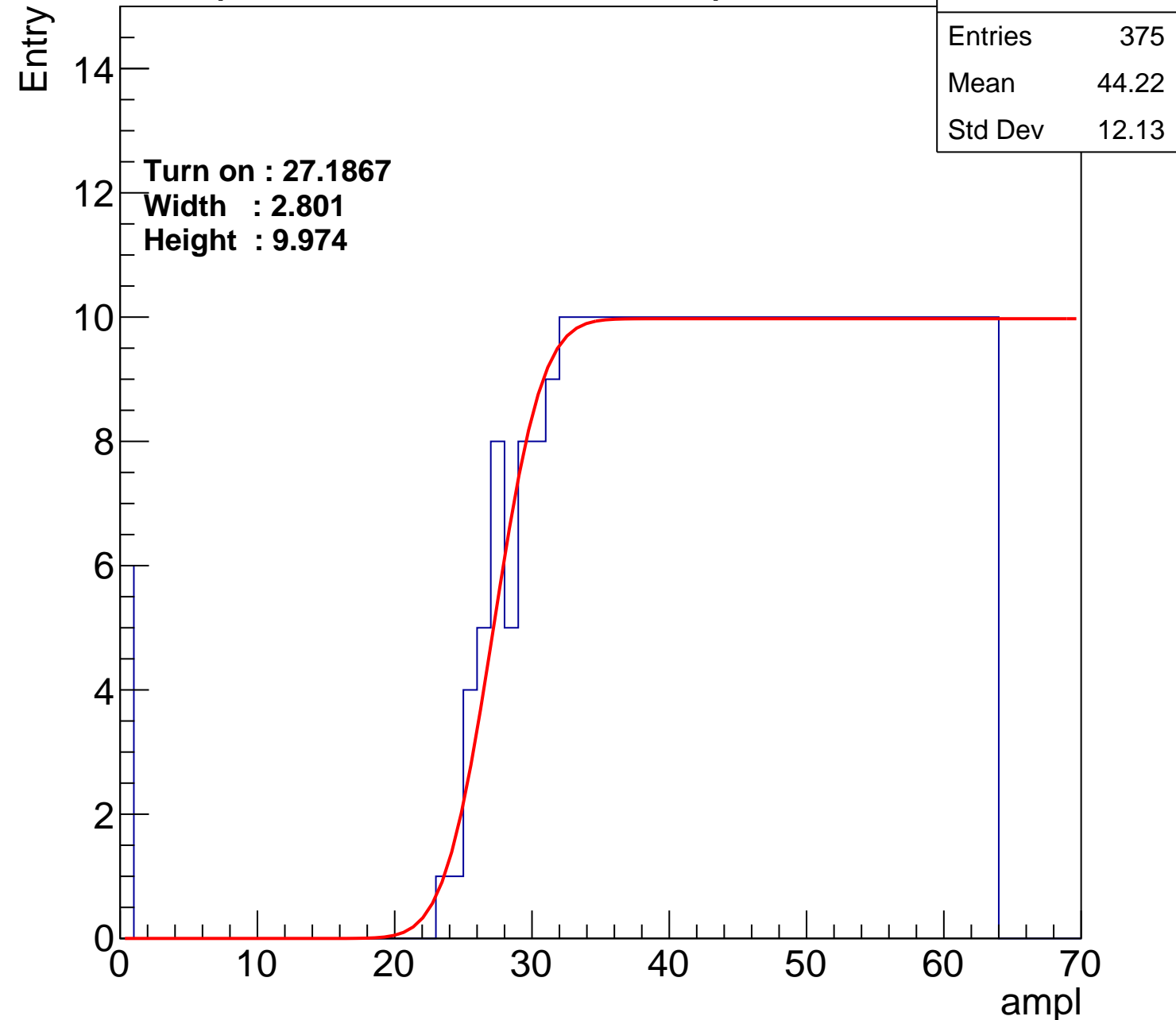
Width : 2.801

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U14-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.22
Std Dev	12.13

Turn on : 27.1867

Width : 2.801

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl

