

B0L001S, U15-ch0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	380
Mean	44.11
Std Dev	11.9

Turn on : 26.6879

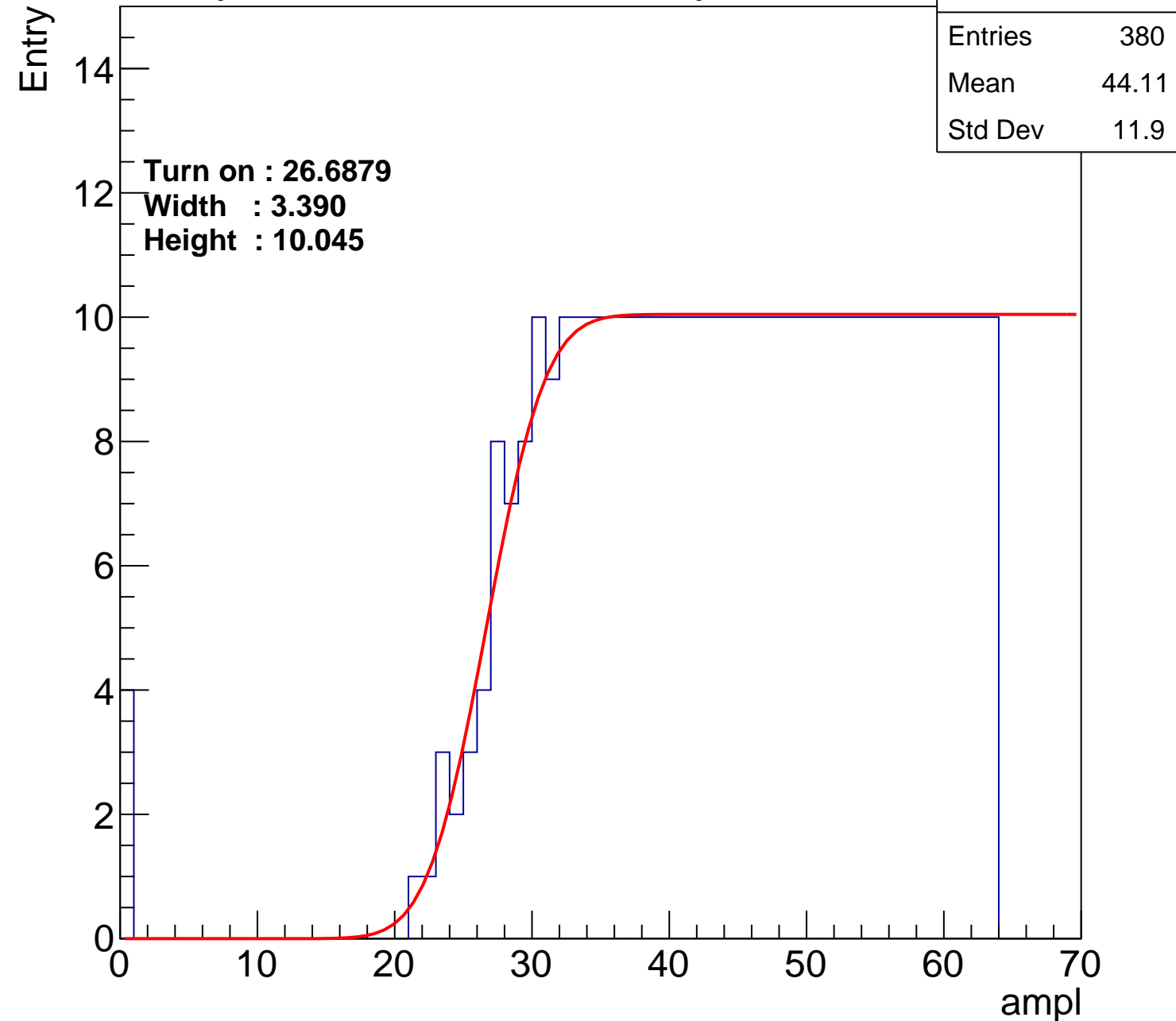
Width : 3.390

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.99
Std Dev	11.34

Turn on : 28.6627

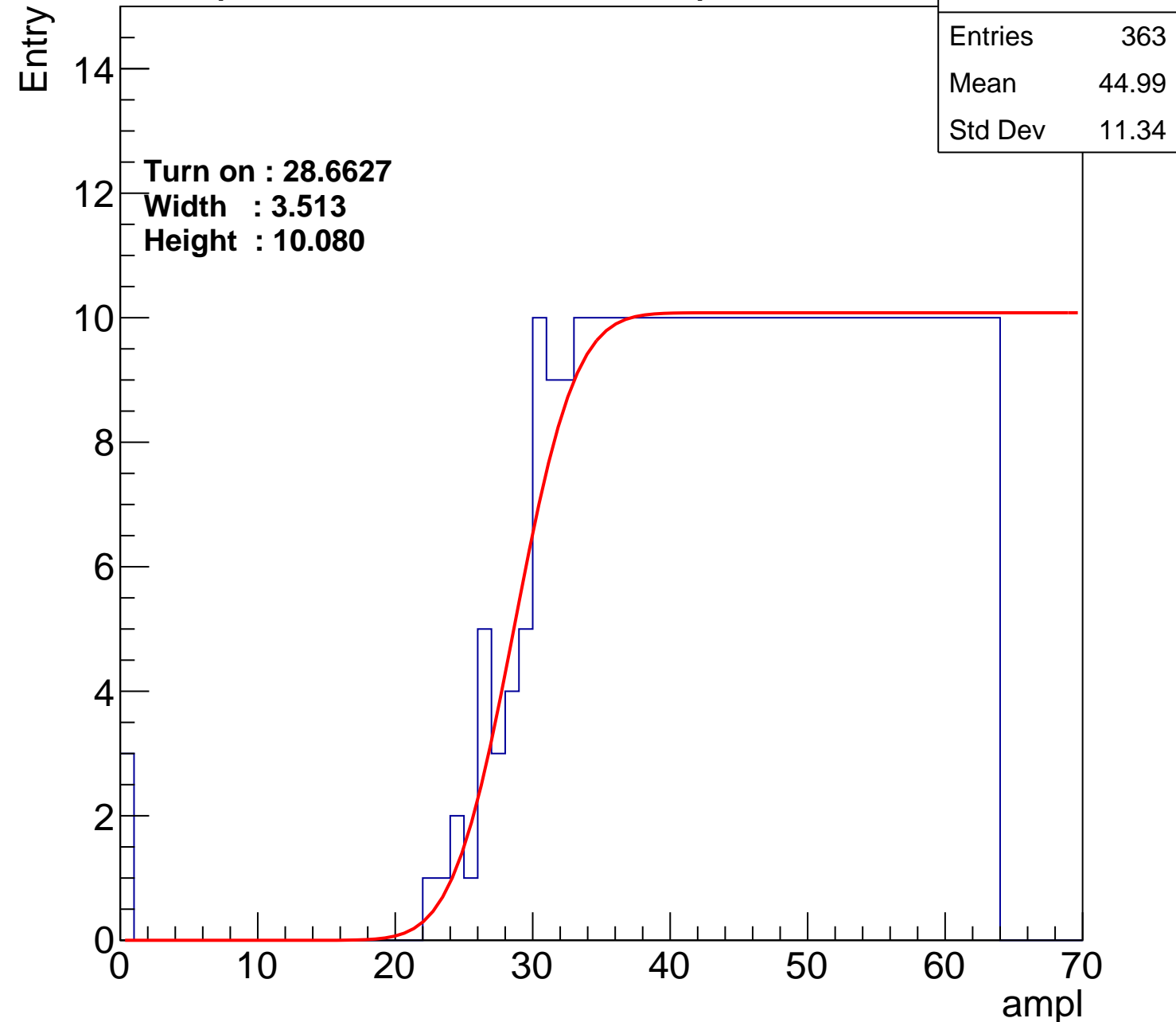
Width : 3.513

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.49
Std Dev	11.92

Turn on : 27.7113

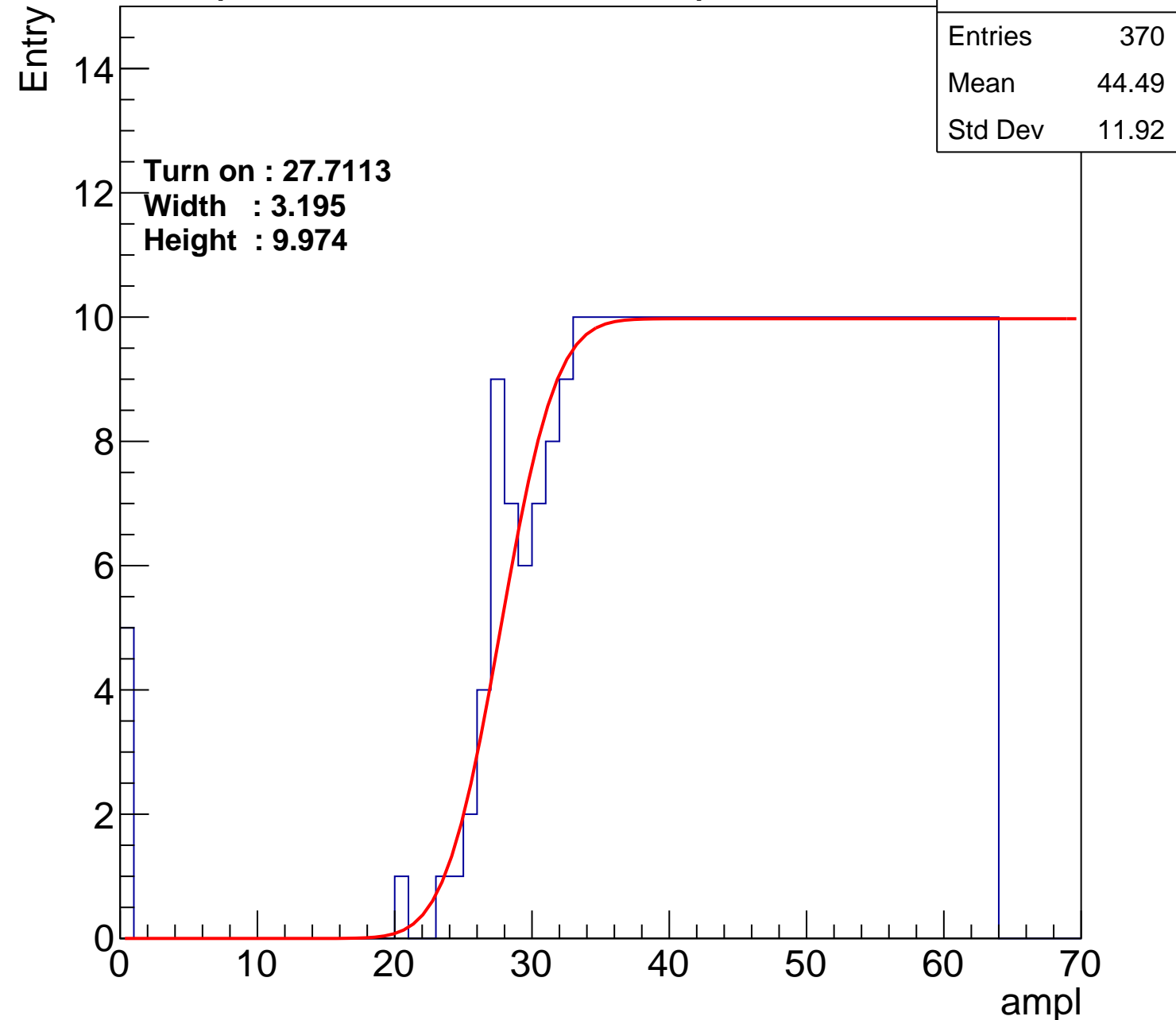
Width : 3.195

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	349
Mean	45.62
Std Dev	11.19

Turn on : 29.2566

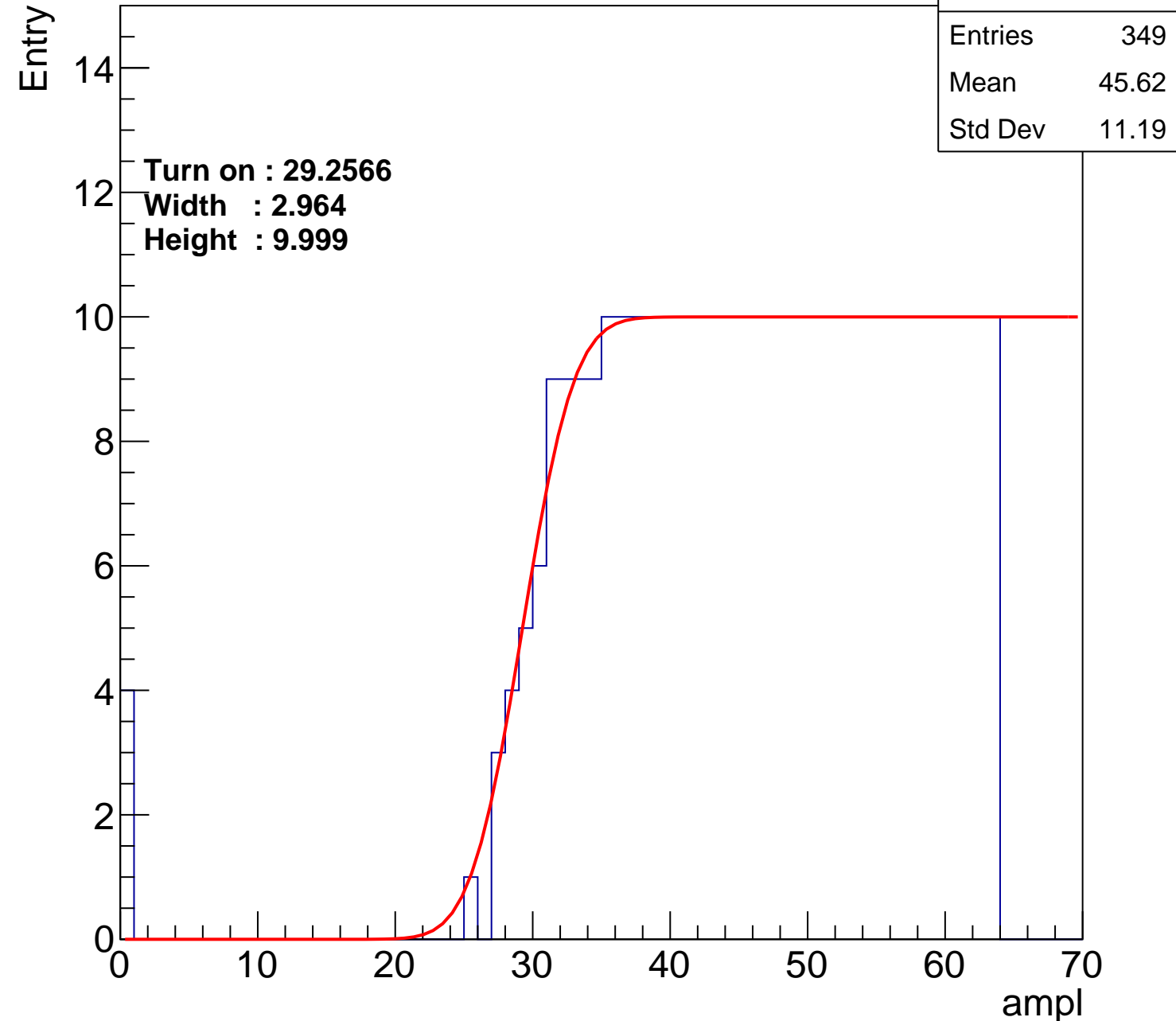
Width : 2.964

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	45.39
Std Dev	10.79

Turn on : 28.2567

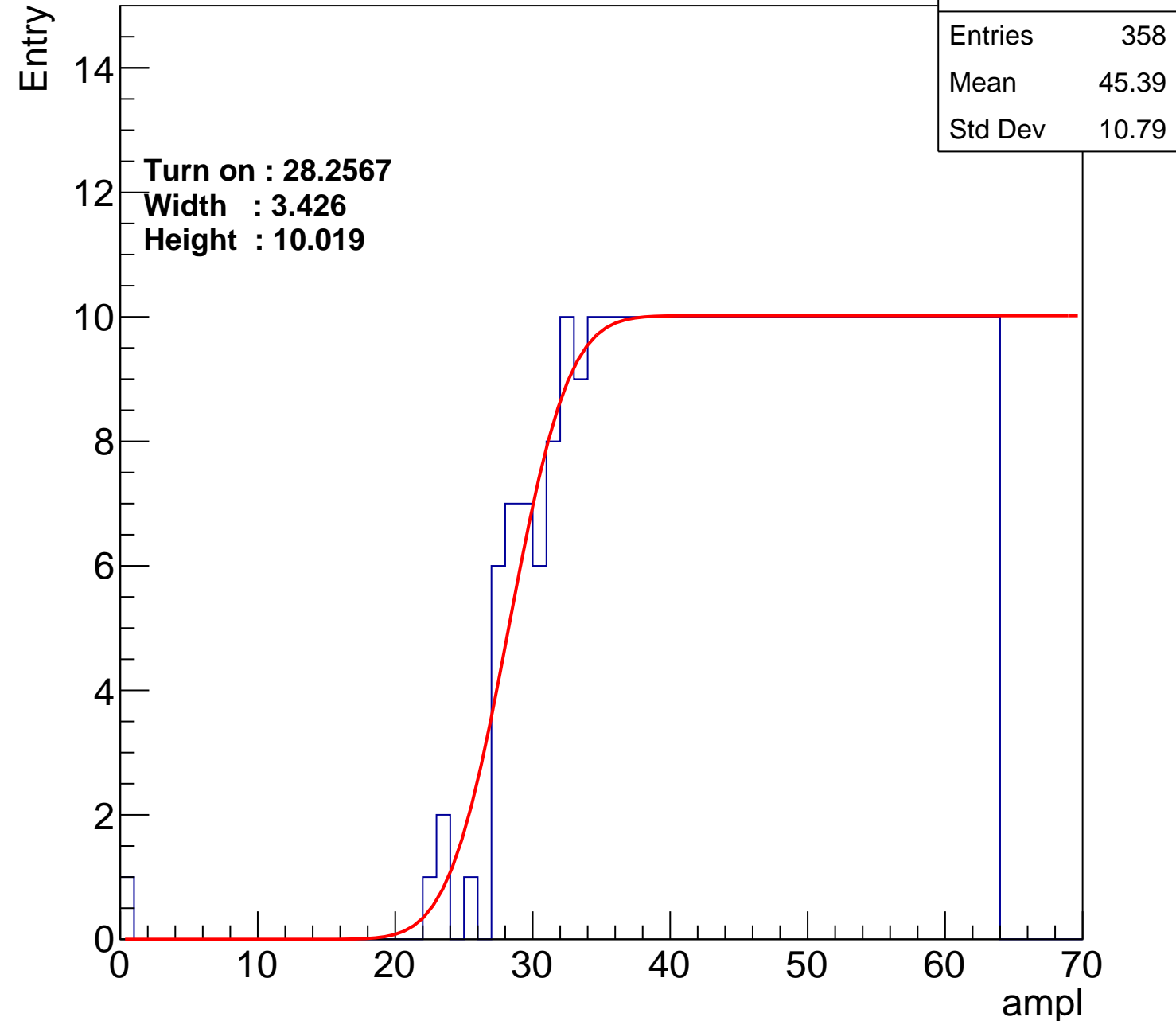
Width : 3.426

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.8
Std Dev	11.48

Turn on : 28.3390

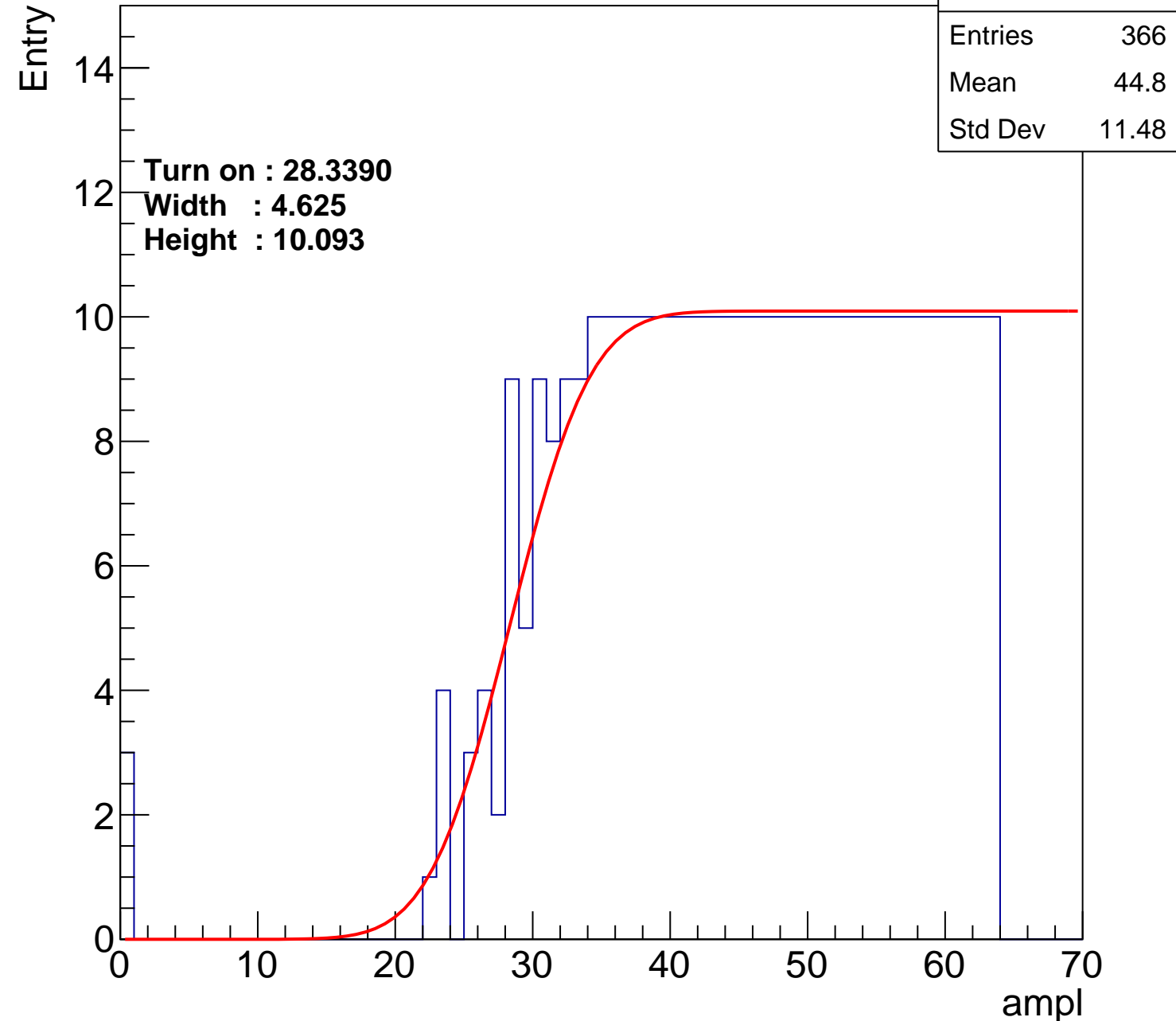
Width : 4.625

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch6

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.19
Std Dev	12.31

Turn on : 27.6187

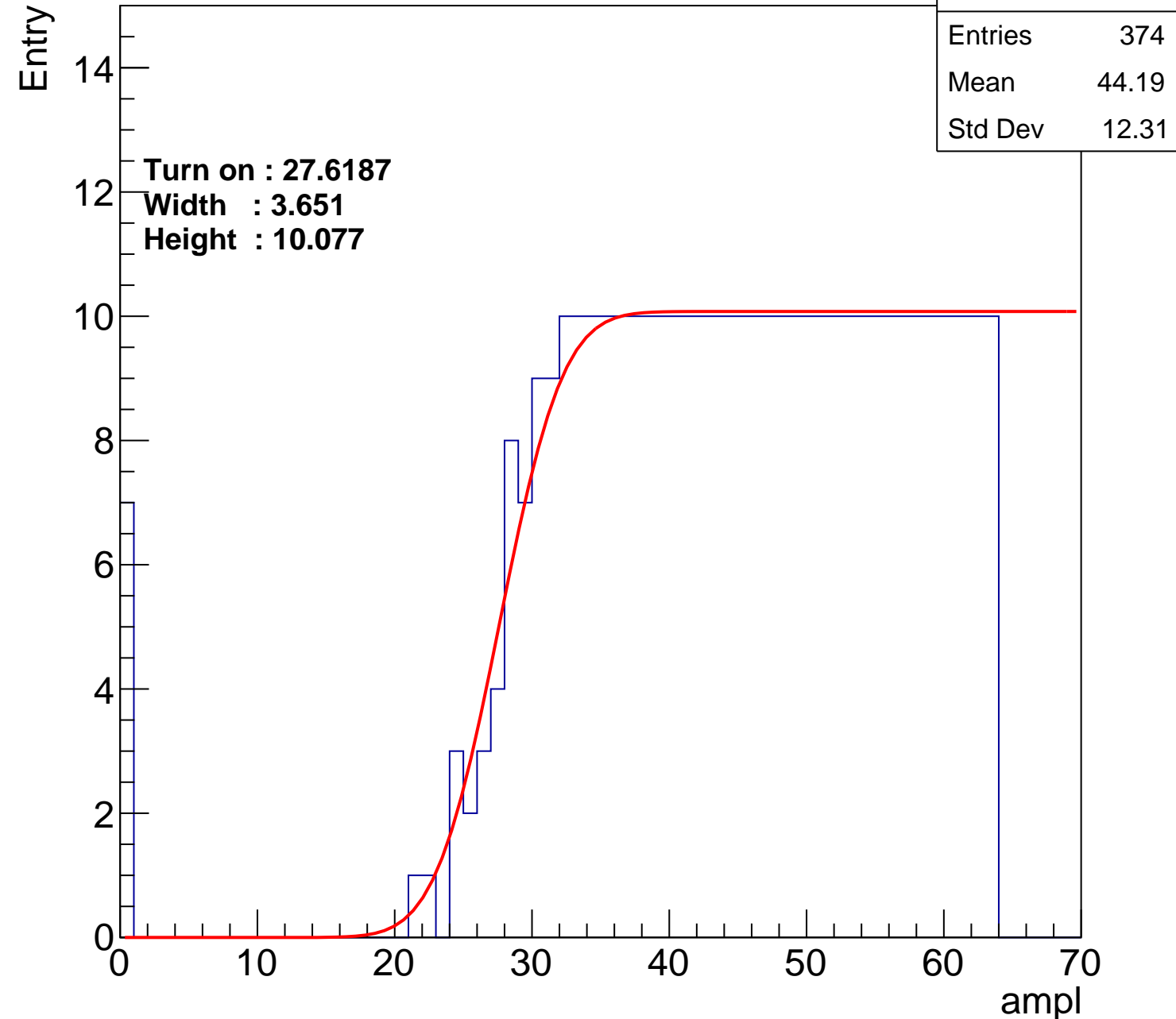
Width : 3.651

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch7

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 28.0728

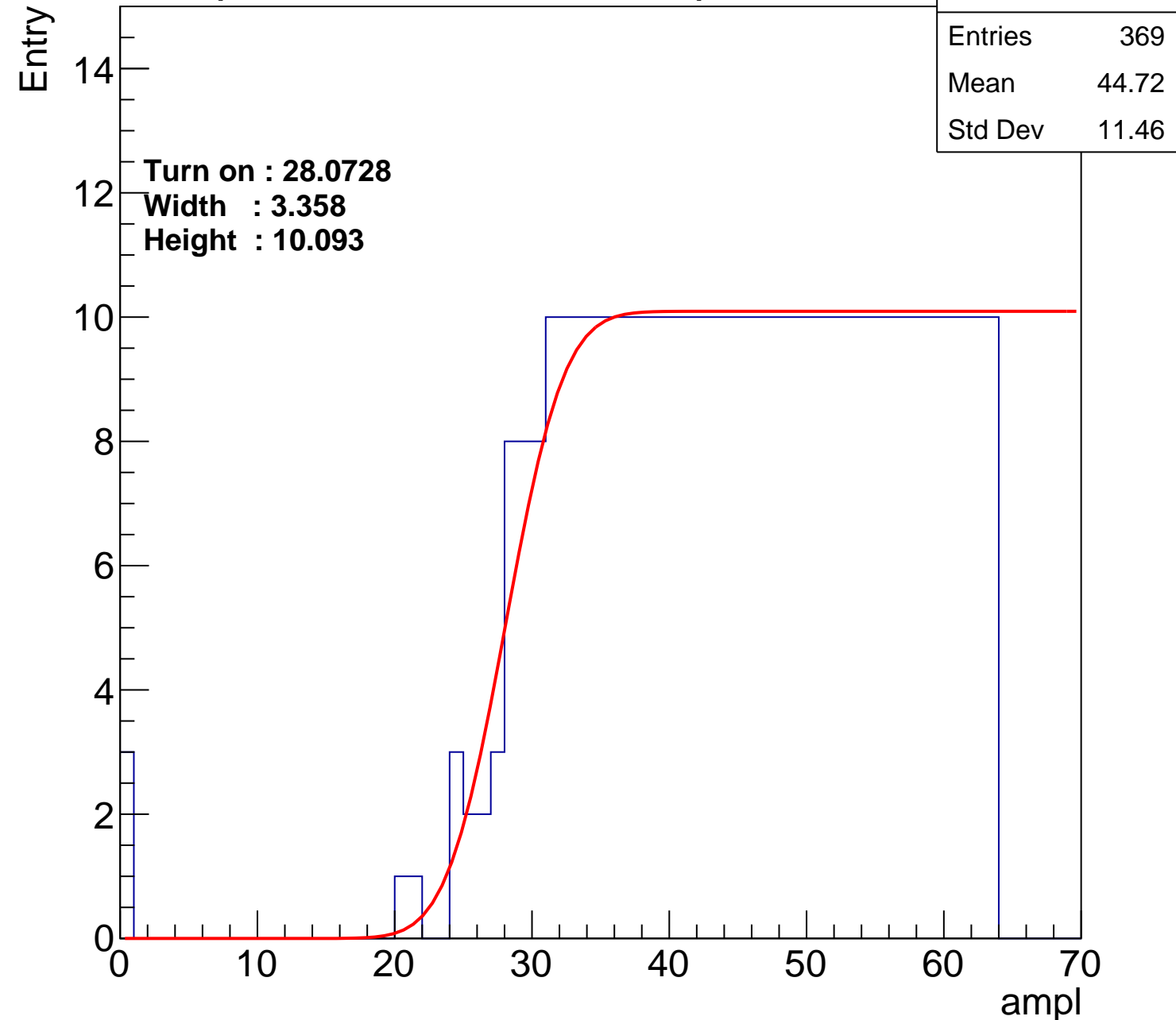
Width : 3.358

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch8

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.05
Std Dev	11.3

Turn on : 28.4295

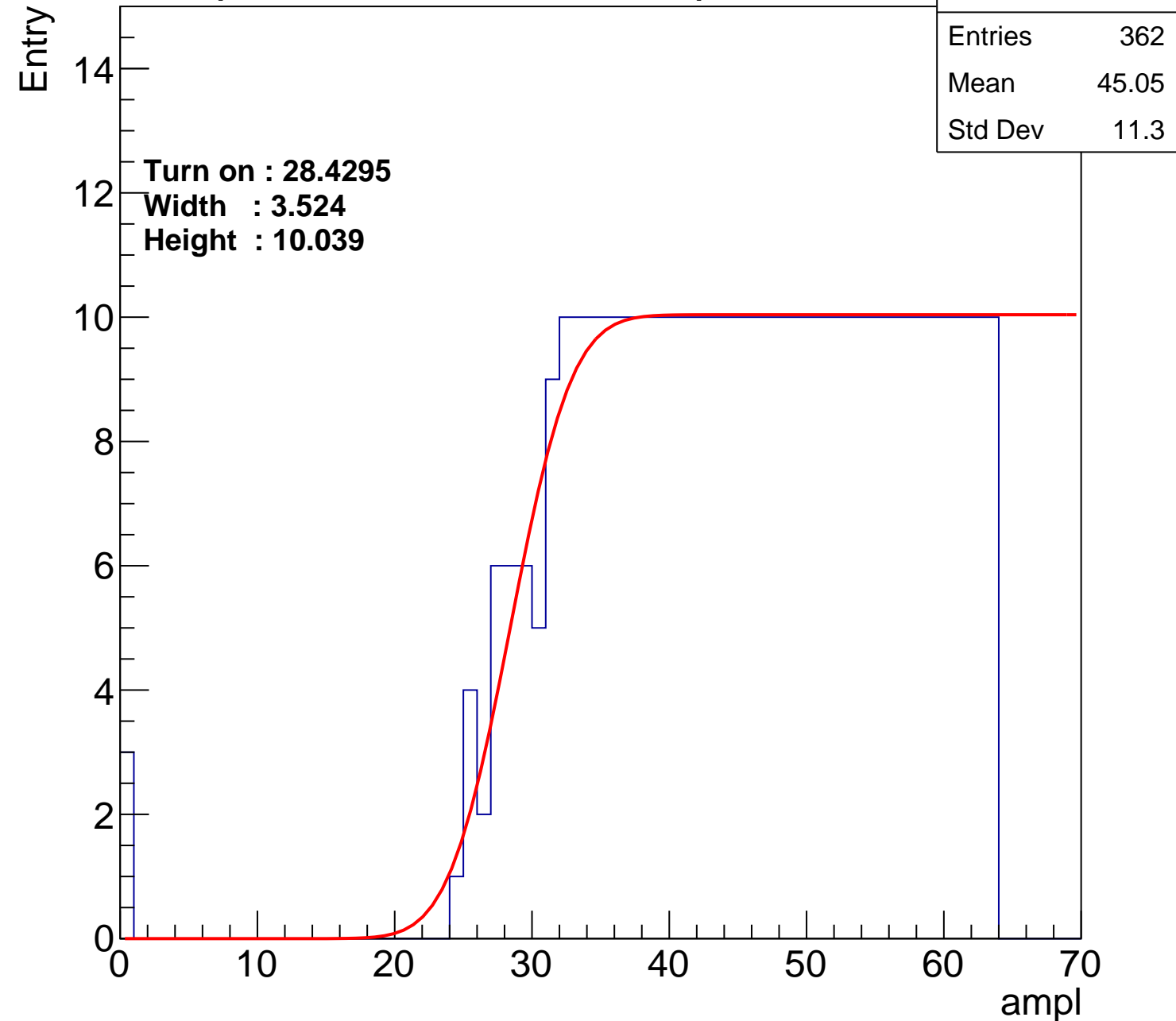
Width : 3.524

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch9

calib_packv5_042523_0143.root, FC#9, port A1

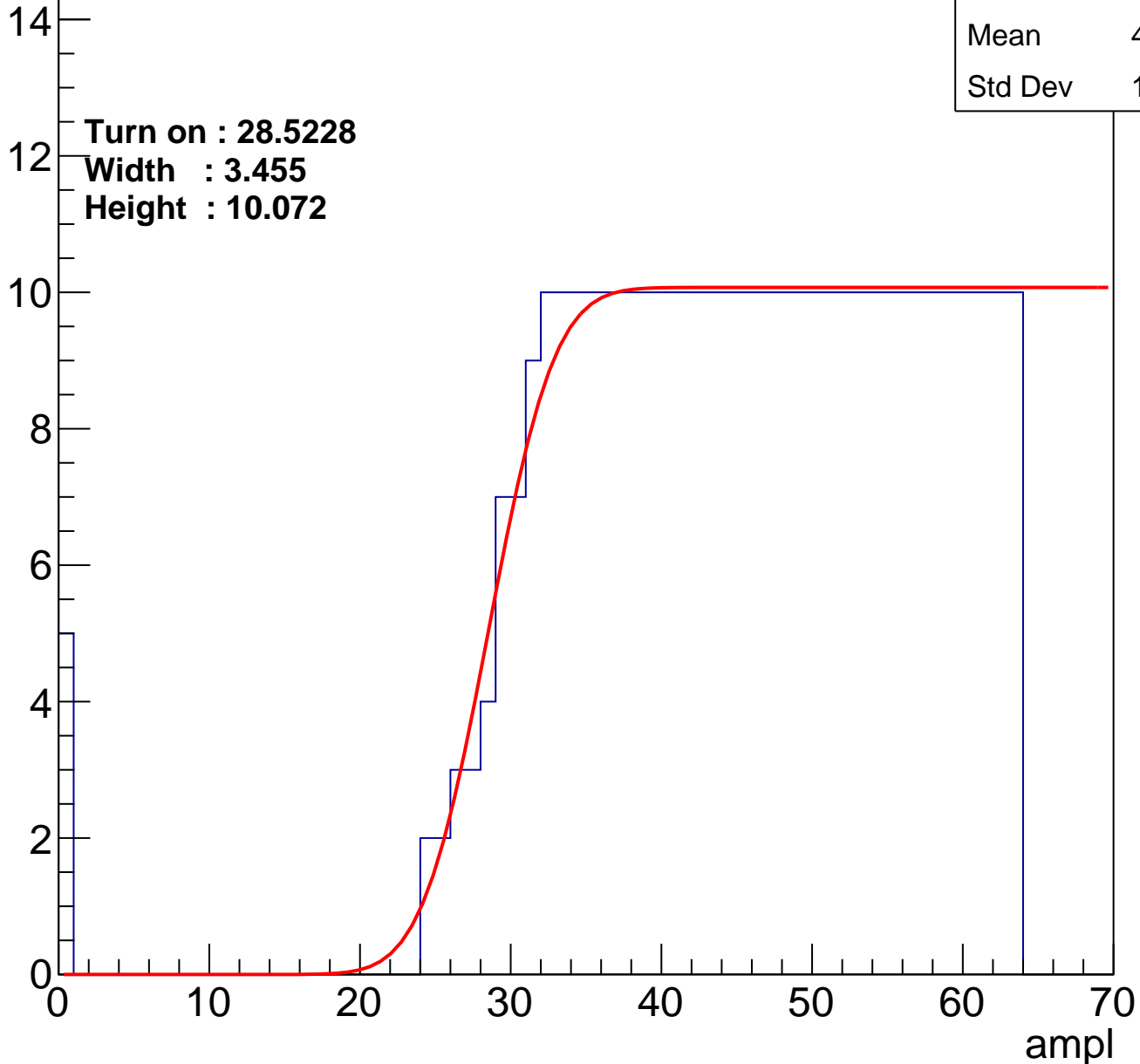
Entries	362
Mean	44.92
Std Dev	11.68

Turn on : 28.5228

Width : 3.455

Height : 10.072

Entry



B0L001S, U15-ch10

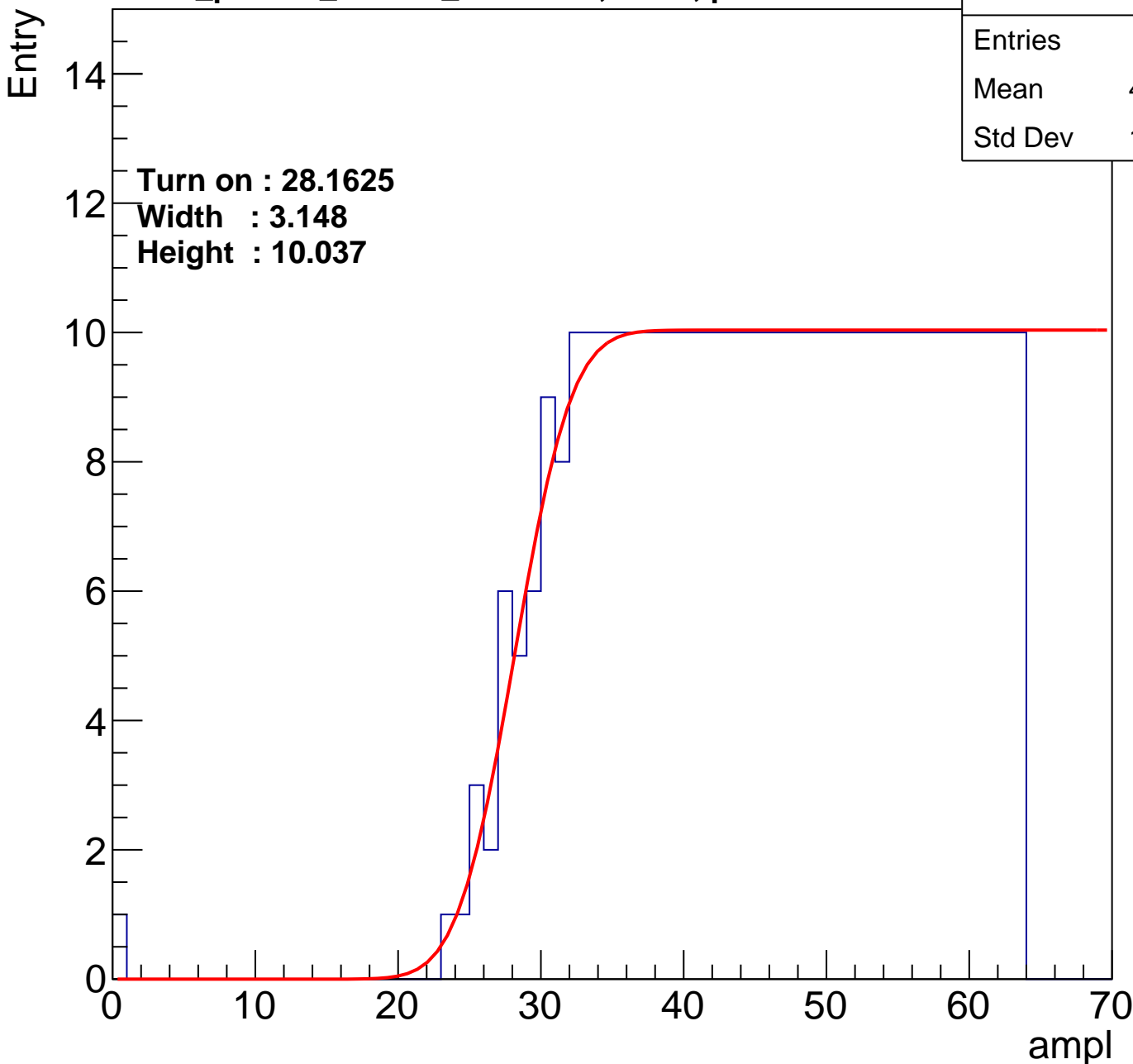
calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.22
Std Dev	10.85

Turn on : 28.1625

Width : 3.148

Height : 10.037



B0L001S, U15-ch11

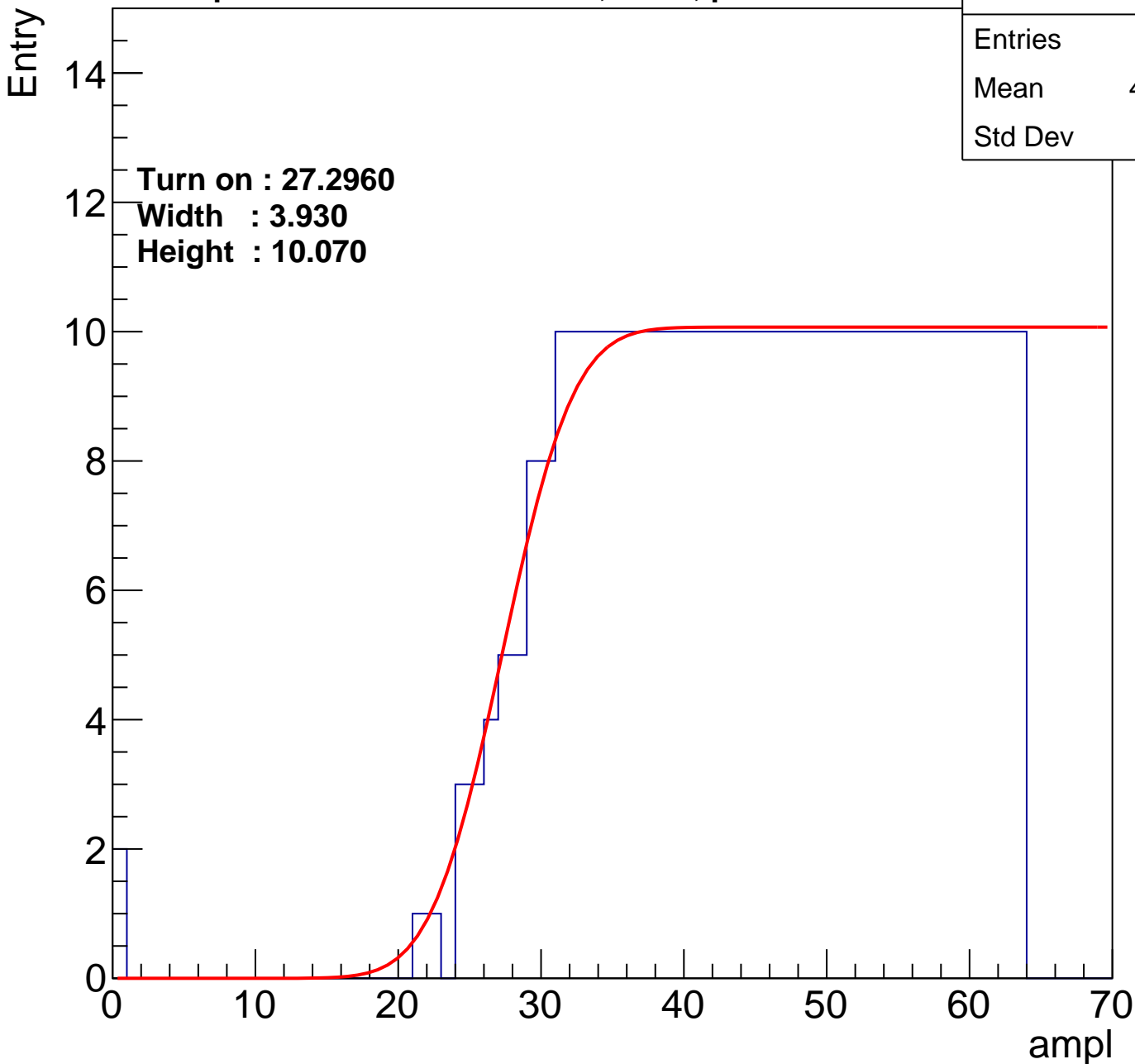
calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 27.2960

Width : 3.930

Height : 10.070



B0L001S, U15-ch12

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.76
Std Dev	11.16

Turn on : 27.3014

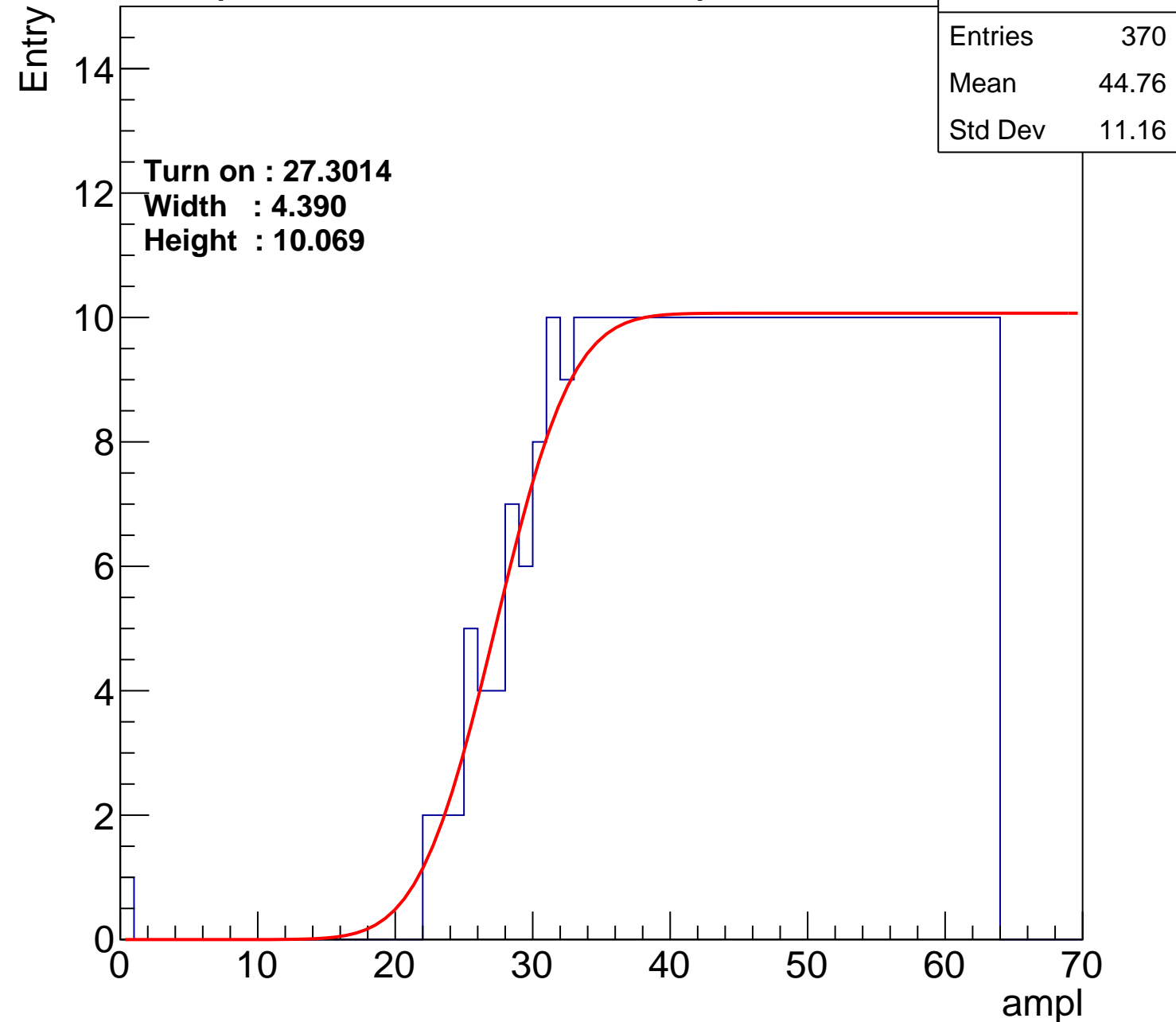
Width : 4.390

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch13

calib_packv5_042523_0143.root, FC#9, port A1

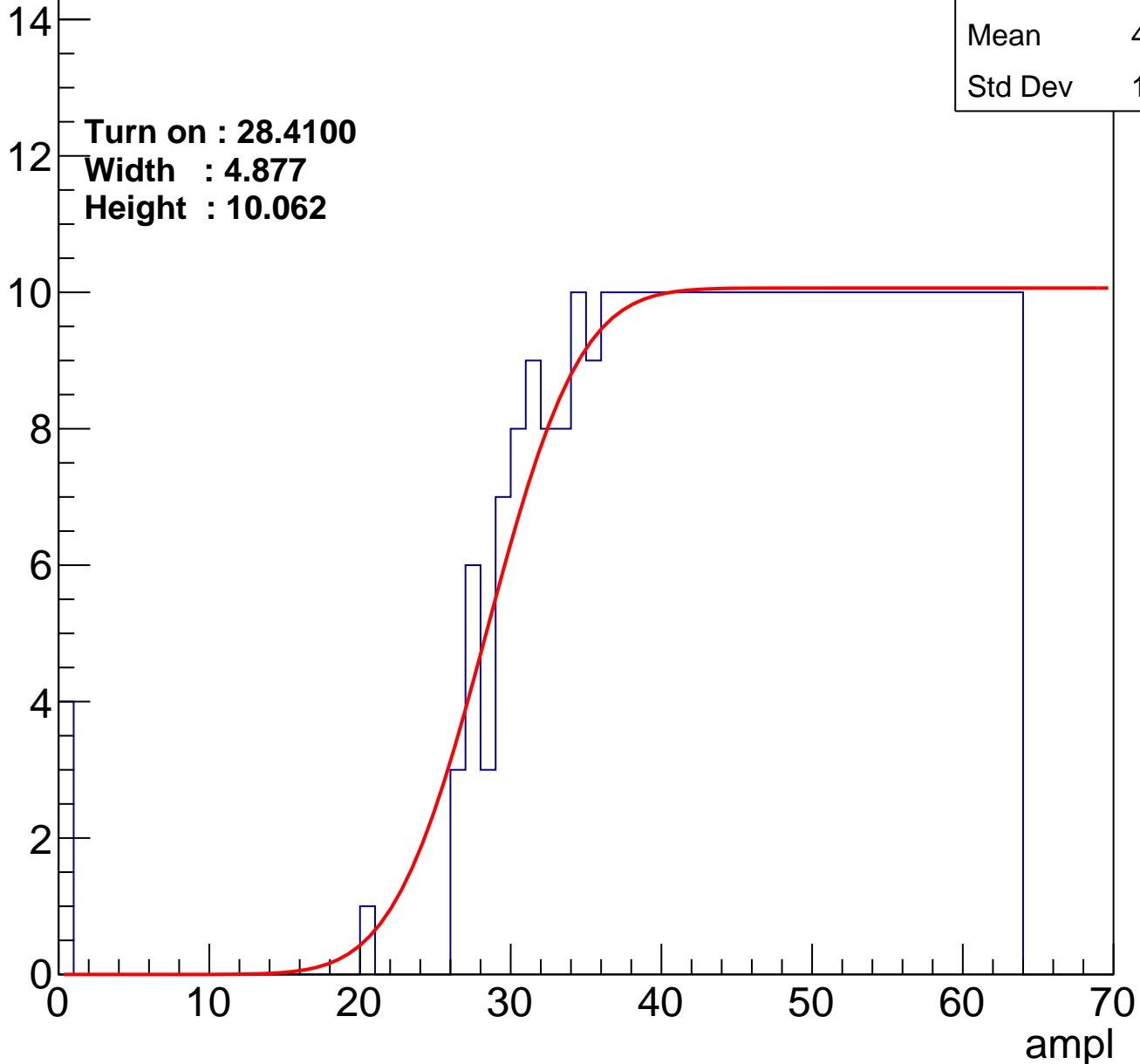
Entries	356
Mean	45.23
Std Dev	11.43

Turn on : 28.4100

Width : 4.877

Height : 10.062

Entry



B0L001S, U15-ch14

calib_packv5_042523_0143.root, FC#9, port A1

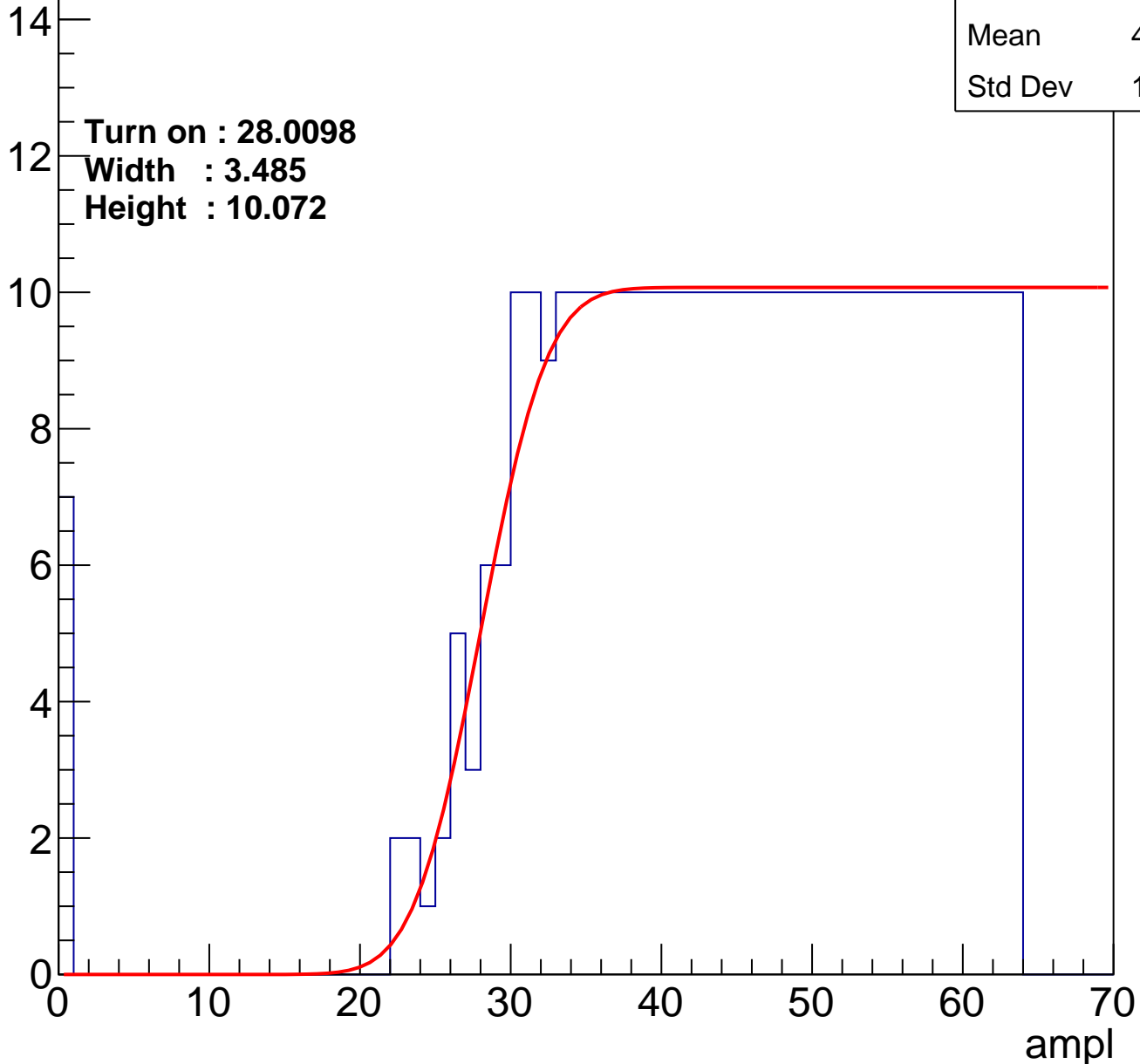
Entries	373
Mean	44.22
Std Dev	12.32

Turn on : 28.0098

Width : 3.485

Height : 10.072

Entry



B0L001S, U15-ch15

calib_packv5_042523_0143.root, FC#9, port A1

Entries	354
Mean	45.51
Std Dev	10.92

Turn on : 29.6648

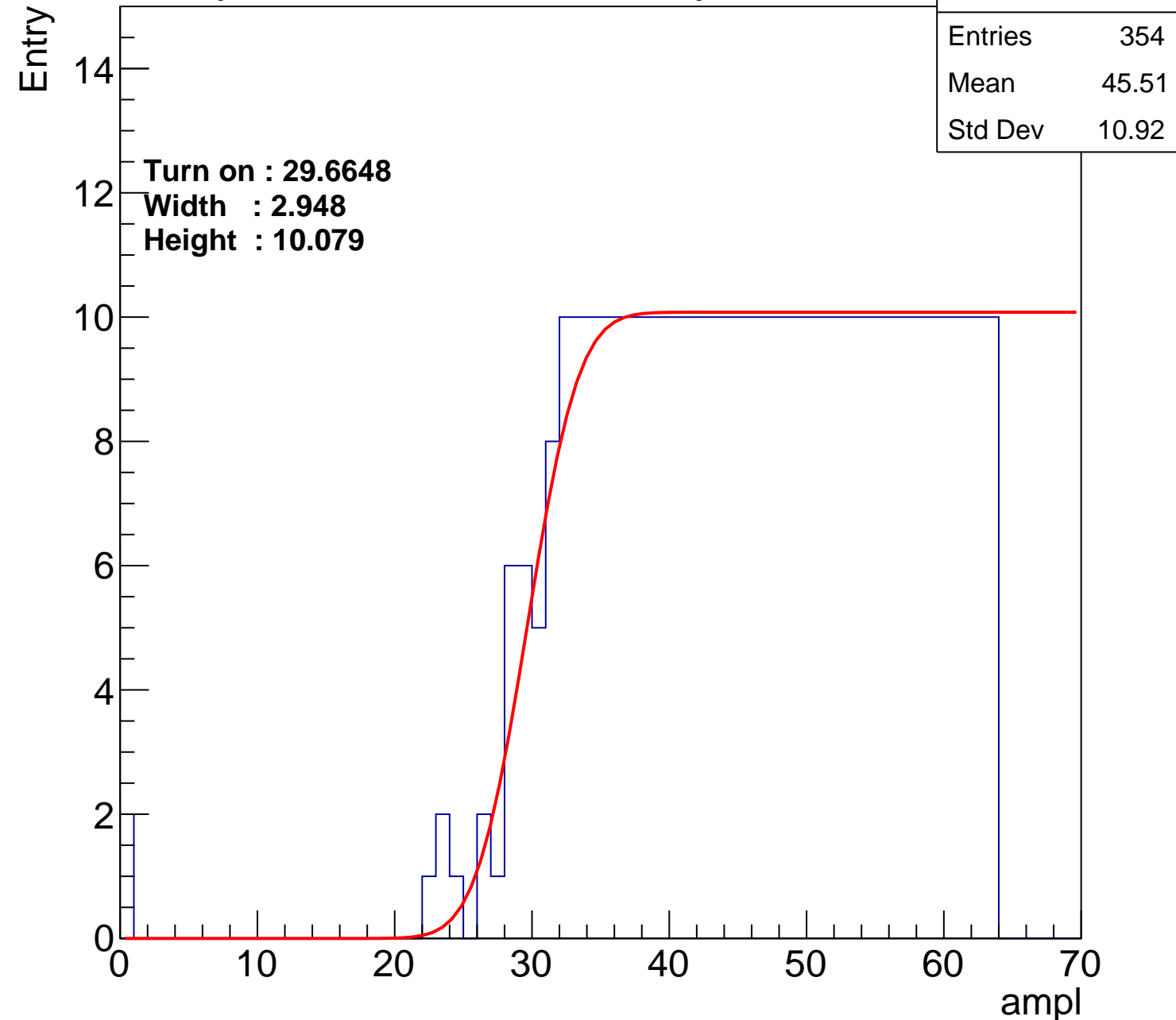
Width : 2.948

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch16

calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.69
Std Dev	11.63

Turn on : 27.5670

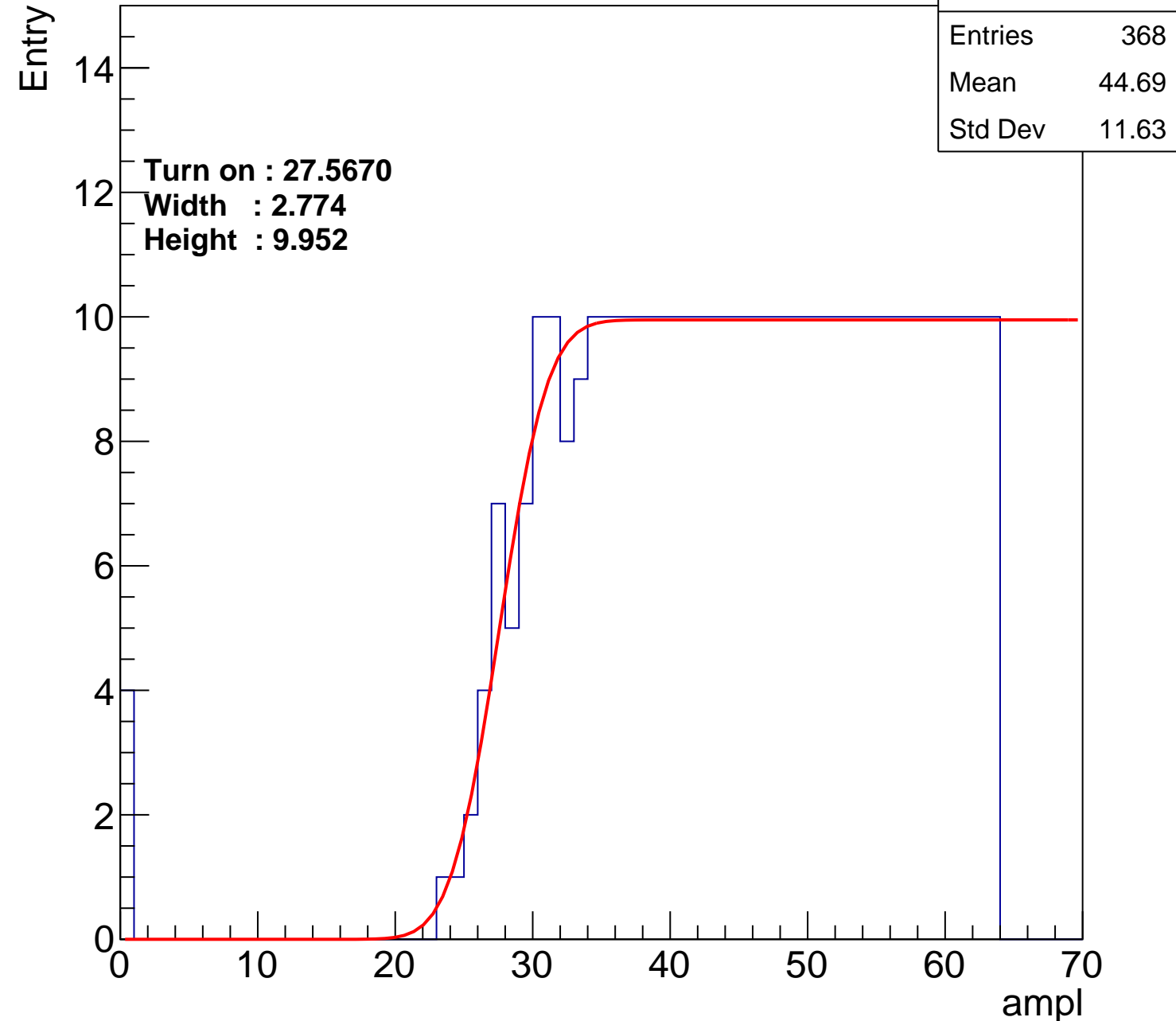
Width : 2.774

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch17

calib_packv5_042523_0143.root, FC#9, port A1

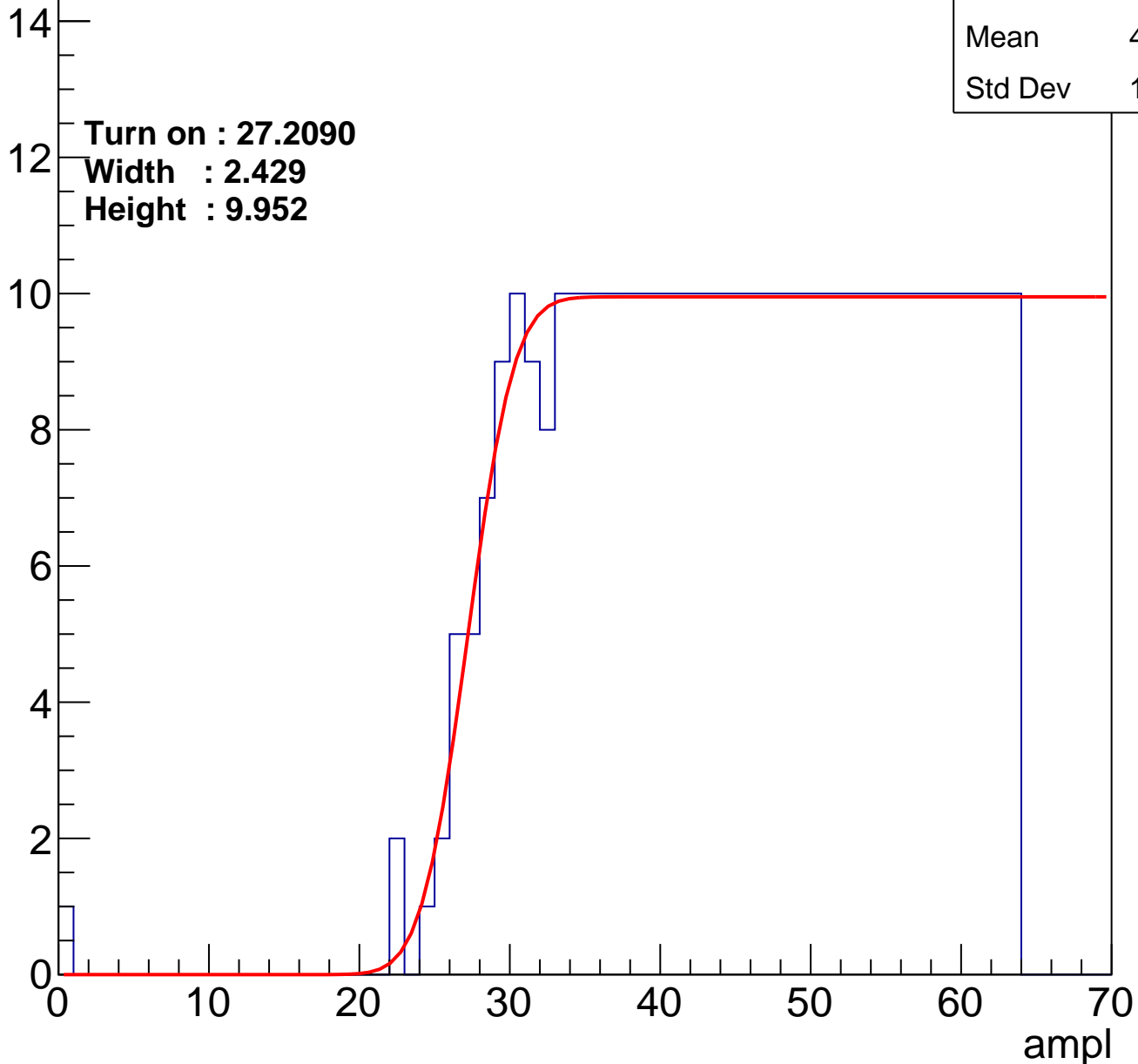
Entries	369
Mean	44.86
Std Dev	11.05

Turn on : 27.2090

Width : 2.429

Height : 9.952

Entry



B0L001S, U15-ch18

calib_packv5_042523_0143.root, FC#9, port A1

Entries	377
Mean	44.2
Std Dev	11.99

Turn on : 26.8187

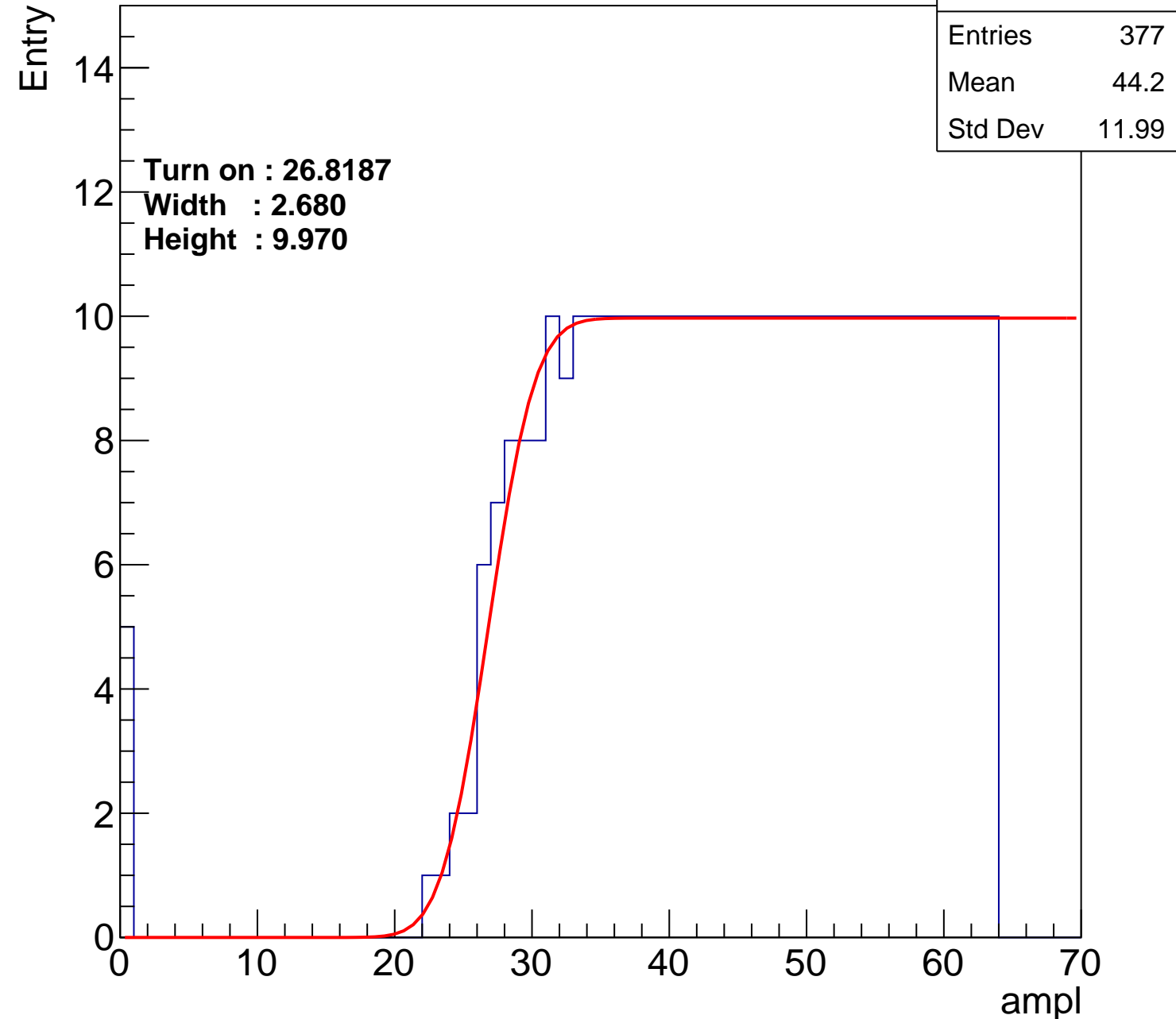
Width : 2.680

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch19

calib_packv5_042523_0143.root, FC#9, port A1

Entries	341
Mean	46.1
Std Dev	10.66

Turn on : 30.9715

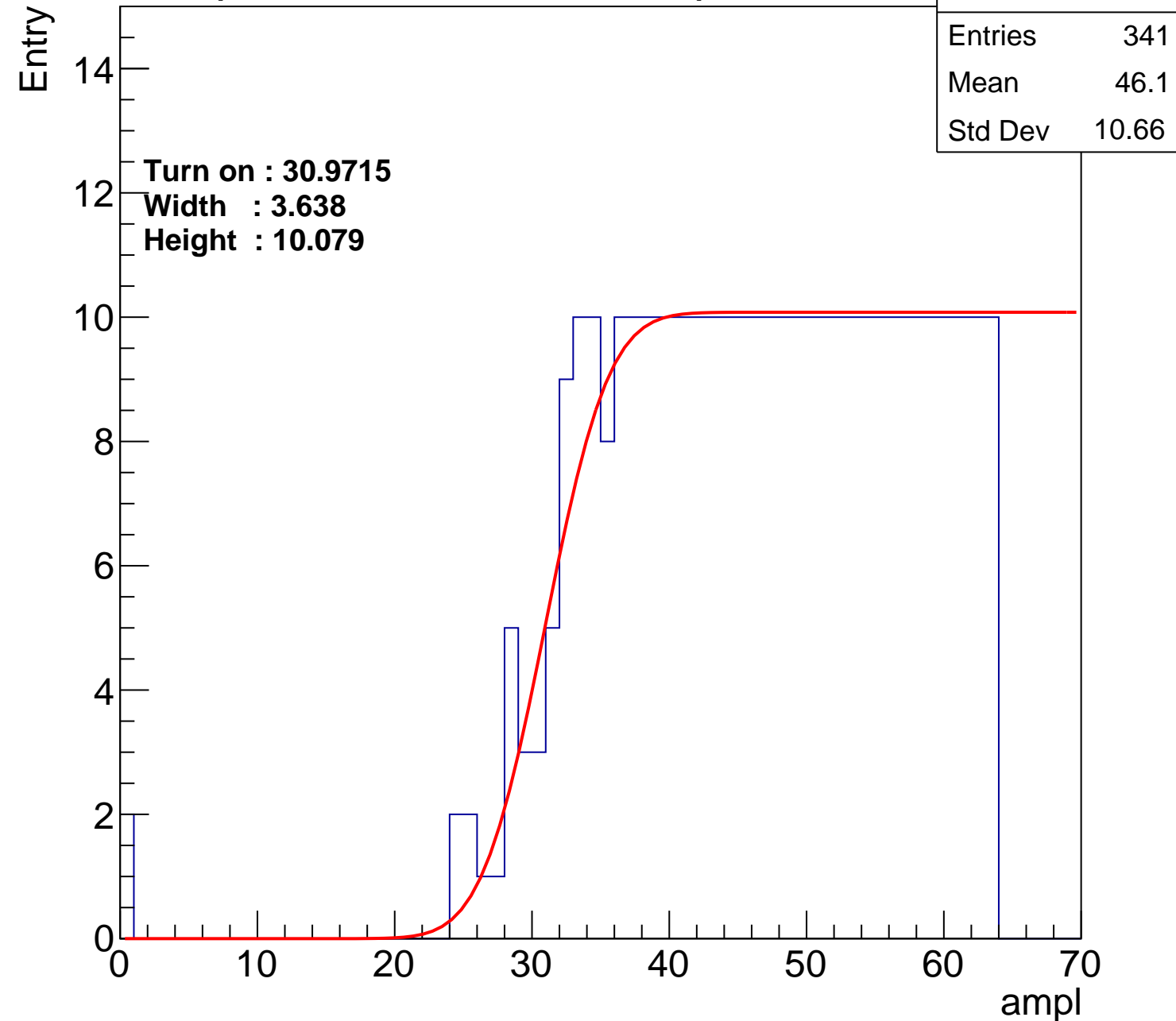
Width : 3.638

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch20

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.17
Std Dev	11.28

Turn on : 28.7099

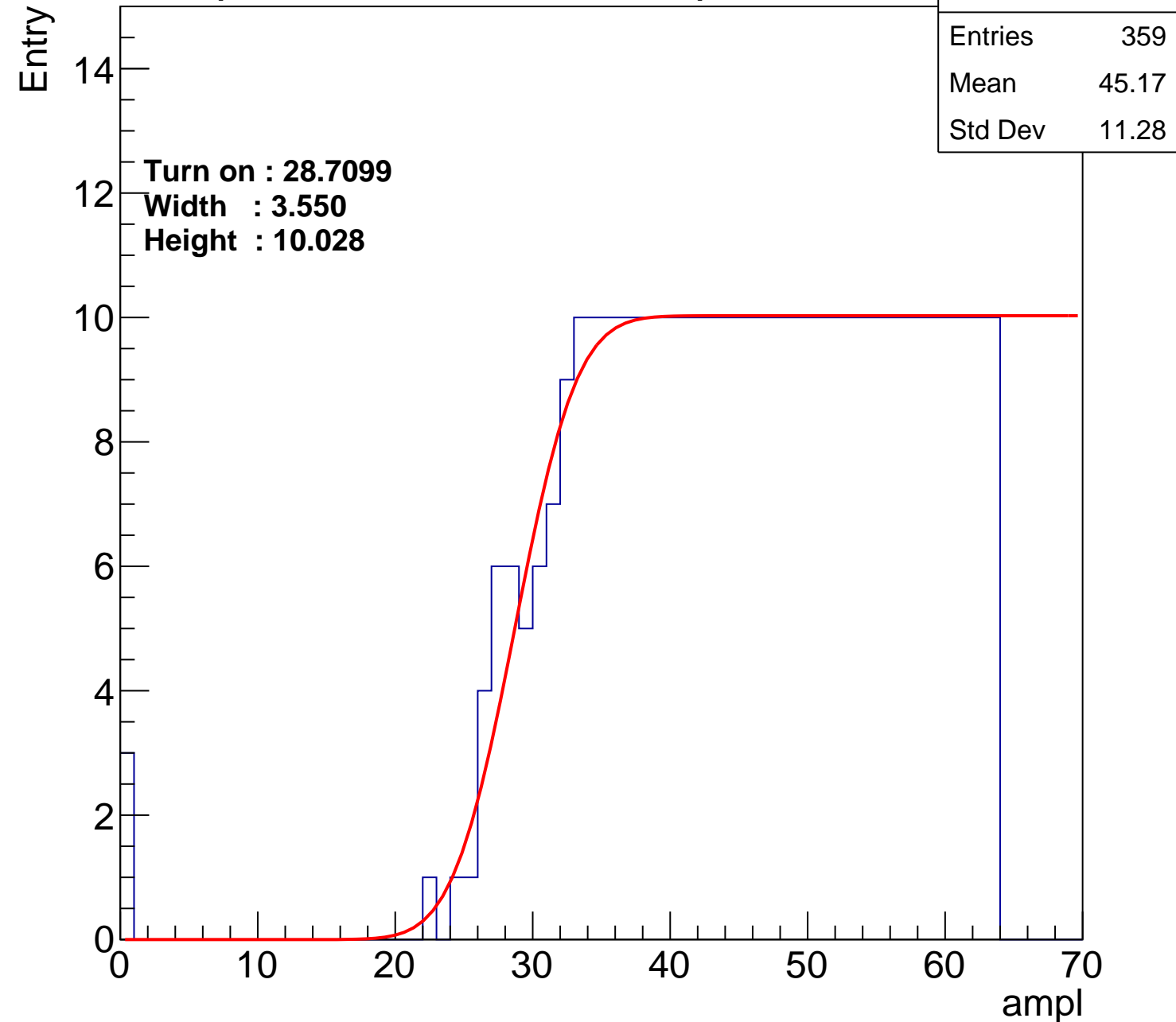
Width : 3.550

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch21

calib_packv5_042523_0143.root, FC#9, port A1

Entries	341
Mean	46.03
Std Dev	10.89

Turn on : 30.4800

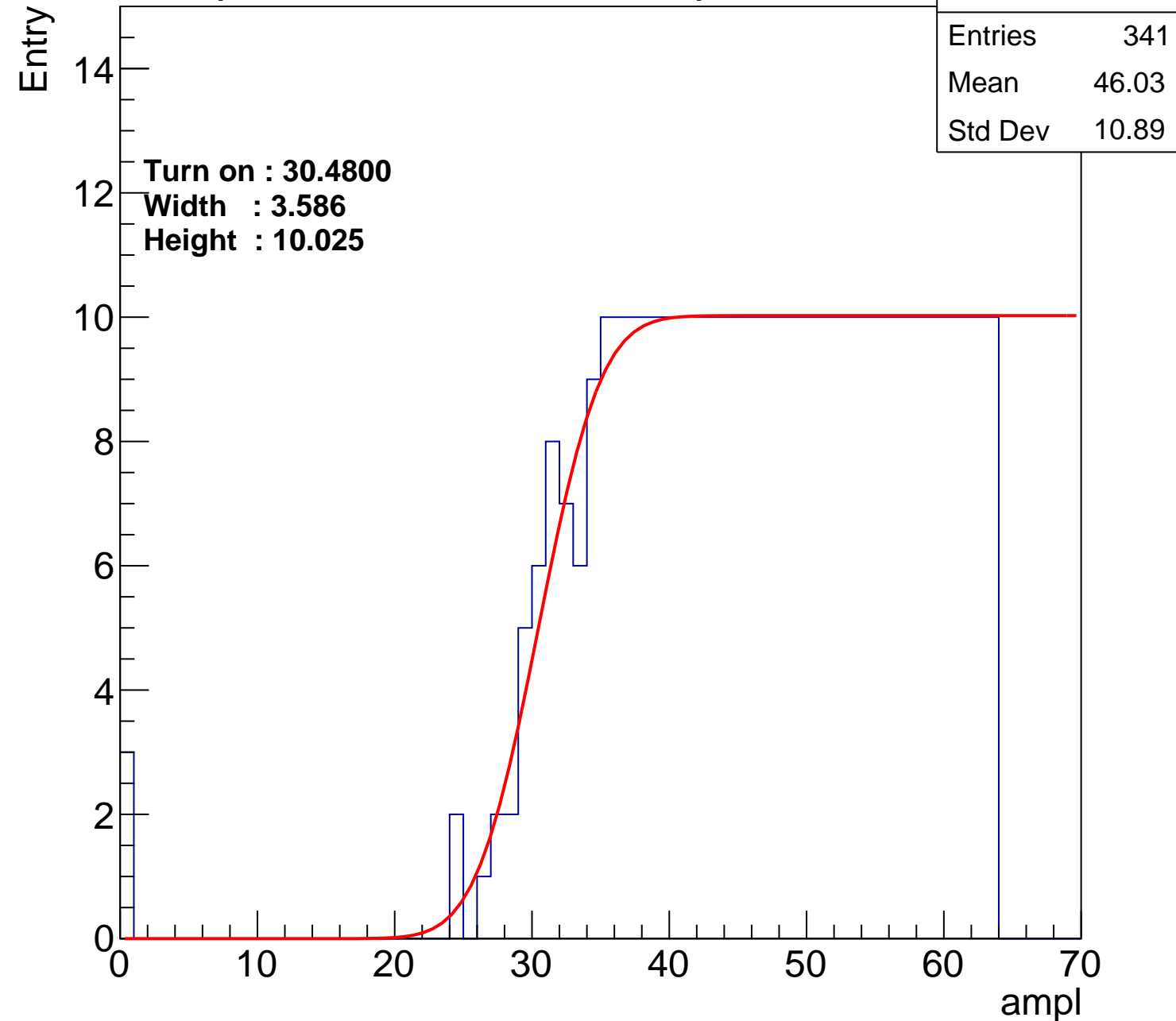
Width : 3.586

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch22

calib_packv5_042523_0143.root, FC#9, port A1

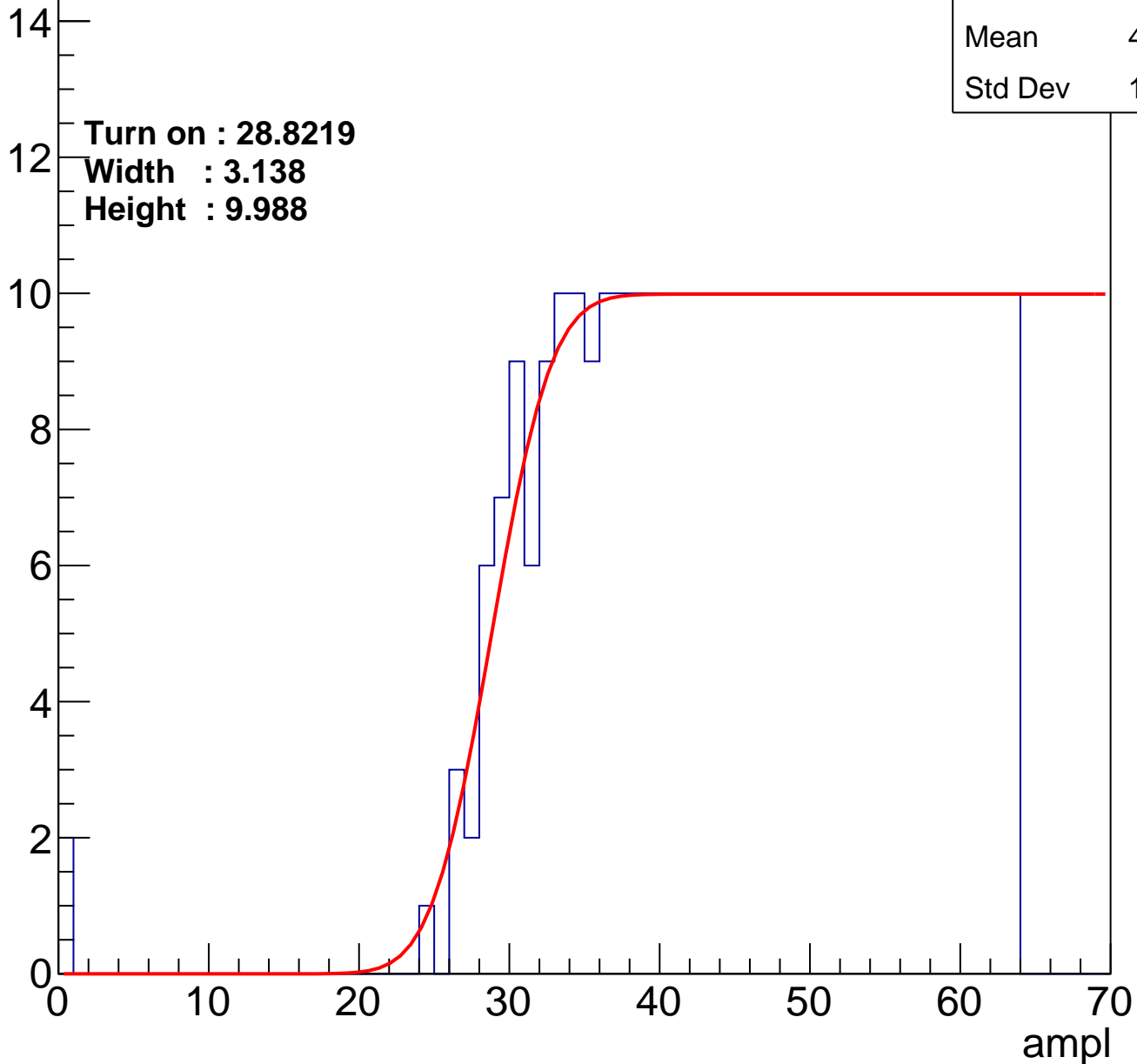
Entries	354
Mean	45.53
Std Dev	10.87

Turn on : 28.8219

Width : 3.138

Height : 9.988

Entry



B0L001S, U15-ch23

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.59
Std Dev	11.37

Turn on : 27.6668

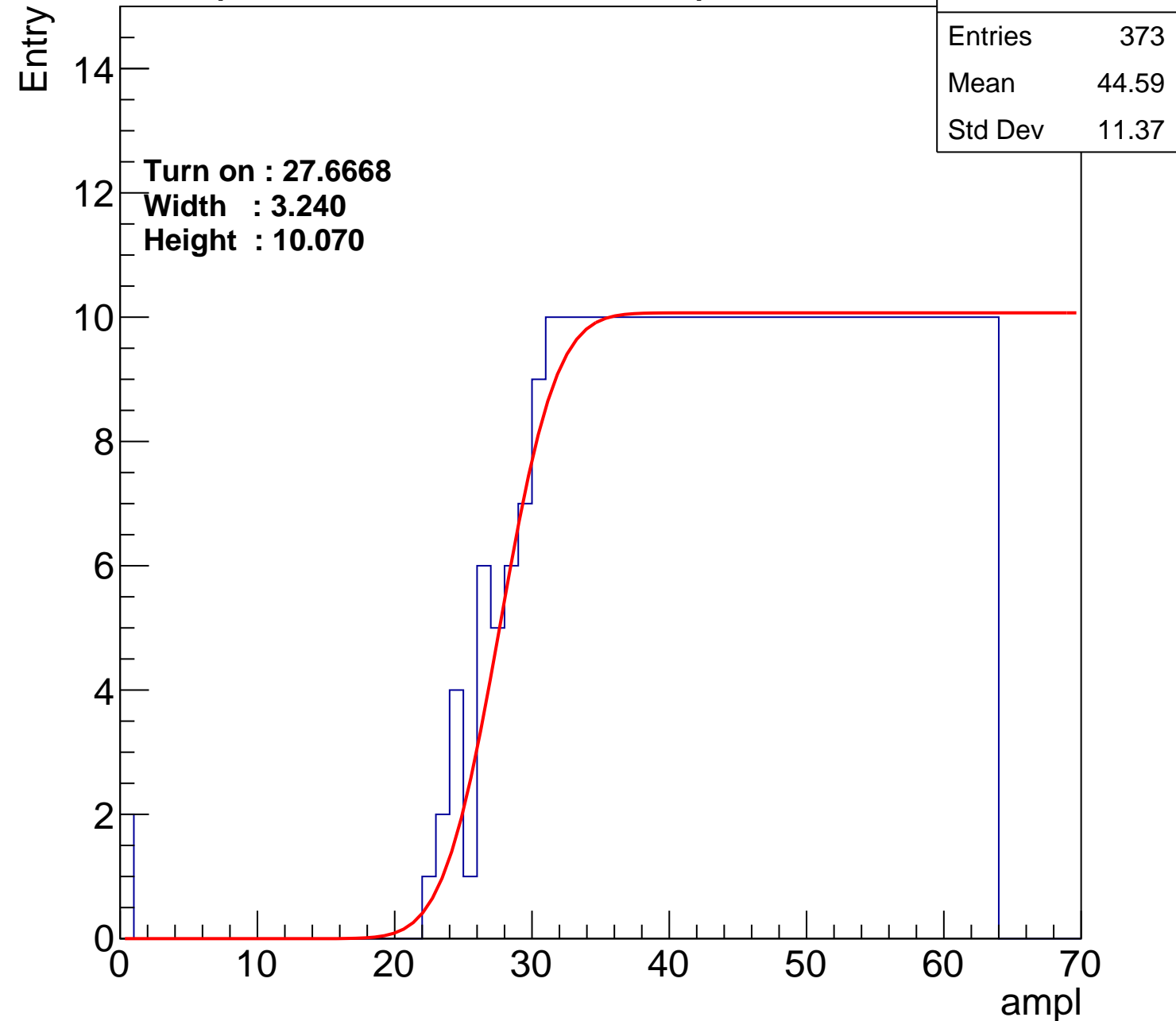
Width : 3.240

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch24

calib_packv5_042523_0143.root, FC#9, port A1

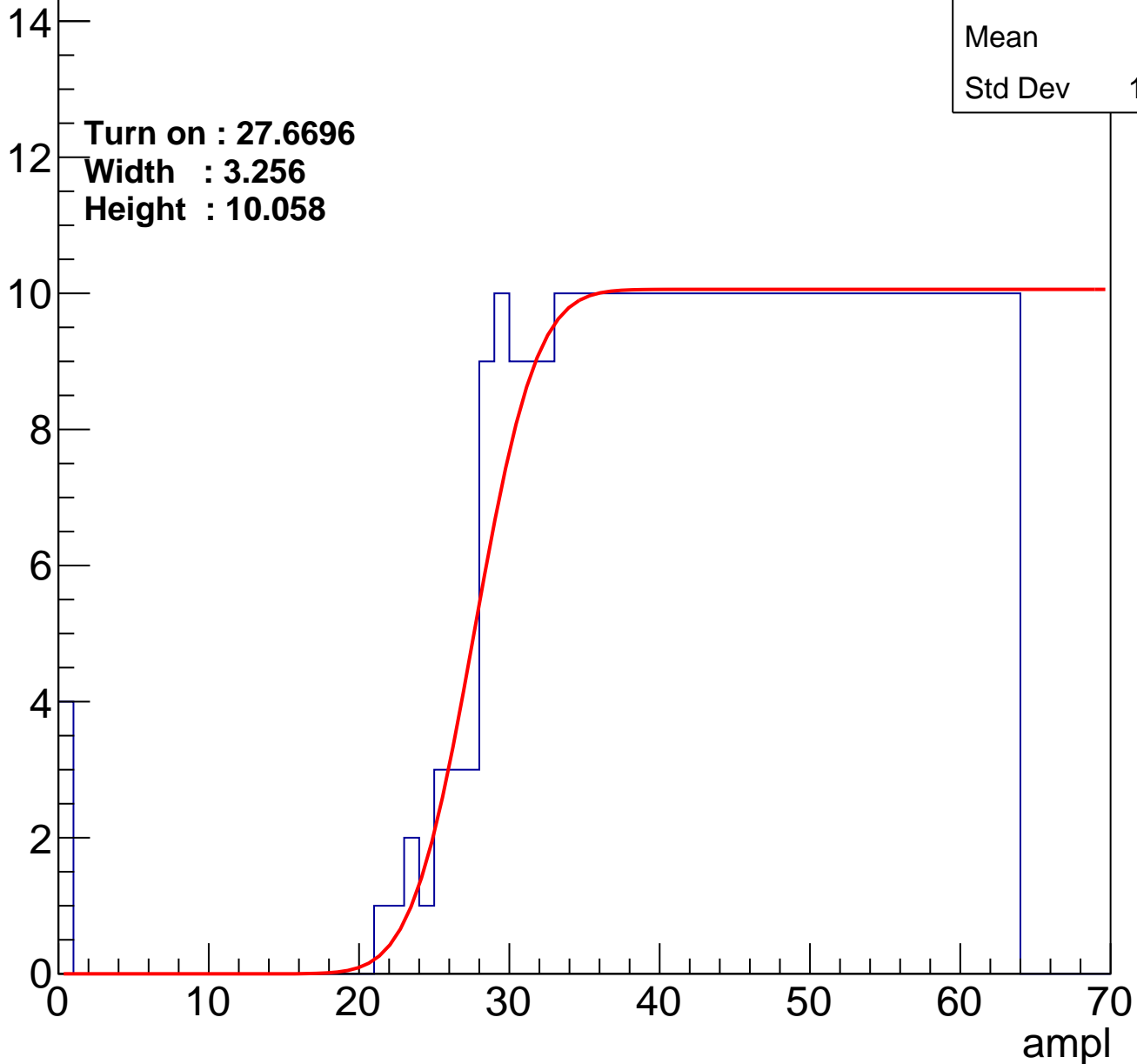
Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 27.6696

Width : 3.256

Height : 10.058

Entry



B0L001S, U15-ch25

calib_packv5_042523_0143.root, FC#9, port A1

Entries	347
Mean	45.75
Std Dev	11

Turn on : 29.2586

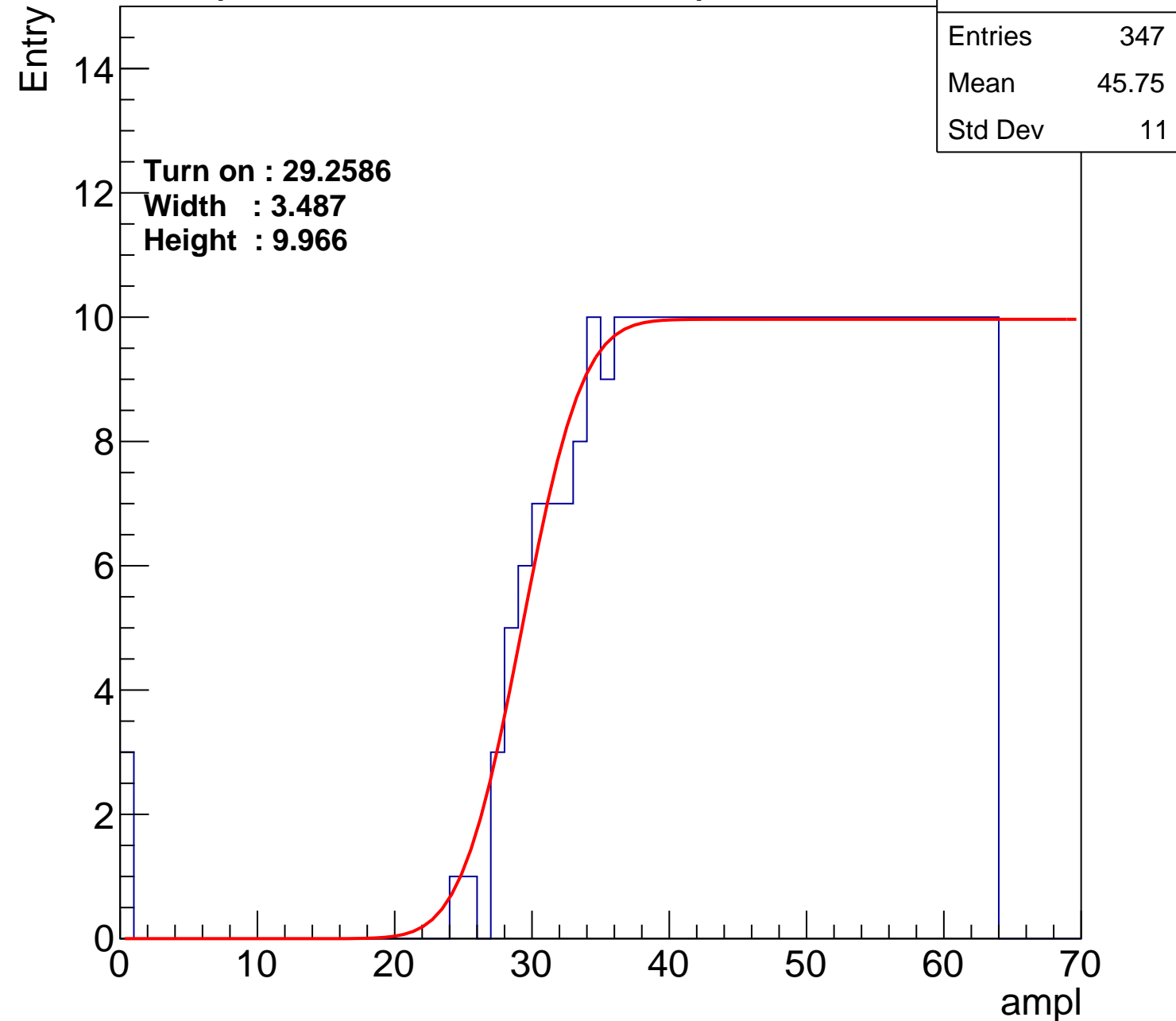
Width : 3.487

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch26

calib_packv5_042523_0143.root, FC#9, port A1

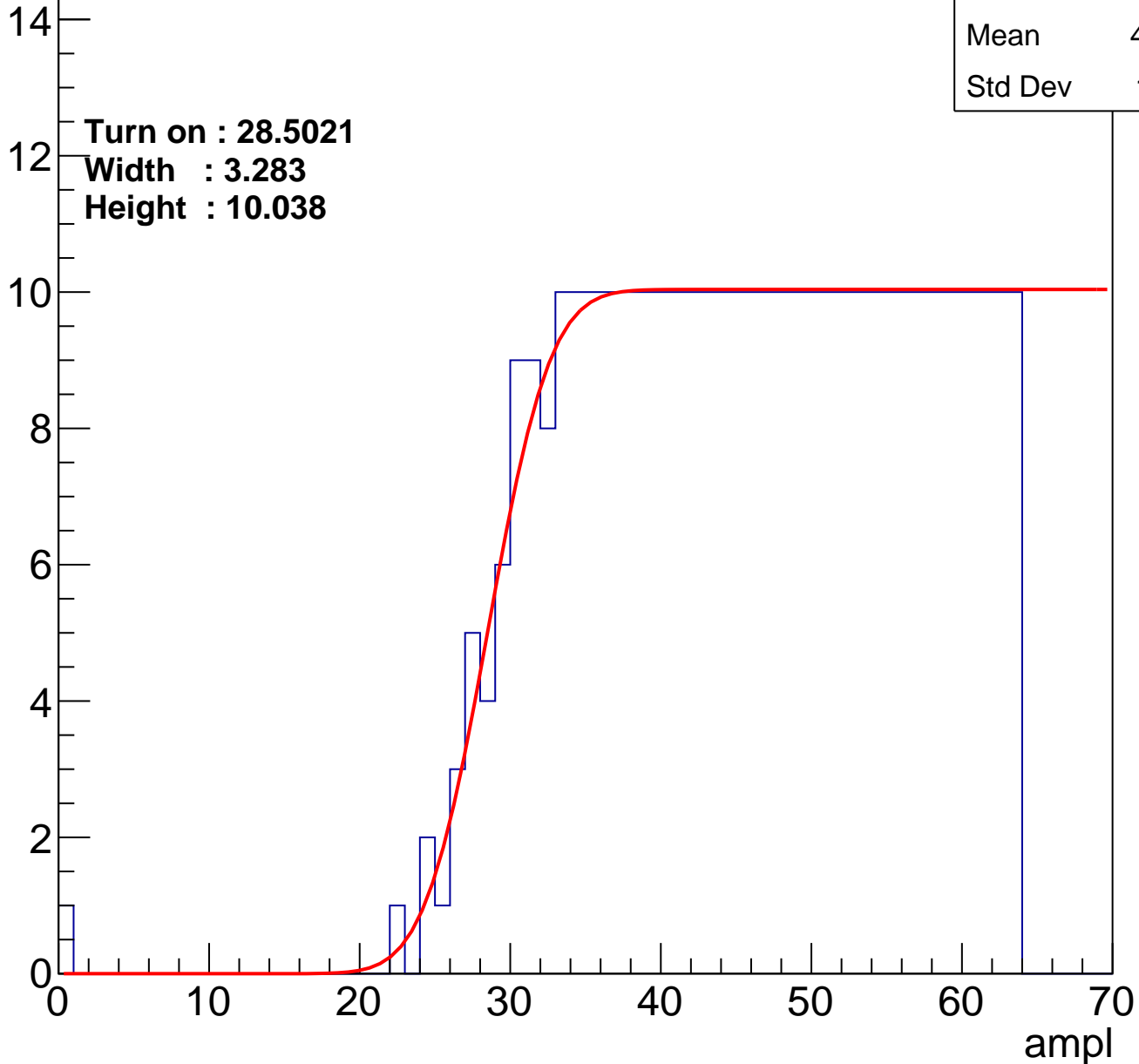
Entries	359
Mean	45.35
Std Dev	10.81

Turn on : 28.5021

Width : 3.283

Height : 10.038

Entry



B0L001S, U15-ch27

calib_packv5_042523_0143.root, FC#9, port A1

Entries	381
Mean	44.18
Std Dev	11.59

Turn on : 26.1740

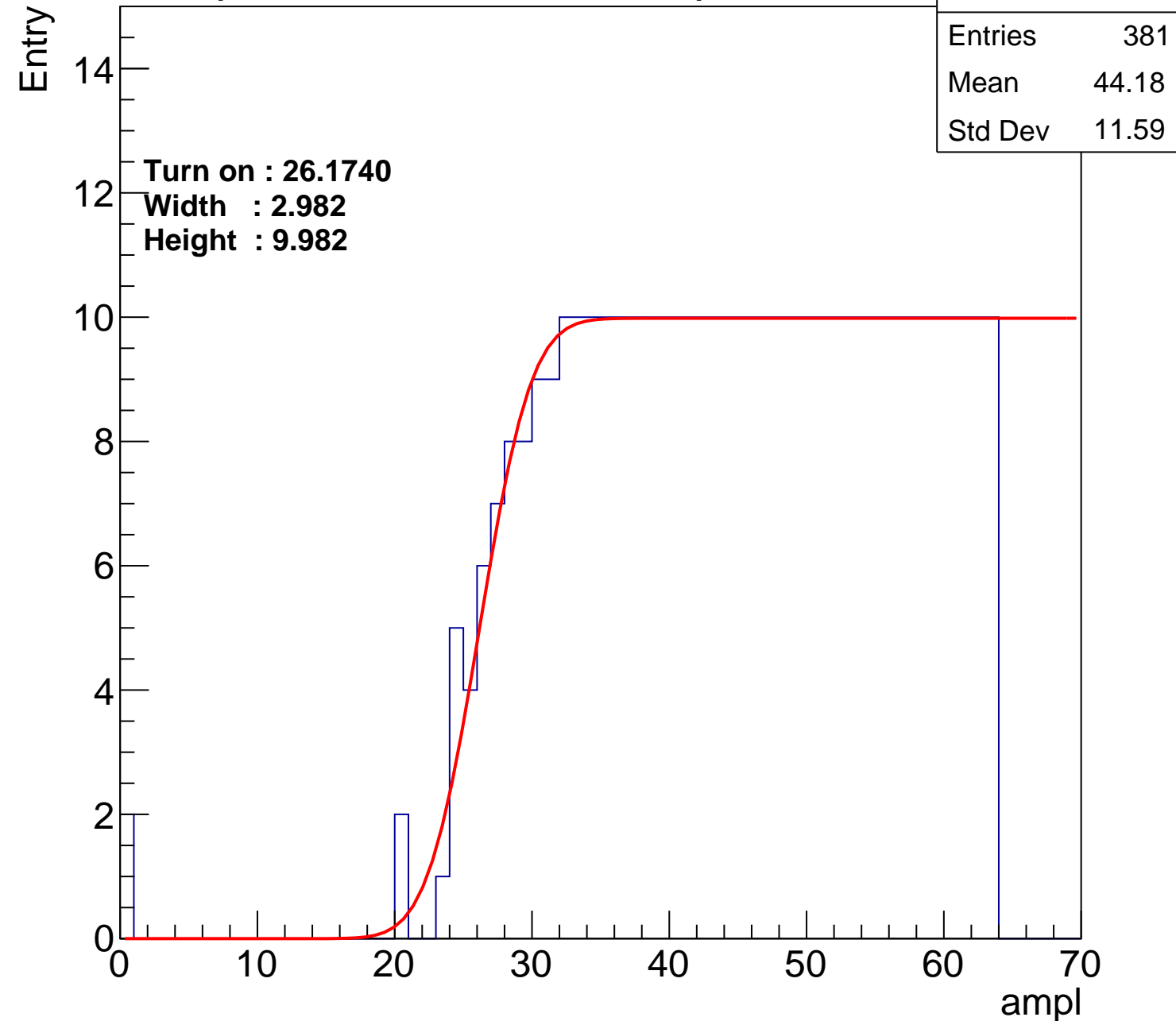
Width : 2.982

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch28

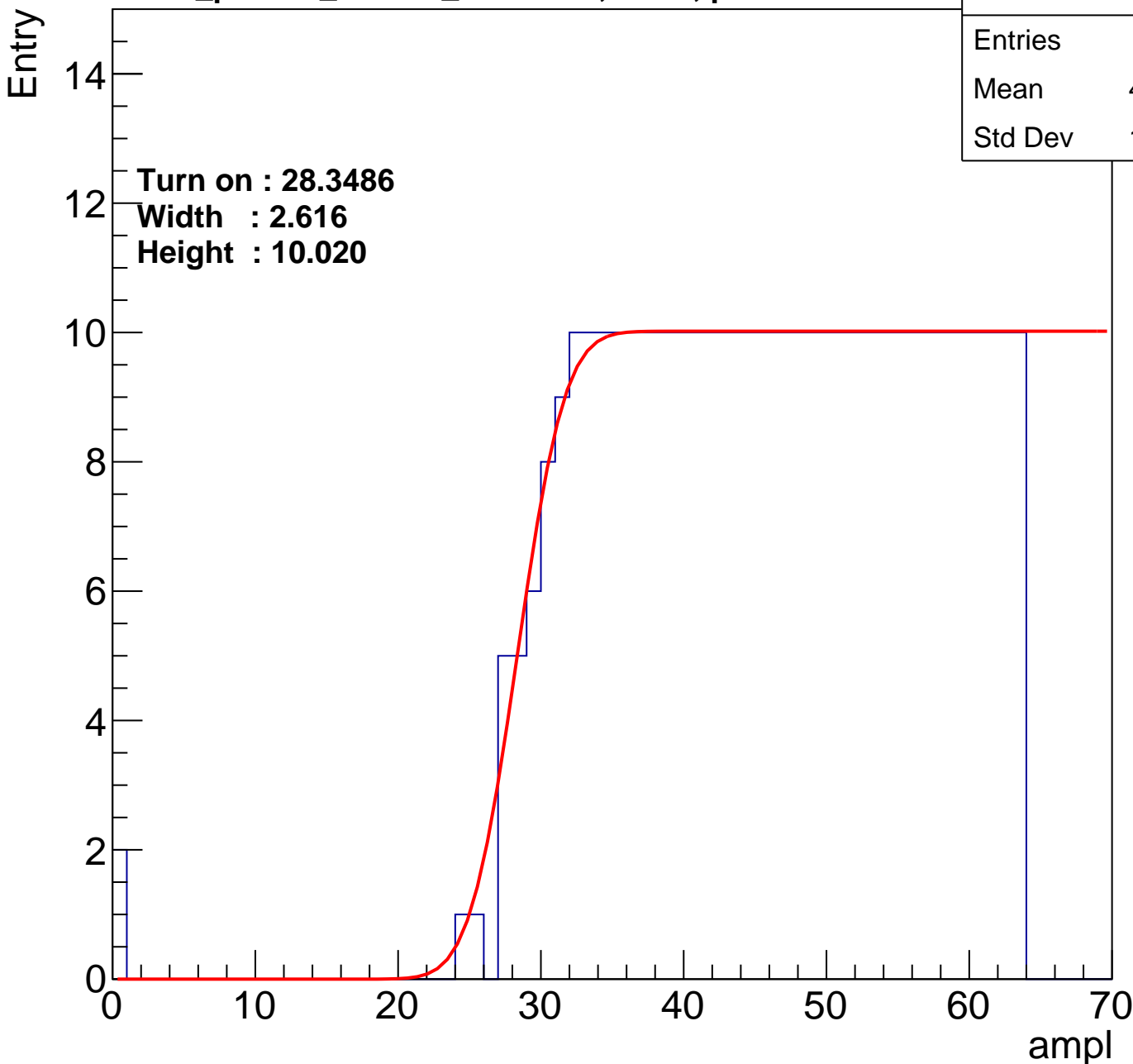
calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.43
Std Dev	10.88

Turn on : 28.3486

Width : 2.616

Height : 10.020



B0L001S, U15-ch29

calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.65
Std Dev	11.79

Turn on : 27.9617

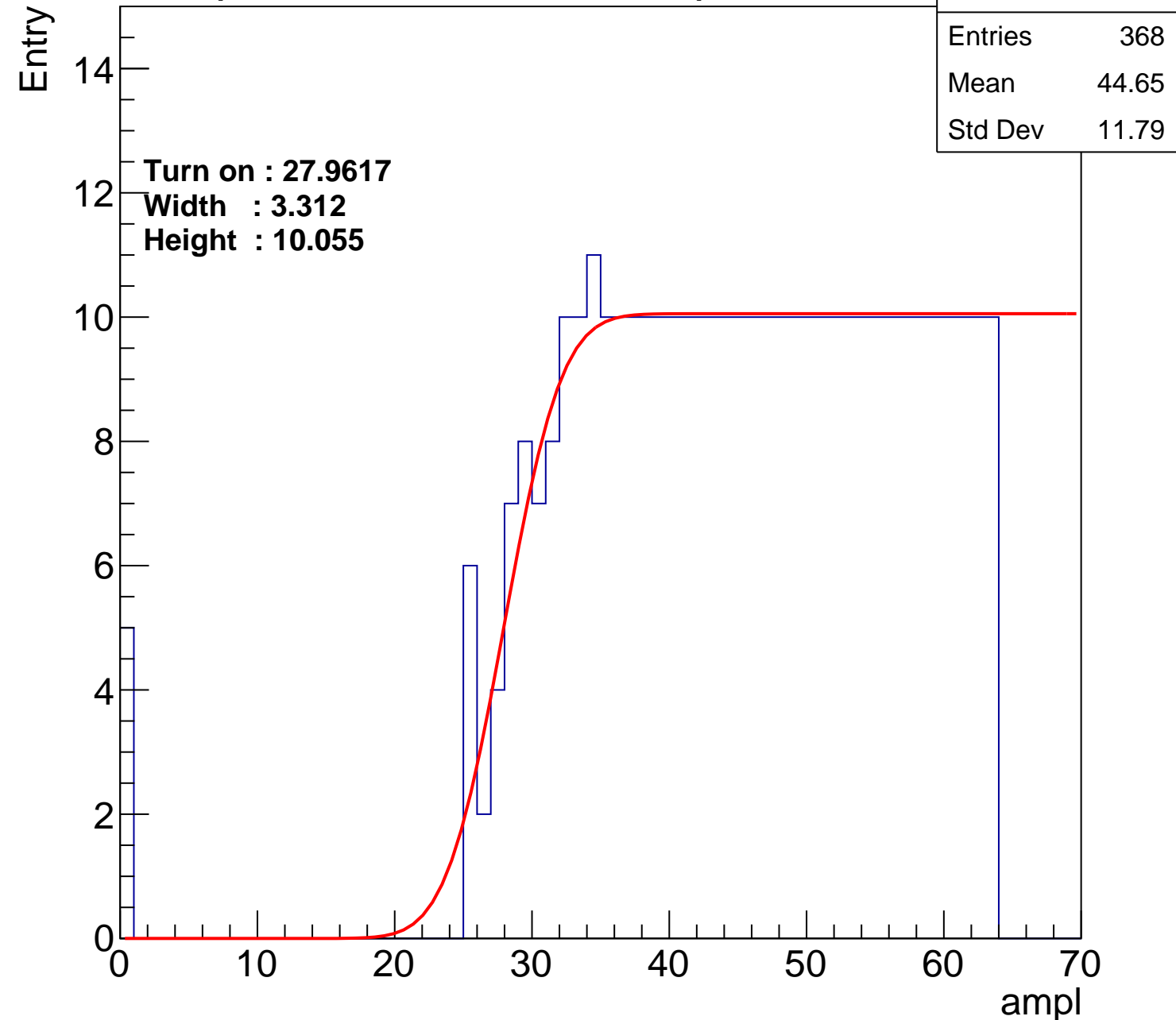
Width : 3.312

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch30

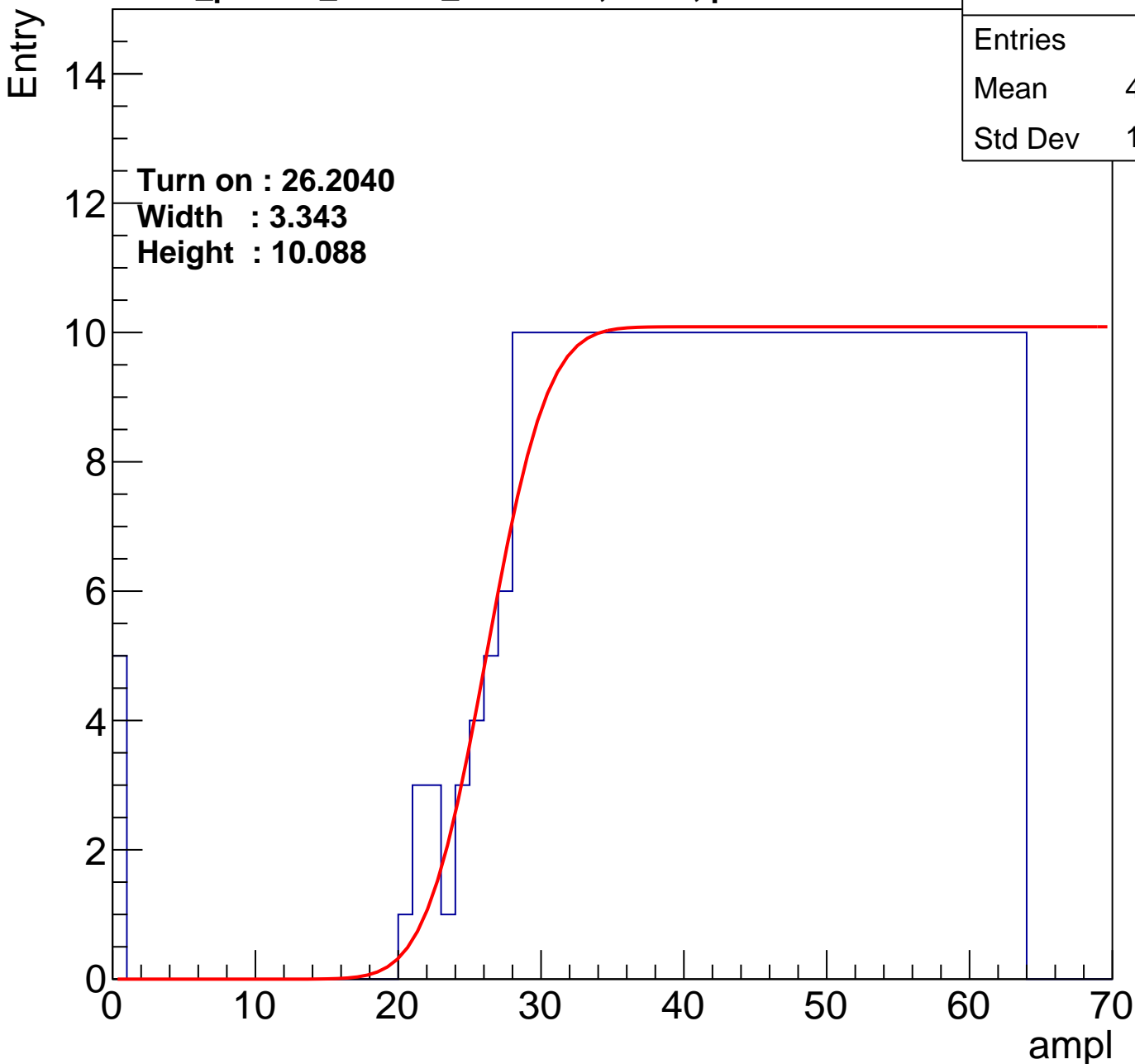
calib_packv5_042523_0143.root, FC#9, port A1

Entries	391
Mean	43.52
Std Dev	12.32

Turn on : 26.2040

Width : 3.343

Height : 10.088



B0L001S, U15-ch31

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.88
Std Dev	11.55

Turn on : 28.8796

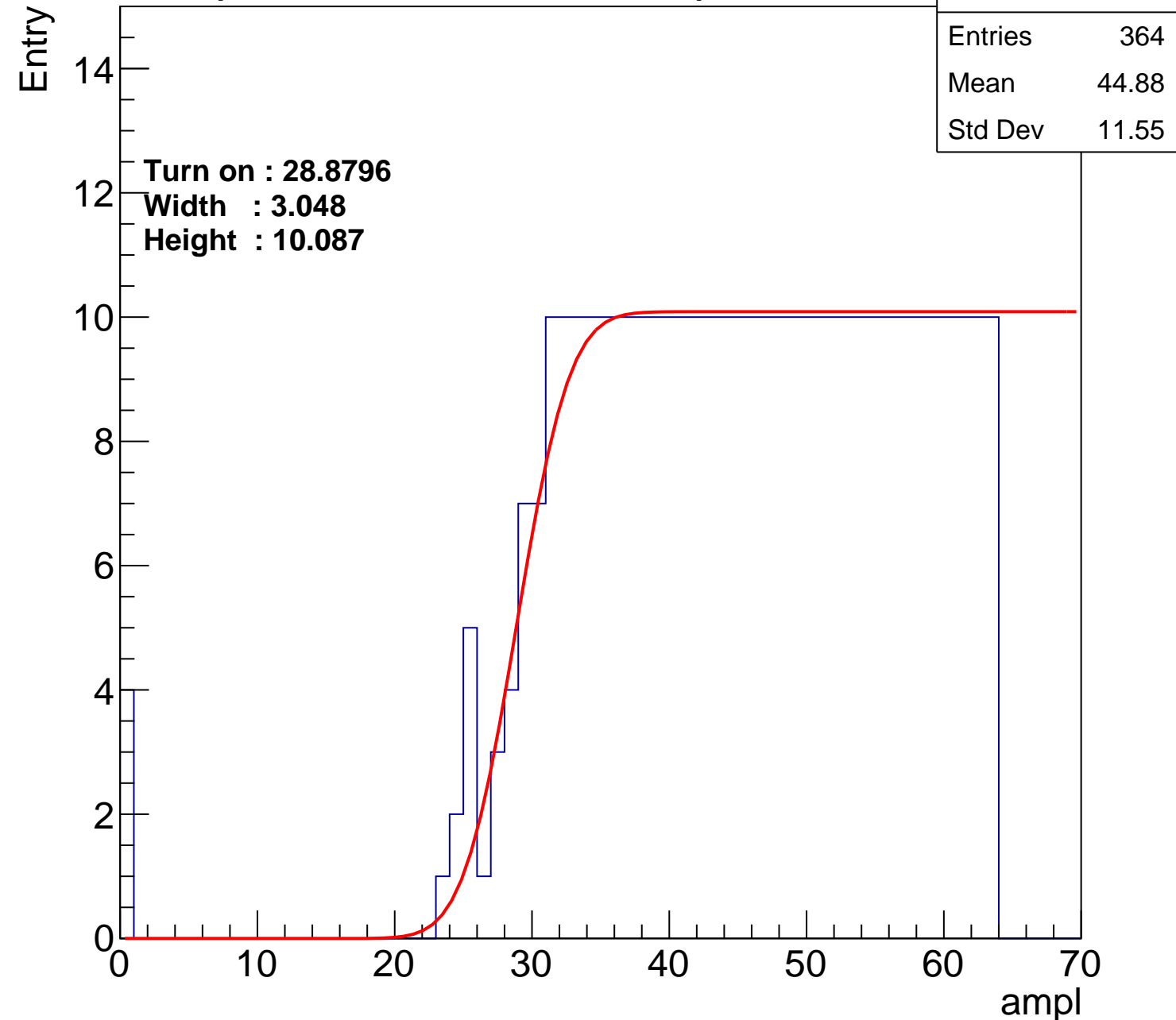
Width : 3.048

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch32

calib_packv5_042523_0143.root, FC#9, port A1

Entries	353
Mean	45.63
Std Dev	10.65

Turn on : 28.7099

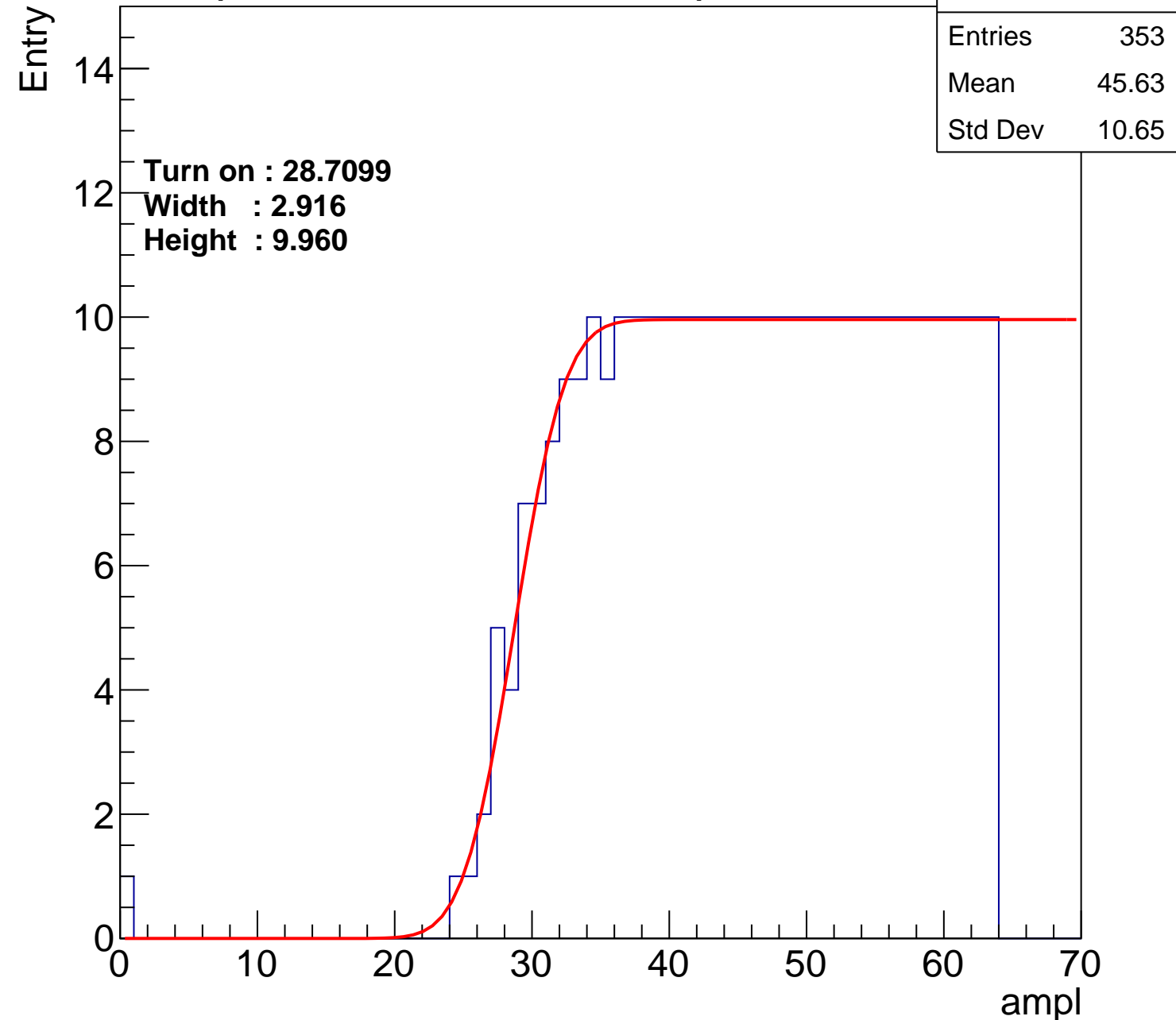
Width : 2.916

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch33

calib_packv5_042523_0143.root, FC#9, port A1

Entries	383
Mean	43.92
Std Dev	12.11

Turn on : 26.4911

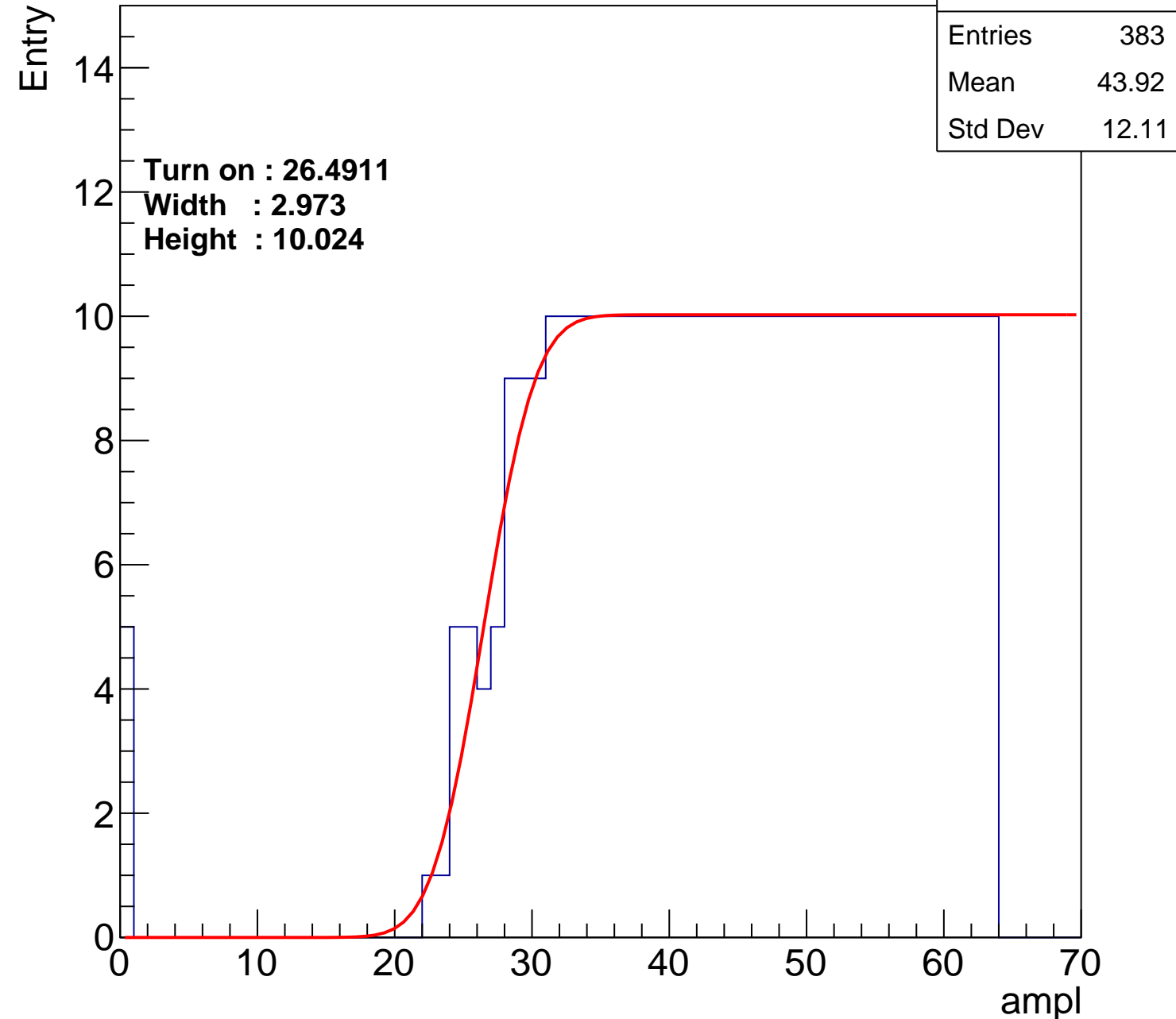
Width : 2.973

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch34

calib_packv5_042523_0143.root, FC#9, port A1

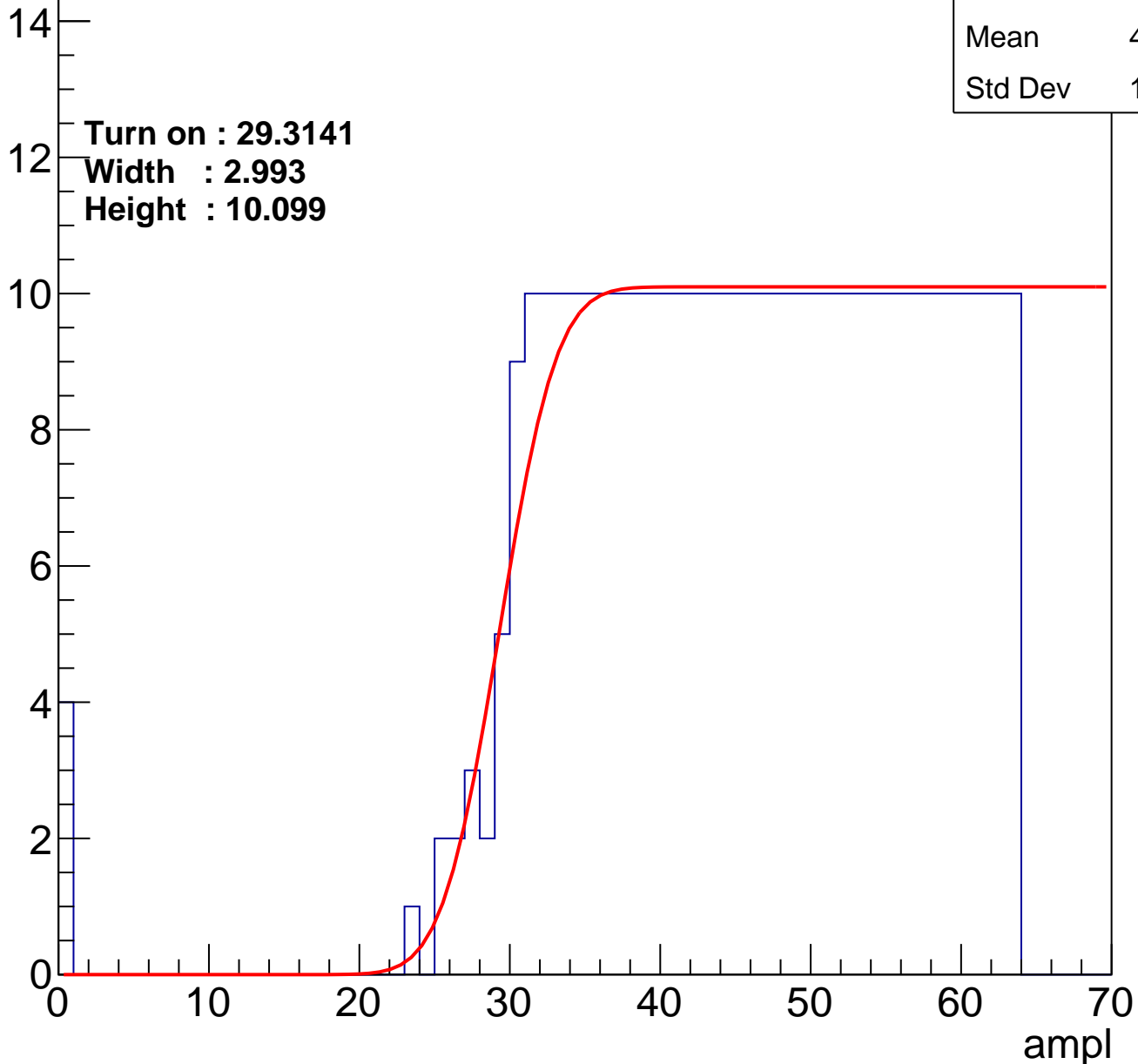
Entries	358
Mean	45.22
Std Dev	11.36

Turn on : 29.3141

Width : 2.993

Height : 10.099

Entry



B0L001S, U15-ch35

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.4930

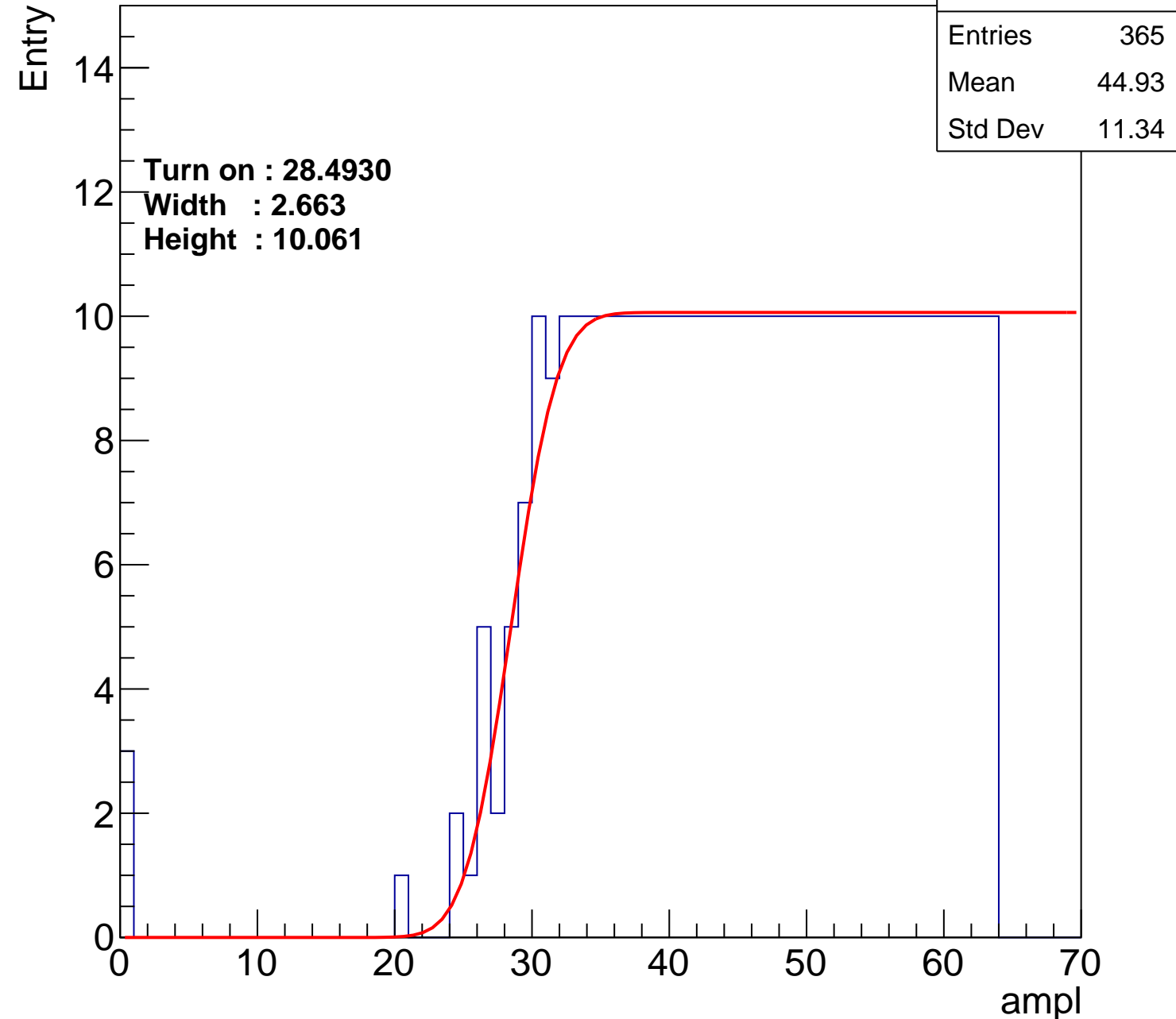
Width : 2.663

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch36

calib_packv5_042523_0143.root, FC#9, port A1

Entries	345
Mean	45.98
Std Dev	10.64

Turn on : 29.8364

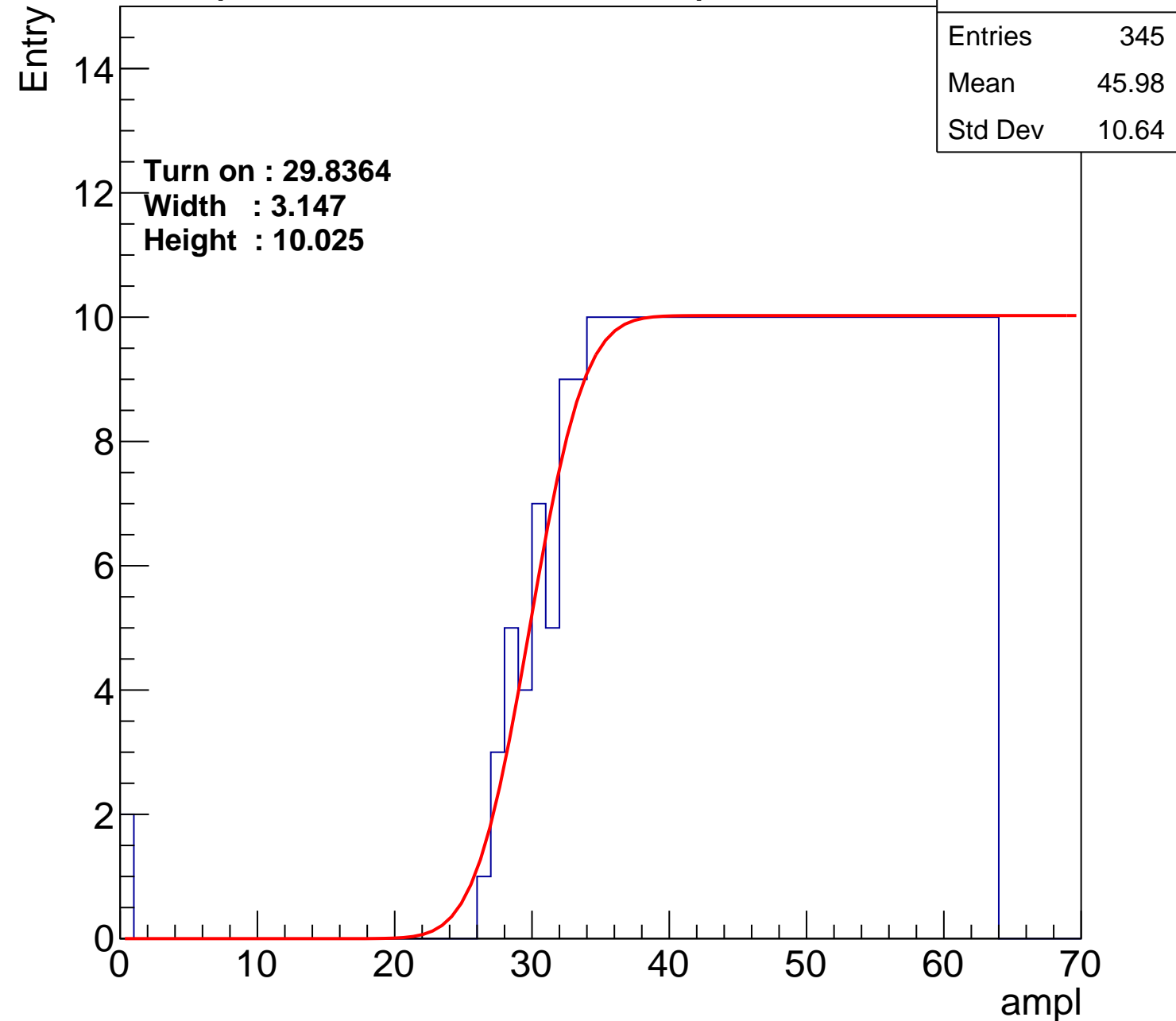
Width : 3.147

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch37

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.89
Std Dev	11.68

Turn on : 27.9632

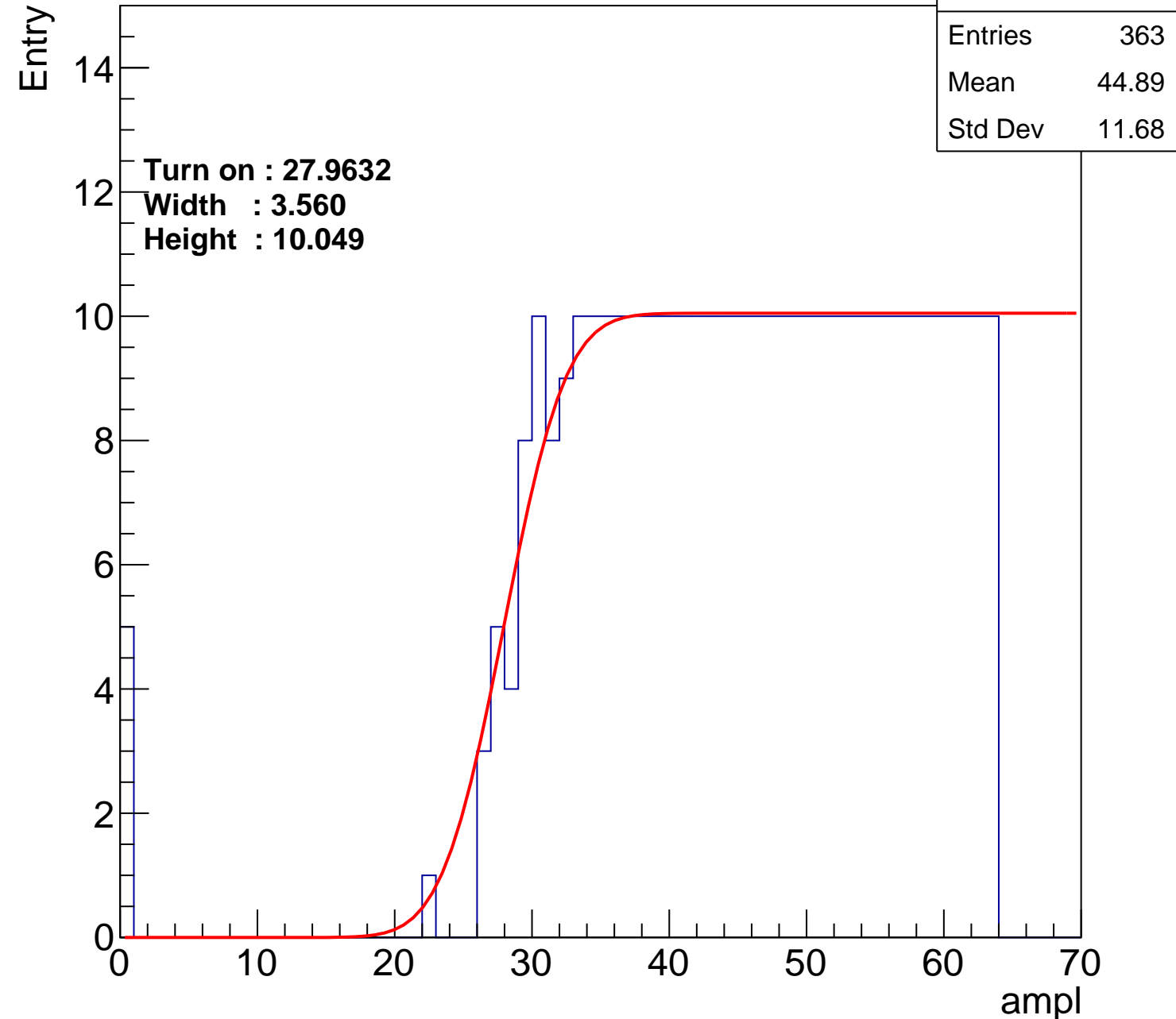
Width : 3.560

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch38

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	44.95
Std Dev	11.51

Turn on : 28.5825

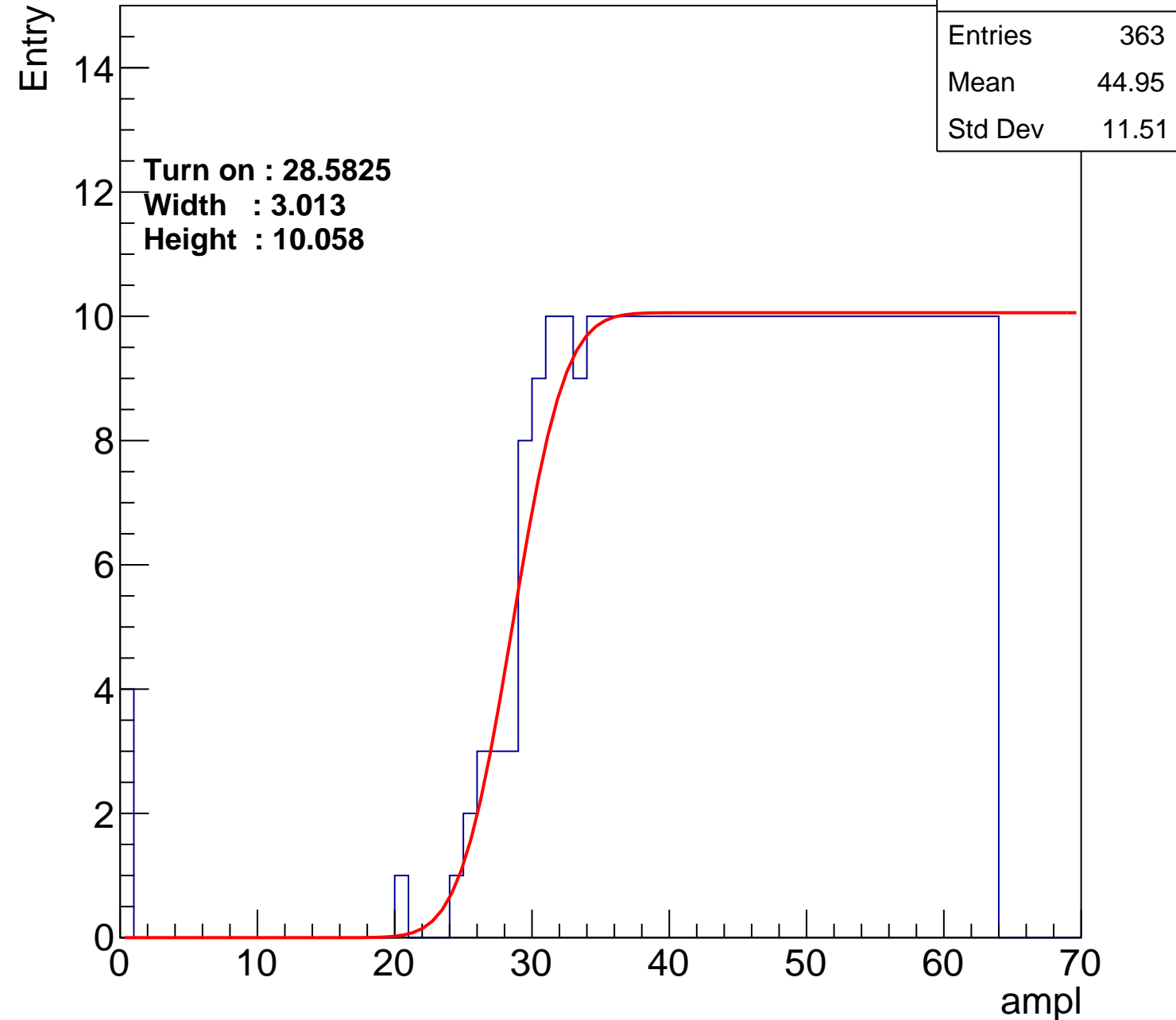
Width : 3.013

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch39

calib_packv5_042523_0143.root, FC#9, port A1

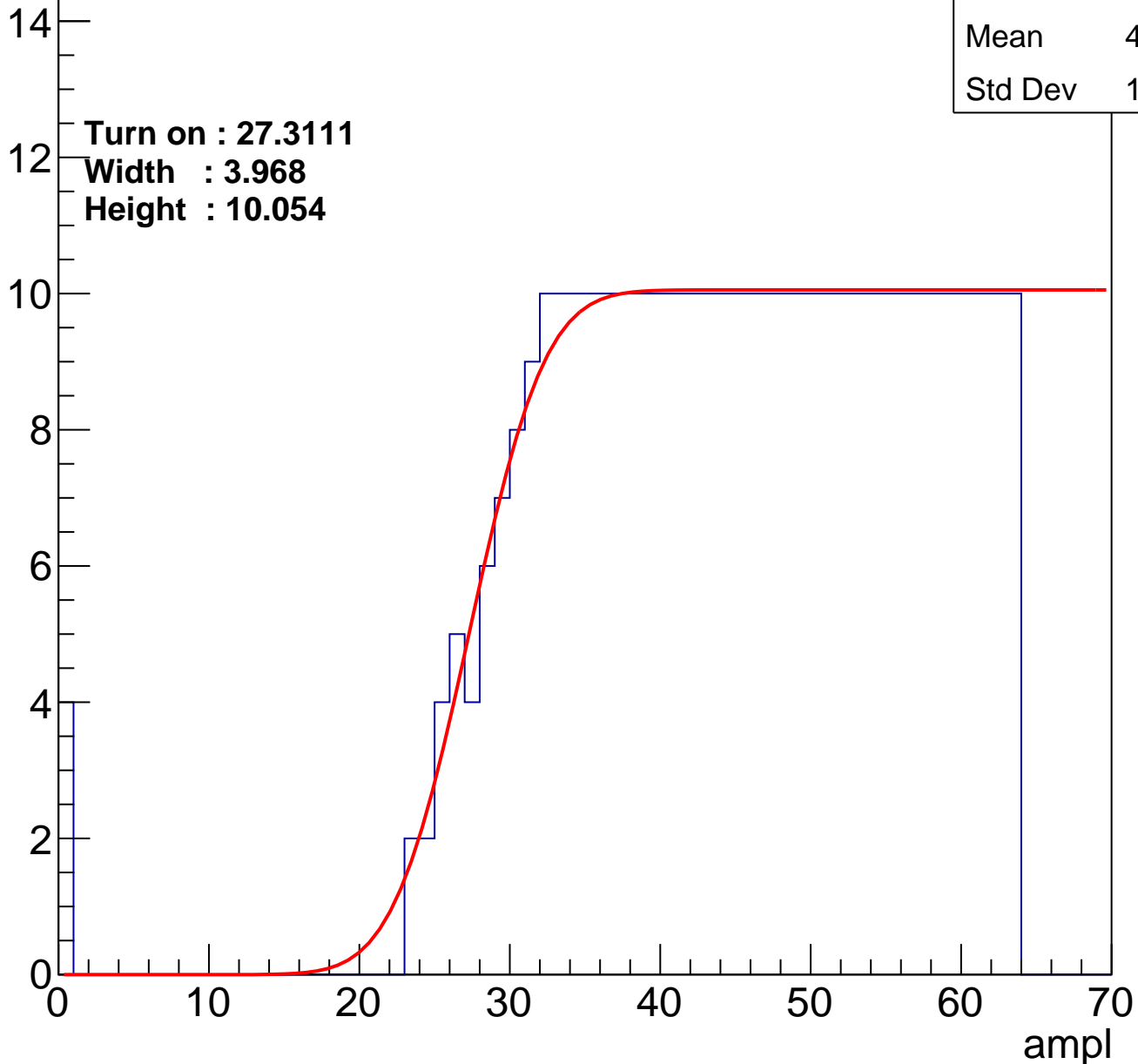
Entries	371
Mean	44.53
Std Dev	11.72

Turn on : 27.3111

Width : 3.968

Height : 10.054

Entry



B0L001S, U15-ch40

calib_packv5_042523_0143.root, FC#9, port A1

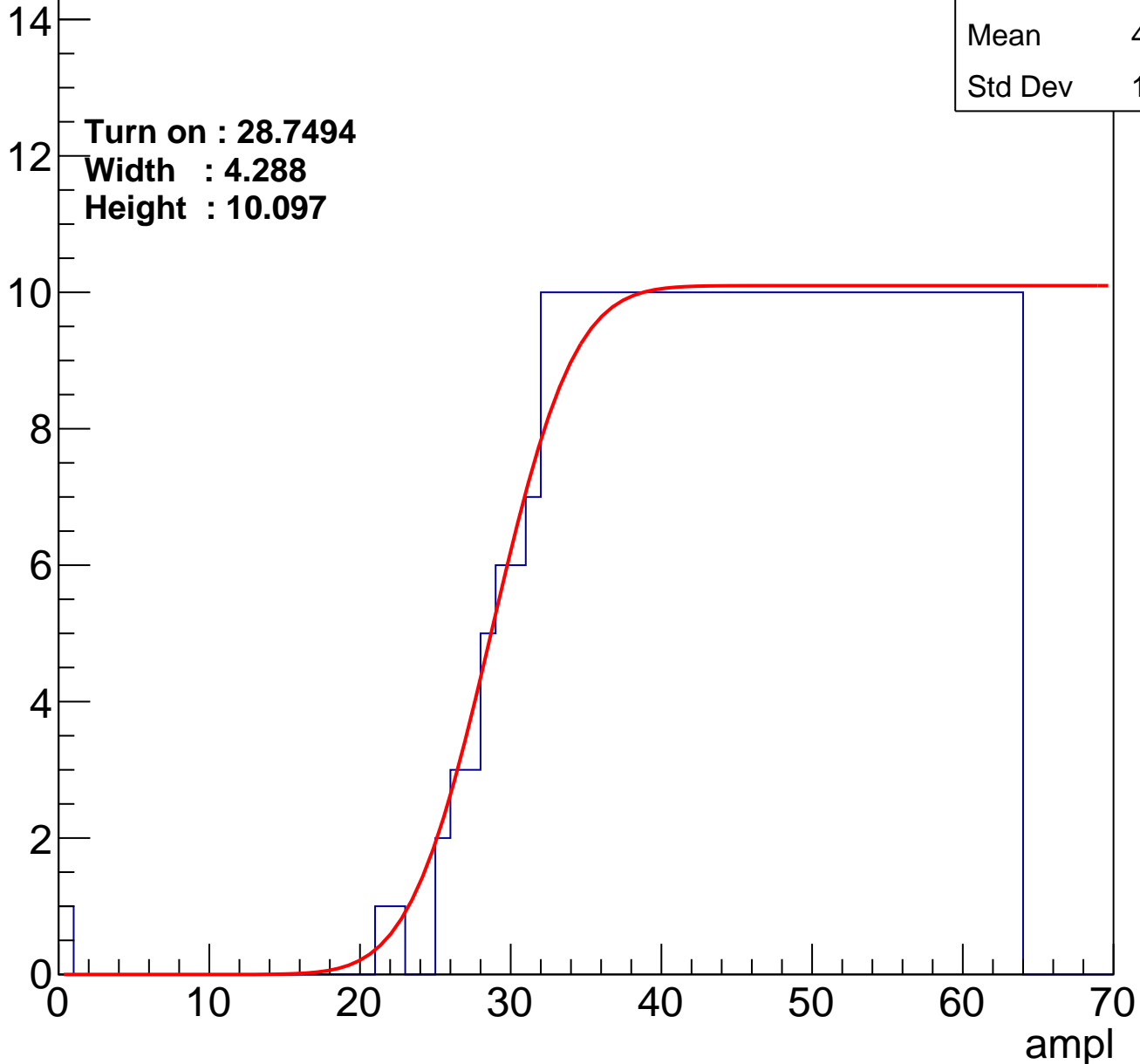
Entries	355
Mean	45.53
Std Dev	10.73

Turn on : 28.7494

Width : 4.288

Height : 10.097

Entry



B0L001S, U15-ch41

calib_packv5_042523_0143.root, FC#9, port A1

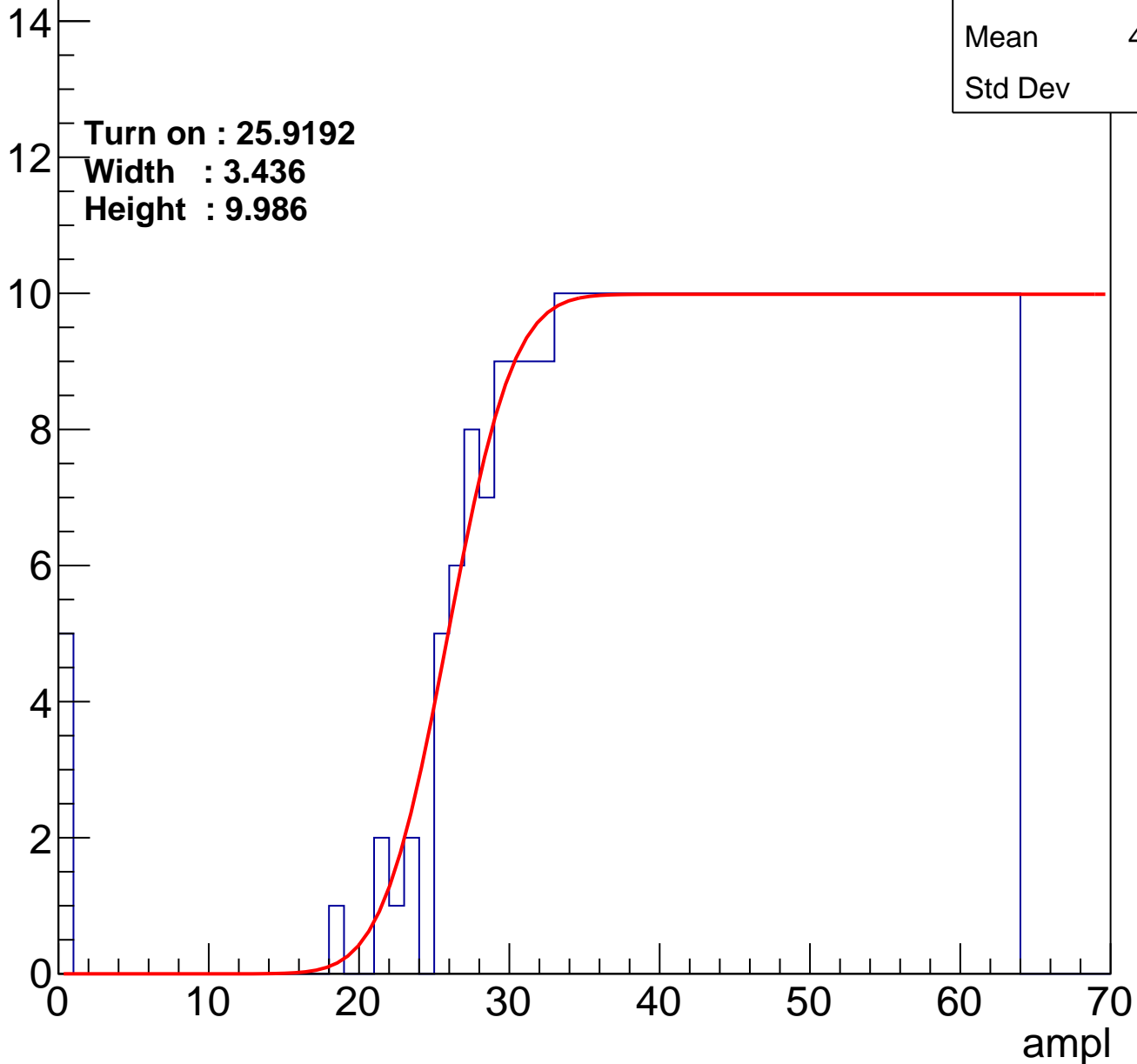
Entries	383
Mean	43.86
Std Dev	12.2

Turn on : 25.9192

Width : 3.436

Height : 9.986

Entry



calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.3
Std Dev	11.18

Std Dev	11.18
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Height : 10.005

0

ampl

B0L001S, U15-ch43

calib_packv5_042523_0143.root, FC#9, port A1

Entries	388
Mean	43.73
Std Dev	12.08

Turn on : 25.5694

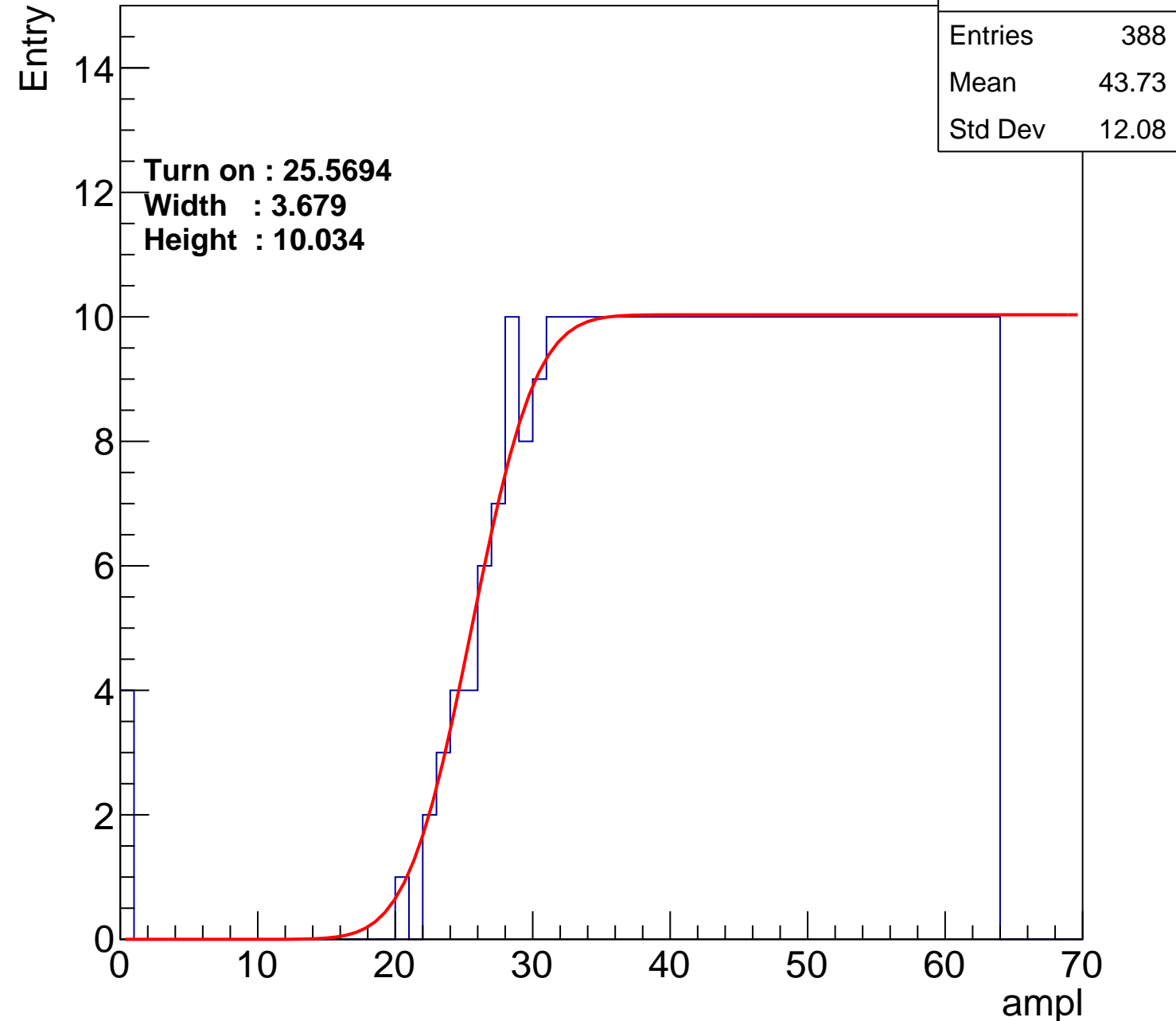
Width : 3.679

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch44

calib_packv5_042523_0143.root, FC#9, port A1

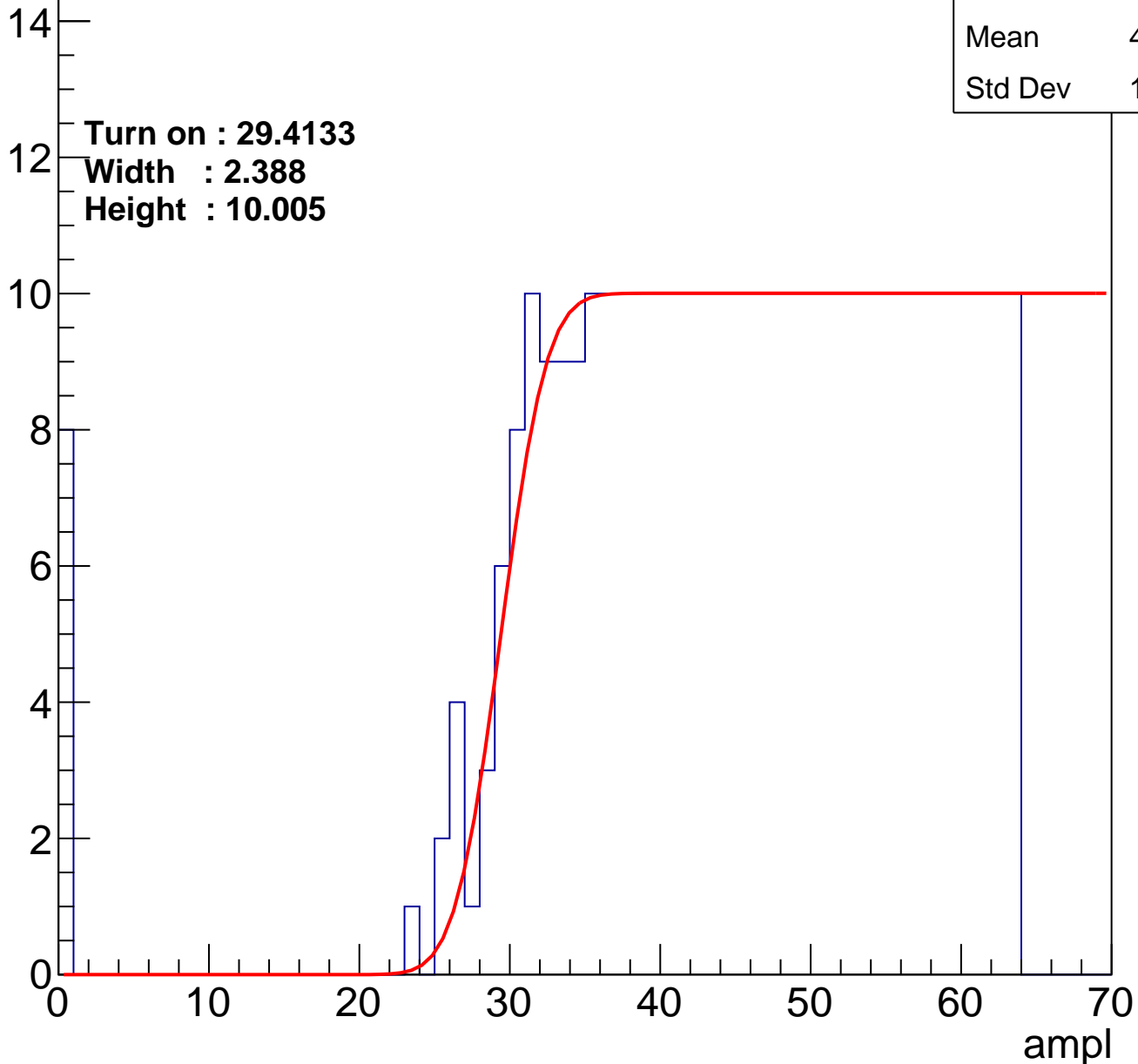
Entries	360
Mean	44.76
Std Dev	12.27

Turn on : 29.4133

Width : 2.388

Height : 10.005

Entry



B0L001S, U15-ch45

calib_packv5_042523_0143.root, FC#9, port A1

Entries	358
Mean	45.25
Std Dev	11.21

Turn on : 28.7405

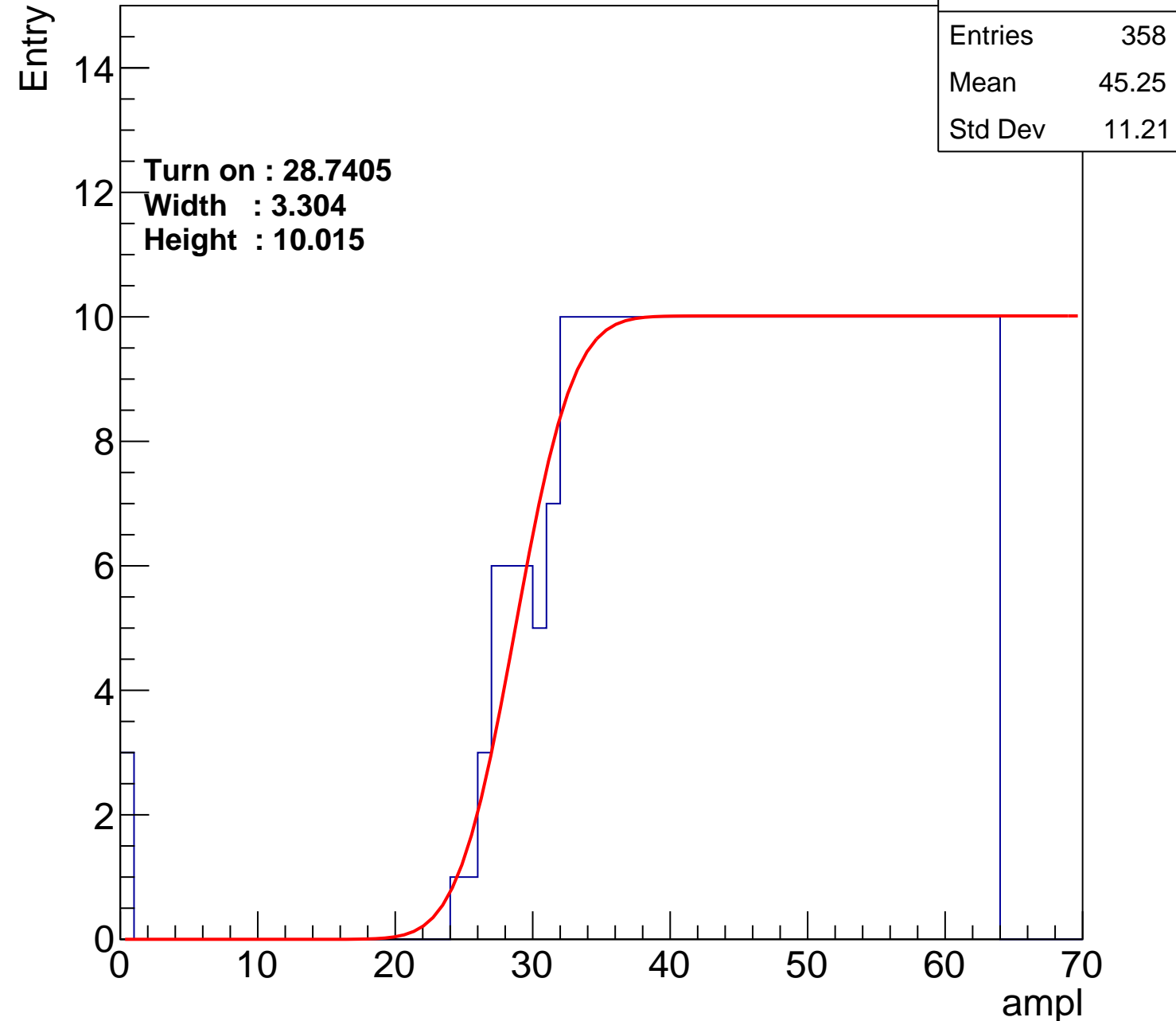
Width : 3.304

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch46

calib_packv5_042523_0143.root, FC#9, port A1

Entries	339
Mean	46.25
Std Dev	10.53

Turn on : 30.3103

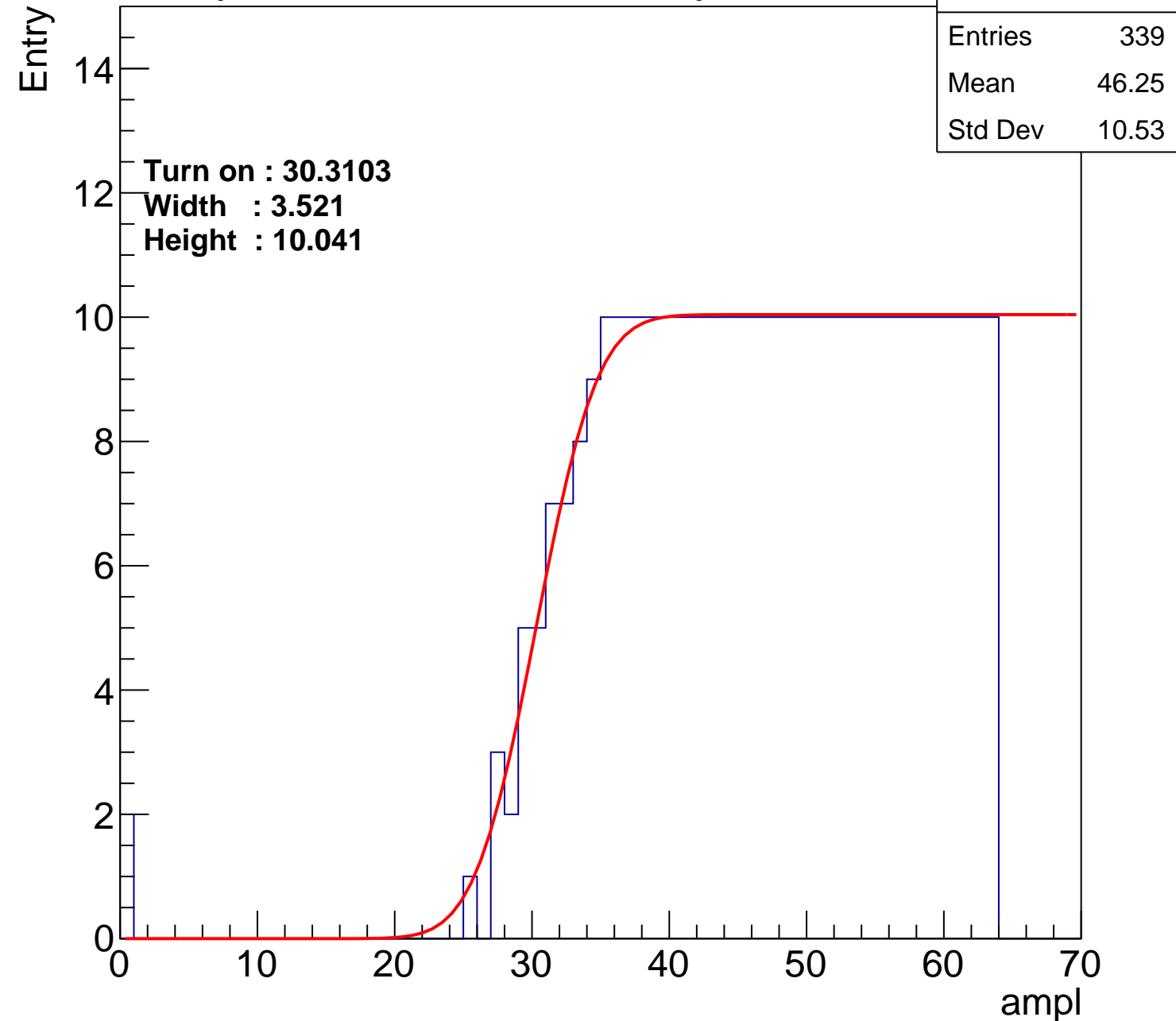
Width : 3.521

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch47

calib_packv5_042523_0143.root, FC#9, port A1

Entries	383
Mean	43.9
Std Dev	12.15

Turn on : 26.7020

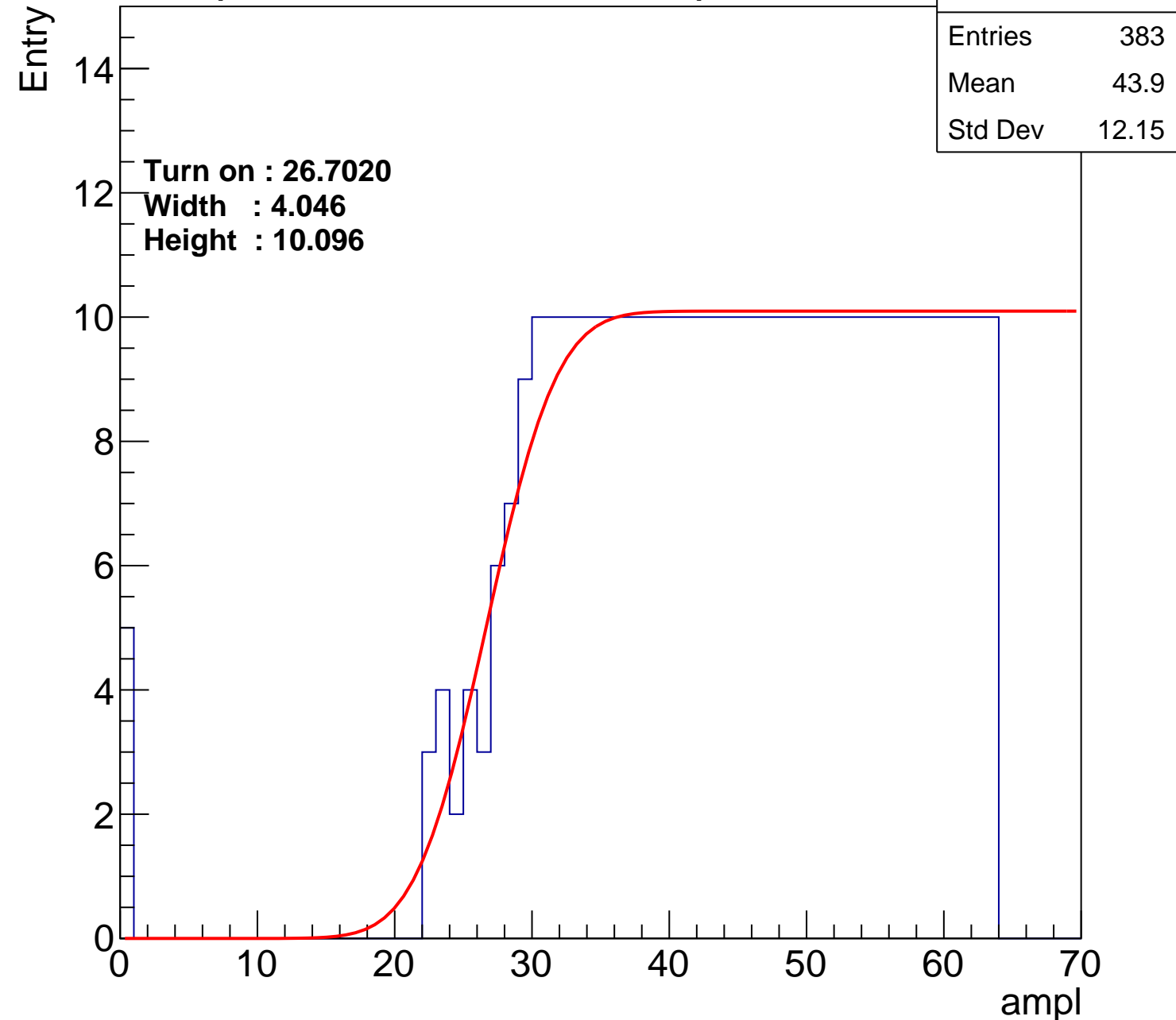
Width : 4.046

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch48

calib_packv5_042523_0143.root, FC#9, port A1

Entries	371
Mean	44.72
Std Dev	11.27

Turn on : 27.1680

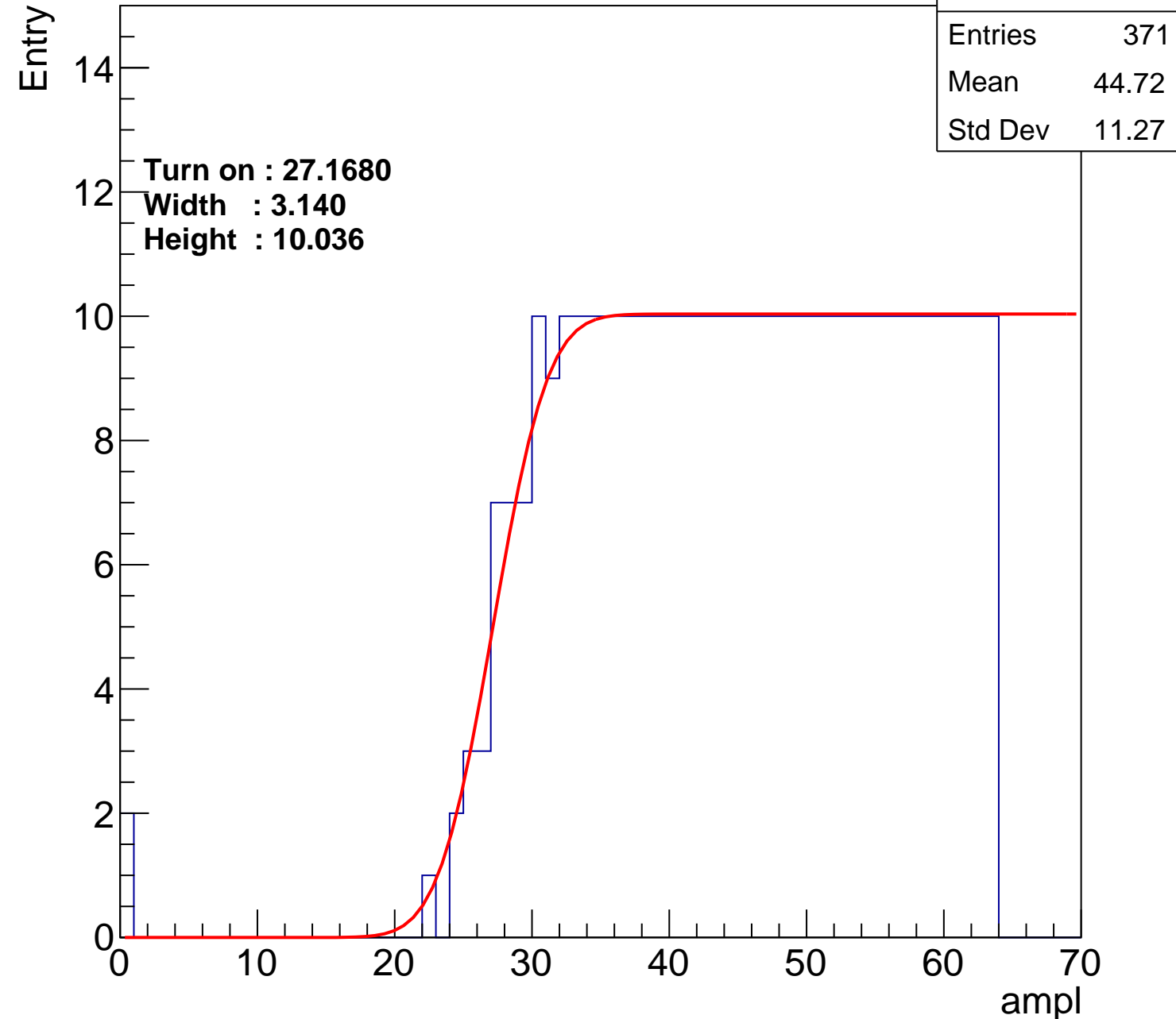
Width : 3.140

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch49

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.13
Std Dev	12.04

Turn on : 26.7208

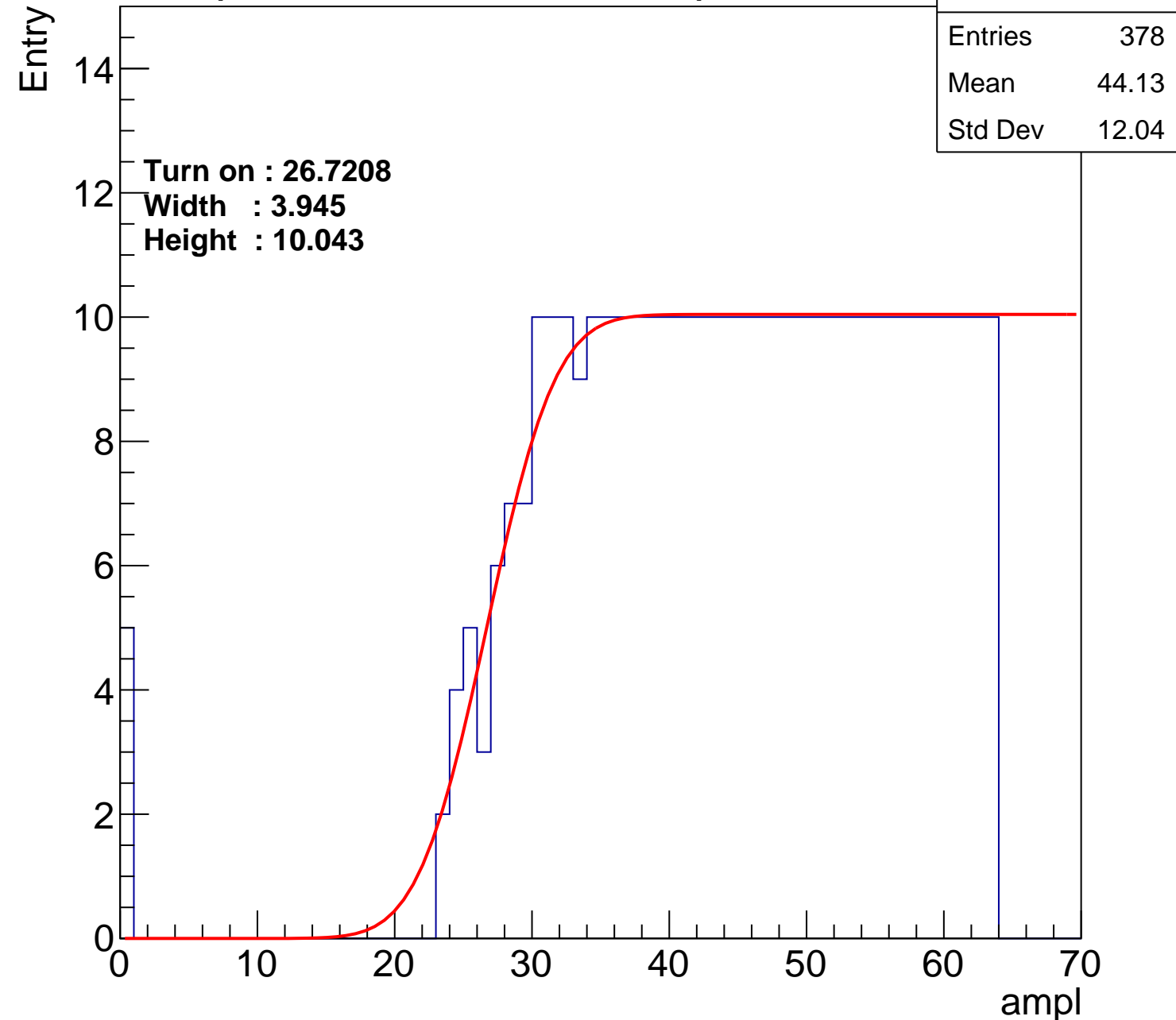
Width : 3.945

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch50

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.23
Std Dev	10.85

Turn on : 28.3540

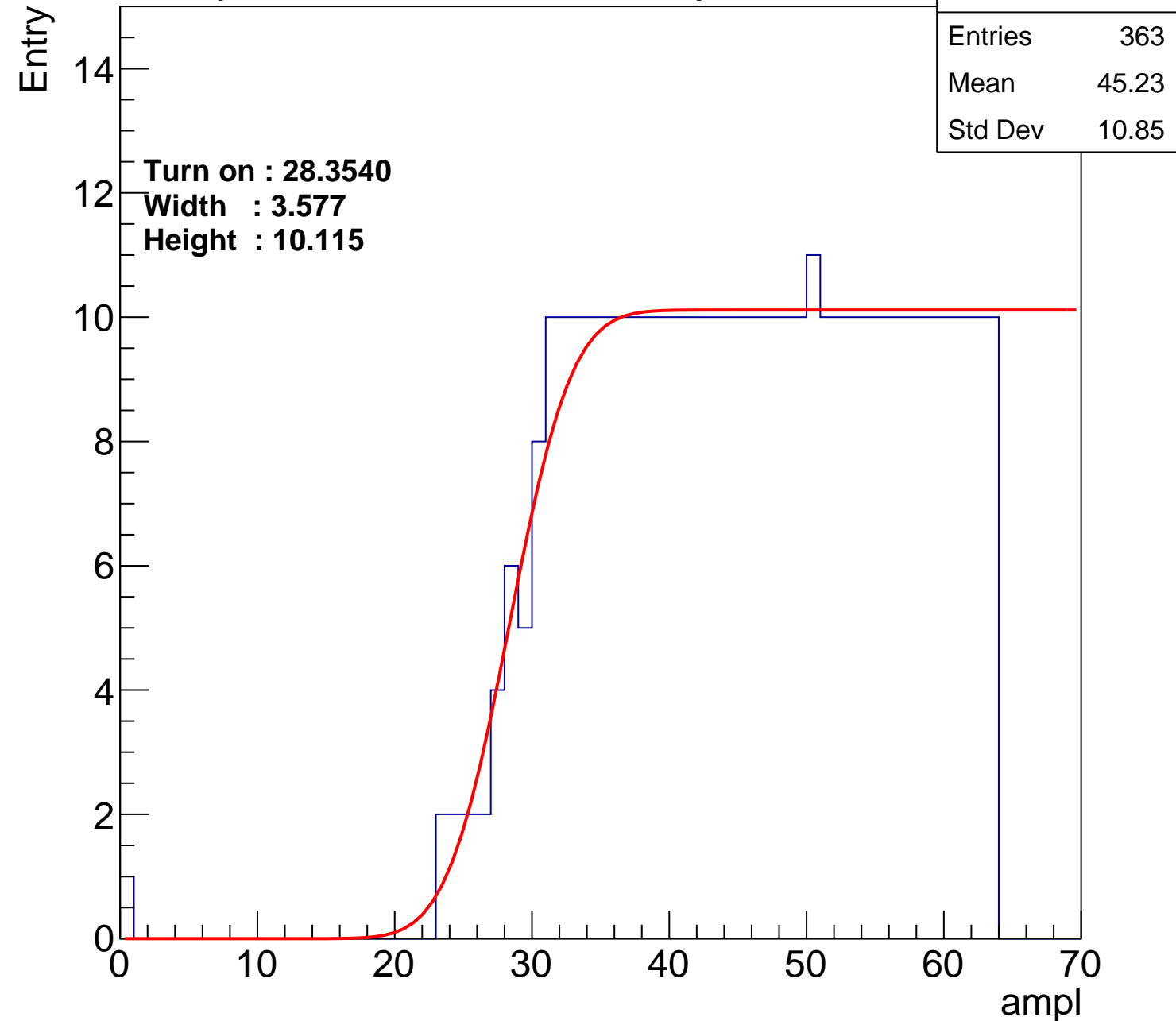
Width : 3.577

Height : 10.115

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch51

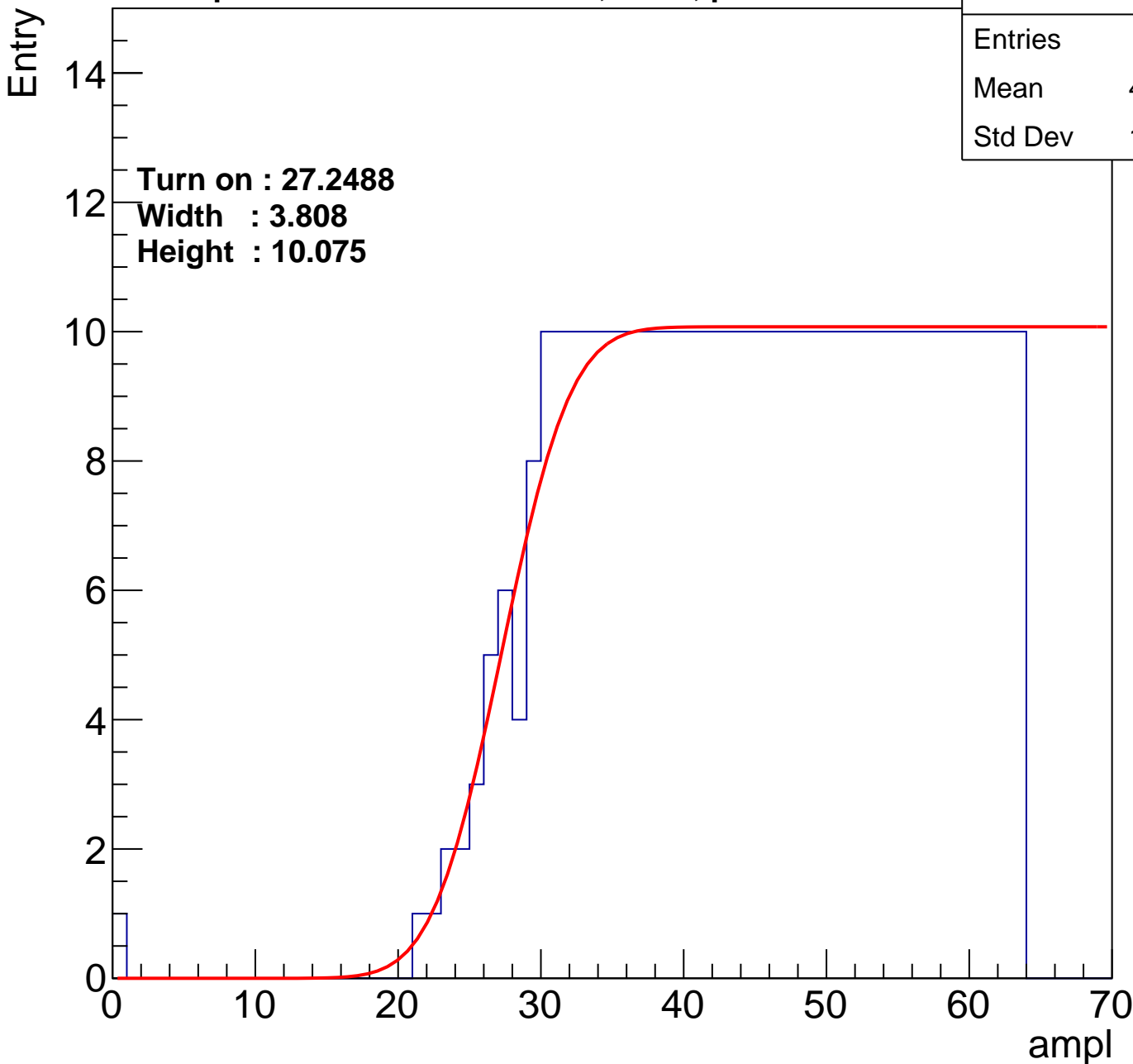
calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.2488

Width : 3.808

Height : 10.075



B0L001S, U15-ch52

calib_packv5_042523_0143.root, FC#9, port A1

Entries	357
Mean	45.29
Std Dev	11.19

Turn on : 28.9624

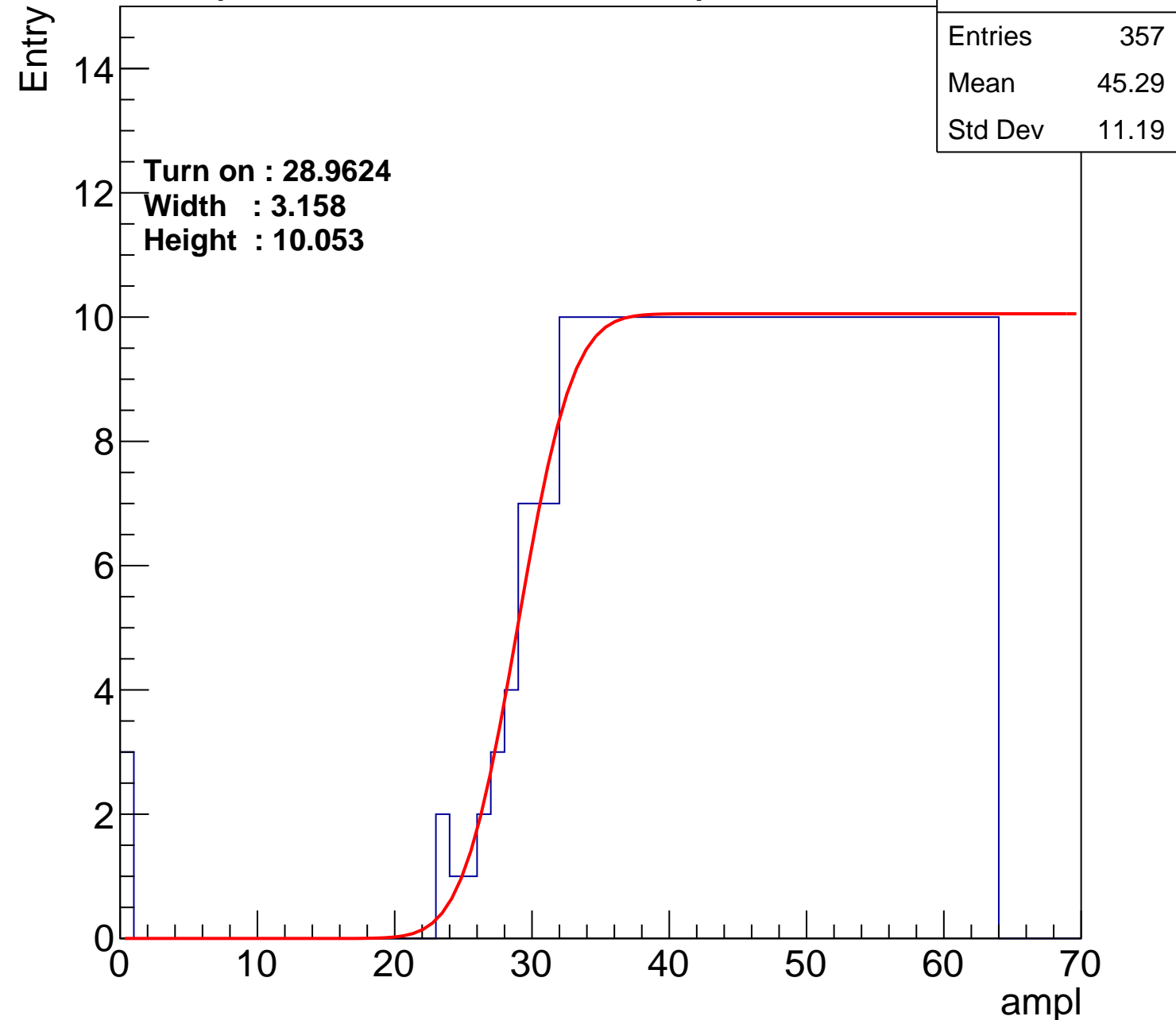
Width : 3.158

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch53

calib_packv5_042523_0143.root, FC#9, port A1

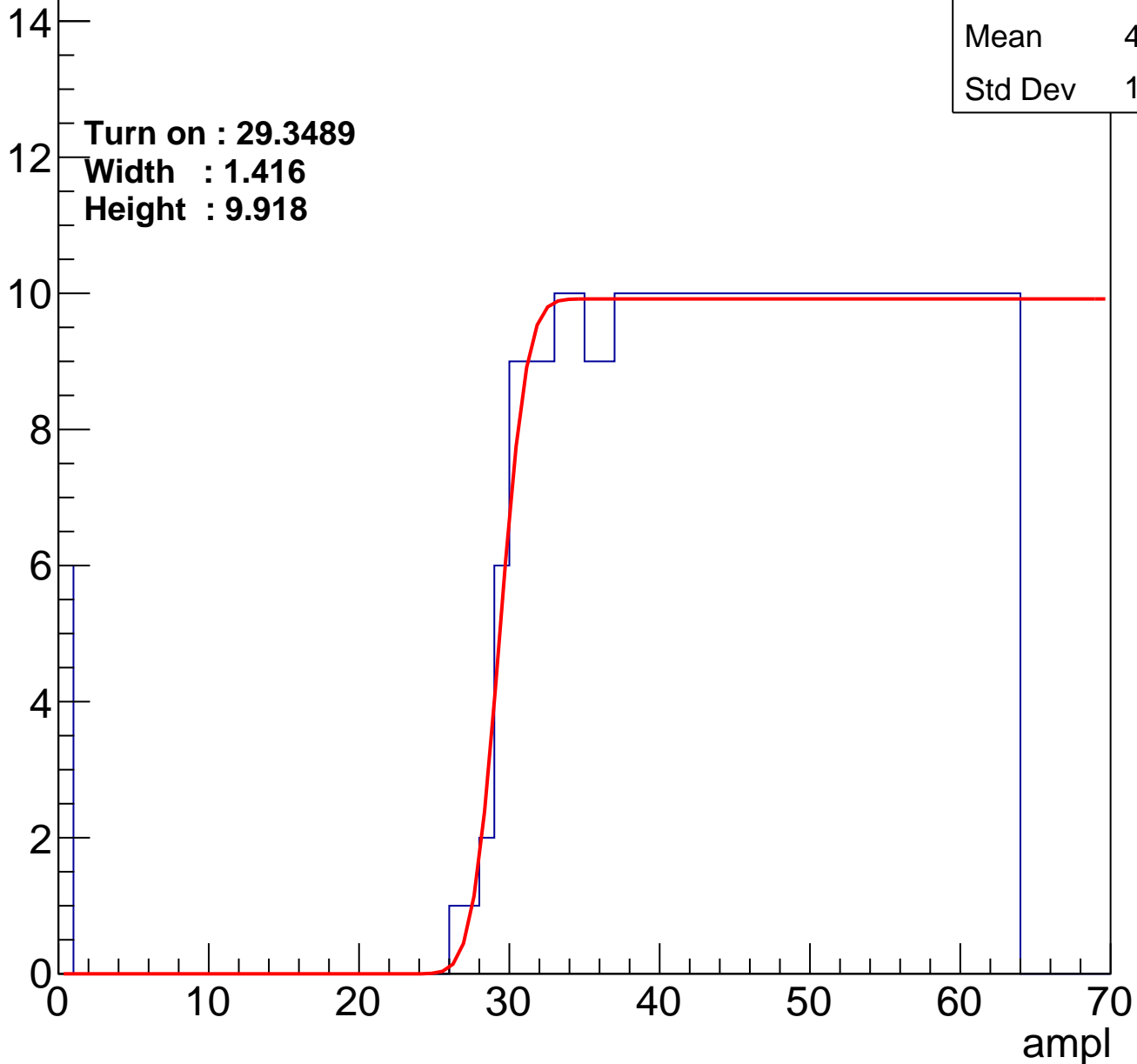
Entries	351
Mean	45.38
Std Dev	11.65

Turn on : 29.3489

Width : 1.416

Height : 9.918

Entry



B0L001S, U15-ch54

calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.2006

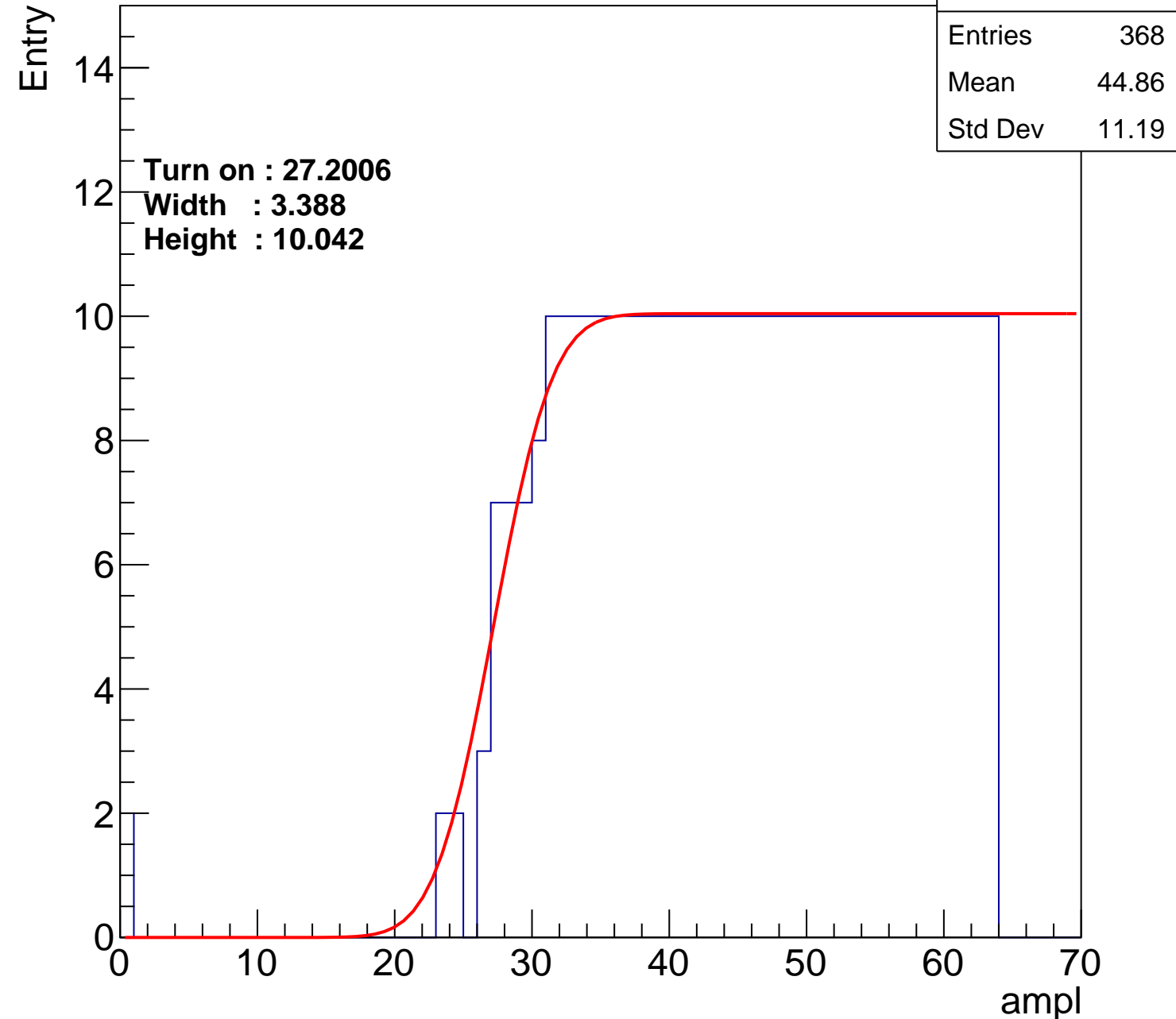
Width : 3.388

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch55

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.8441

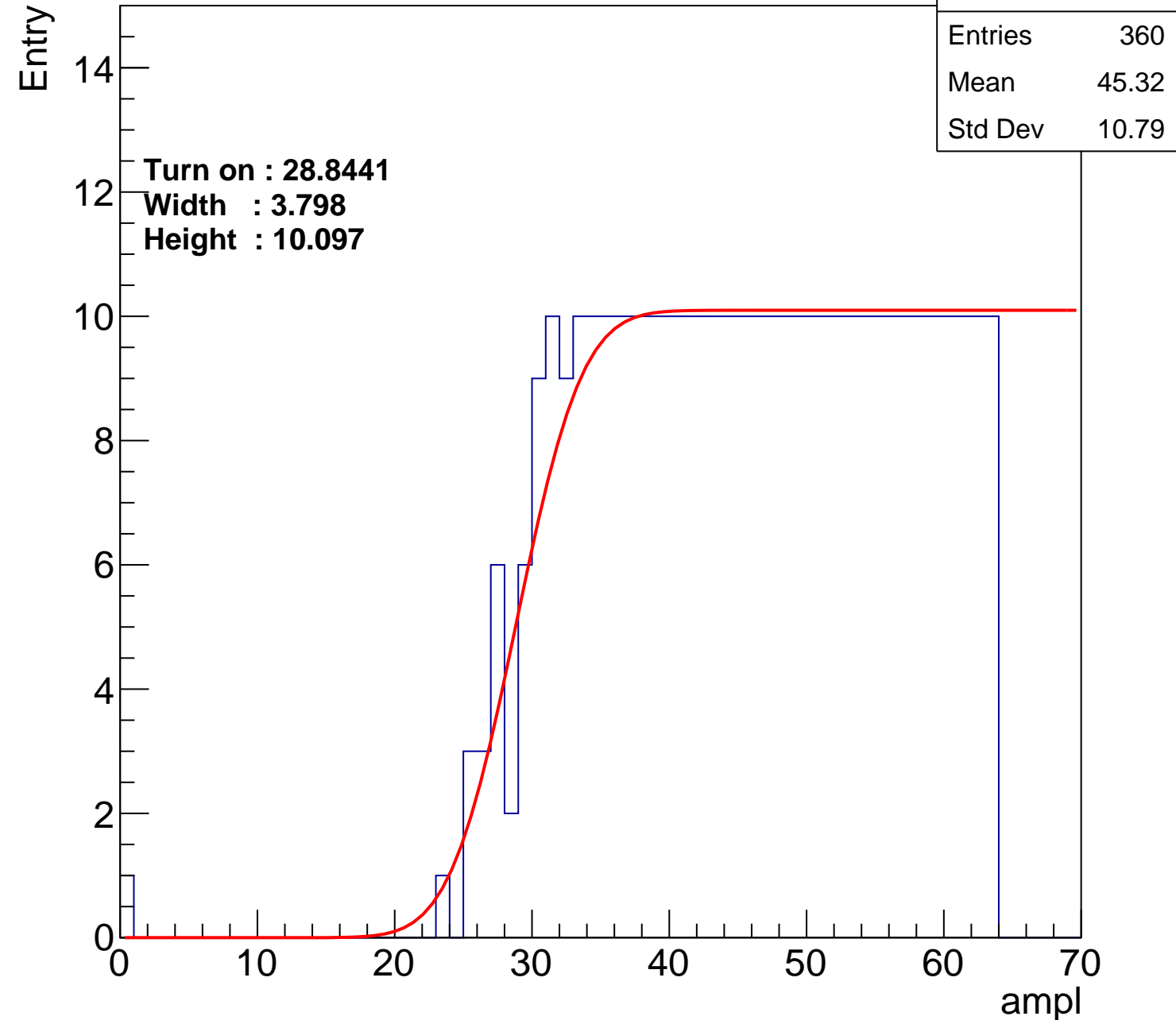
Width : 3.798

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch56

calib_packv5_042523_0143.root, FC#9, port A1

Entries	353
Mean	45.69
Std Dev	10.56

Turn on : 29.4616

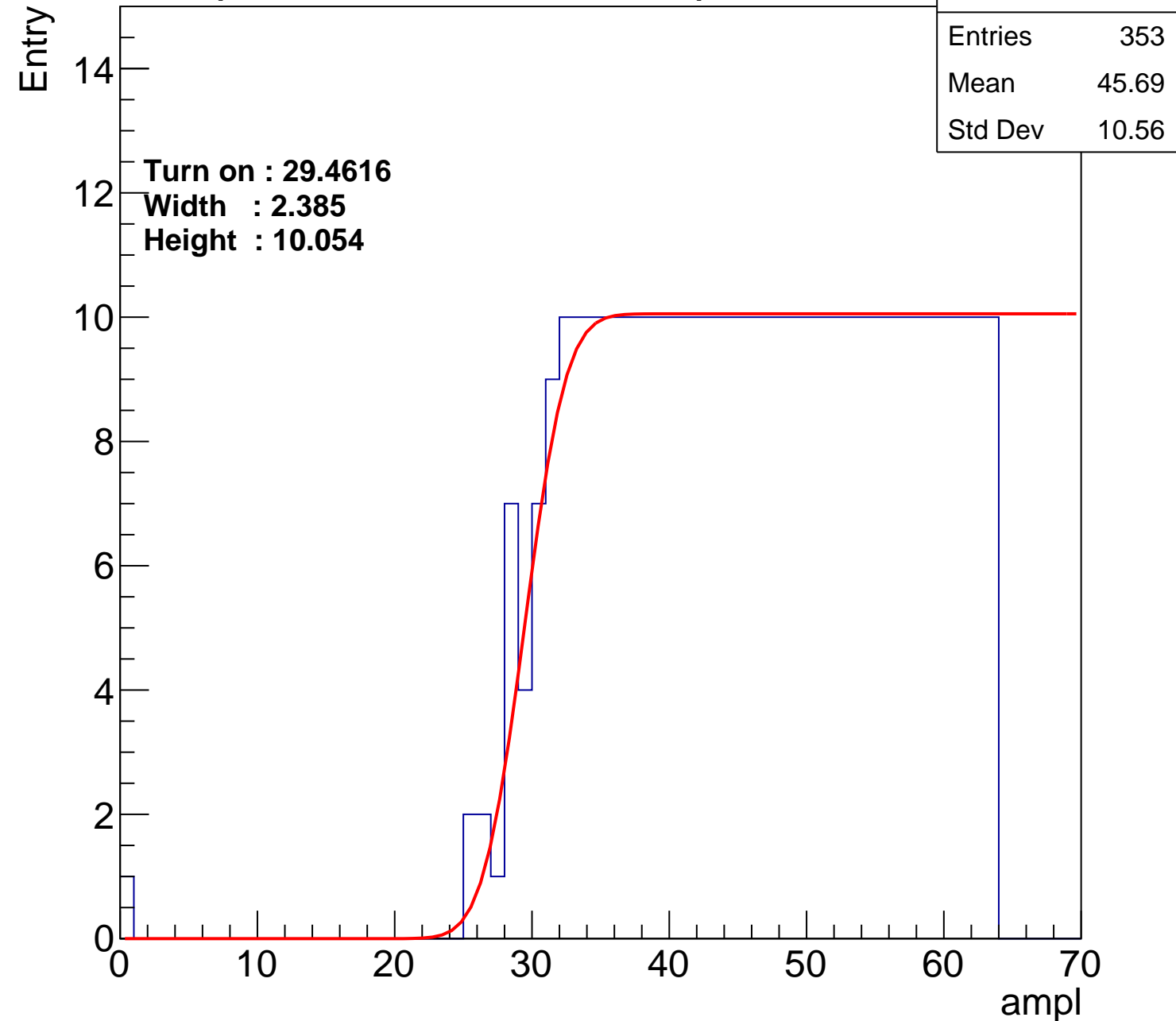
Width : 2.385

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch57

calib_packv5_042523_0143.root, FC#9, port A1

Entries	380
Mean	44.09
Std Dev	11.94

Turn on : 26.5945

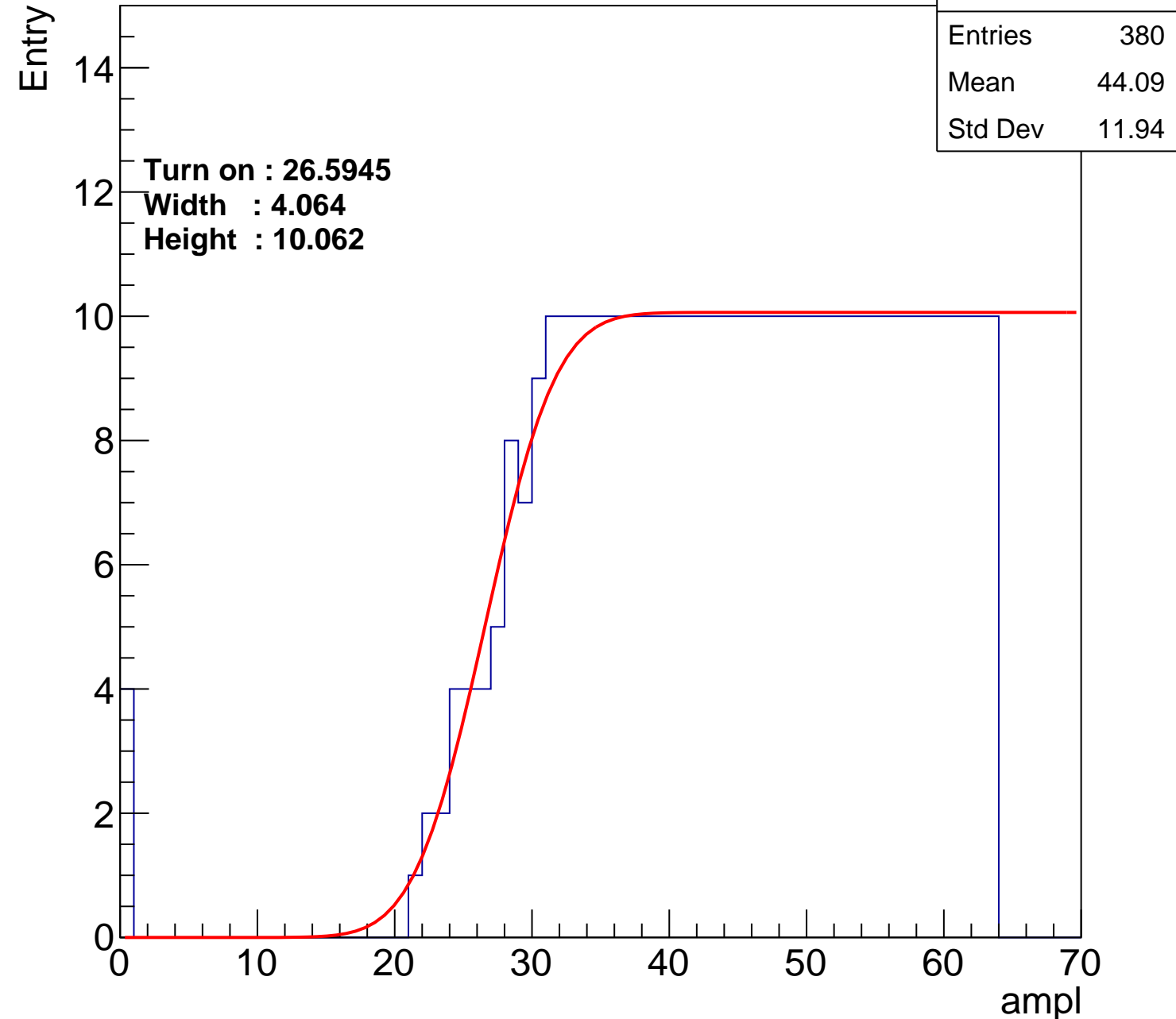
Width : 4.064

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch58

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.3
Std Dev	10.97

Turn on : 28.0737

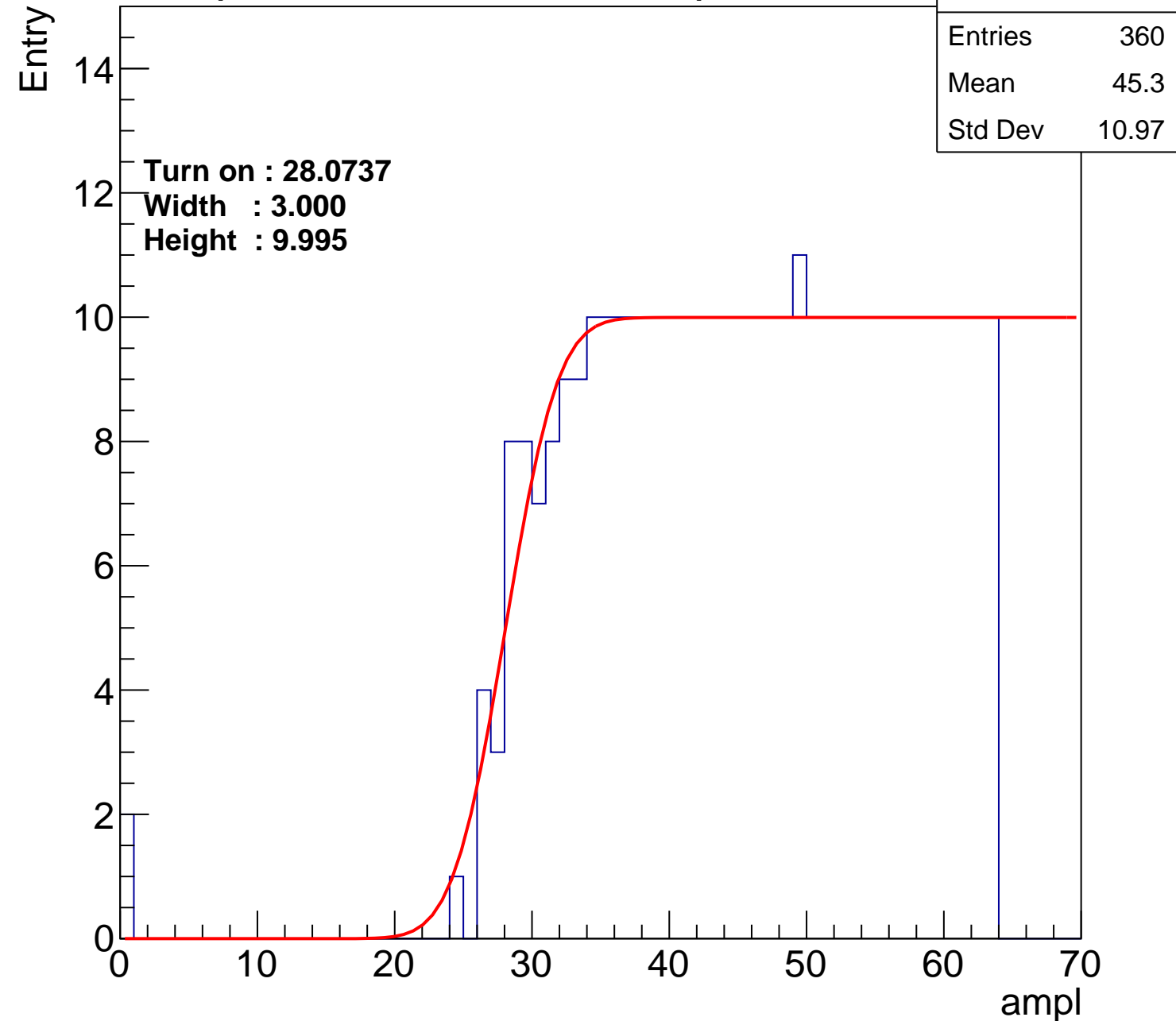
Width : 3.000

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch59

calib_packv5_042523_0143.root, FC#9, port A1

Entries	375
Mean	44.37
Std Dev	11.69

Turn on : 27.1738

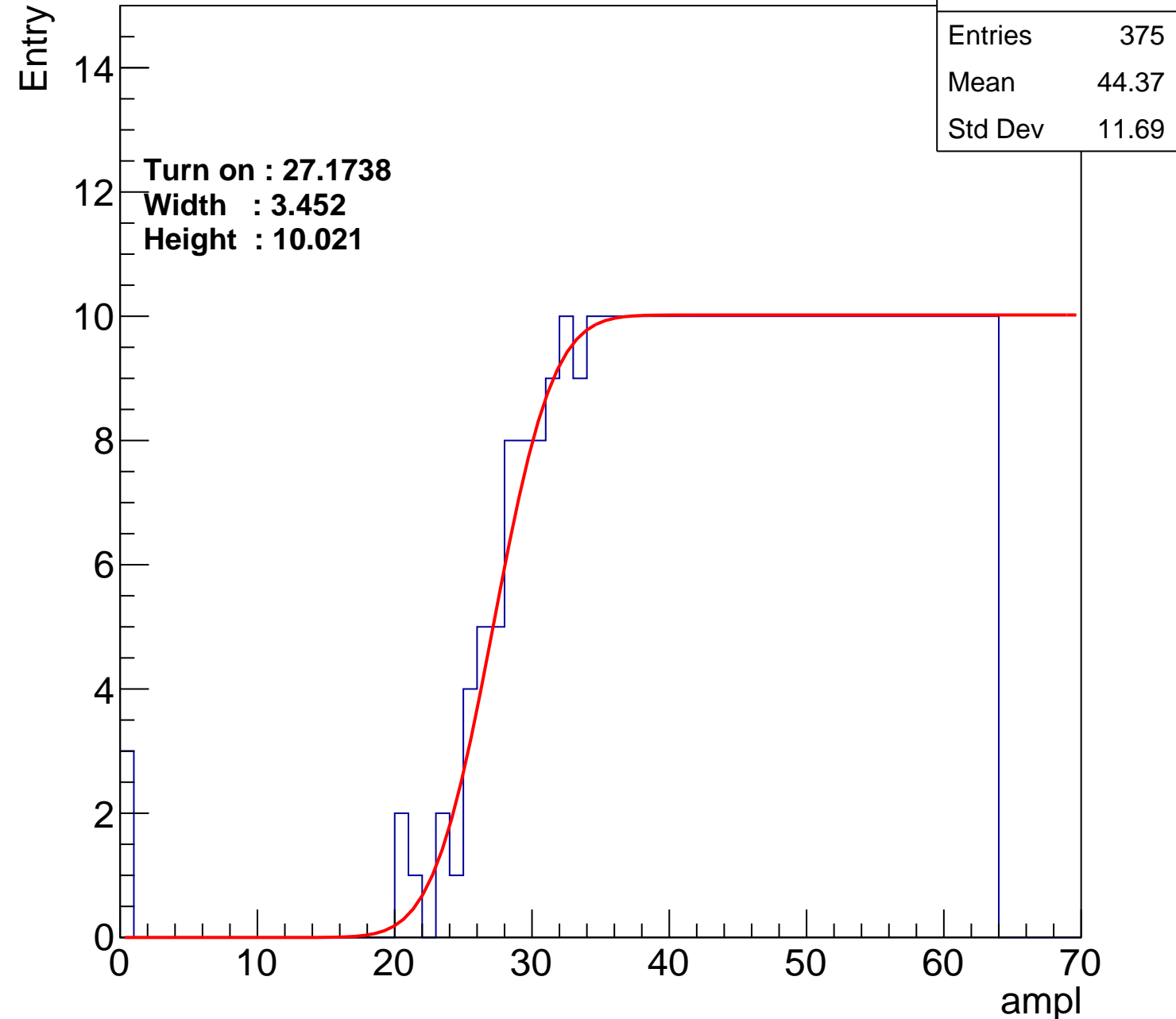
Width : 3.452

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch60

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.52
Std Dev	11.67

Turn on : 27.3809

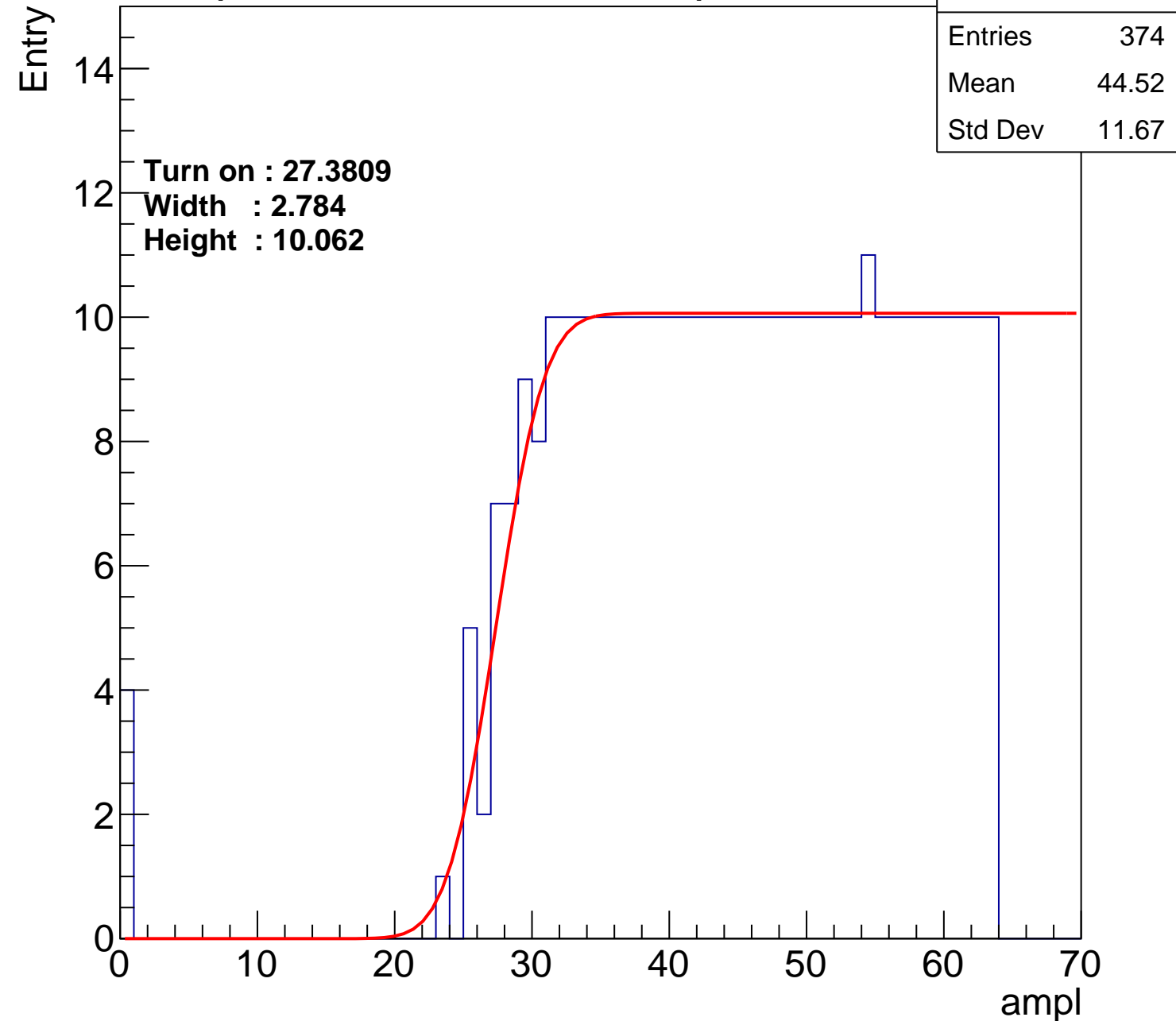
Width : 2.784

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch61

calib_packv5_042523_0143.root, FC#9, port A1

Entries	350
Mean	45.74
Std Dev	10.76

Turn on : 29.0774

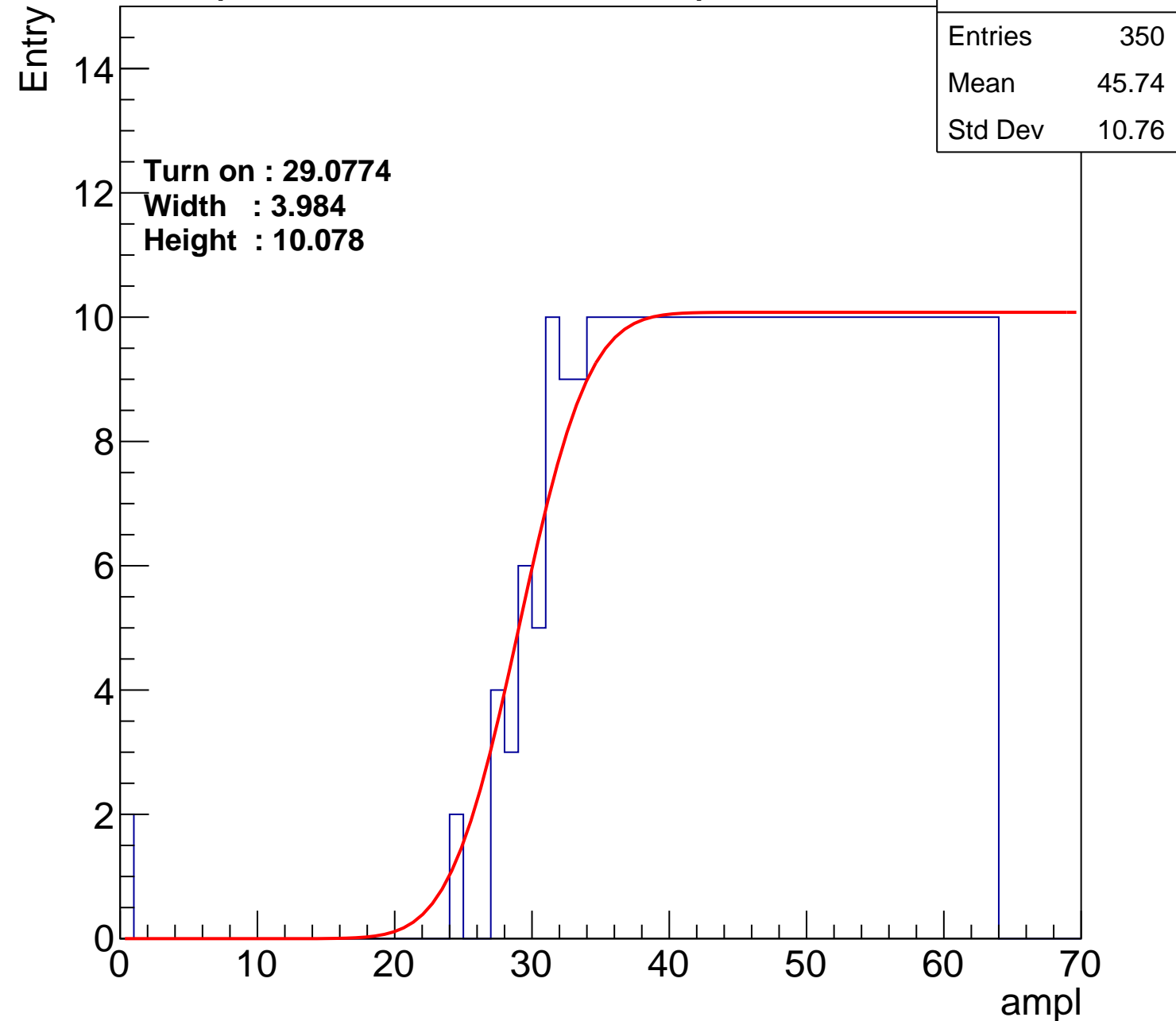
Width : 3.984

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch62

calib_packv5_042523_0143.root, FC#9, port A1

Entries	376
Mean	44.13
Std Dev	12.22

Turn on : 27.5488

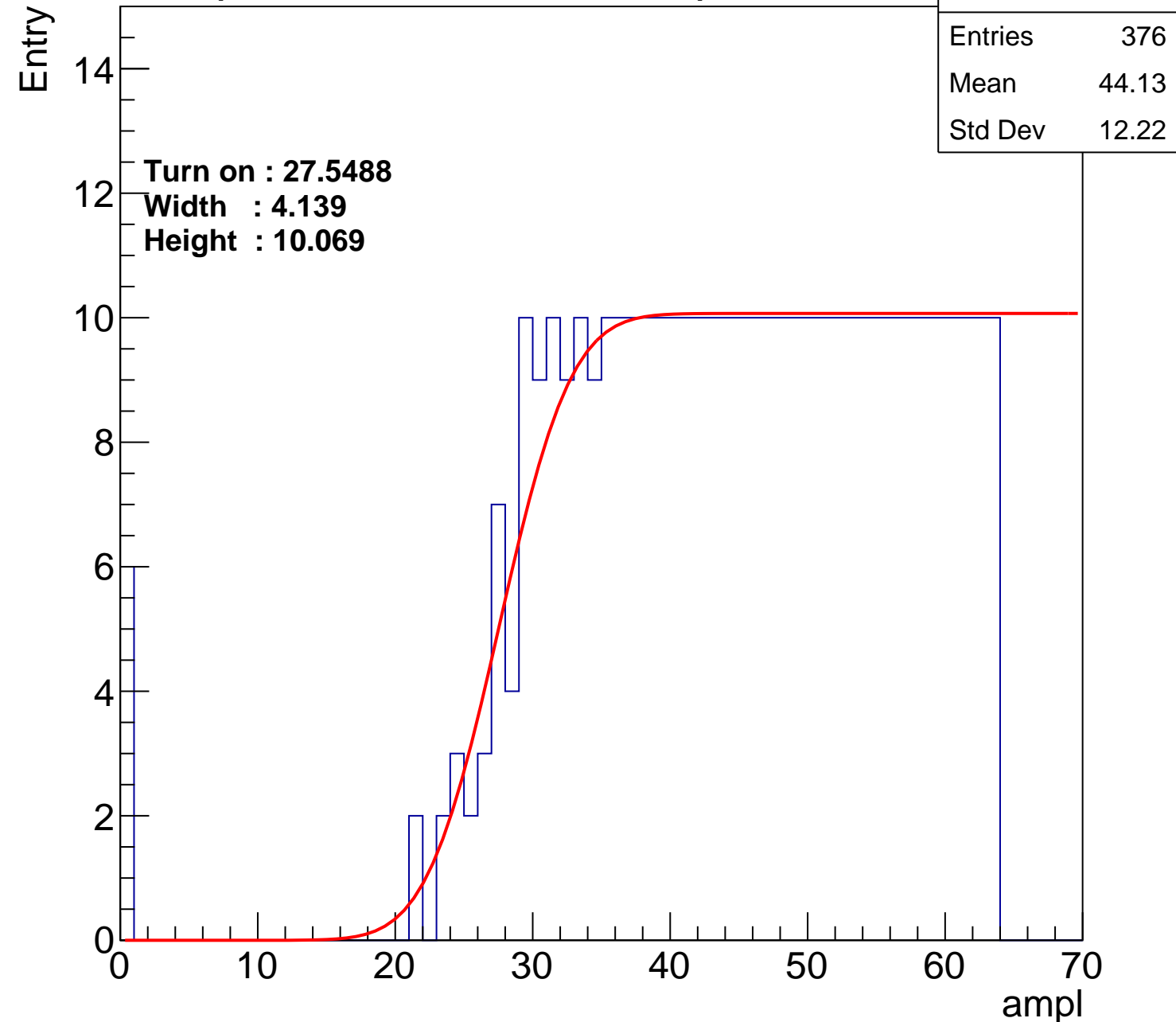
Width : 4.139

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch63

calib_packv5_042523_0143.root, FC#9, port A1

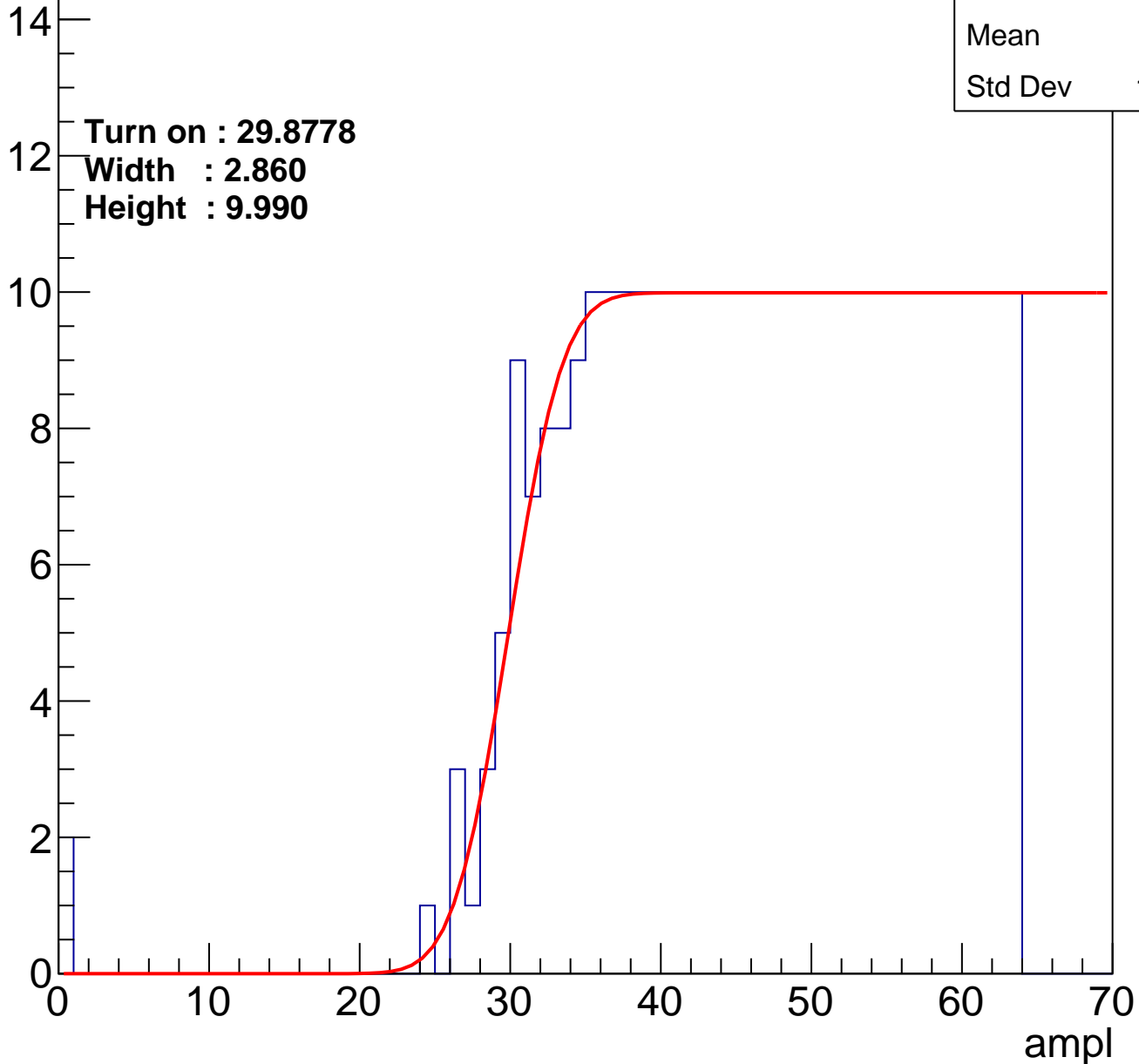
Entries	346
Mean	45.9
Std Dev	10.71

Turn on : 29.8778

Width : 2.860

Height : 9.990

Entry



B0L001S, U15-ch64

calib_packv5_042523_0143.root, FC#9, port A1

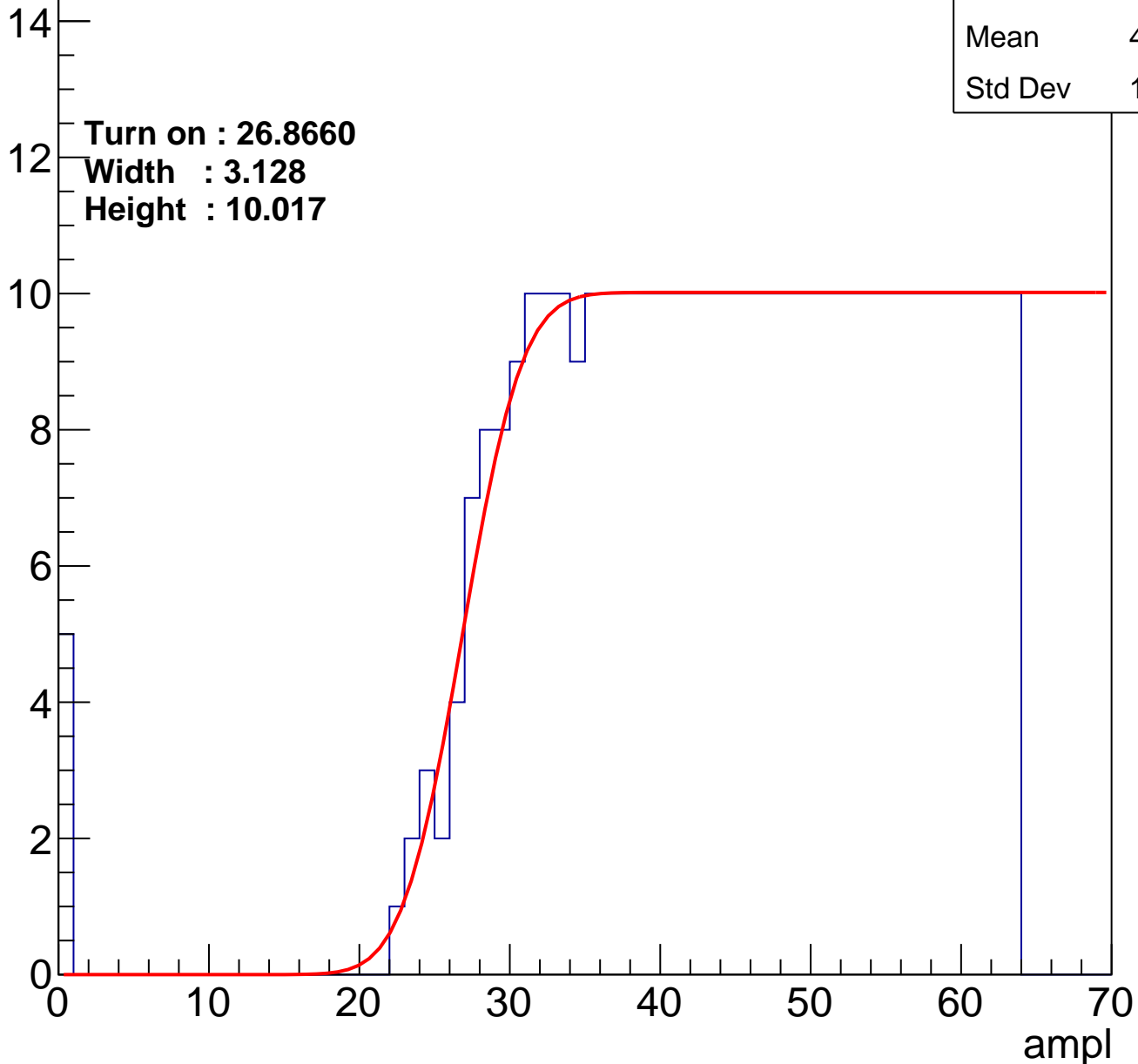
Entries	378
Mean	44.14
Std Dev	12.03

Turn on : 26.8660

Width : 3.128

Height : 10.017

Entry



B0L001S, U15-ch65

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.24
Std Dev	11.73

Turn on : 26.5684

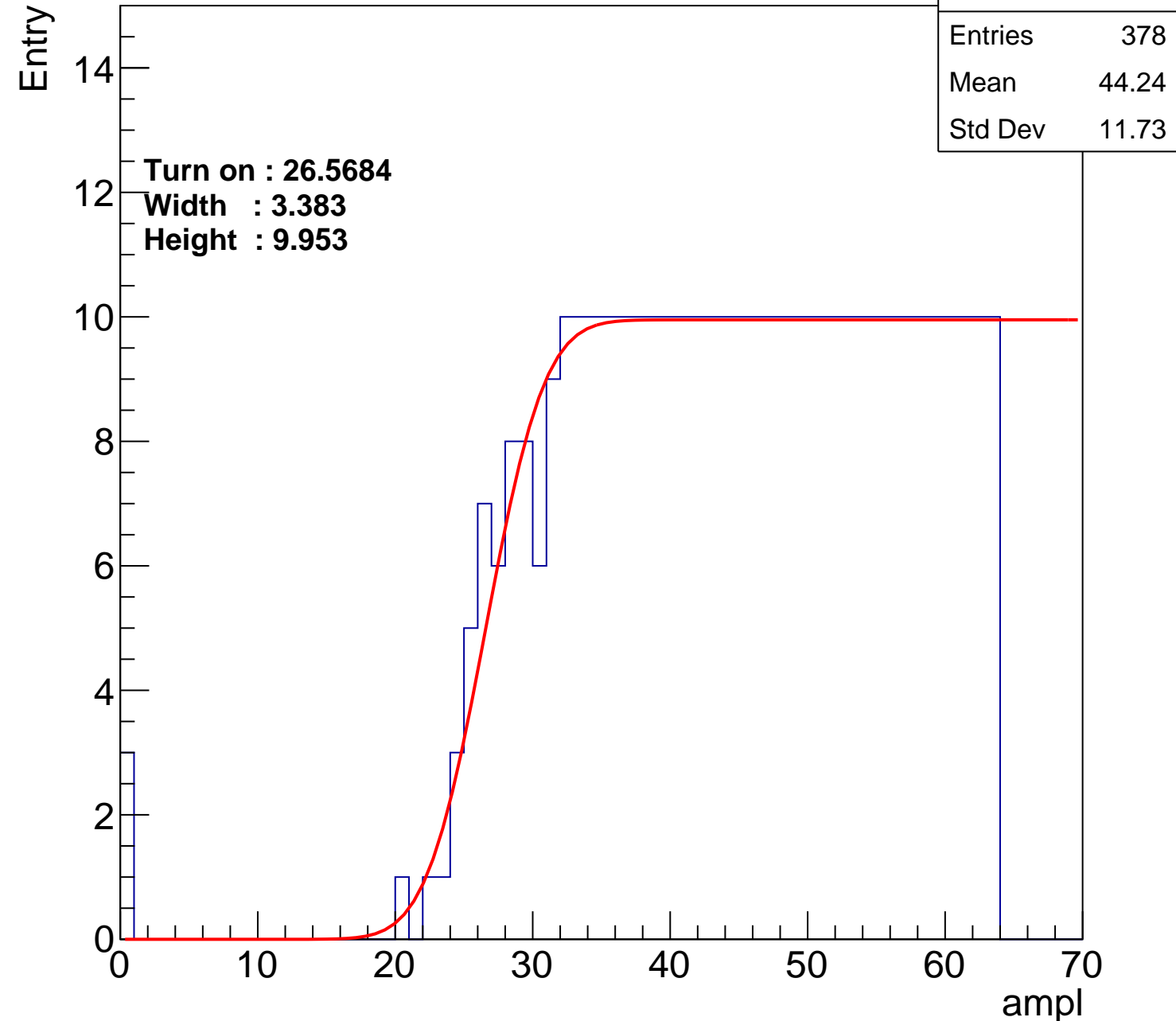
Width : 3.383

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch66

calib_packv5_042523_0143.root, FC#9, port A1

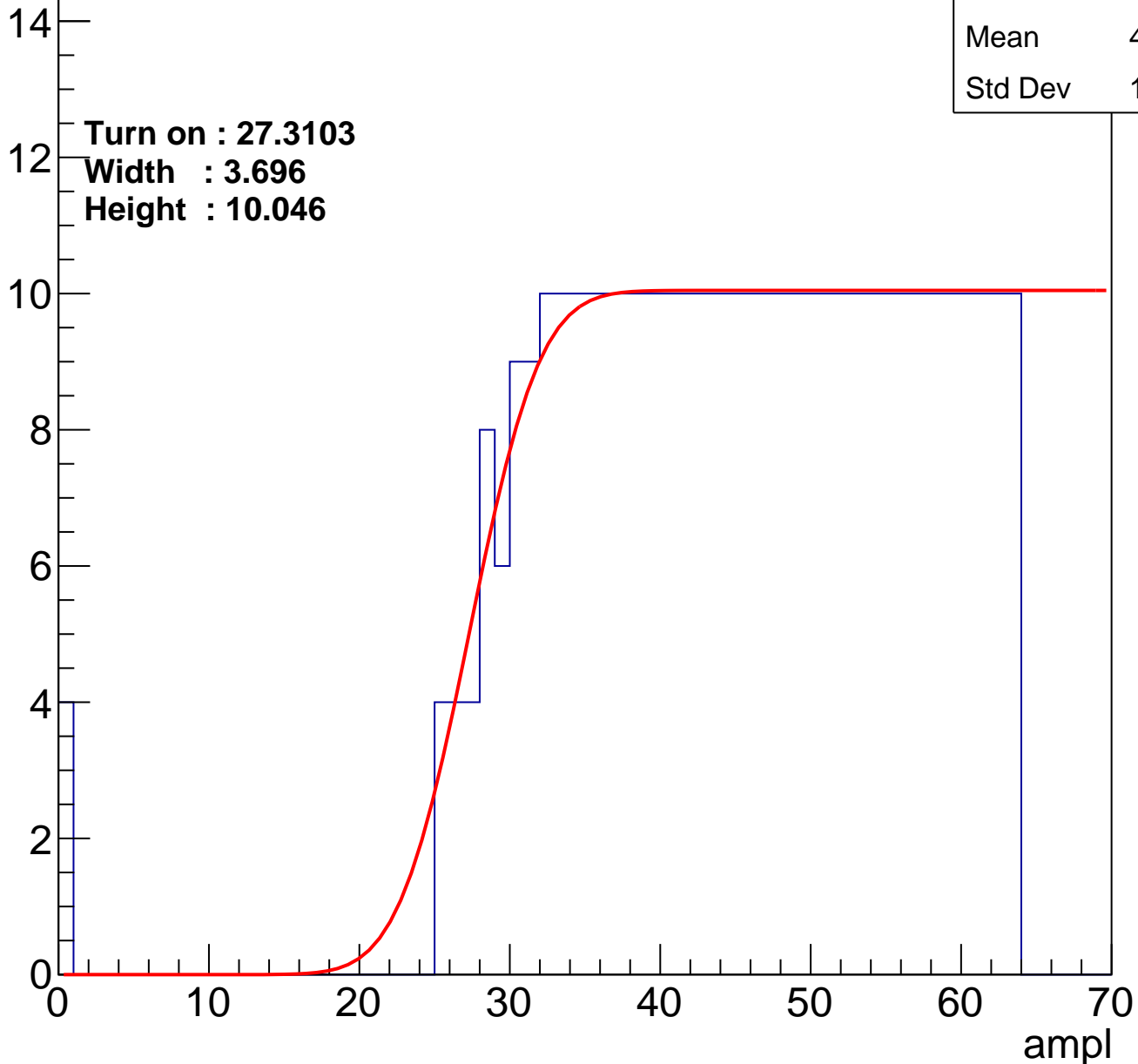
Entries	368
Mean	44.73
Std Dev	11.58

Turn on : 27.3103

Width : 3.696

Height : 10.046

Entry



B0L001S, U15-ch67

calib_packv5_042523_0143.root, FC#9, port A1

Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 25.9538

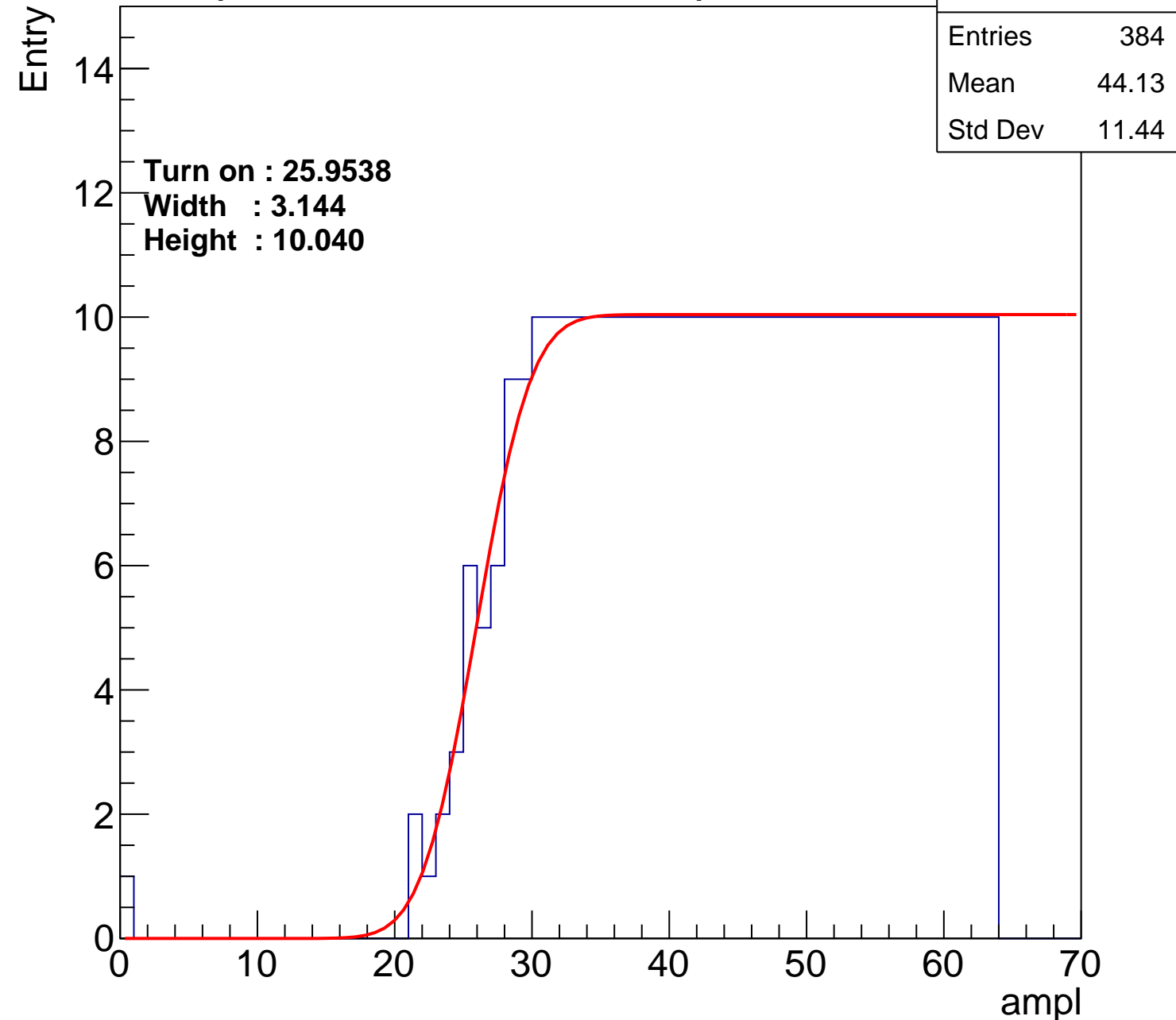
Width : 3.144

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch68

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.08
Std Dev	11.45

Turn on : 28.7838

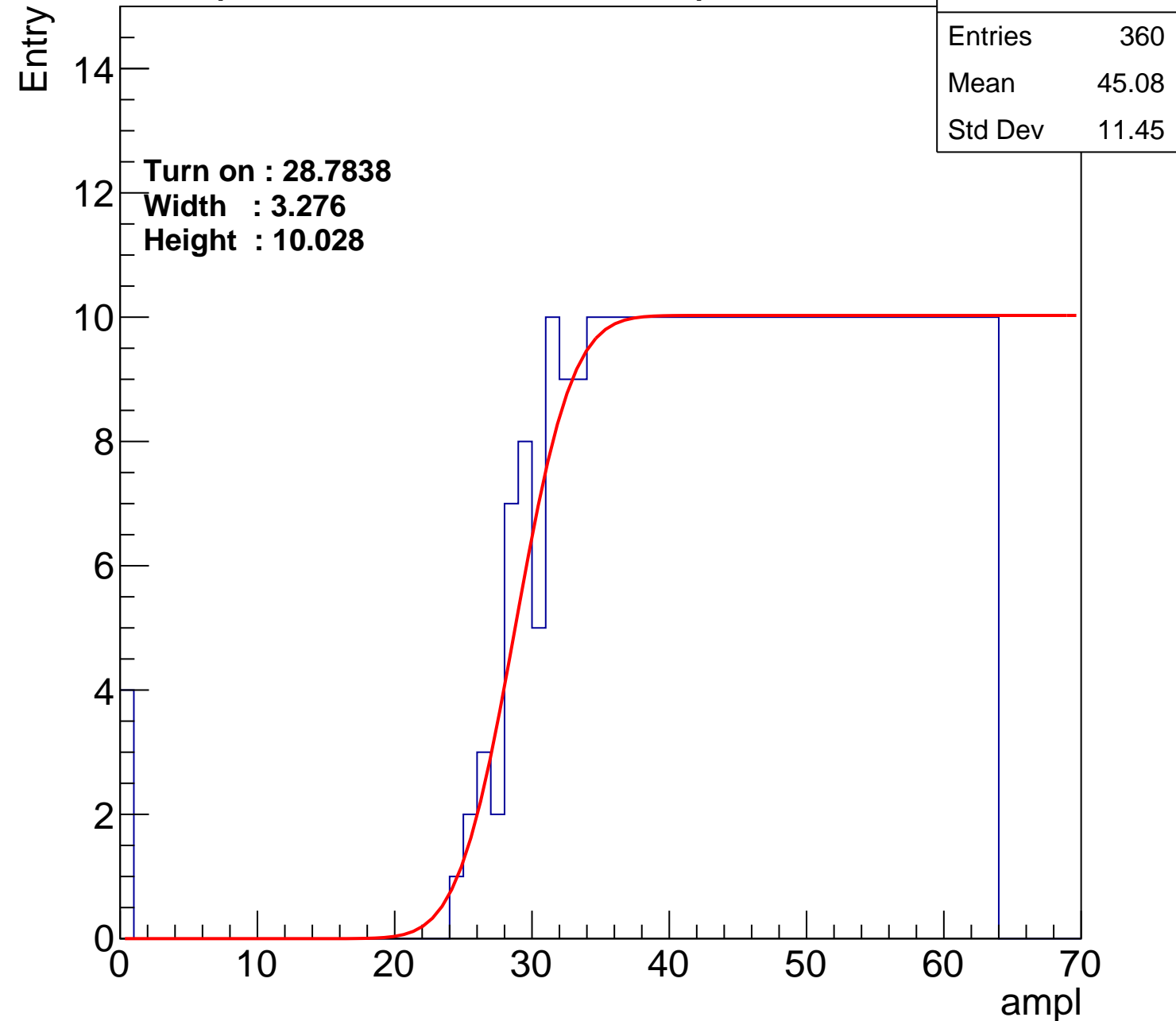
Width : 3.276

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch69

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.62
Std Dev	11.56

Turn on : 27.8000

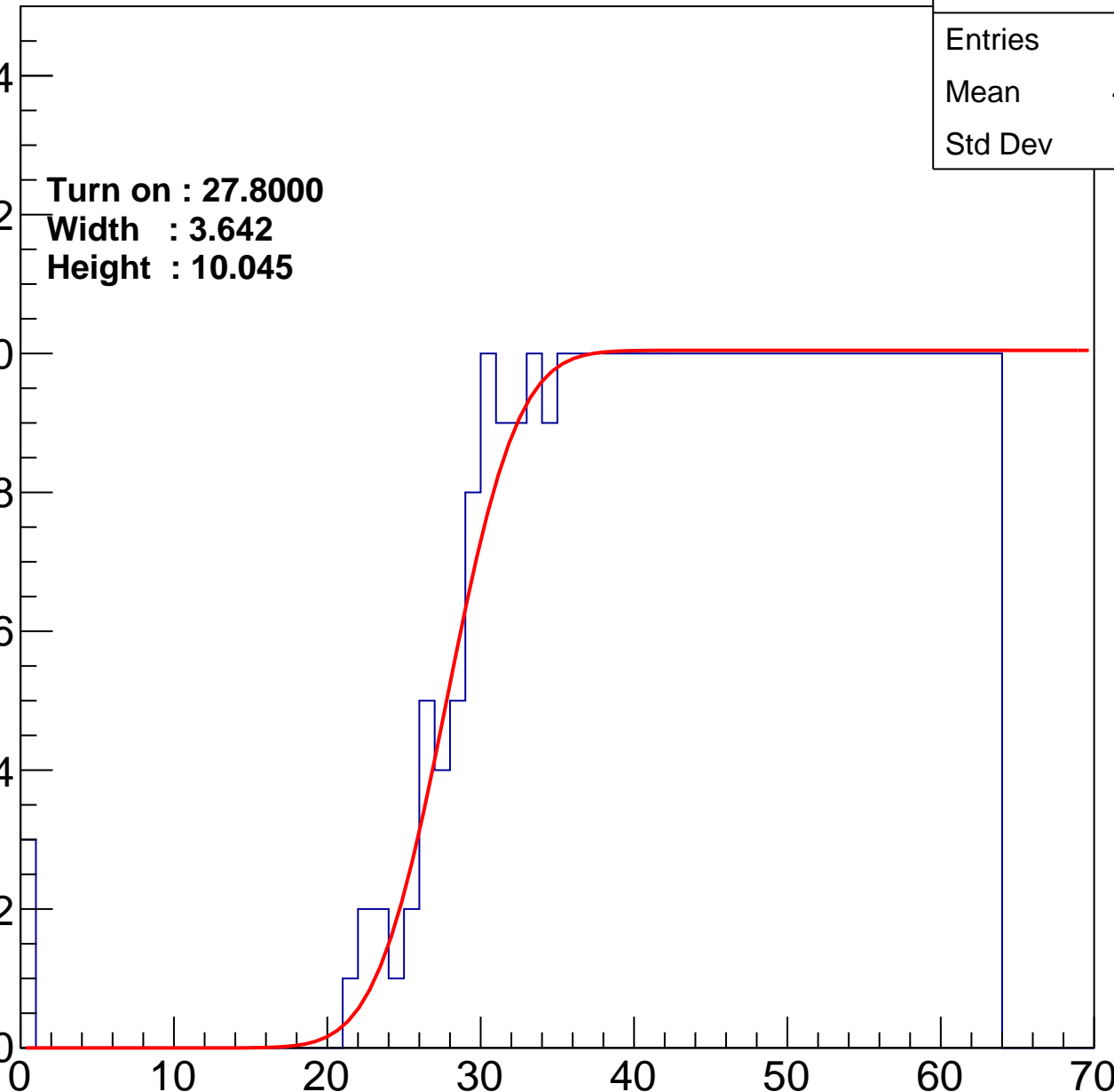
Width : 3.642

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch70

calib_packv5_042523_0143.root, FC#9, port A1

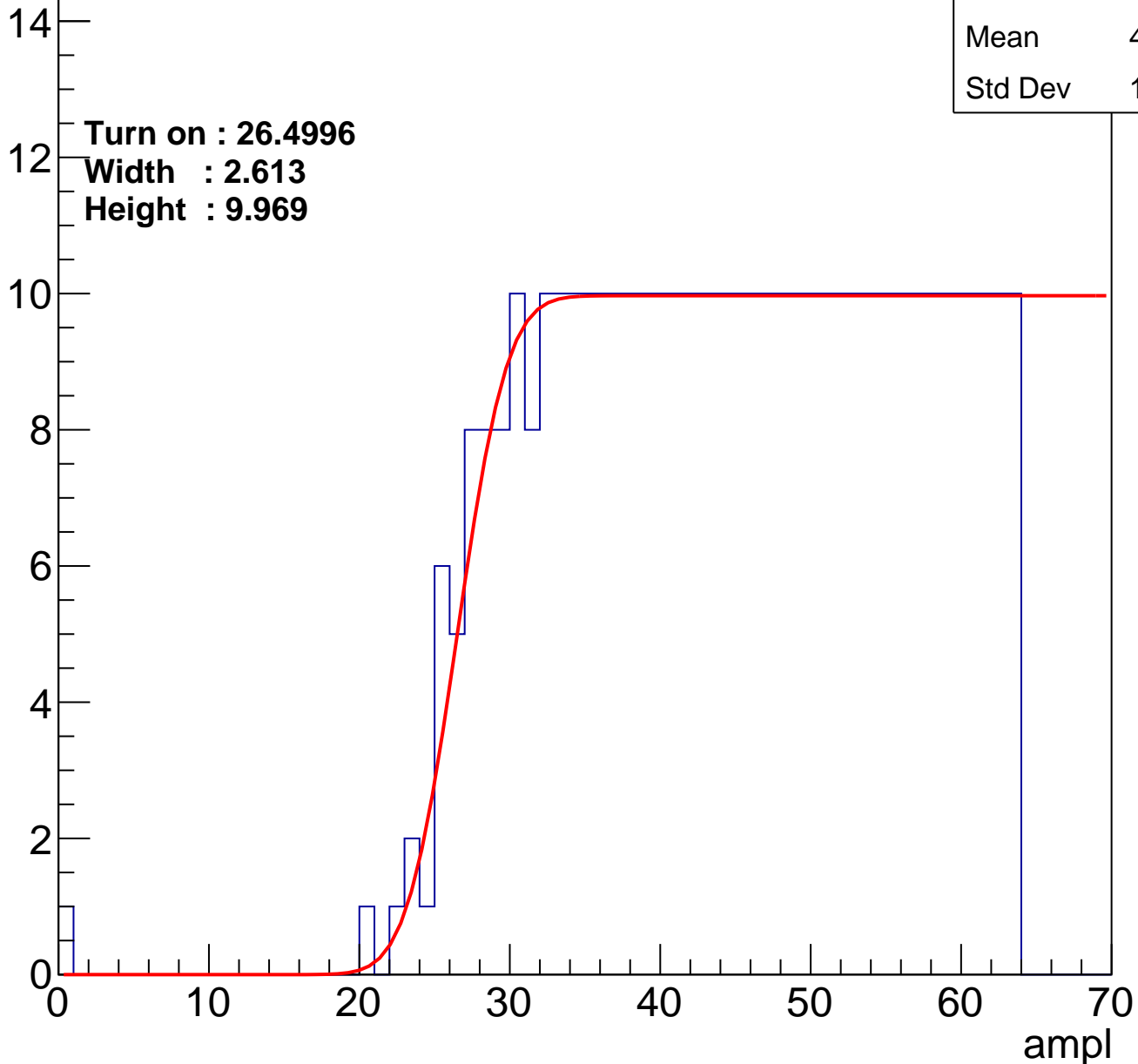
Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 26.4996

Width : 2.613

Height : 9.969

Entry



B0L001S, U15-ch71

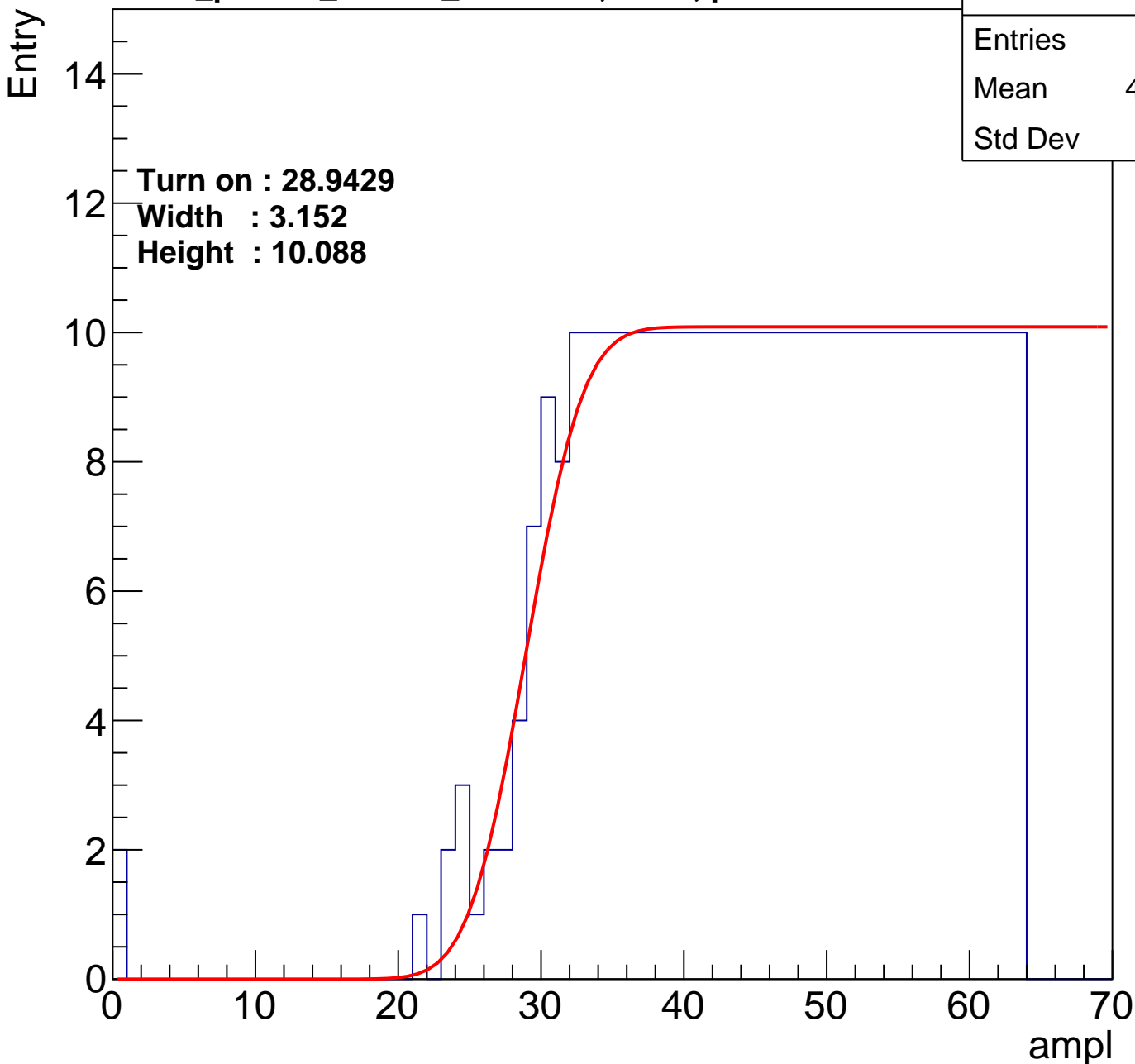
calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 28.9429

Width : 3.152

Height : 10.088

Entries	361
Mean	45.16
Std Dev	11.1



B0L001S, U15-ch72

calib_packv5_042523_0143.root, FC#9, port A1

Entries	377
Mean	44.22
Std Dev	11.89

Turn on : 27.0135

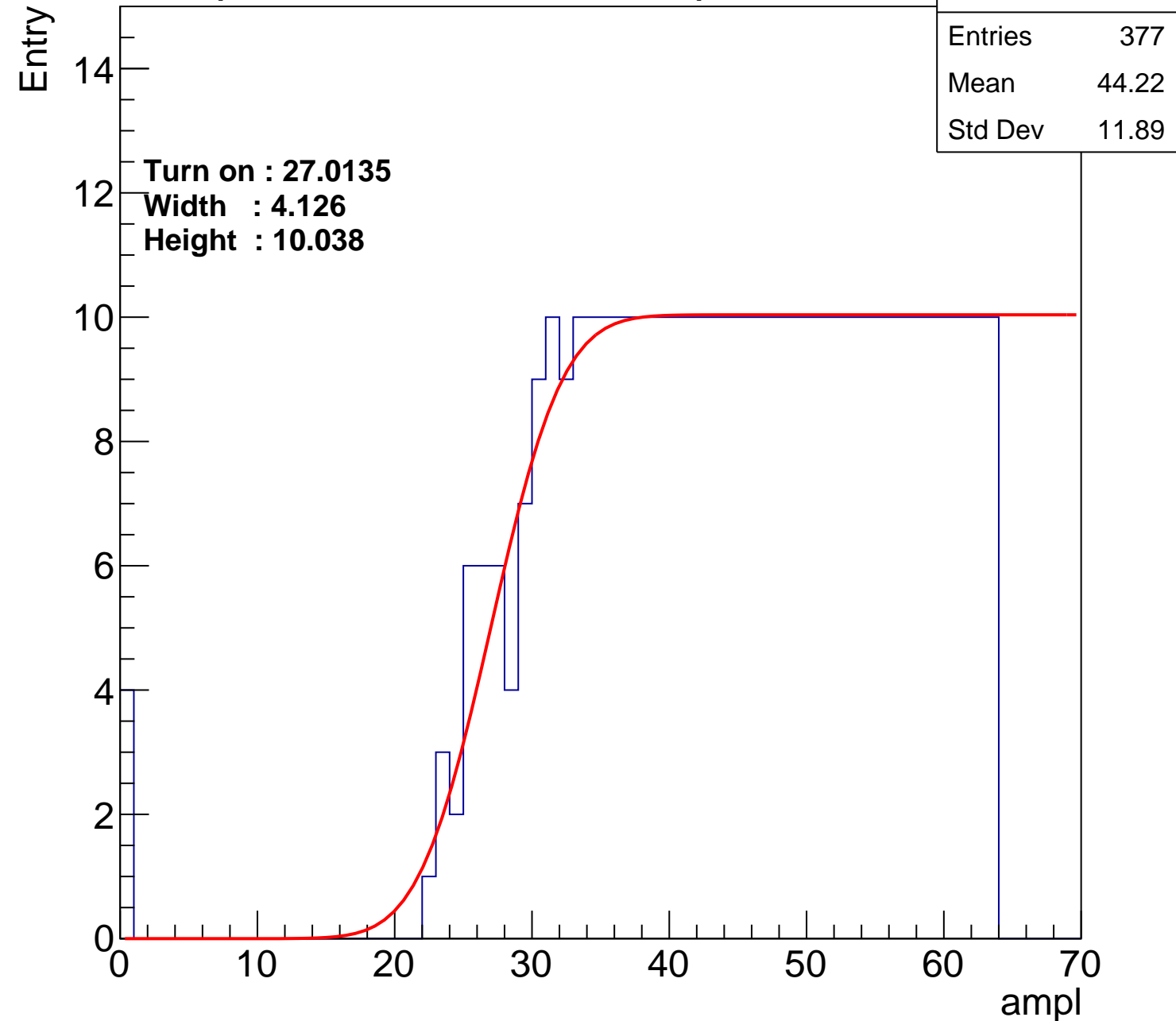
Width : 4.126

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch73

calib_packv5_042523_0143.root, FC#9, port A1

Entries	359
Mean	45.26
Std Dev	11.04

Turn on : 28.1970

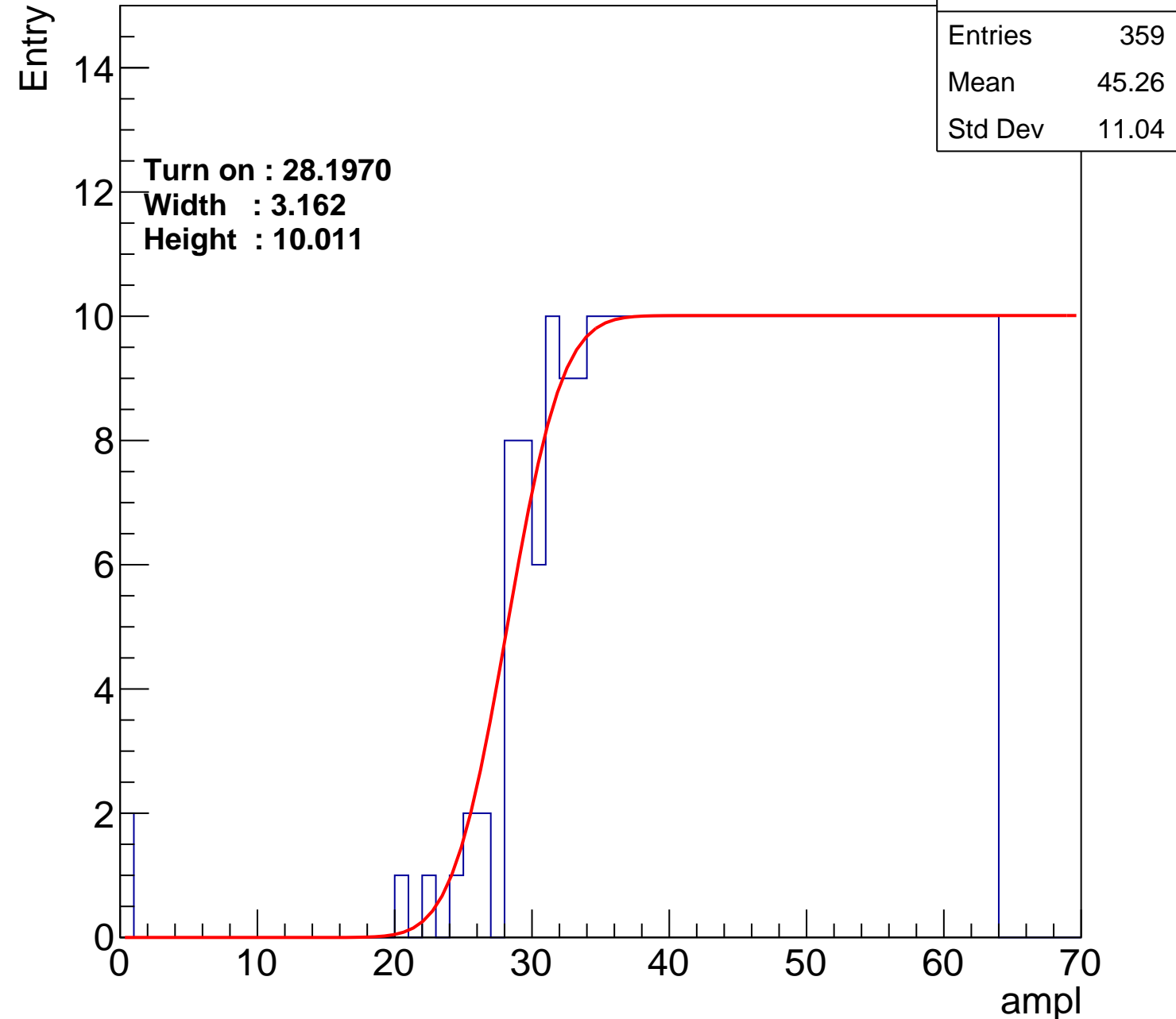
Width : 3.162

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch74

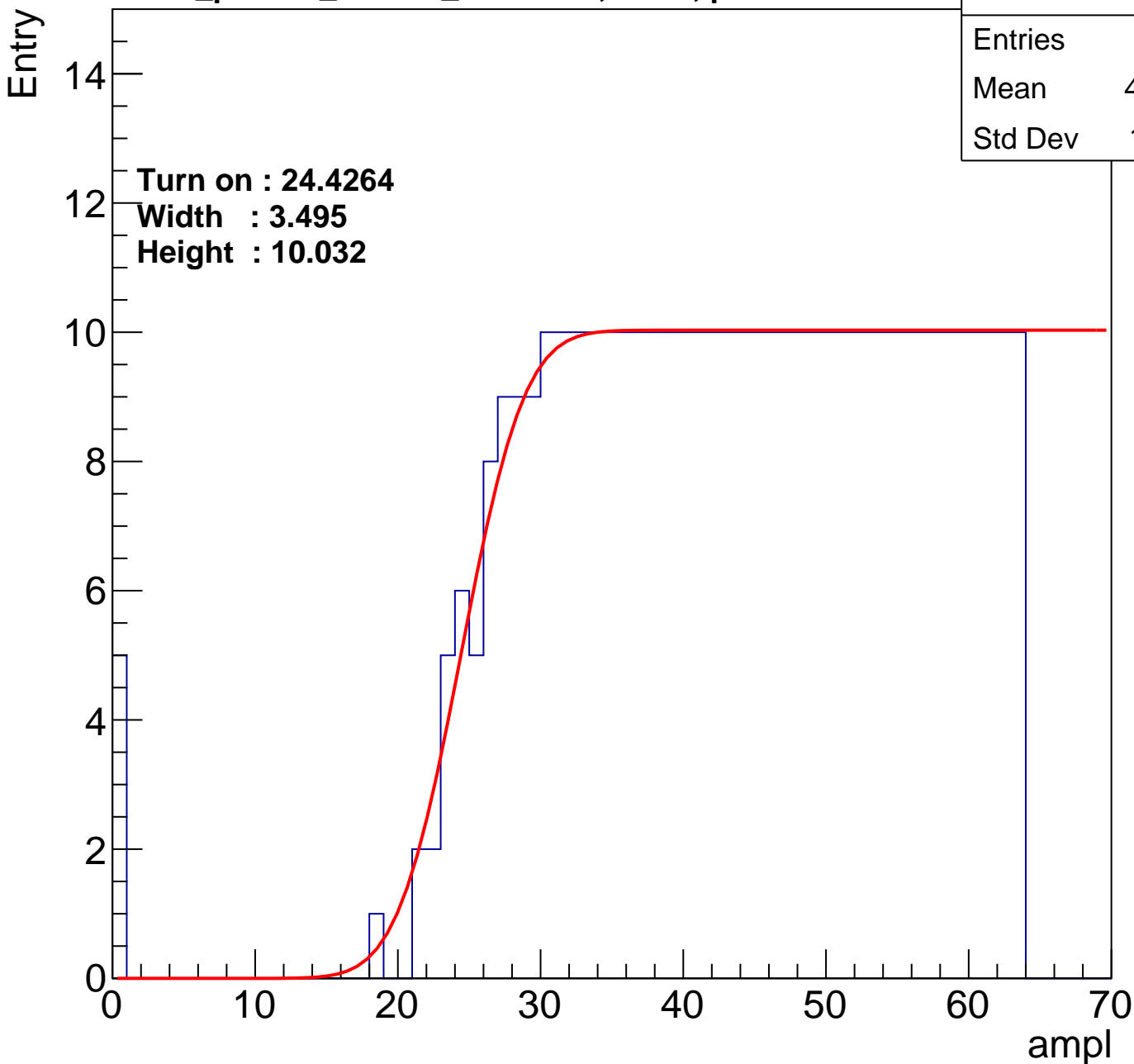
calib_packv5_042523_0143.root, FC#9, port A1

Entries	401
Mean	43.05
Std Dev	12.51

Turn on : 24.4264

Width : 3.495

Height : 10.032



B0L001S, U15-ch75

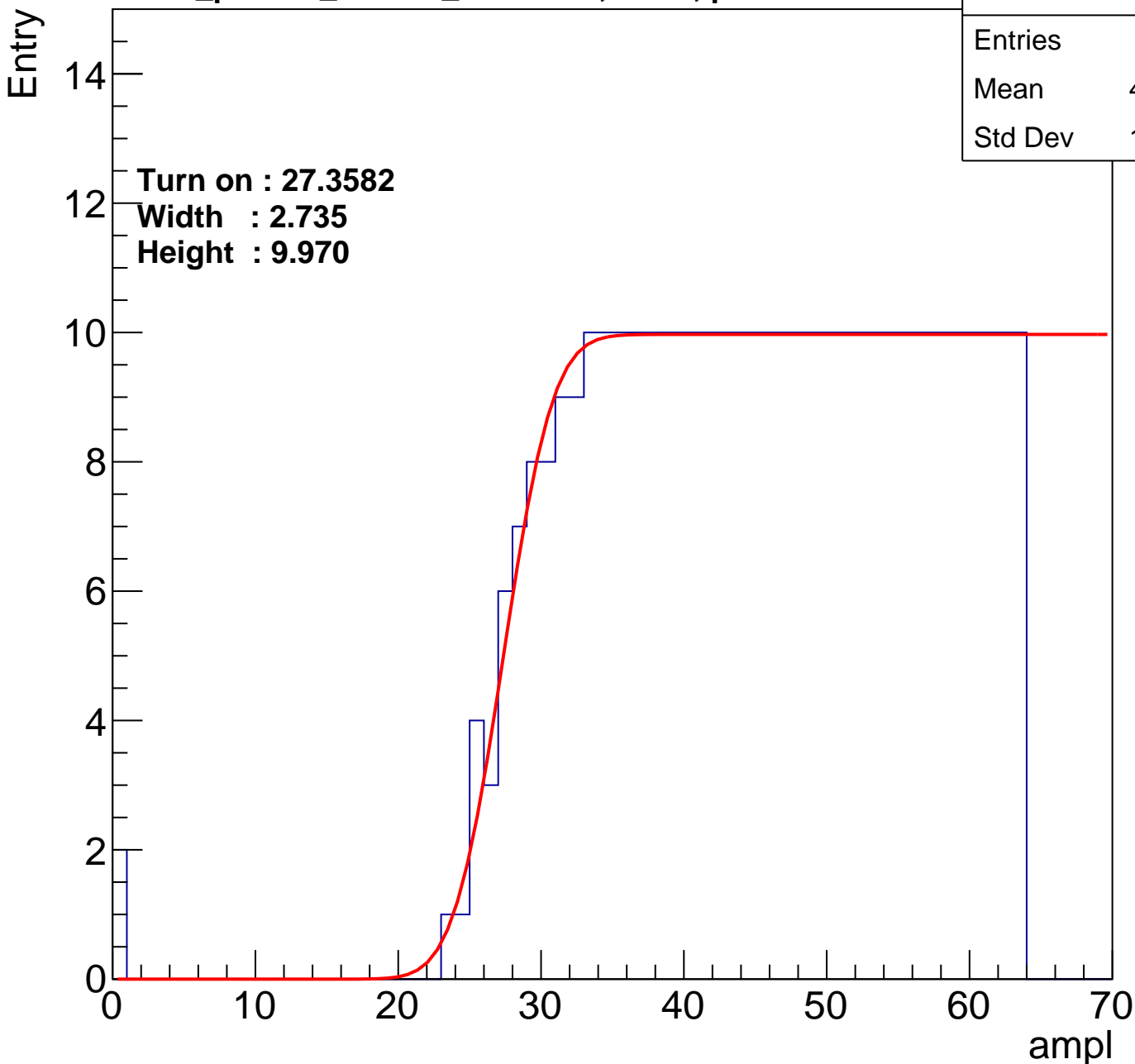
calib_packv5_042523_0143.root, FC#9, port A1

Entries	368
Mean	44.84
Std Dev	11.22

Turn on : 27.3582

Width : 2.735

Height : 9.970



B0L001S, U15-ch76

calib_packv5_042523_0143.root, FC#9, port A1

Entries	373
Mean	44.47
Std Dev	11.7

Turn on : 27.4224

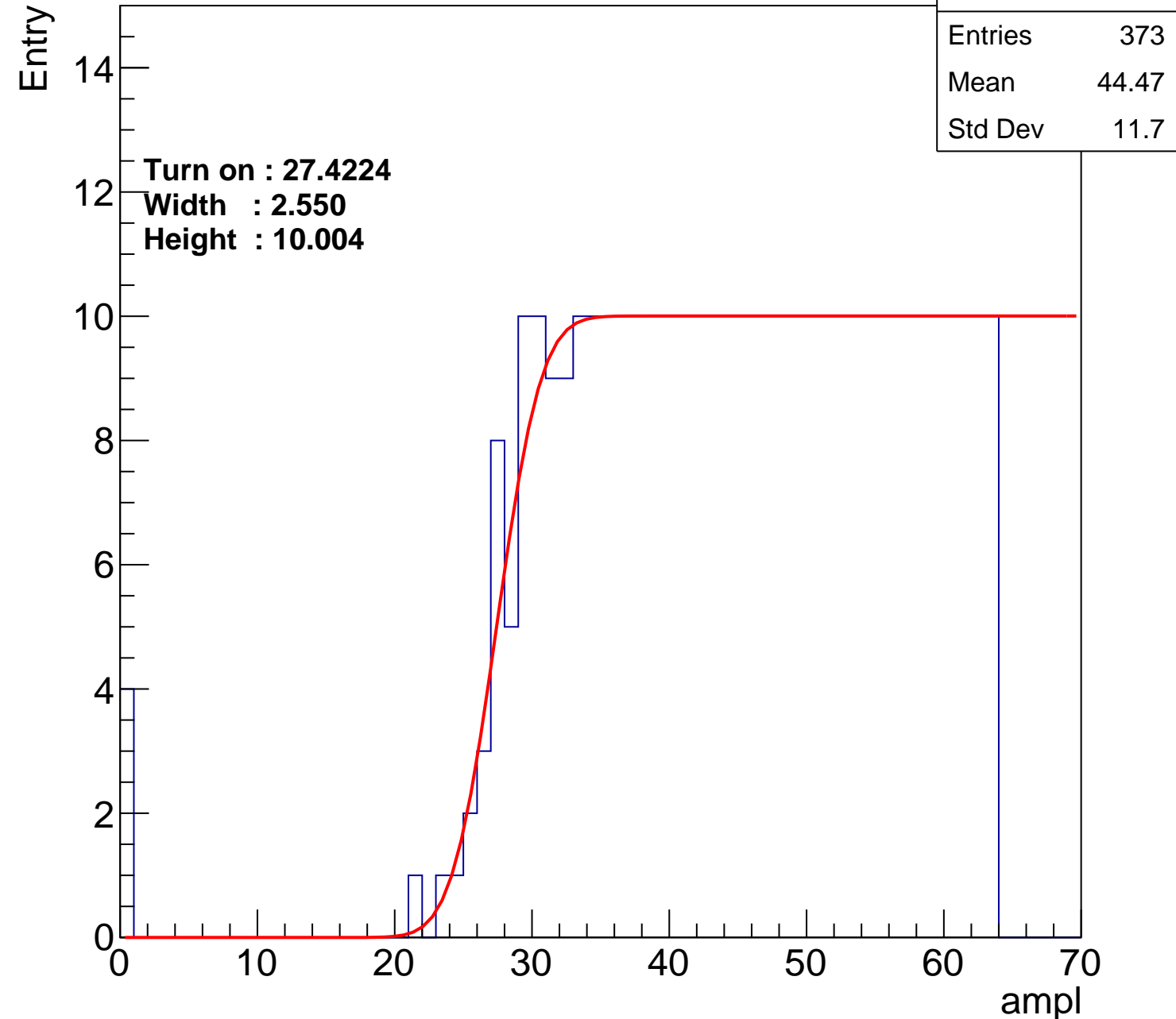
Width : 2.550

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch77

calib_packv5_042523_0143.root, FC#9, port A1

Entries	379
Mean	44.08
Std Dev	12.07

Turn on : 27.0107

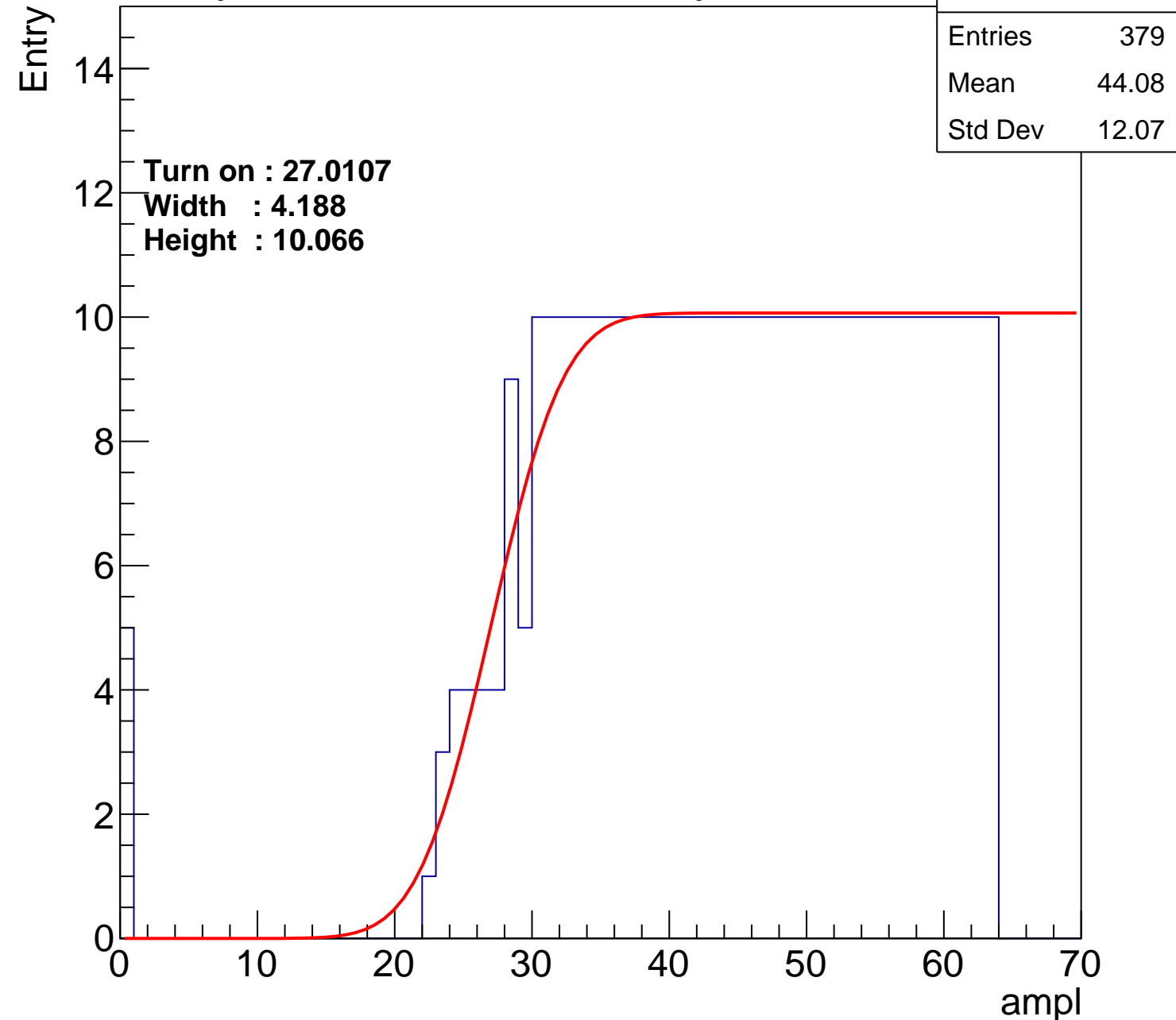
Width : 4.188

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch78

calib_packv5_042523_0143.root, FC#9, port A1

Entries	366
Mean	44.85
Std Dev	11.41

Turn on : 28.6329

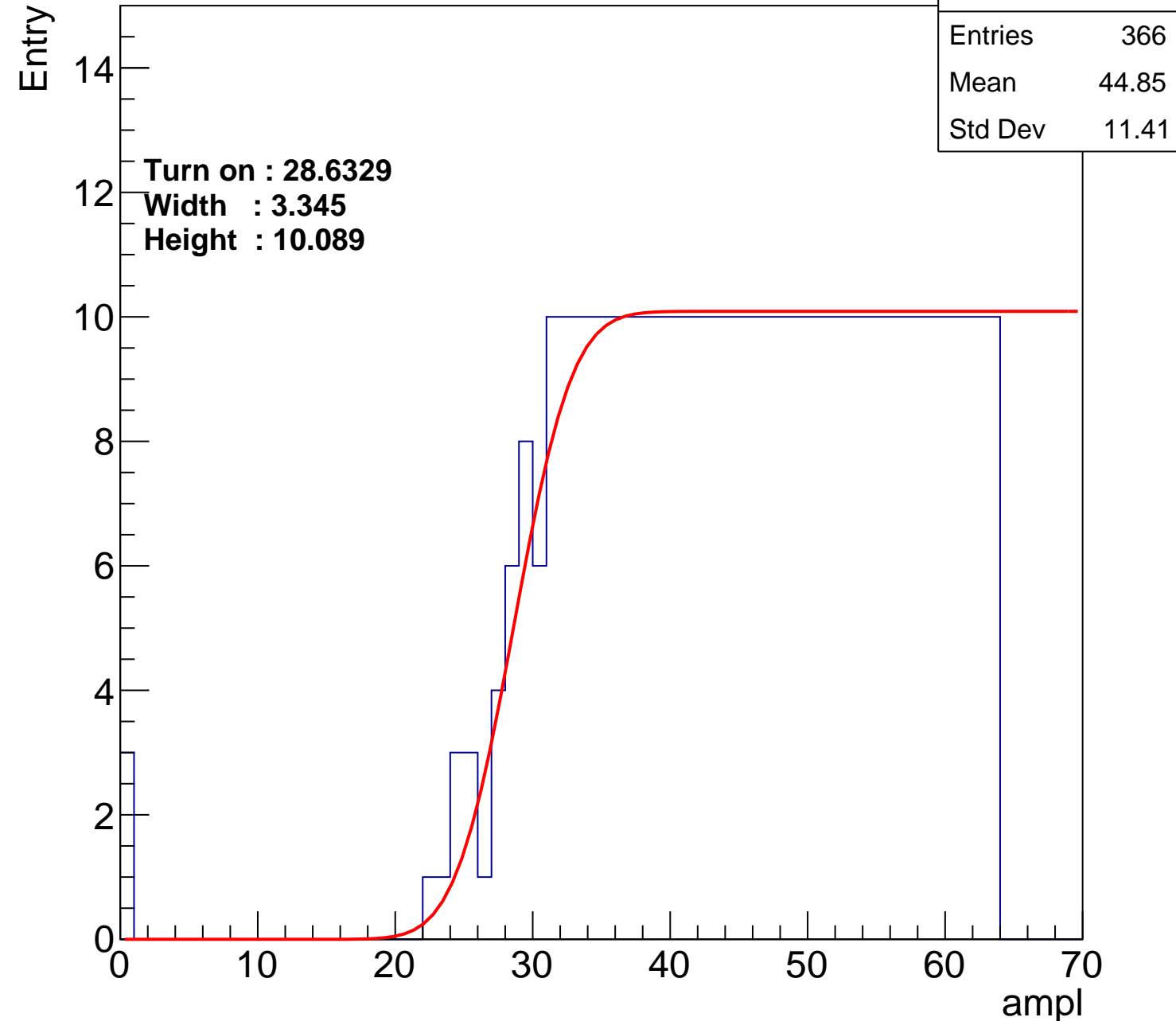
Width : 3.345

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch79

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.18
Std Dev	11.11

Turn on : 28.3535

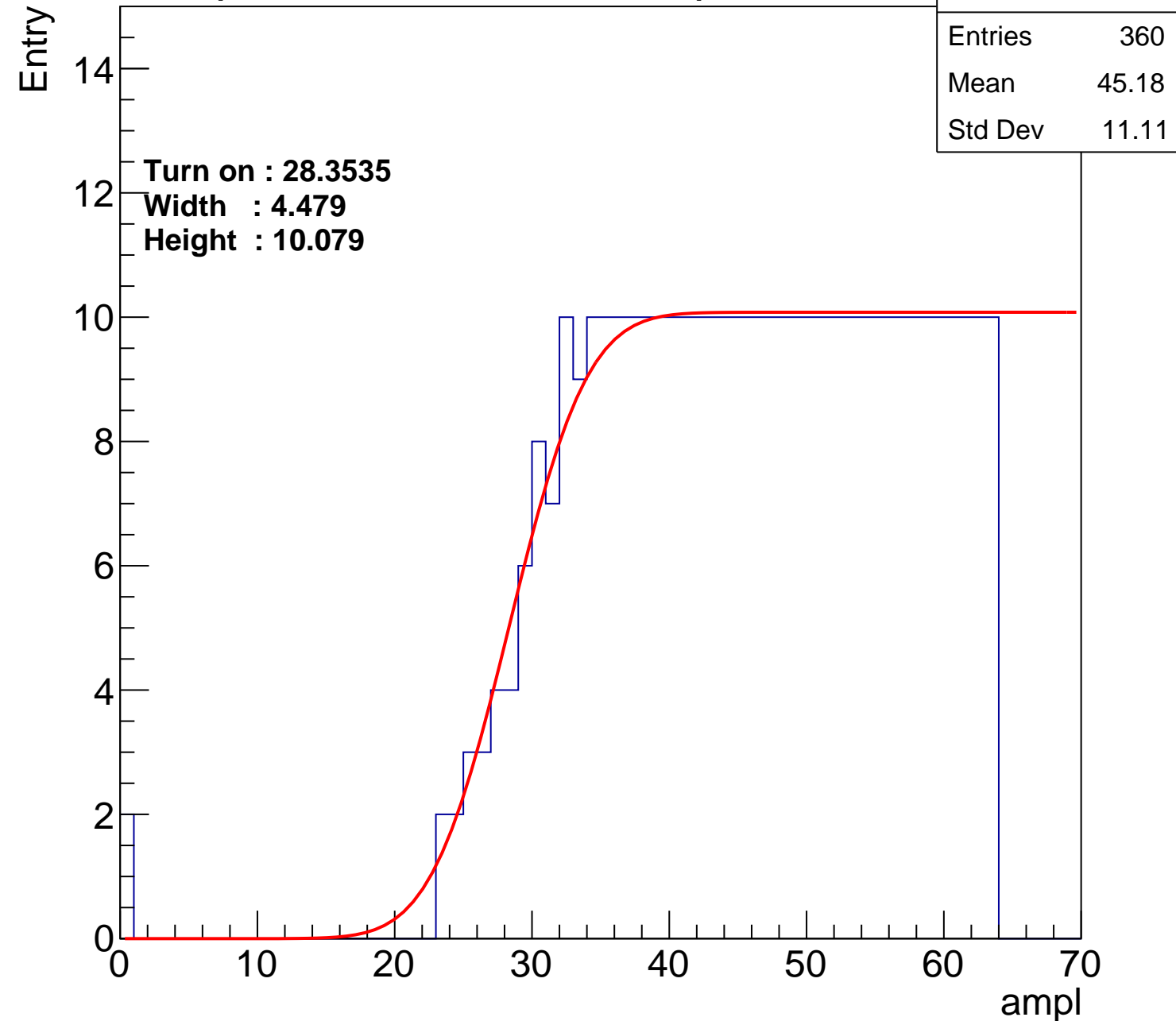
Width : 4.479

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch80

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 27.3331

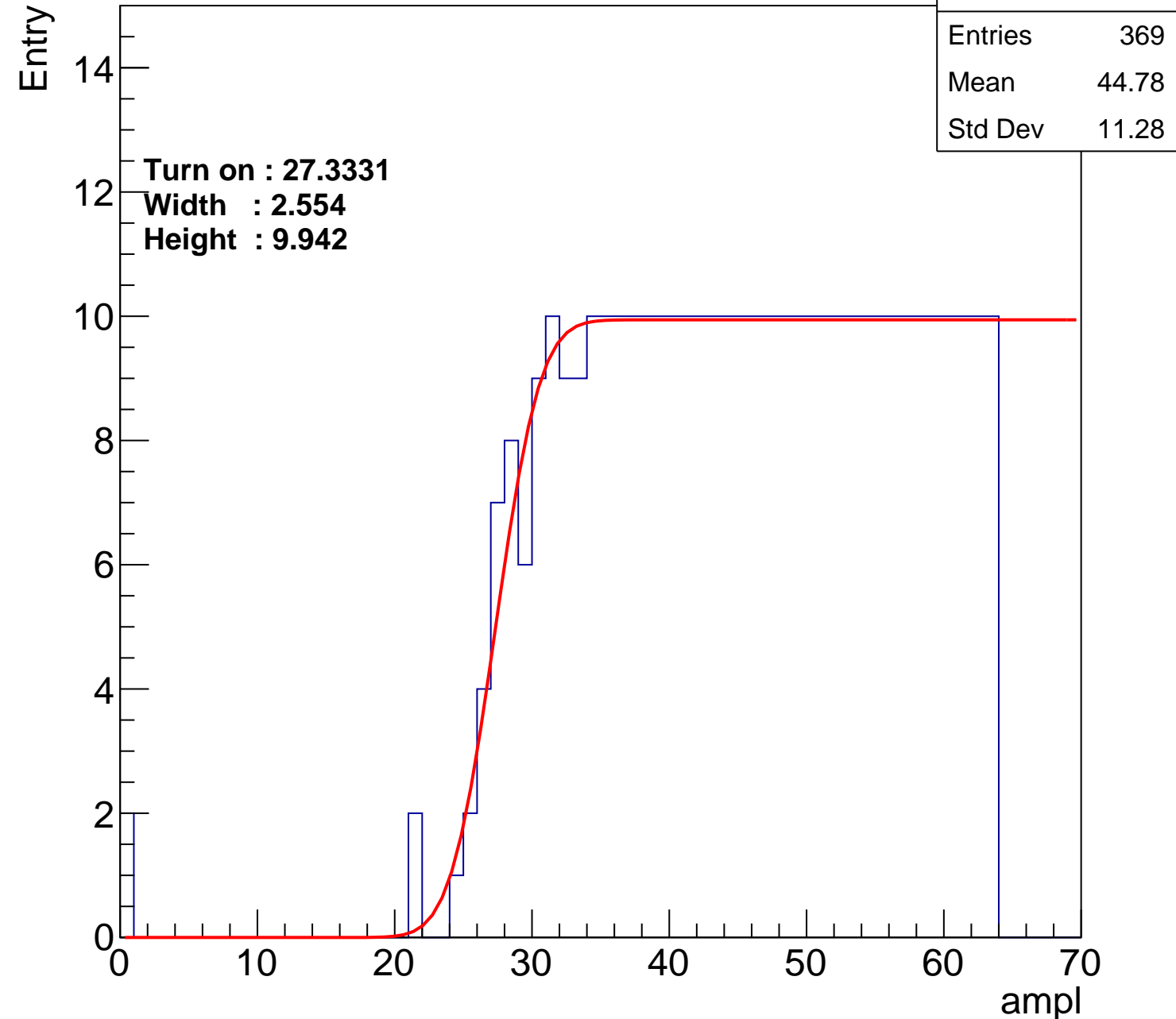
Width : 2.554

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch81

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.06
Std Dev	12.22

Turn on : 26.6495

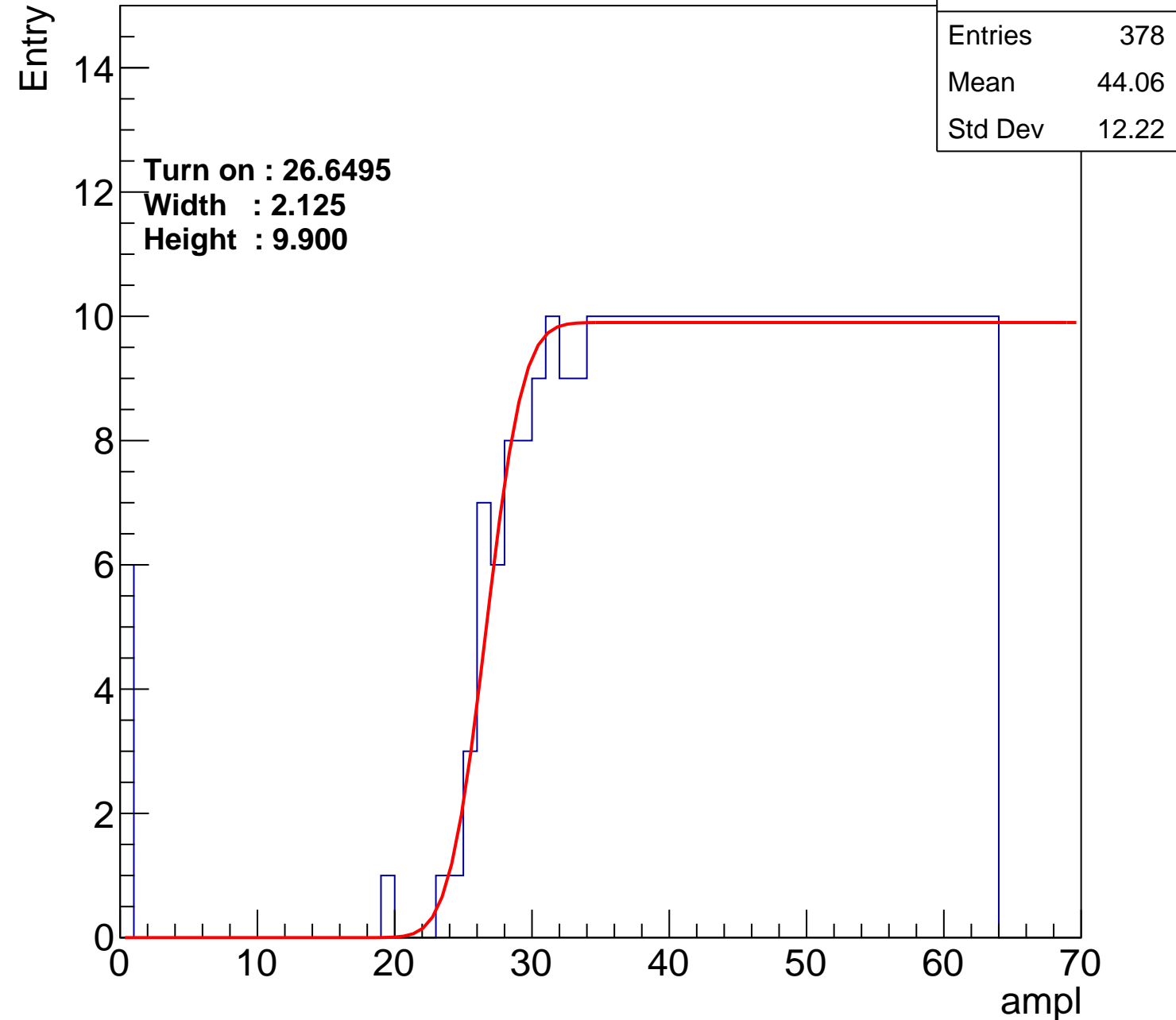
Width : 2.125

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch82

calib_packv5_042523_0143.root, FC#9, port A1

Entries	379
Mean	44.07
Std Dev	12.15

Turn on : 26.4723

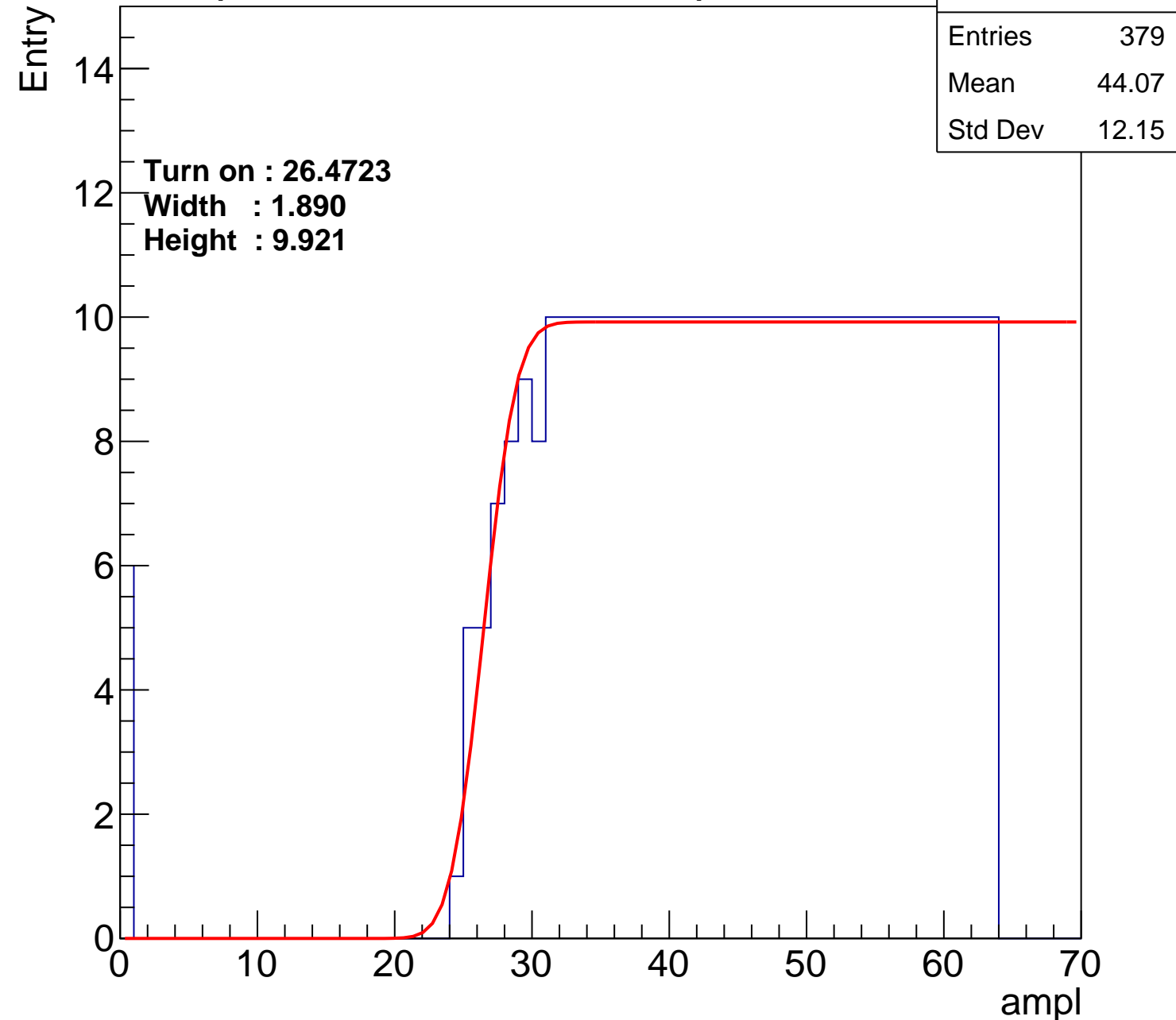
Width : 1.890

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch83

calib_packv5_042523_0143.root, FC#9, port A1

Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 27.8009

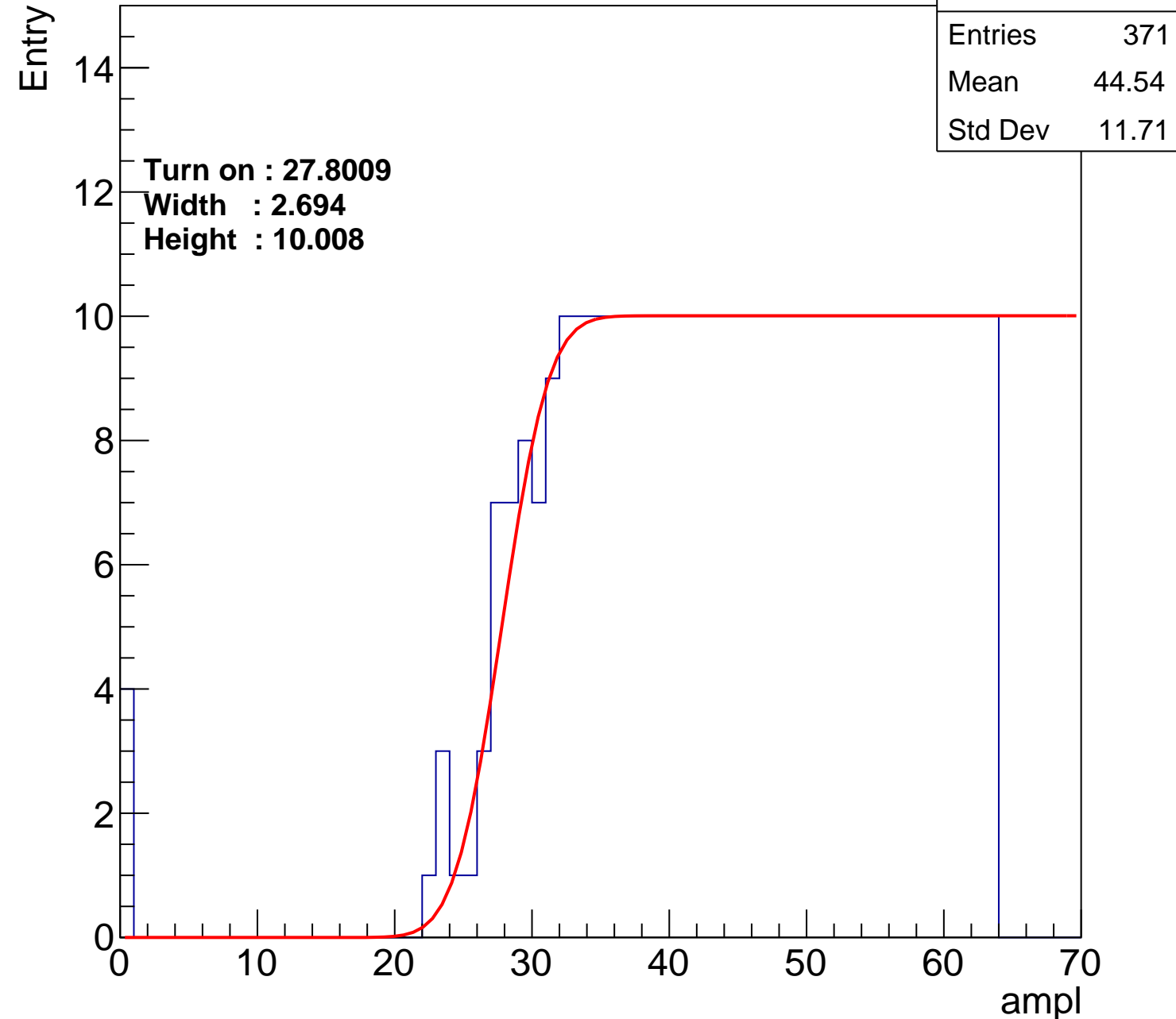
Width : 2.694

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch84

calib_packv5_042523_0143.root, FC#9, port A1

Entries	382
Mean	44.19
Std Dev	11.45

Turn on : 26.1488

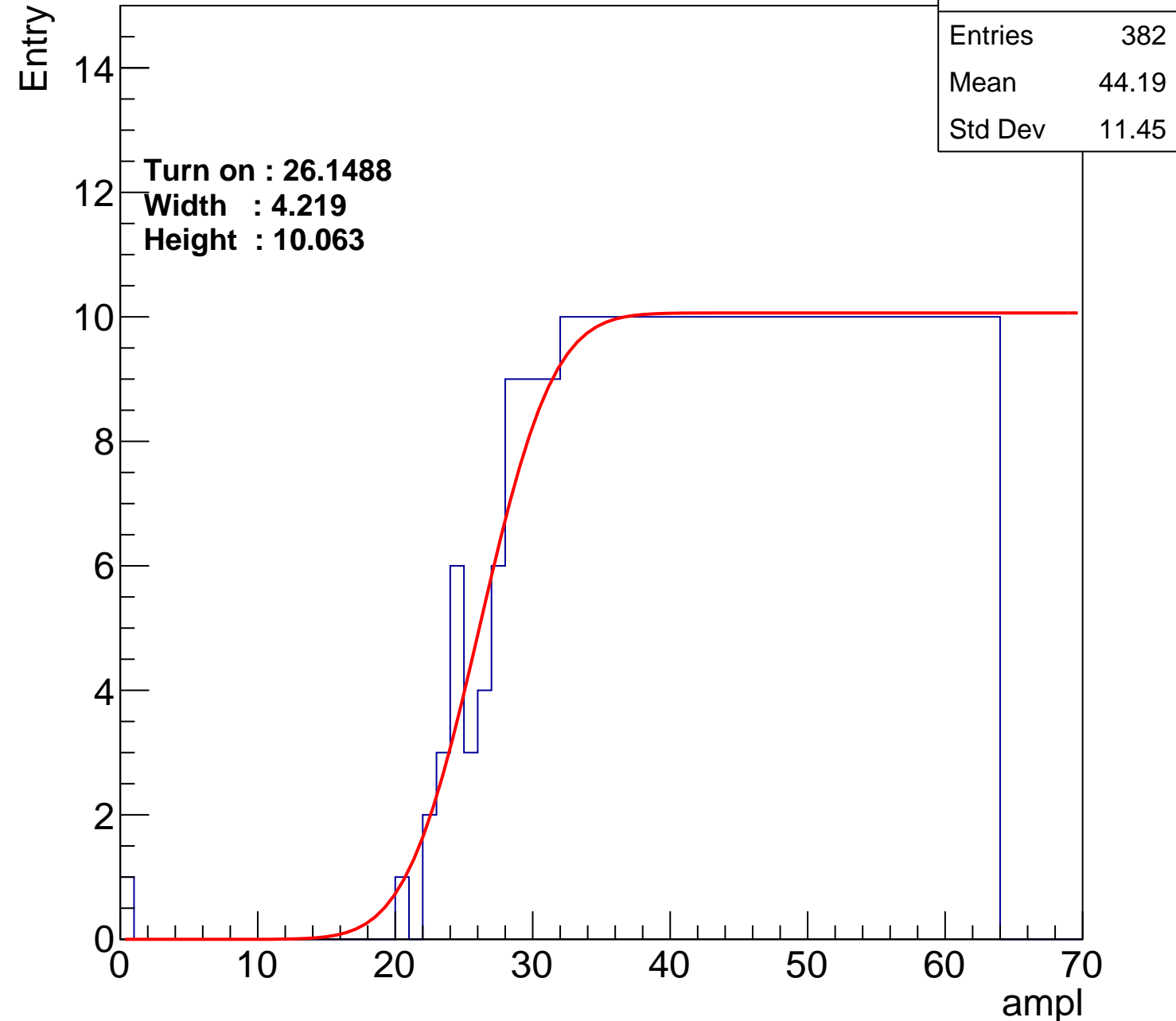
Width : 4.219

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch85

calib_packv5_042523_0143.root, FC#9, port A1

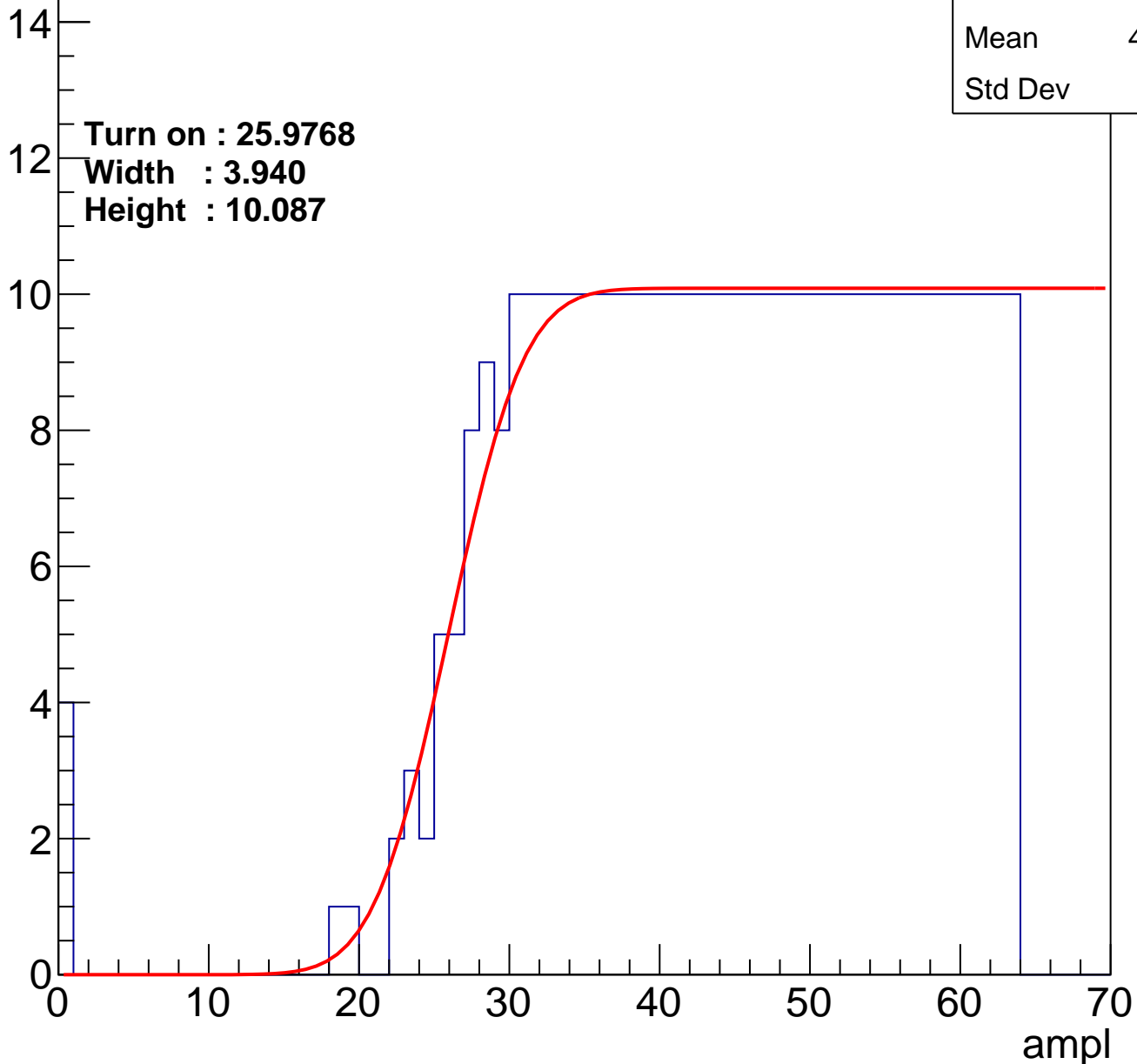
Entries	388
Mean	43.72
Std Dev	12.1

Turn on : 25.9768

Width : 3.940

Height : 10.087

Entry



B0L001S, U15-ch86

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.6
Std Dev	11.49

Turn on : 27.3947

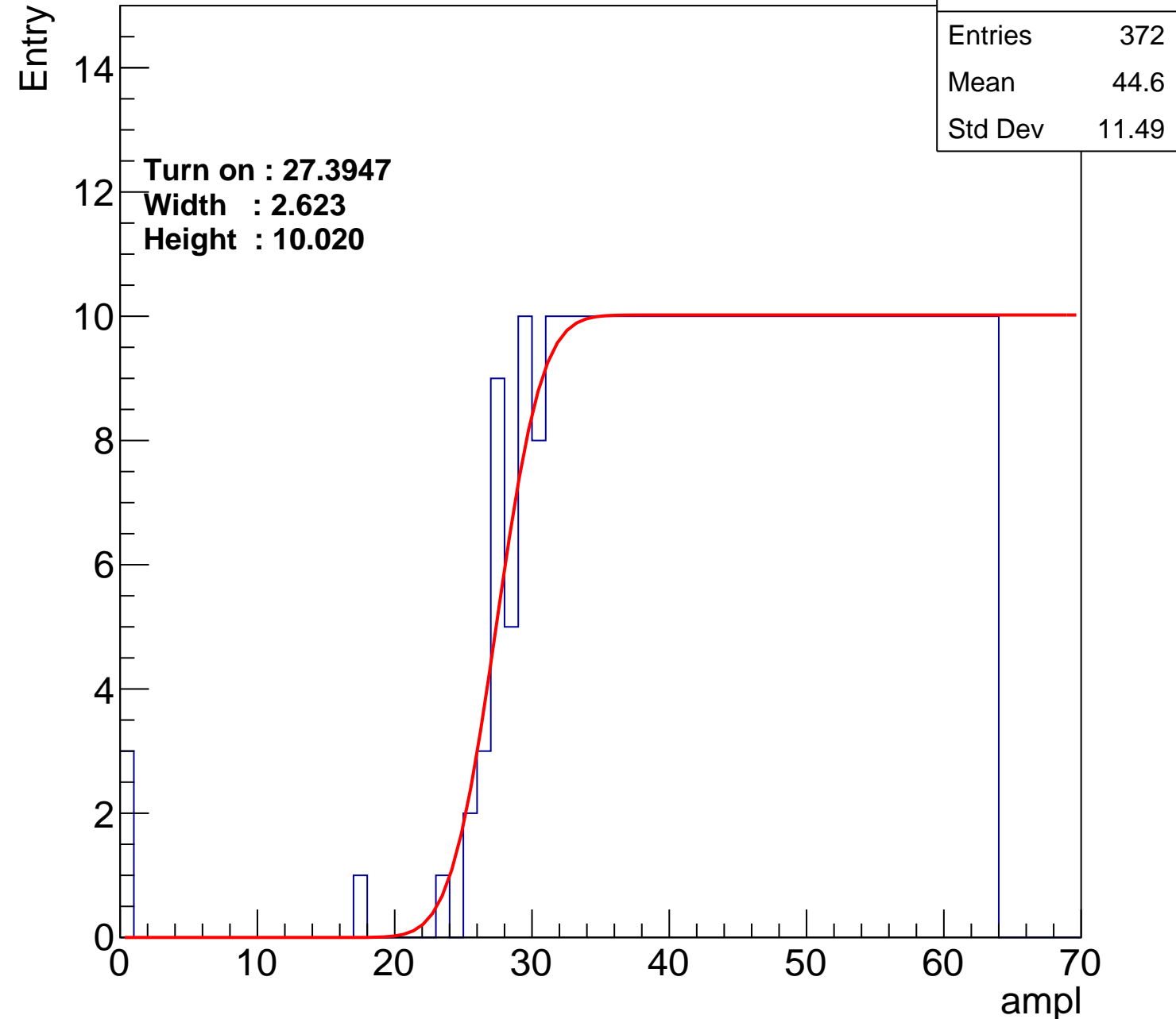
Width : 2.623

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch87

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.94
Std Dev	11.32

Turn on : 28.0215

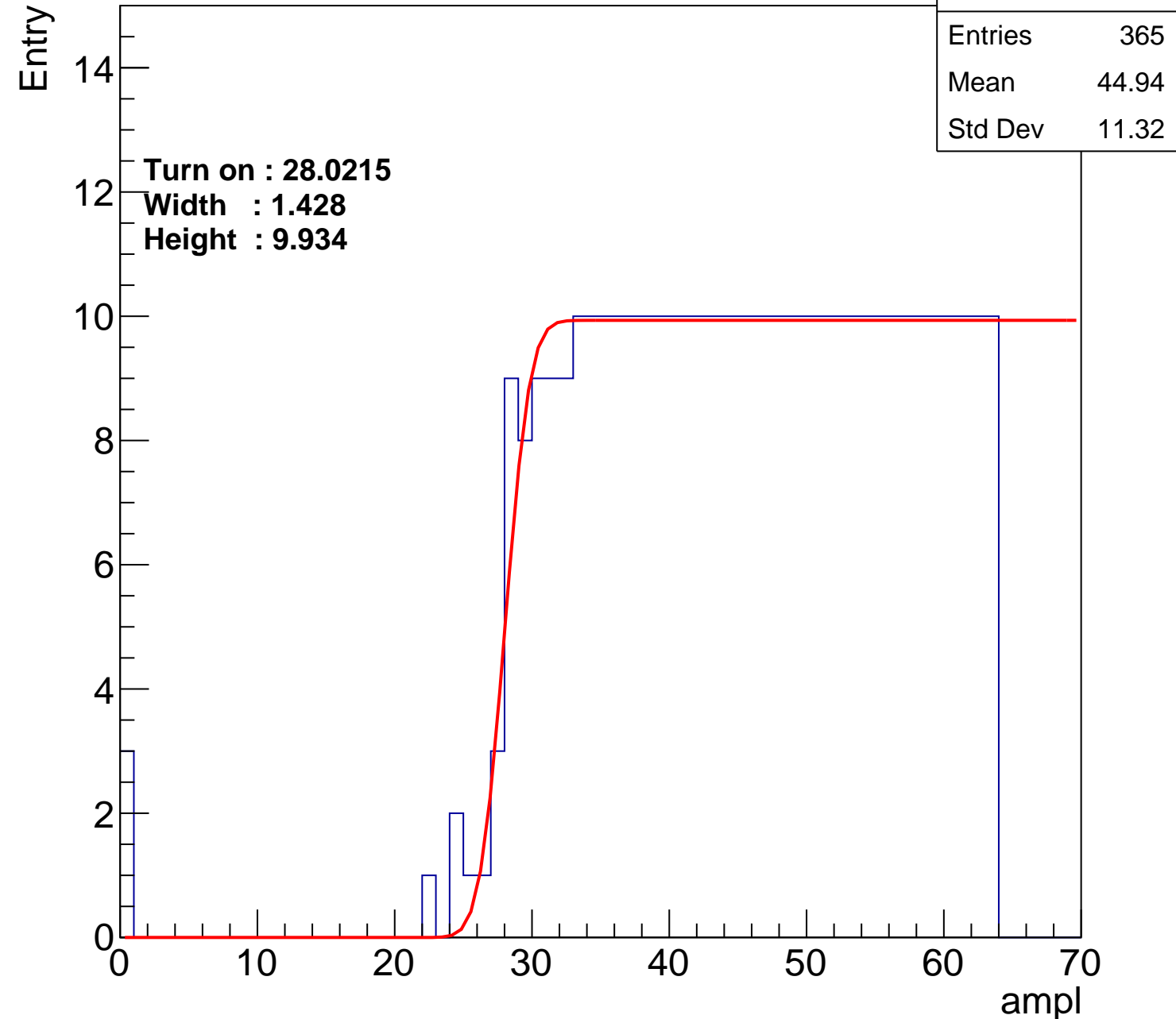
Width : 1.428

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch88

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45.08
Std Dev	11.12

Turn on : 28.7106

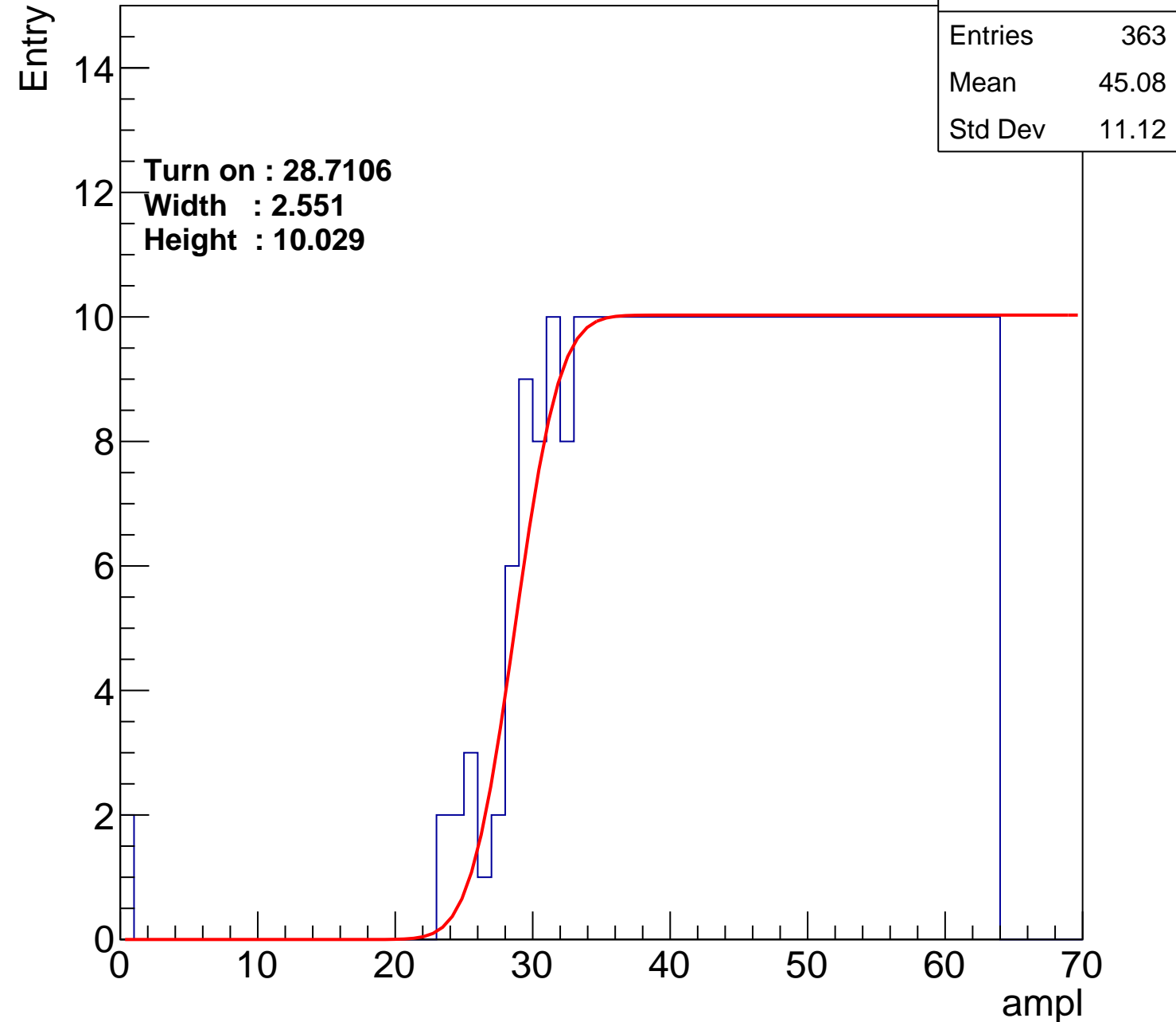
Width : 2.551

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch89

calib_packv5_042523_0143.root, FC#9, port A1

Entries	378
Mean	44.18
Std Dev	11.9

Turn on : 26.9242

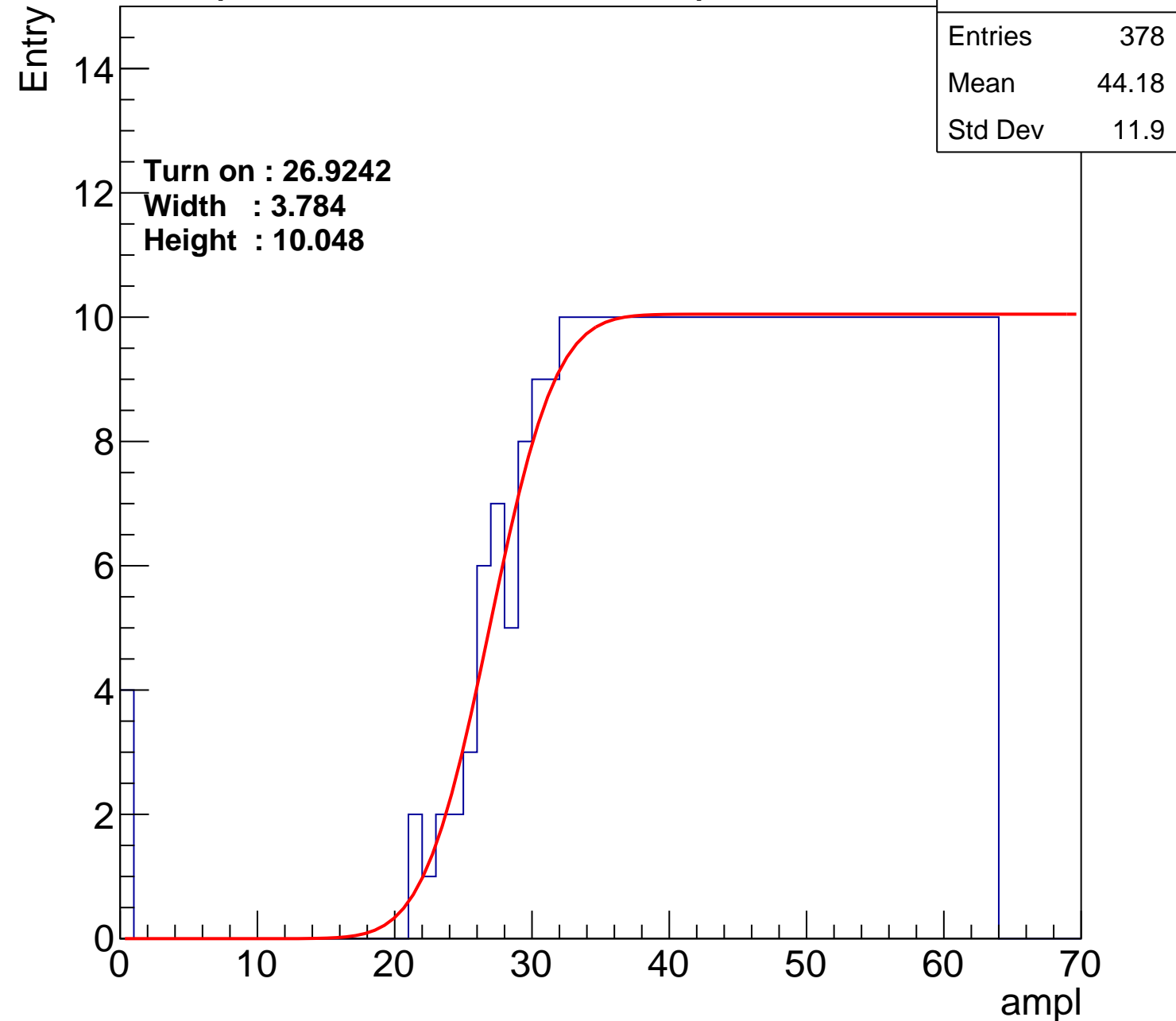
Width : 3.784

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch90

calib_packv5_042523_0143.root, FC#9, port A1

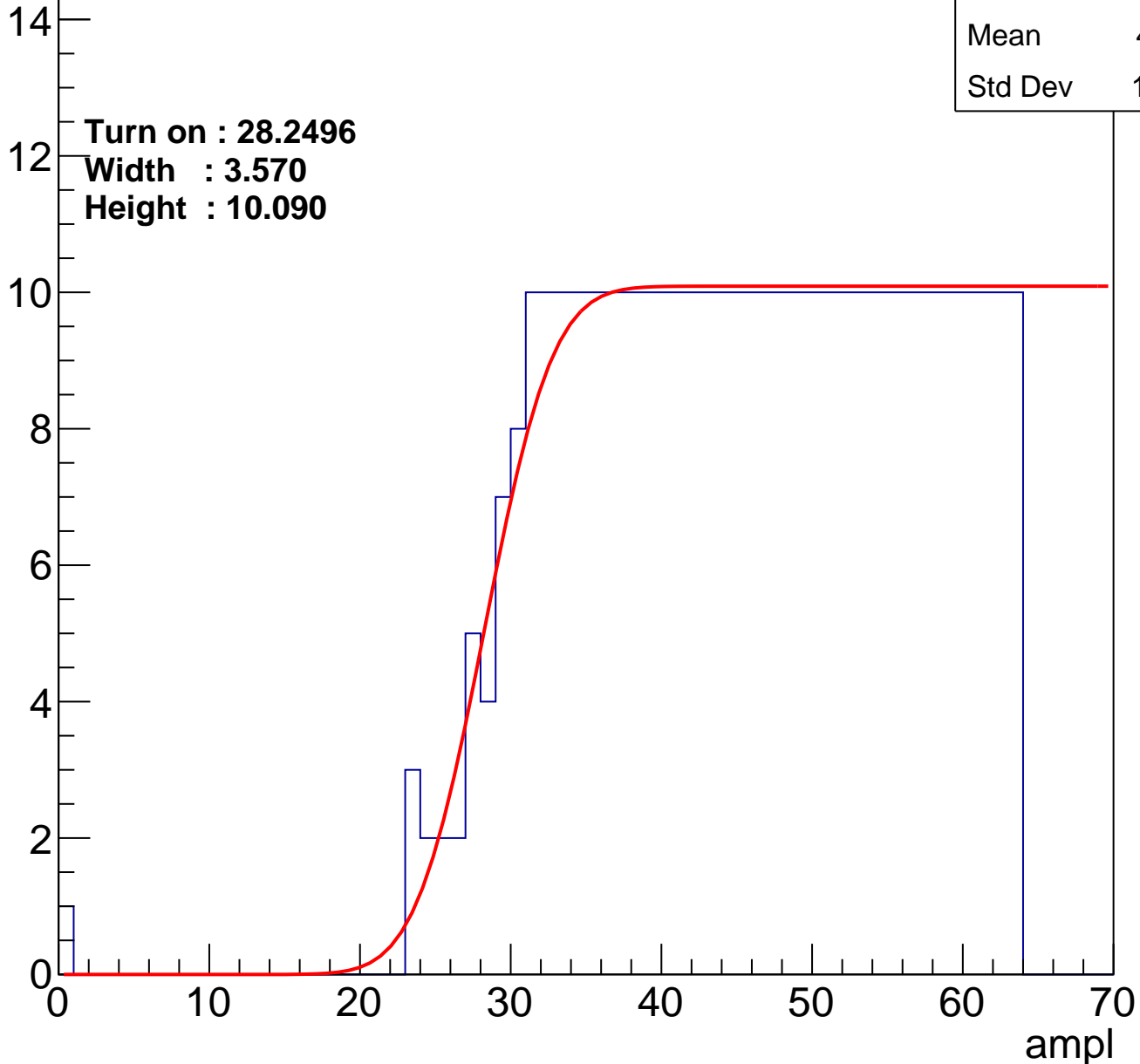
Entries	364
Mean	45.11
Std Dev	10.93

Turn on : 28.2496

Width : 3.570

Height : 10.090

Entry



B0L001S, U15-ch91

calib_packv5_042523_0143.root, FC#9, port A1

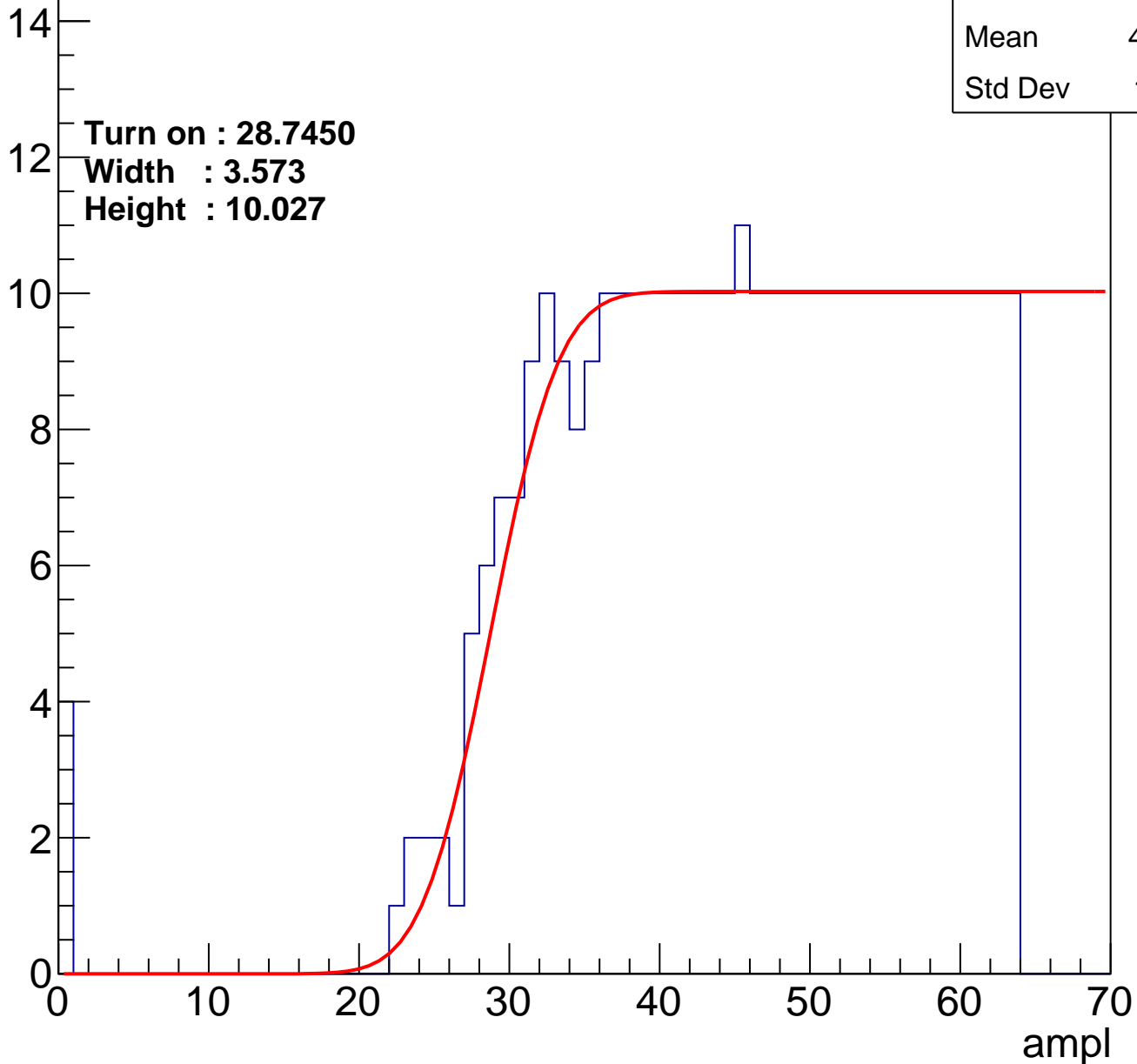
Entries	363
Mean	44.89
Std Dev	11.61

Turn on : 28.7450

Width : 3.573

Height : 10.027

Entry



B0L001S, U15-ch92

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.91
Std Dev	11.36

Turn on : 28.0679

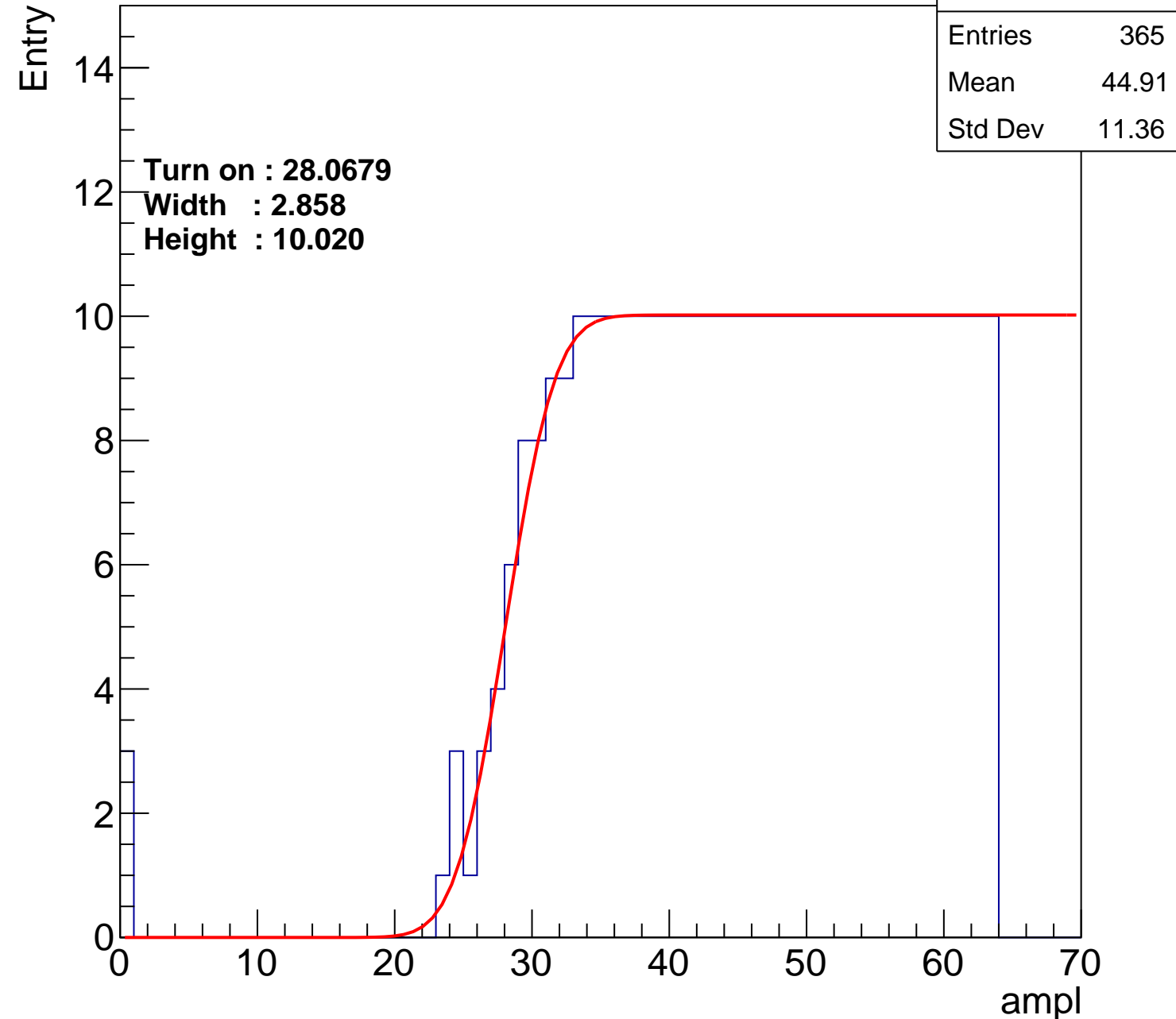
Width : 2.858

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch93

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.09
Std Dev	11.44

Turn on : 29.1564

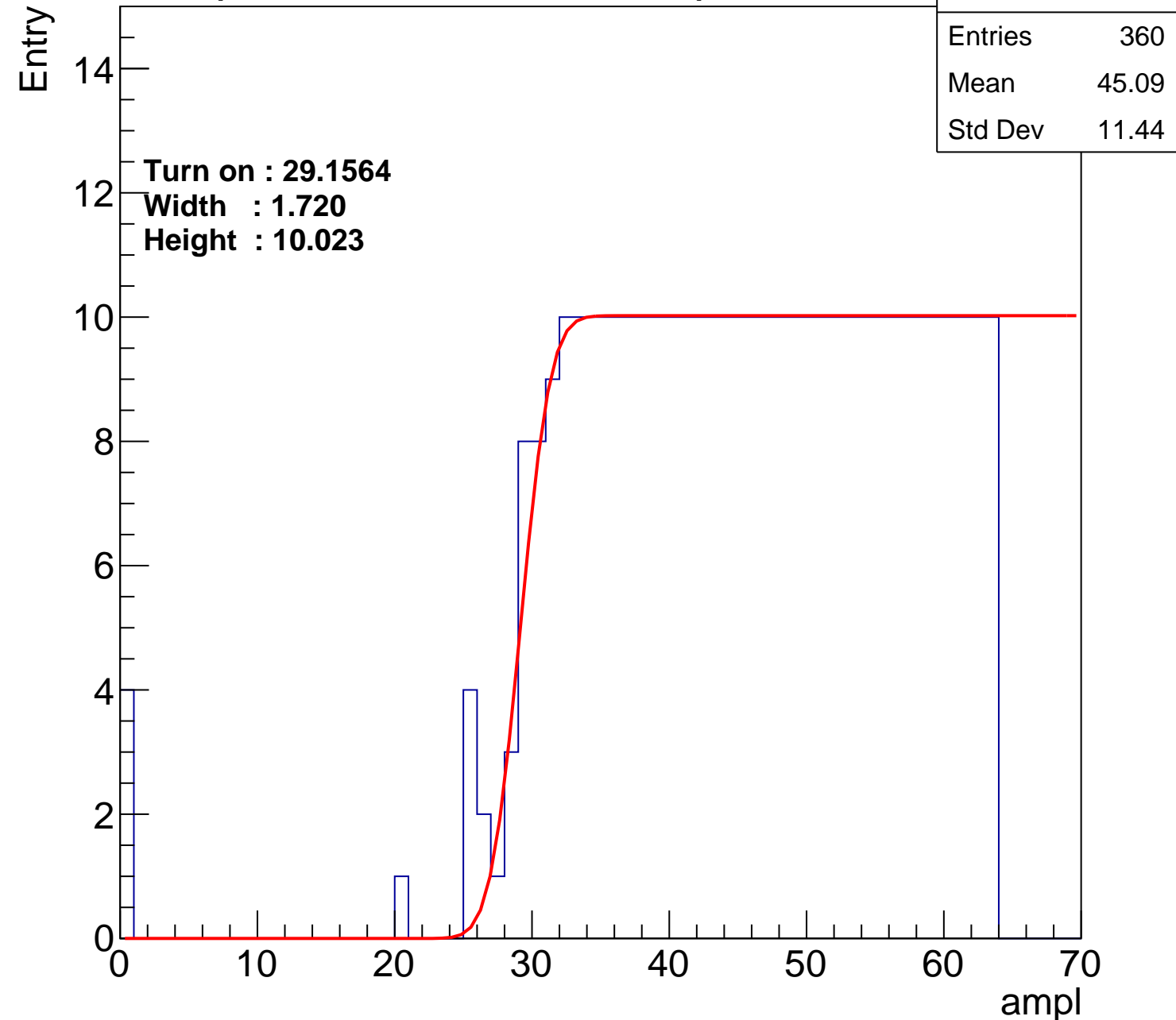
Width : 1.720

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch94

calib_packv5_042523_0143.root, FC#9, port A1

Entries	383
Mean	44.08
Std Dev	11.65

Turn on : 25.5923

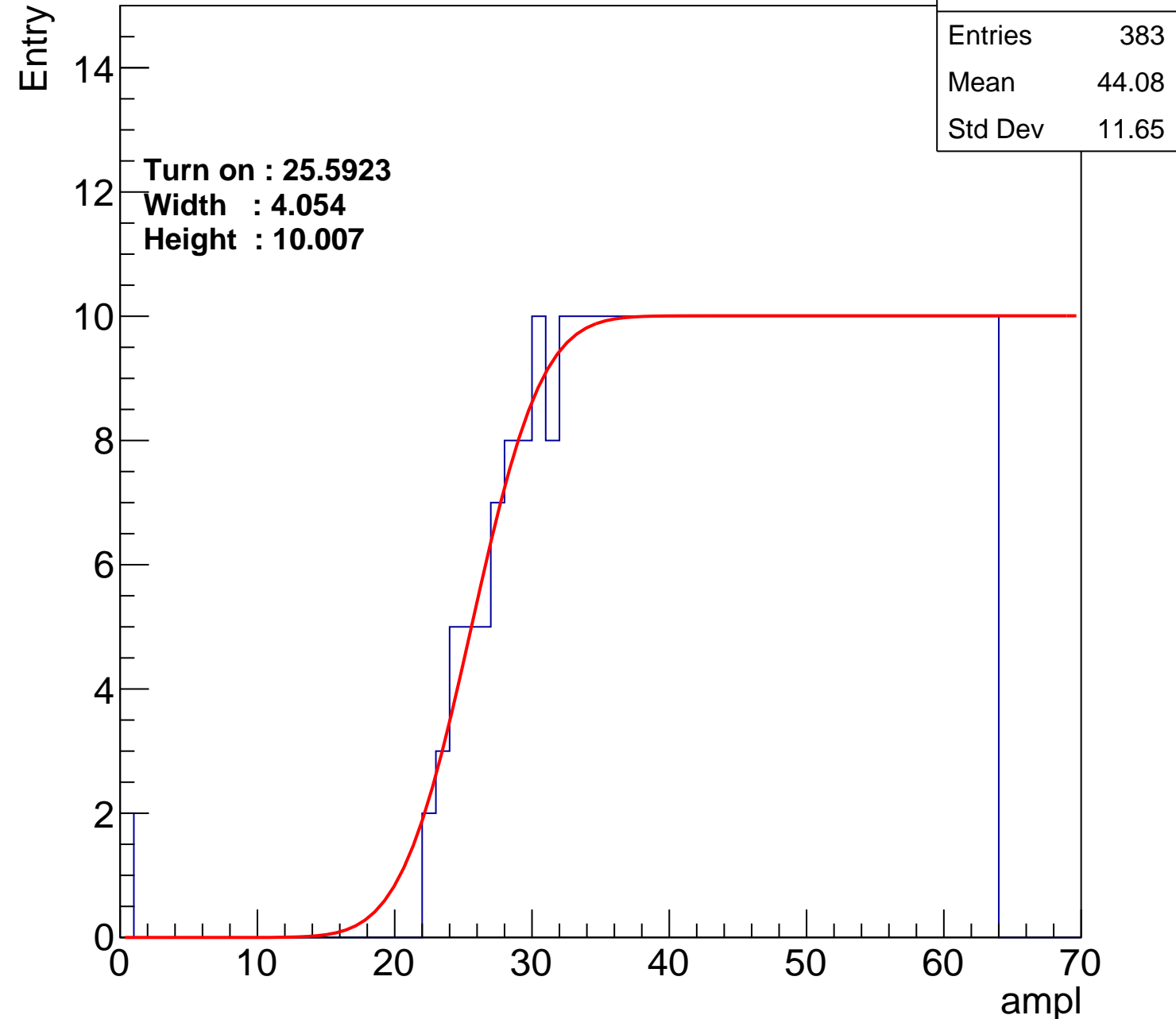
Width : 4.054

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch95

calib_packv5_042523_0143.root, FC#9, port A1

Entries	395
Mean	43.52
Std Dev	11.9

Turn on : 24.9083

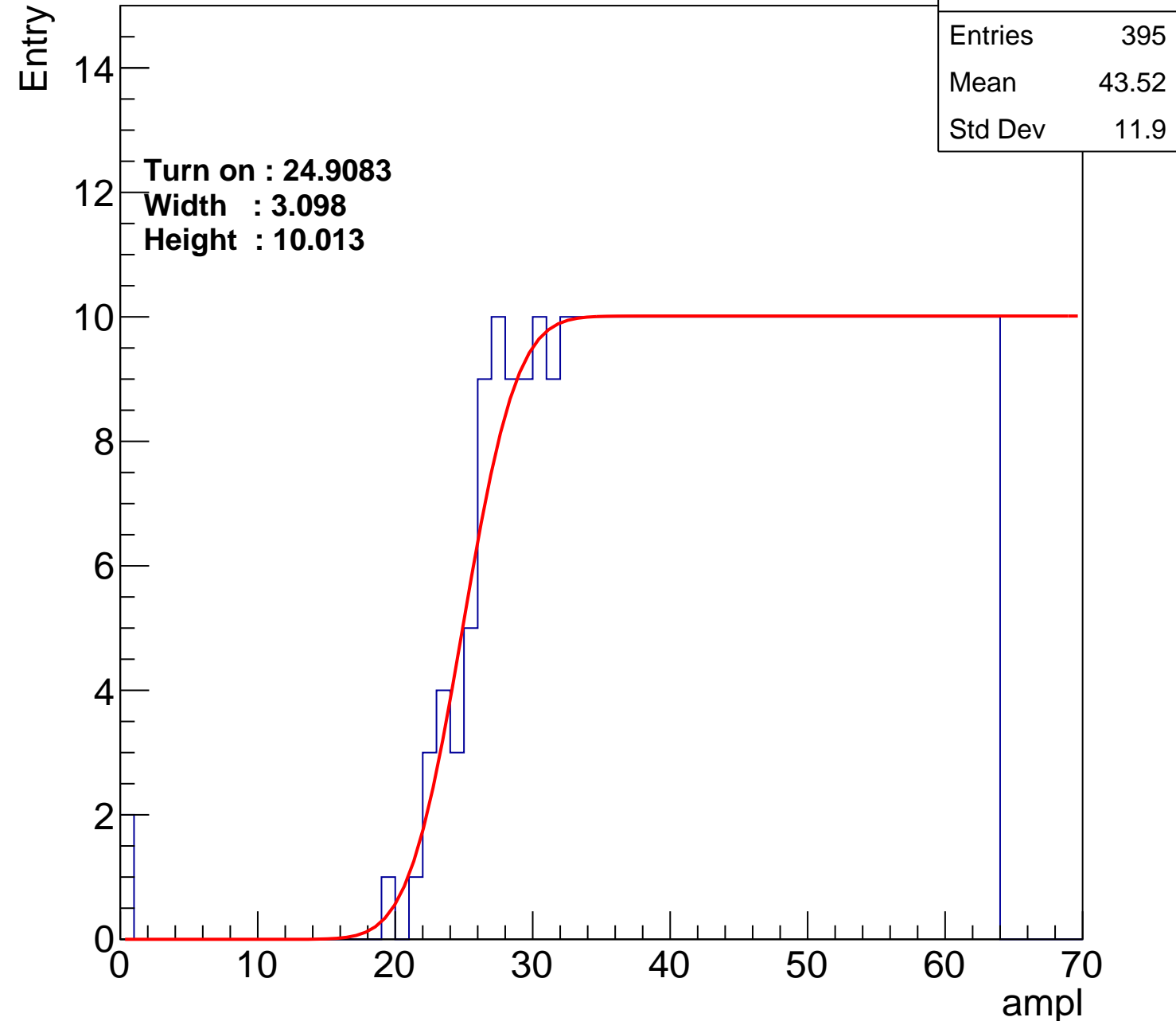
Width : 3.098

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch96

calib_packv5_042523_0143.root, FC#9, port A1

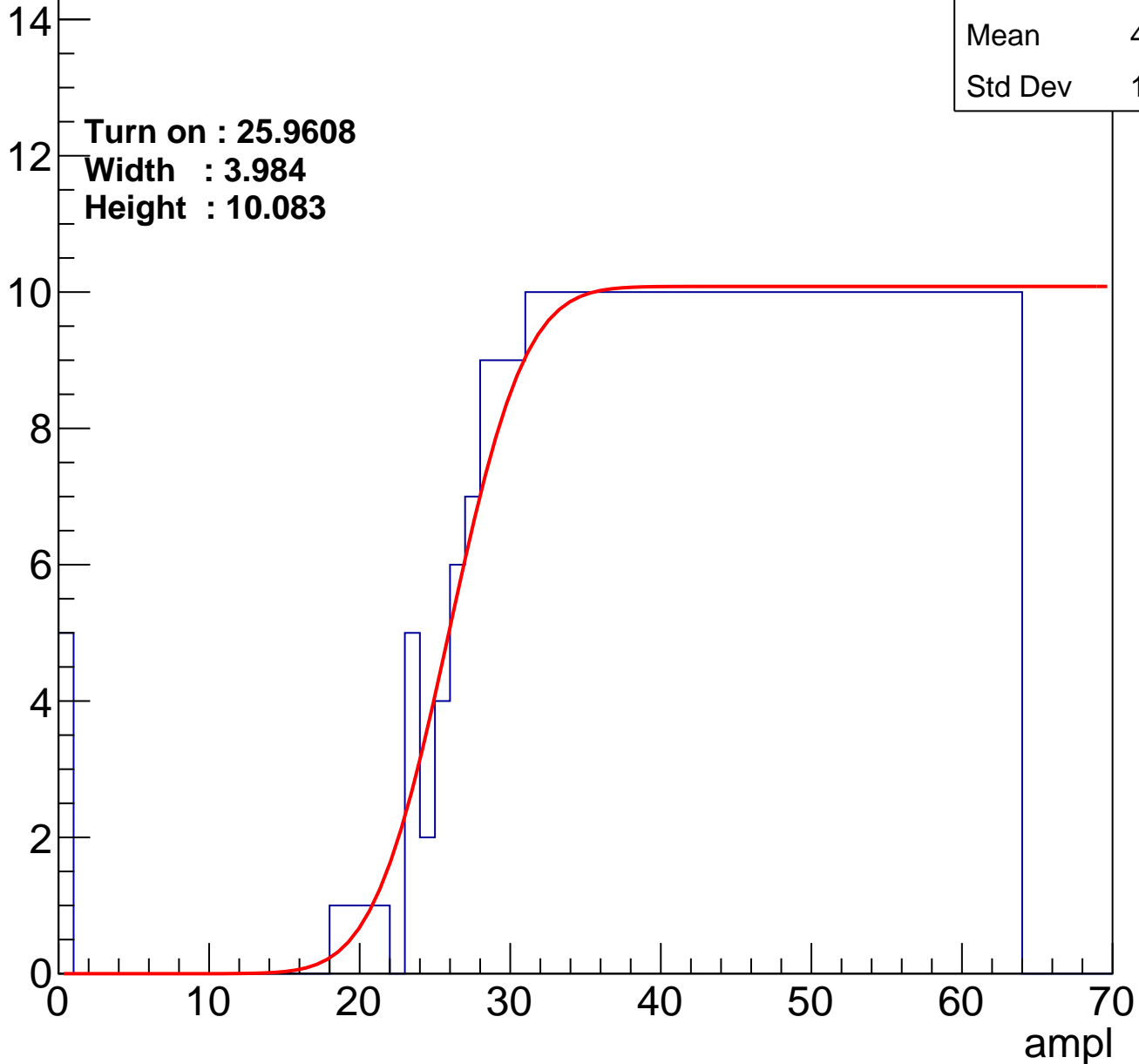
Entries	390
Mean	43.54
Std Dev	12.34

Turn on : 25.9608

Width : 3.984

Height : 10.083

Entry



B0L001S, U15-ch97

calib_packv5_042523_0143.root, FC#9, port A1

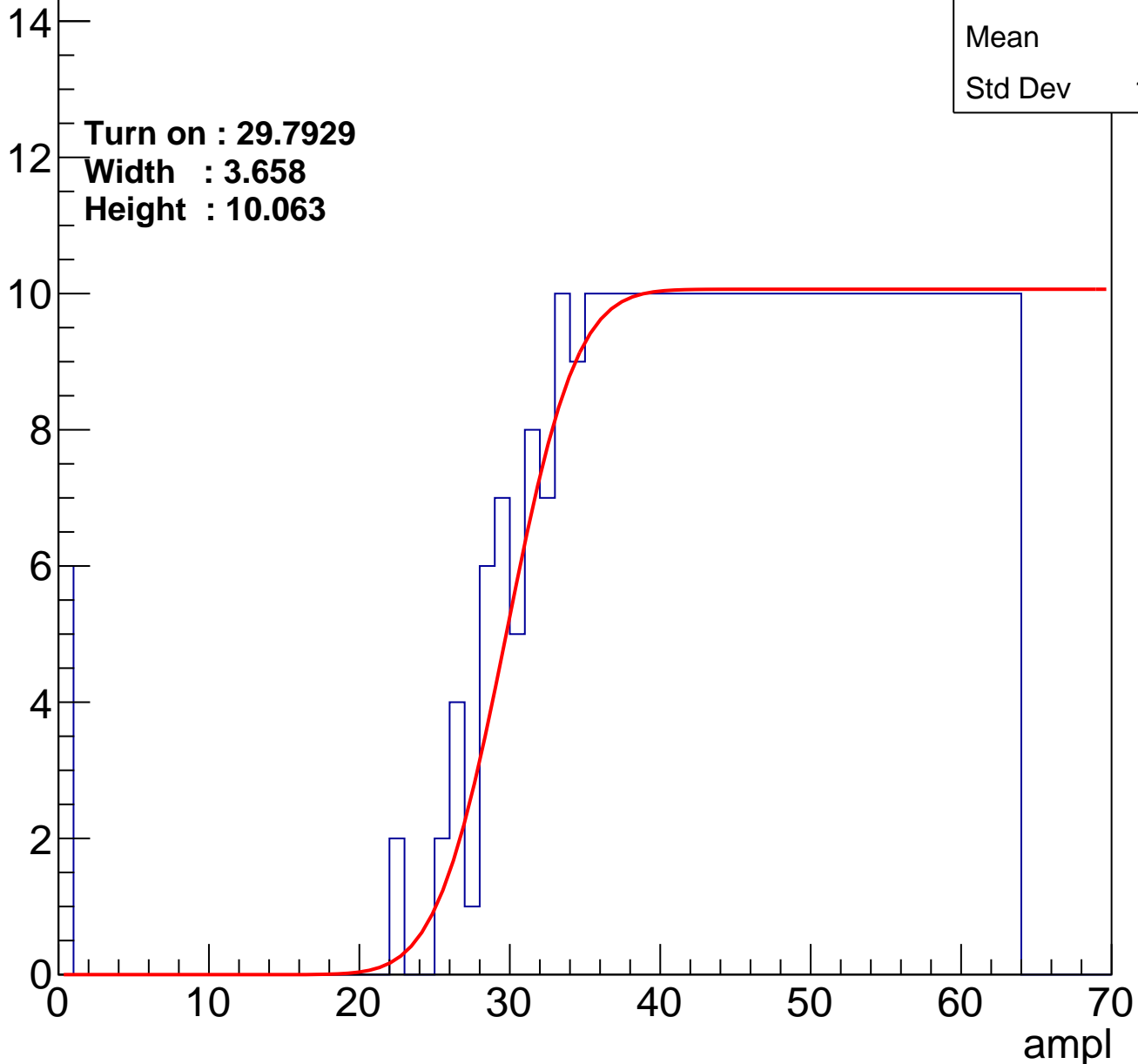
Entries	357
Mean	45
Std Dev	11.91

Turn on : 29.7929

Width : 3.658

Height : 10.063

Entry



B0L001S, U15-ch98

calib_packv5_042523_0143.root, FC#9, port A1

Entries	363
Mean	45
Std Dev	11.33

Turn on : 28.7670

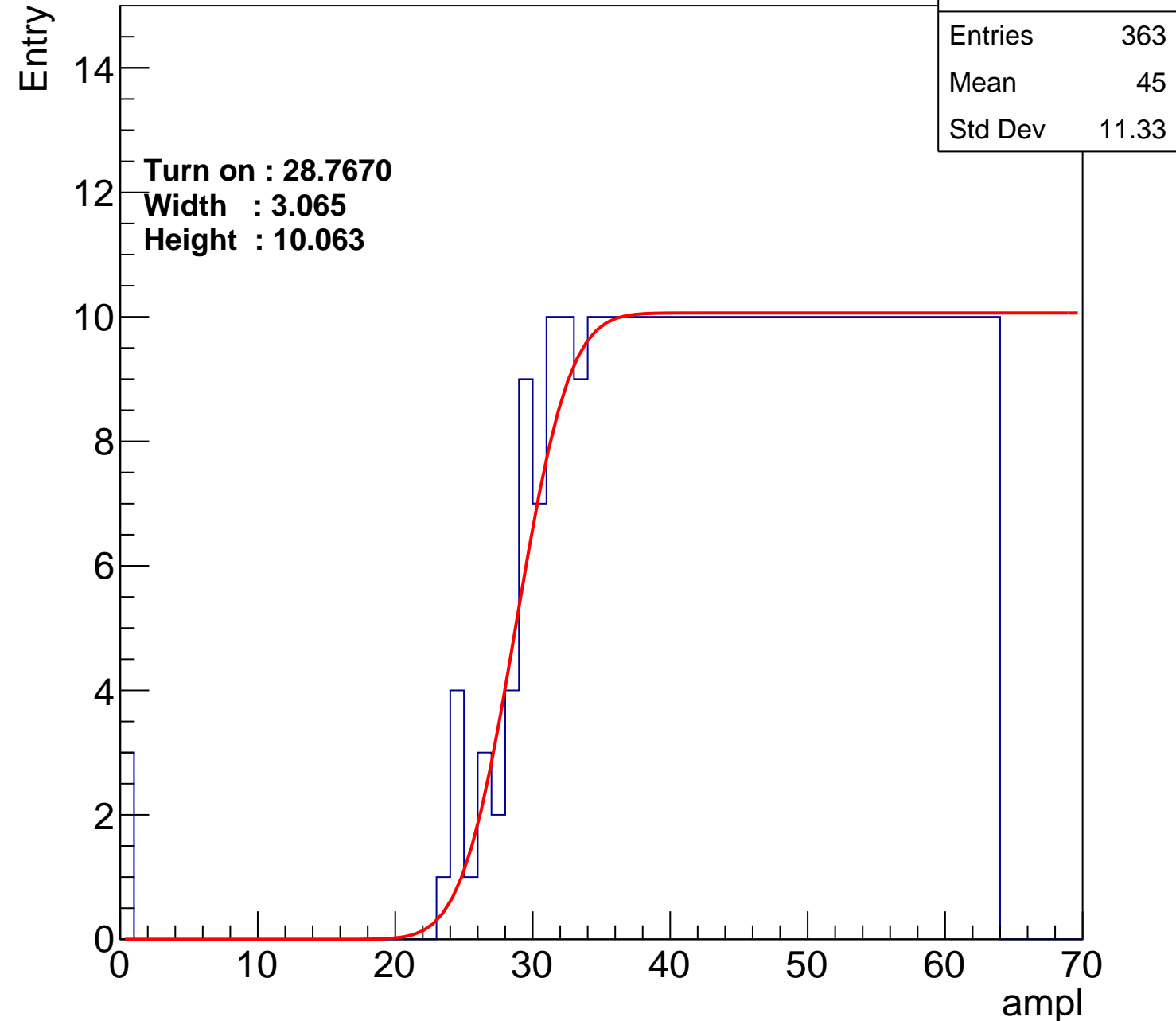
Width : 3.065

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch99

calib_packv5_042523_0143.root, FC#9, port A1

Entries	355
Mean	45.55
Std Dev	10.69

Turn on : 28.4466

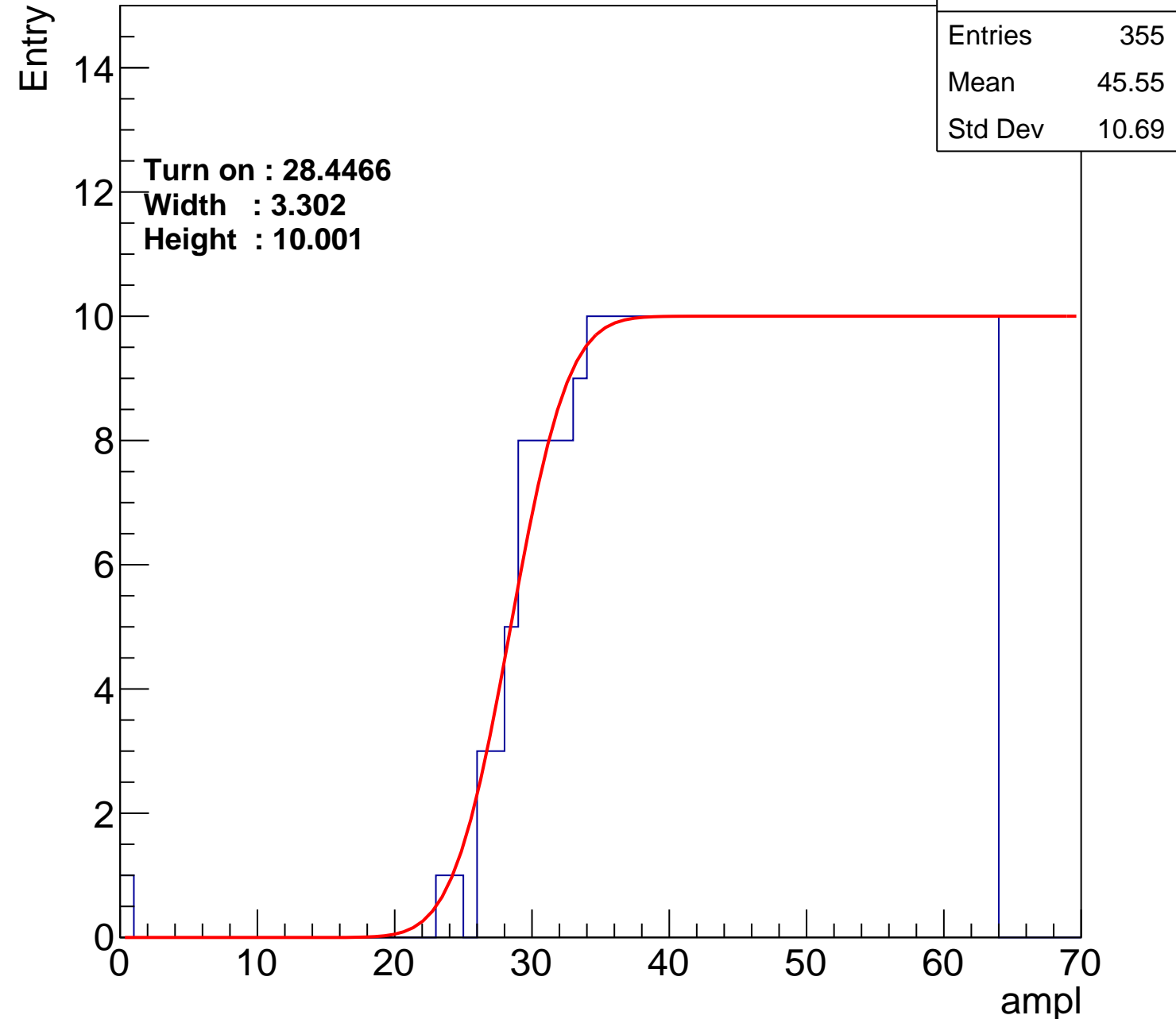
Width : 3.302

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch100

calib_packv5_042523_0143.root, FC#9, port A1

Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.8766

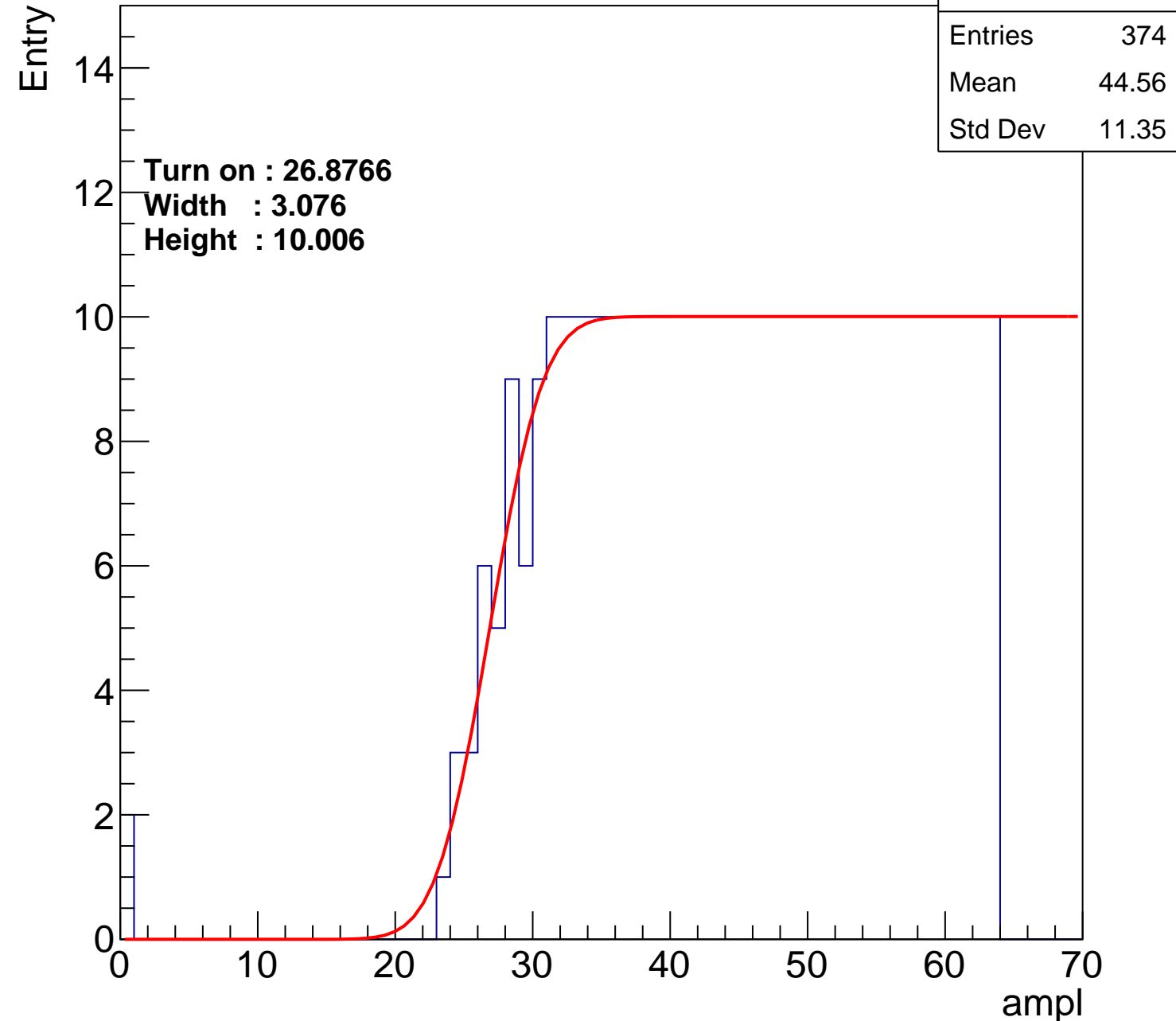
Width : 3.076

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch101

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	45.1
Std Dev	10.94

Turn on : 27.6085

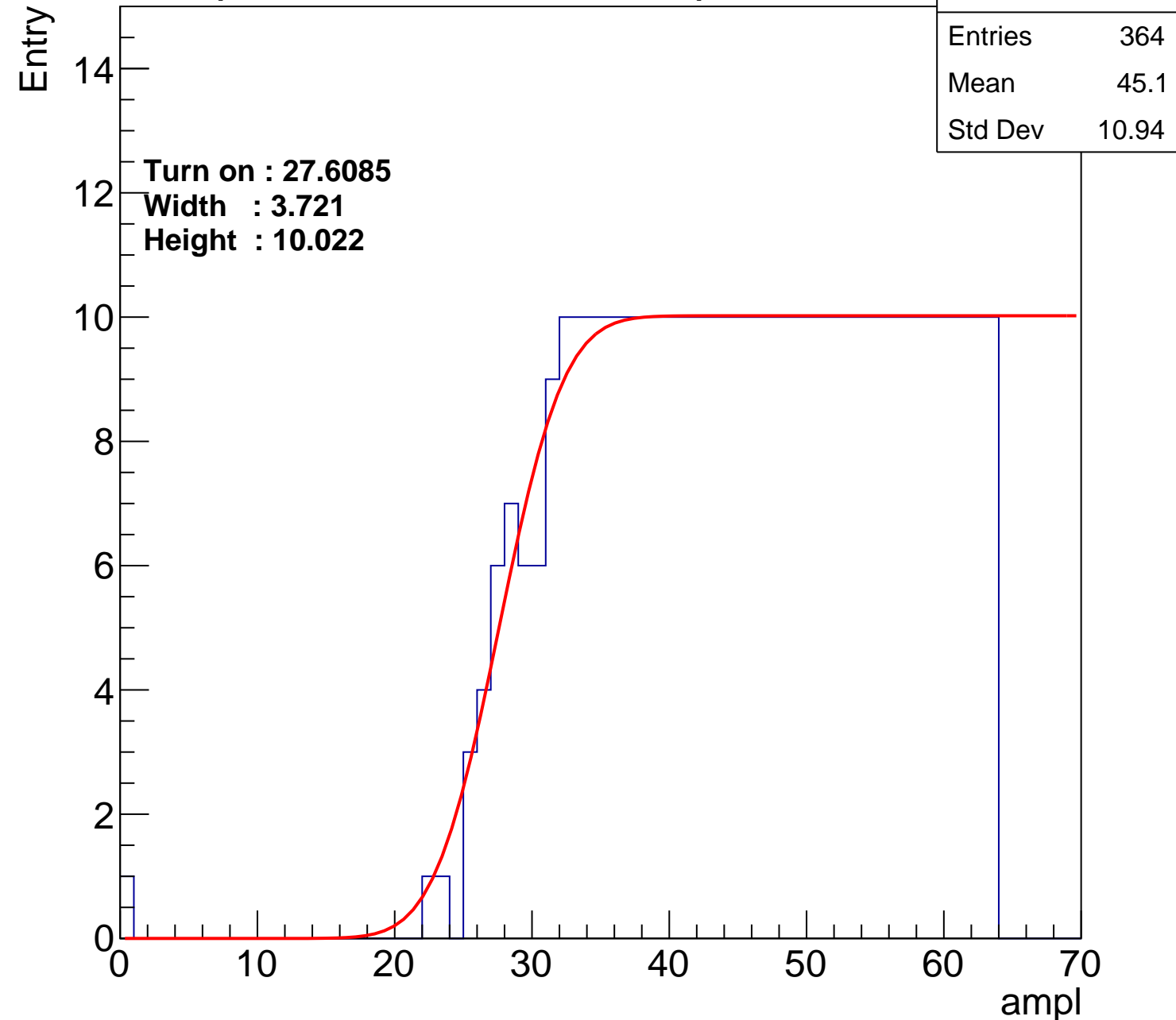
Width : 3.721

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch102

calib_packv5_042523_0143.root, FC#9, port A1

Entries	365
Mean	44.97
Std Dev	11.18

Turn on : 28.1735

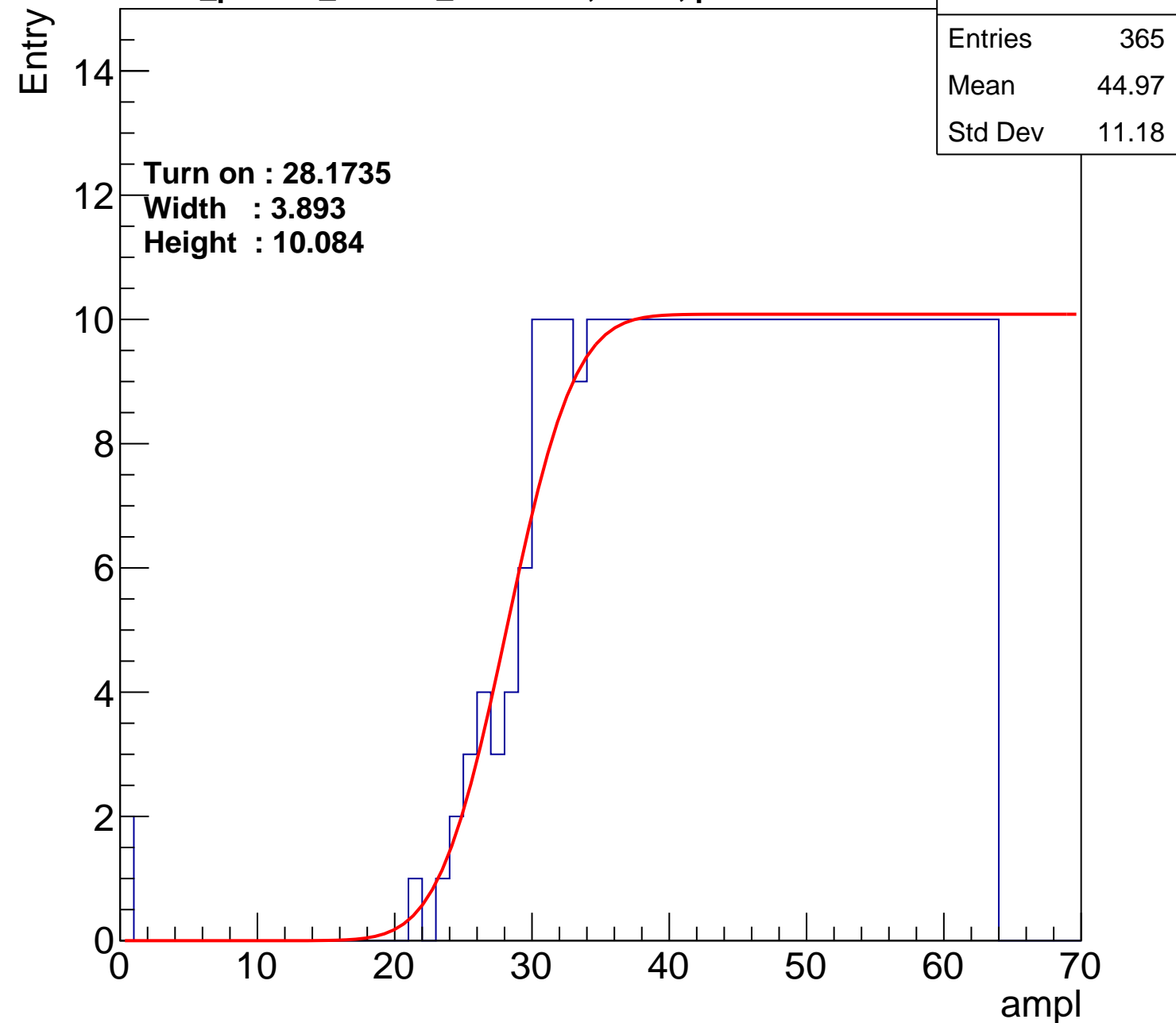
Width : 3.893

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch103

calib_packv5_042523_0143.root, FC#9, port A1

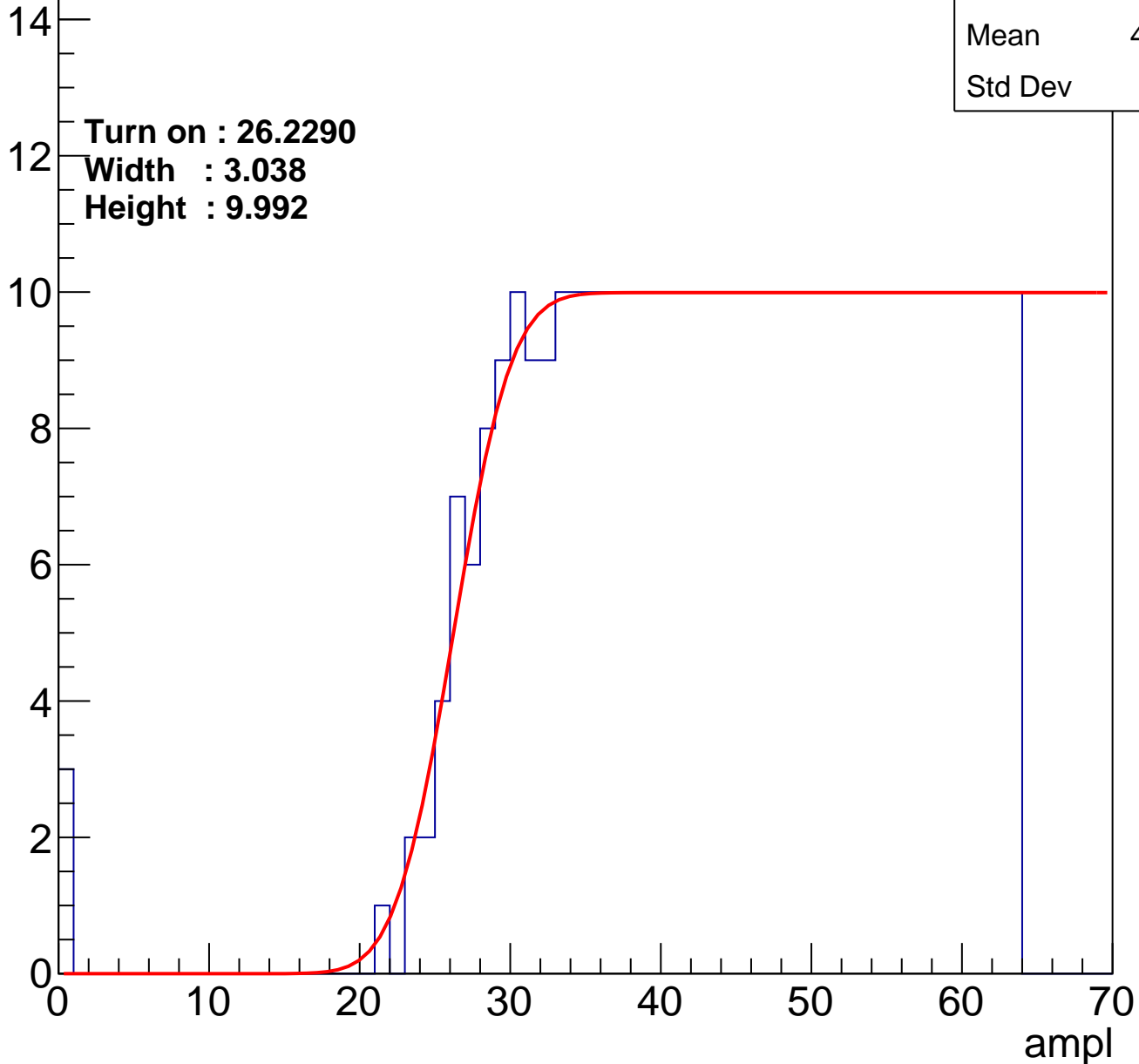
Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.2290

Width : 3.038

Height : 9.992

Entry



B0L001S, U15-ch104

calib_packv5_042523_0143.root, FC#9, port A1

Entries	385
Mean	43.66
Std Dev	12.52

Turn on : 26.4452

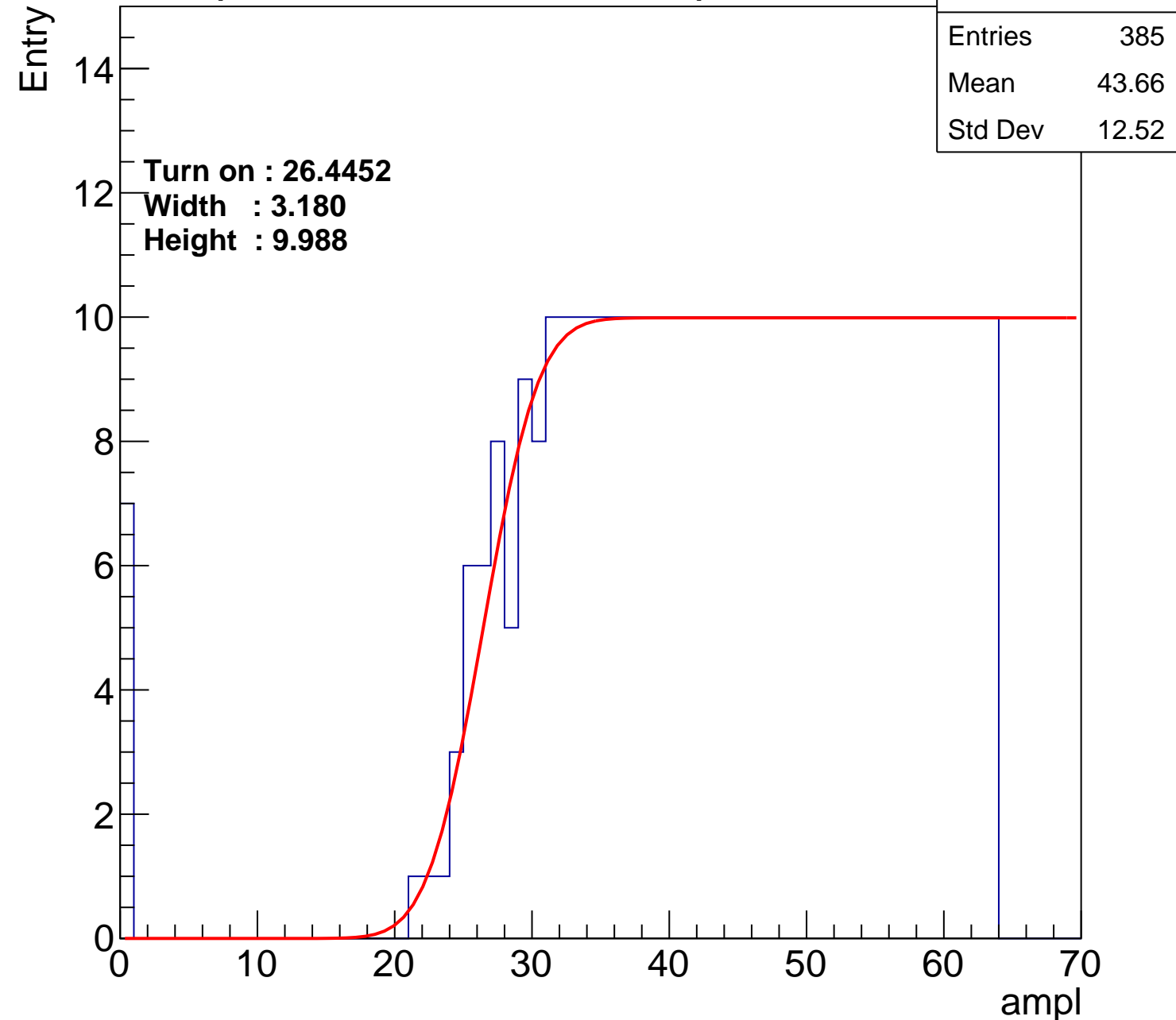
Width : 3.180

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch105

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.45
Std Dev	11.79

Turn on : 27.5761

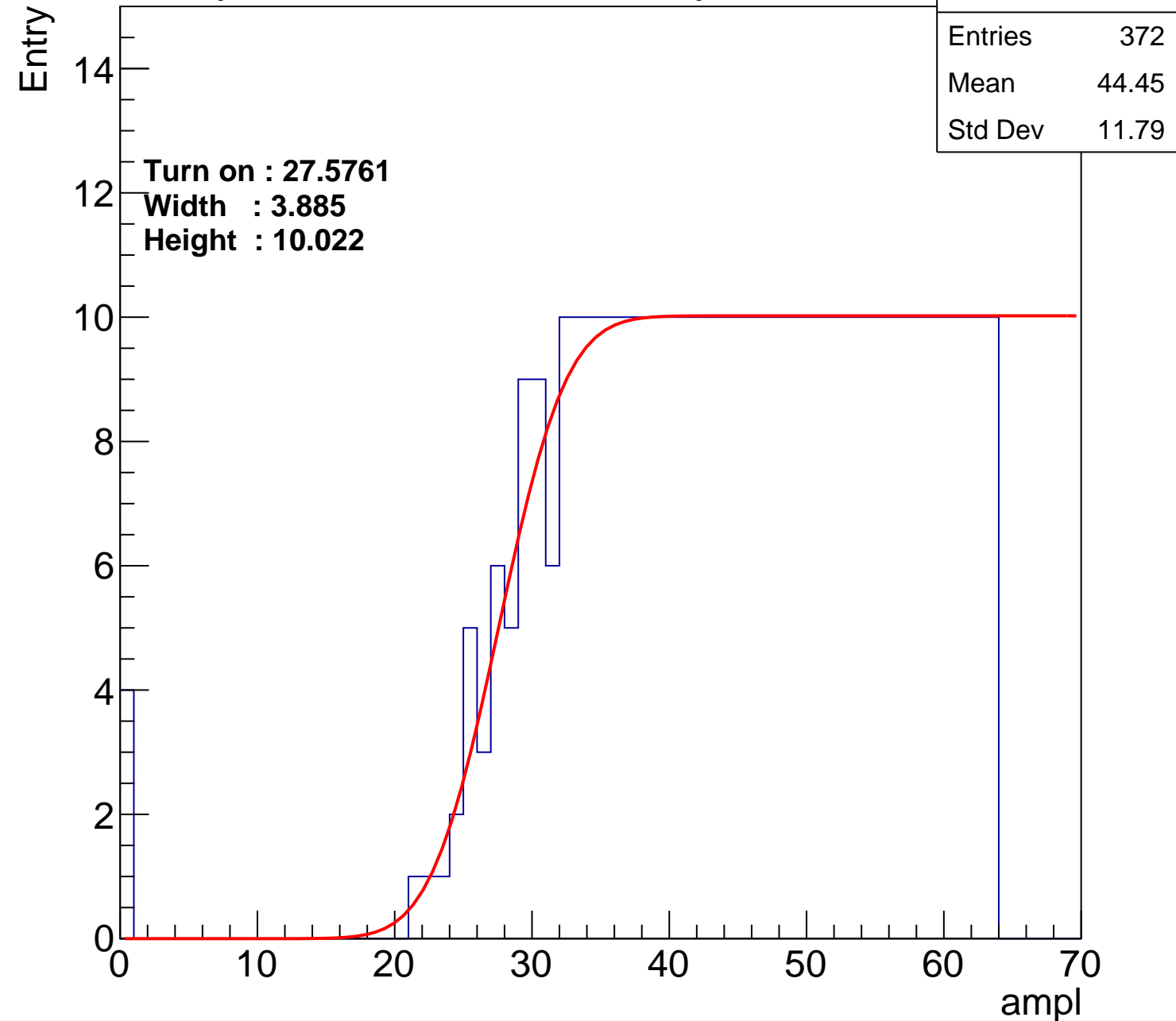
Width : 3.885

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch106

calib_packv5_042523_0143.root, FC#9, port A1

Entries	370
Mean	44.51
Std Dev	11.88

Turn on : 27.4898

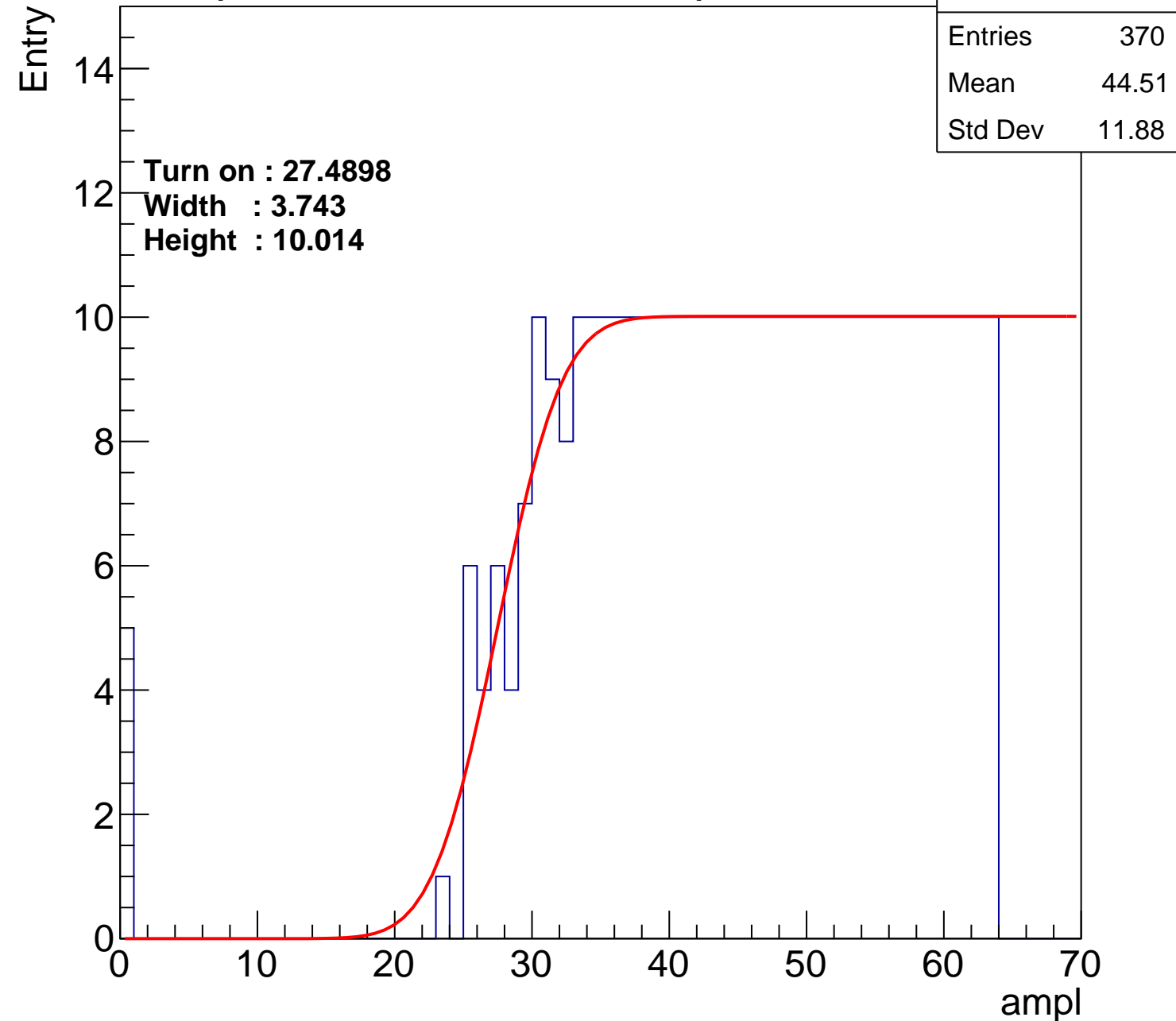
Width : 3.743

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch107

calib_packv5_042523_0143.root, FC#9, port A1

Entries	382
Mean	43.92
Std Dev	12.16

Turn on : 26.5289

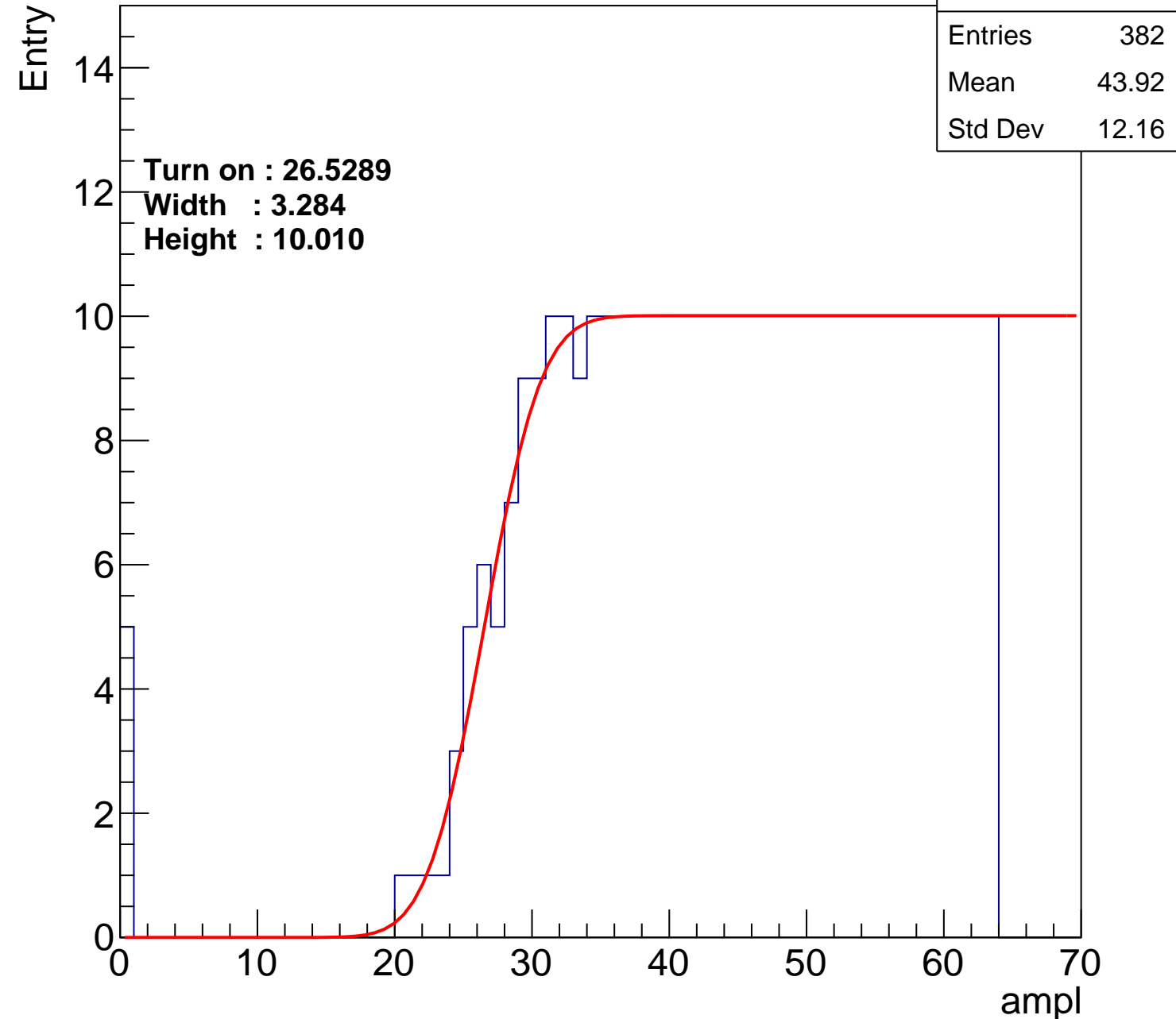
Width : 3.284

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch108

calib_packv5_042523_0143.root, FC#9, port A1

Entries	387
Mean	43.79
Std Dev	12.04

Turn on : 25.8346

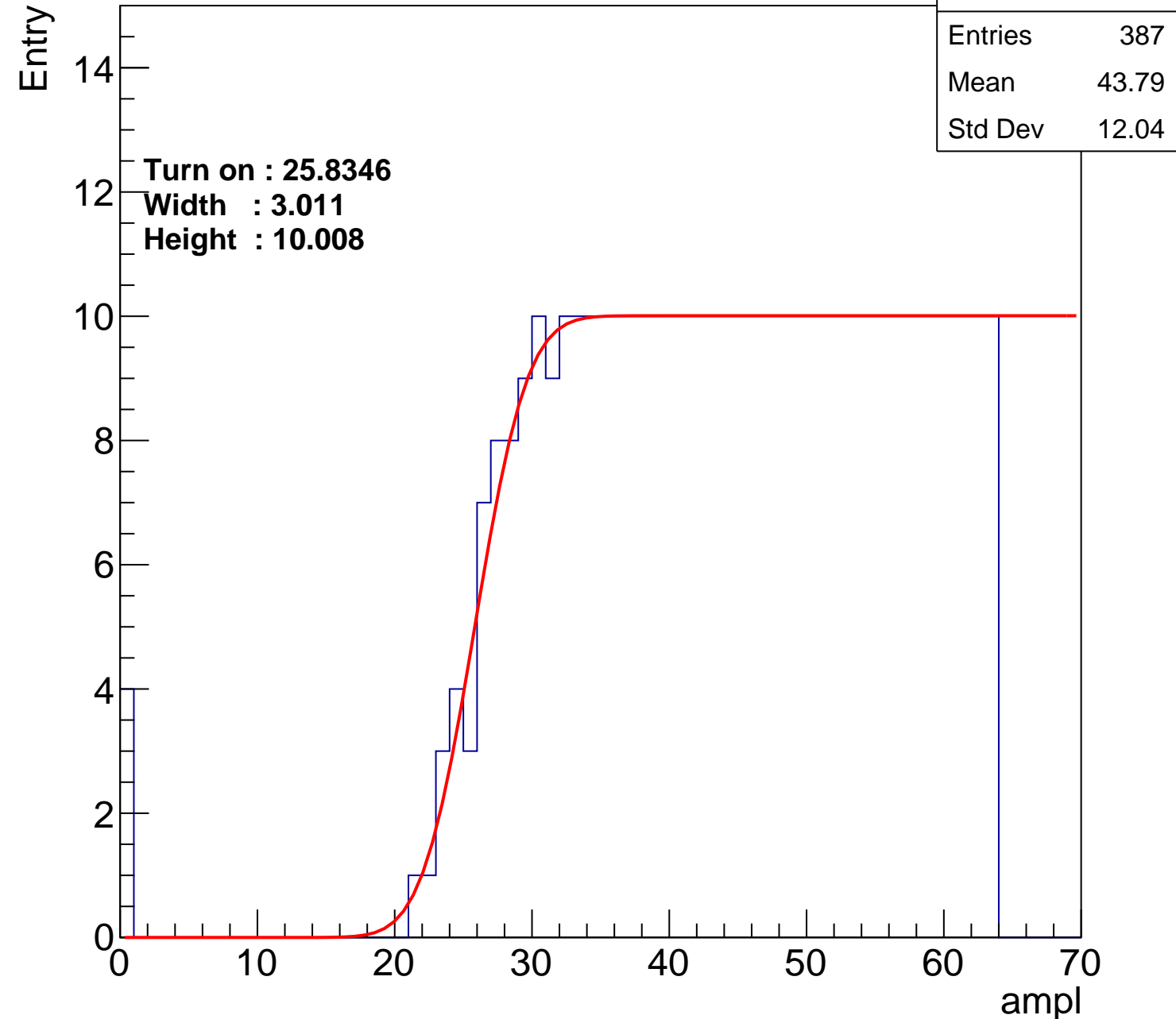
Width : 3.011

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch109

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	45.12
Std Dev	11.29

Turn on : 28.4225

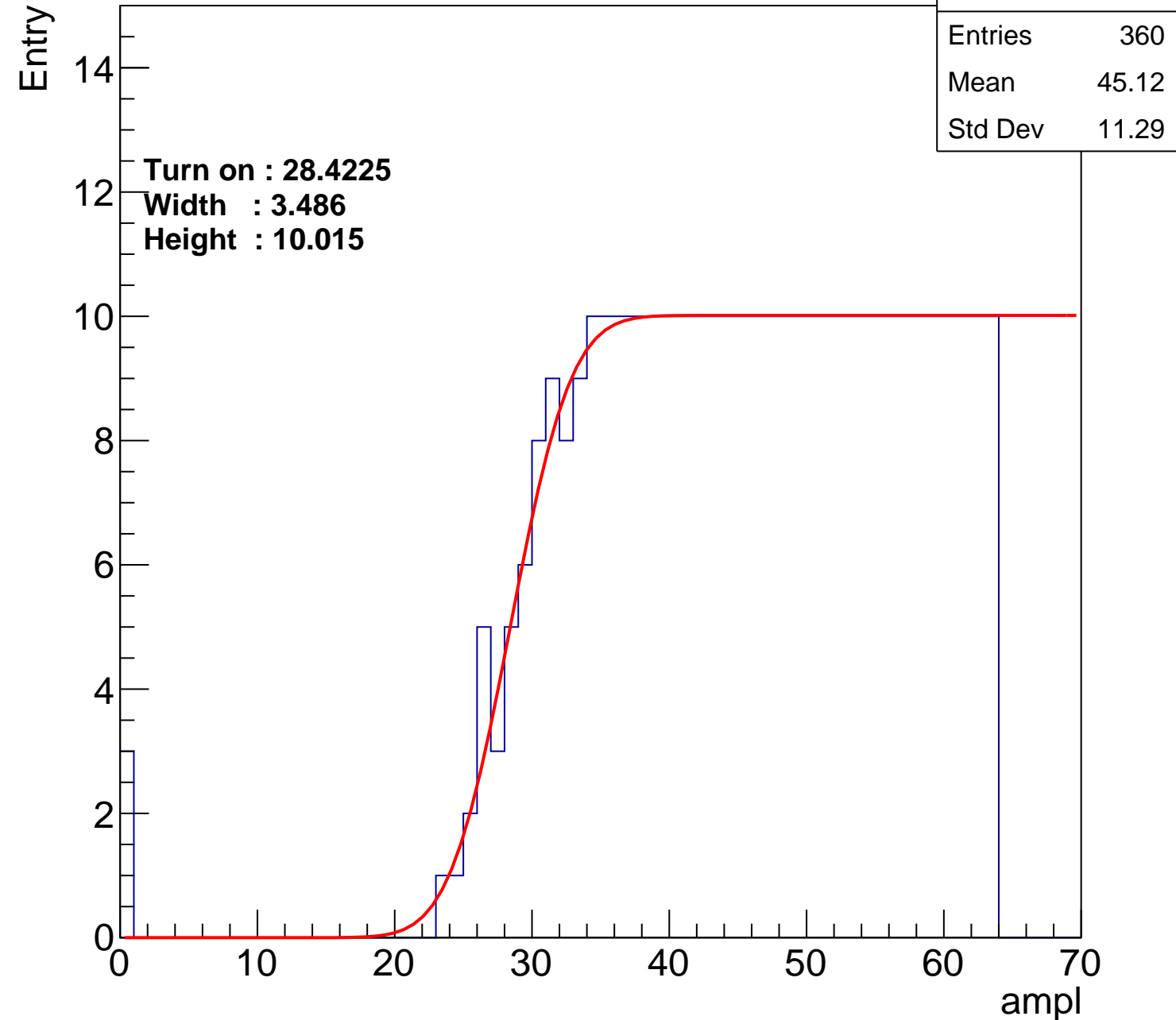
Width : 3.486

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch110

calib_packv5_042523_0143.root, FC#9, port A1

Entries	381
Mean	43.82
Std Dev	12.56

Turn on : 26.8871

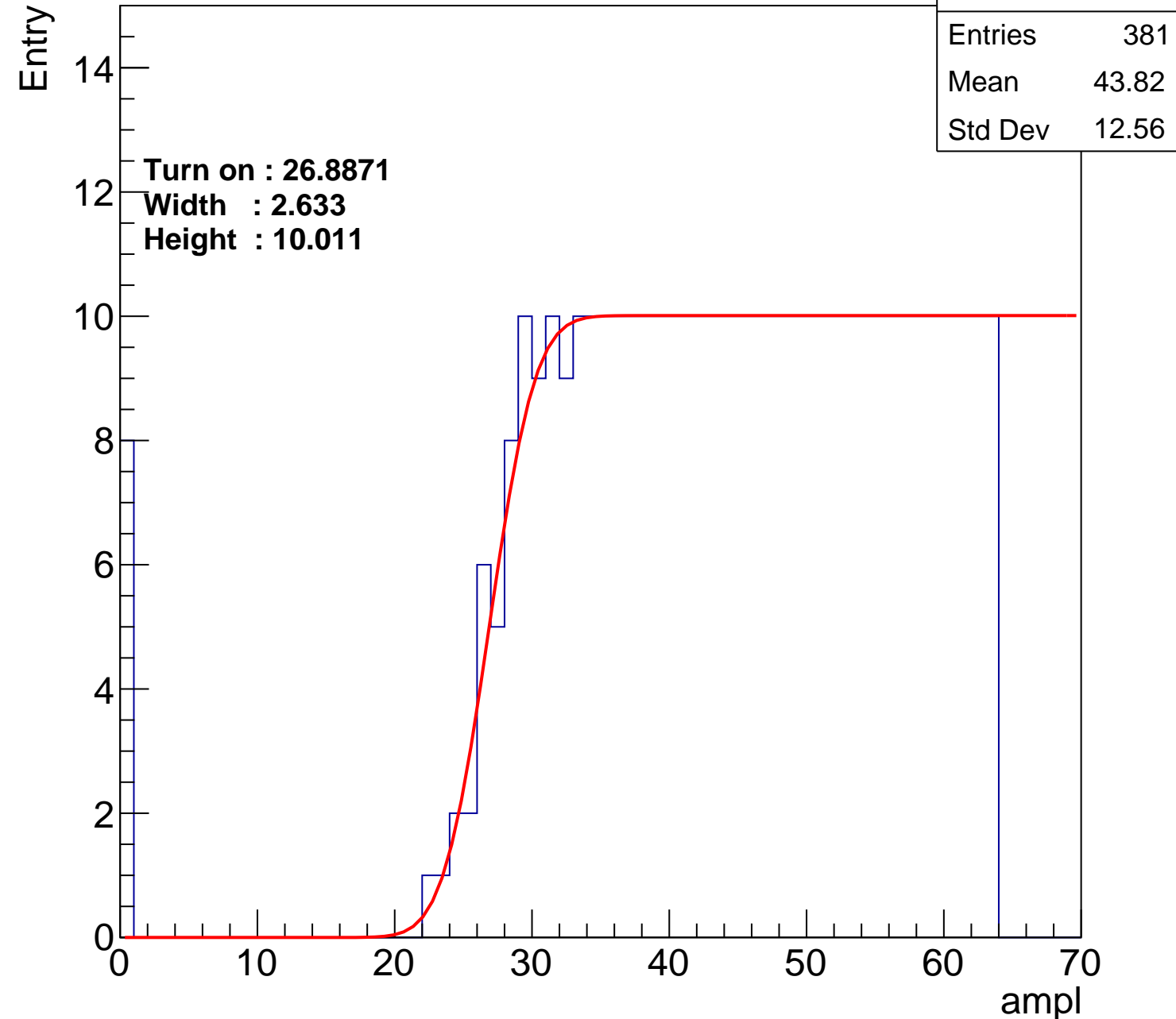
Width : 2.633

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch111

calib_packv5_042523_0143.root, FC#9, port A1

Entries	360
Mean	44.97
Std Dev	11.72

Turn on : 29.0379

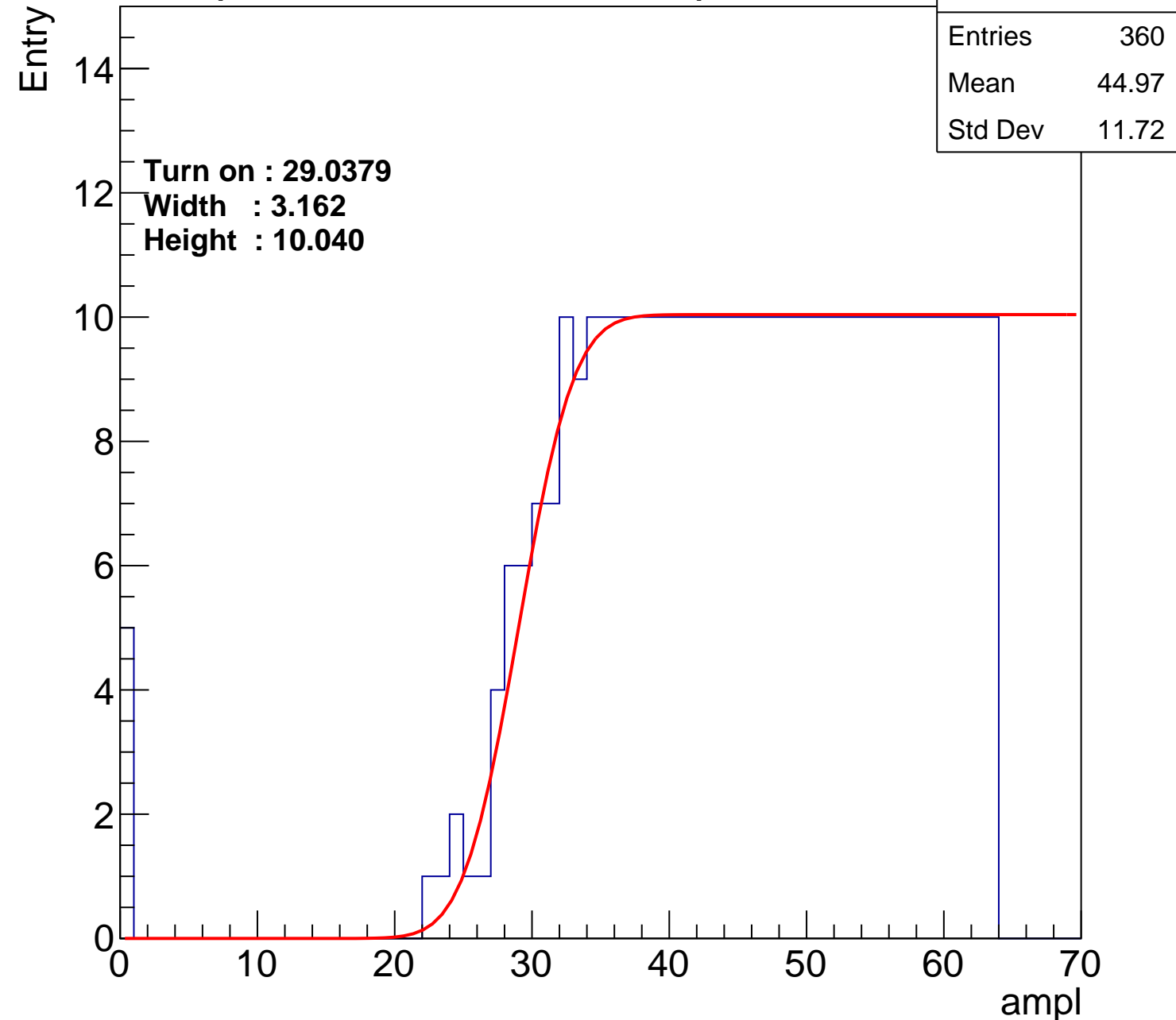
Width : 3.162

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch112

calib_packv5_042523_0143.root, FC#9, port A1

Entries	377
Mean	44.24
Std Dev	11.85

Turn on : 26.8071

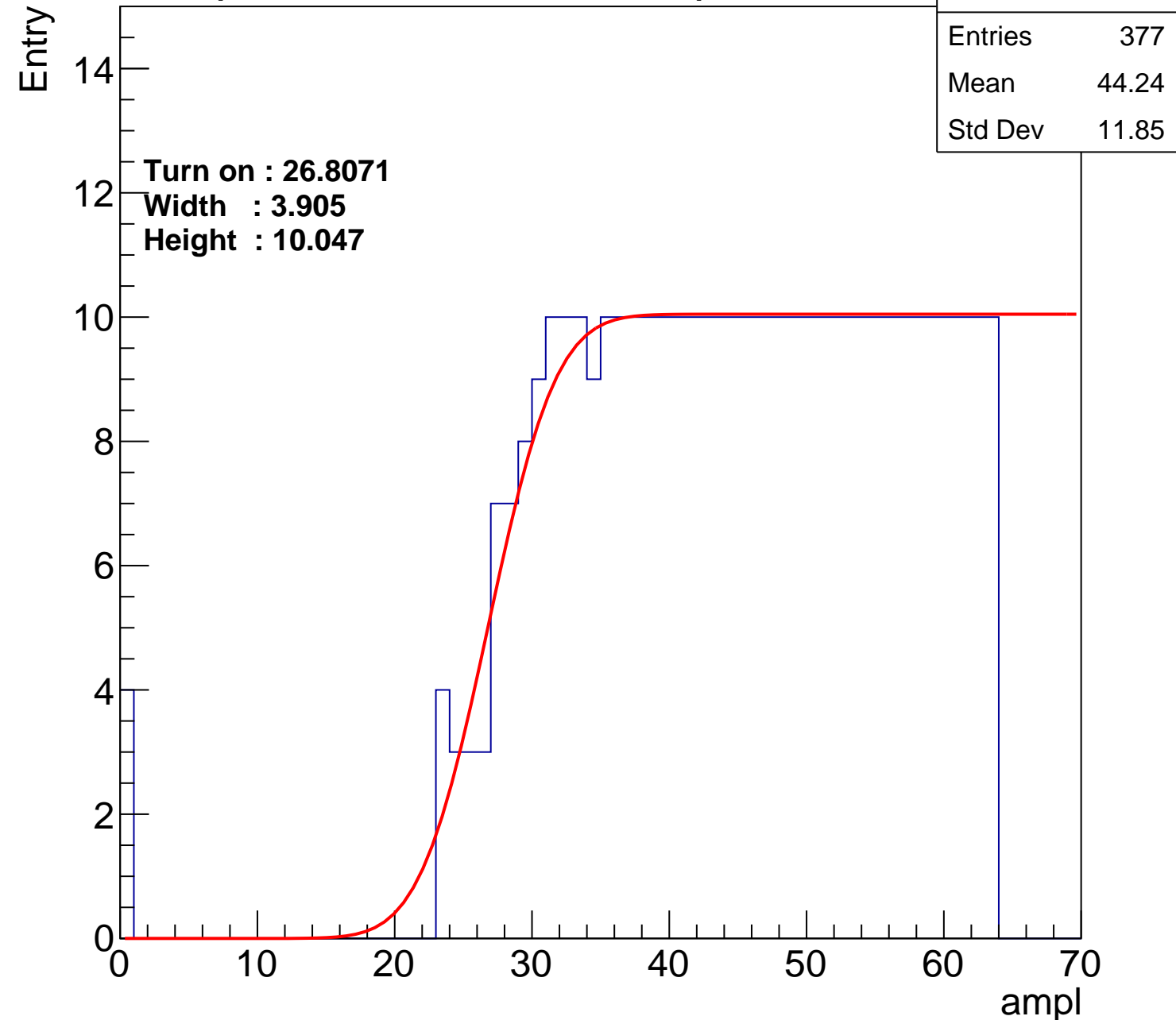
Width : 3.905

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch113

calib_packv5_042523_0143.root, FC#9, port A1

Entries	372
Mean	44.61
Std Dev	11.48

Turn on : 27.5322

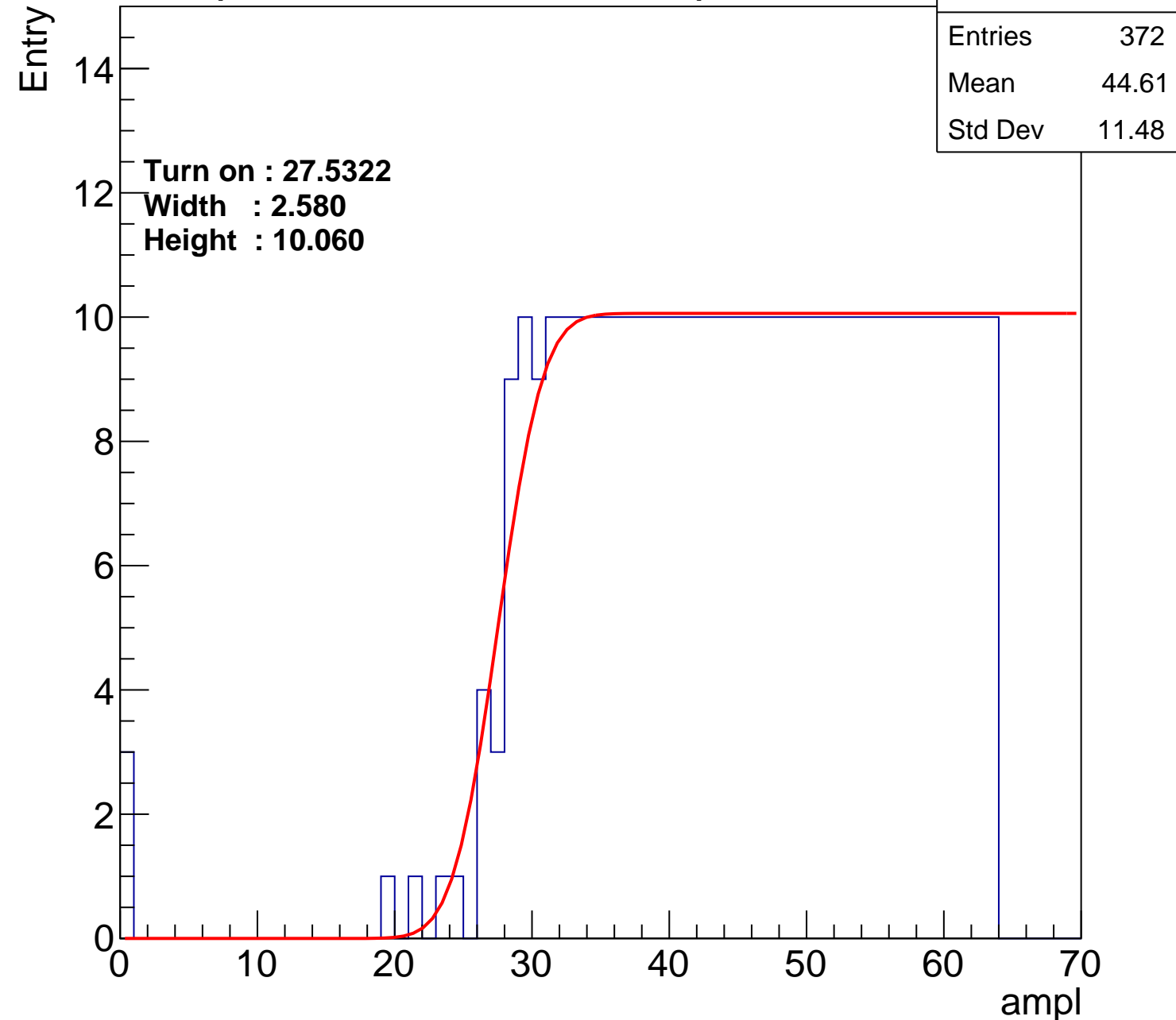
Width : 2.580

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch114

calib_packv5_042523_0143.root, FC#9, port A1

Entries	377
Mean	44.37
Std Dev	11.51

Turn on : 27.1977

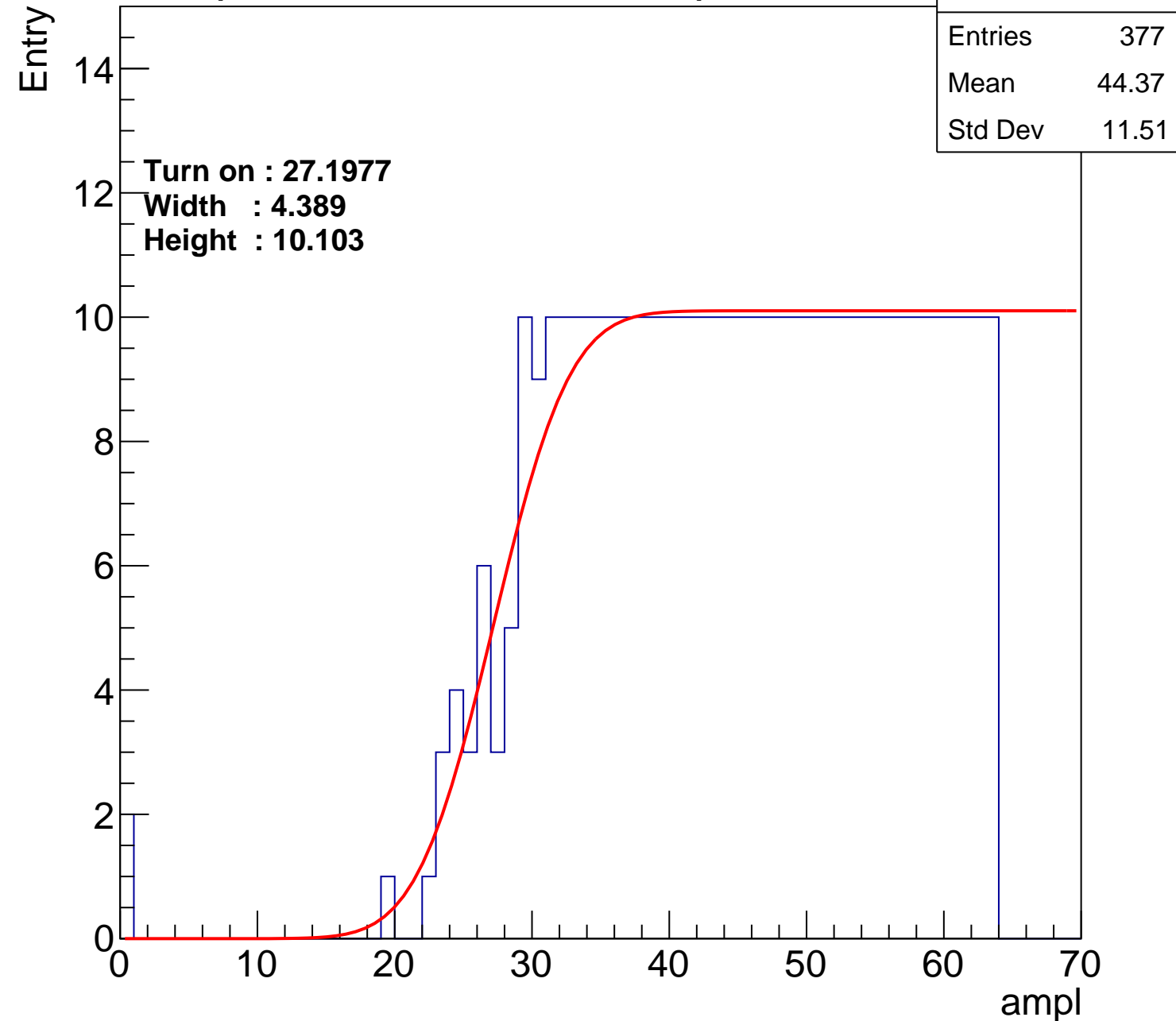
Width : 4.389

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch115

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.83
Std Dev	11.1

Turn on : 27.6304

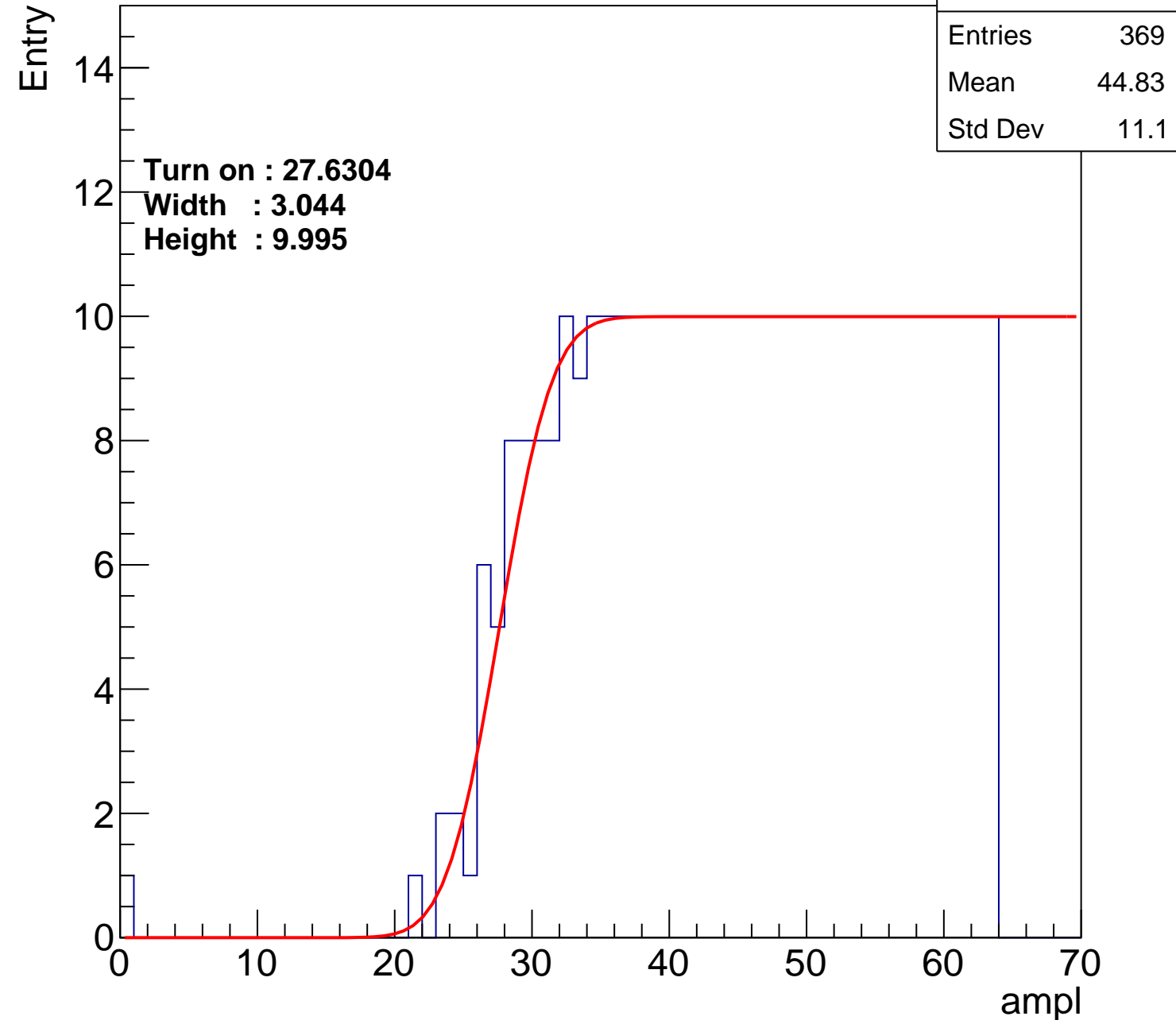
Width : 3.044

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch116

calib_packv5_042523_0143.root, FC#9, port A1

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.2524

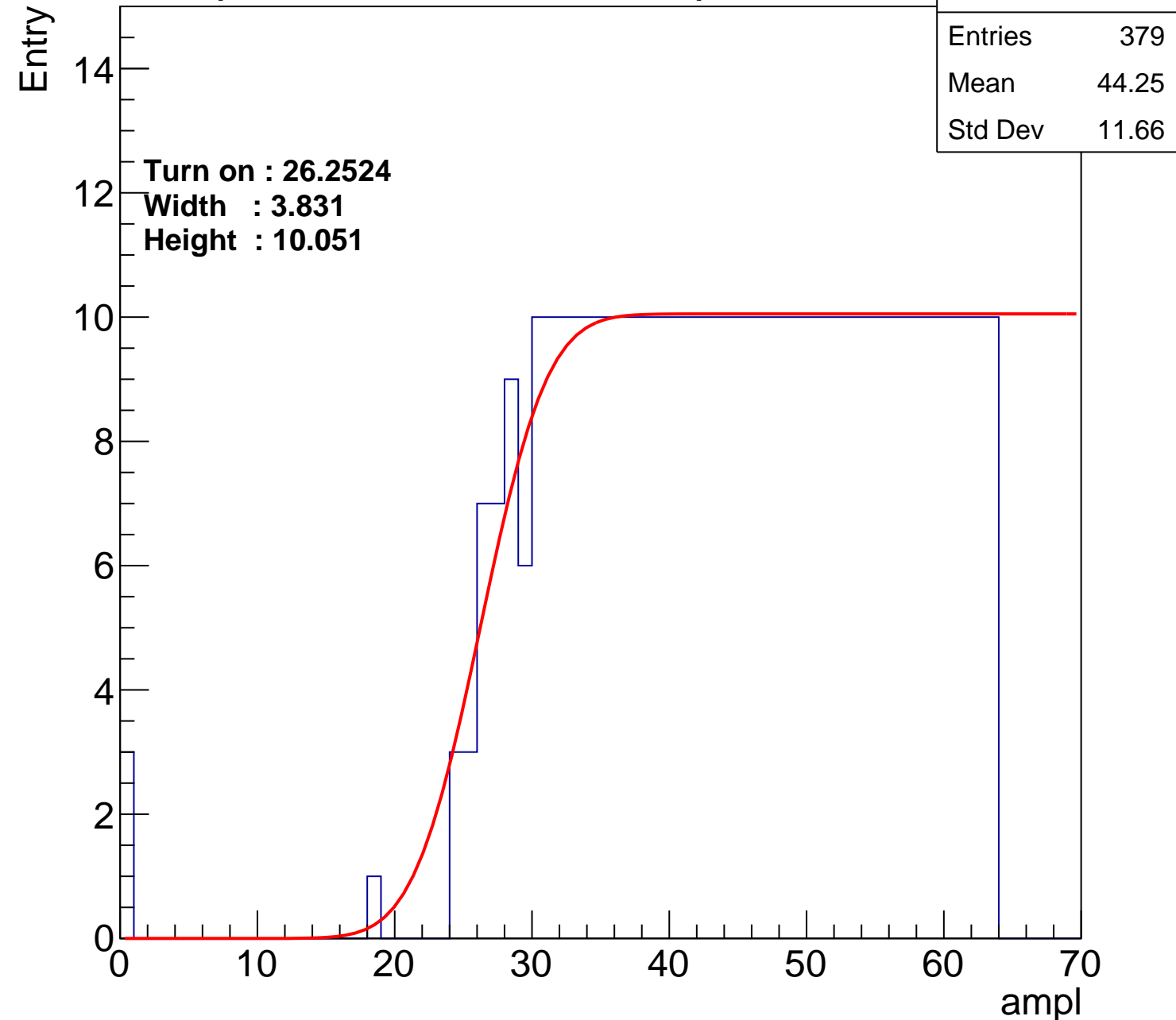
Width : 3.831

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch117

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.93
Std Dev	11.39

Turn on : 28.5555

Width : 4.145

Height : 10.079

Entry

14

12

10

8

6

4

2

0

0

10

20

30

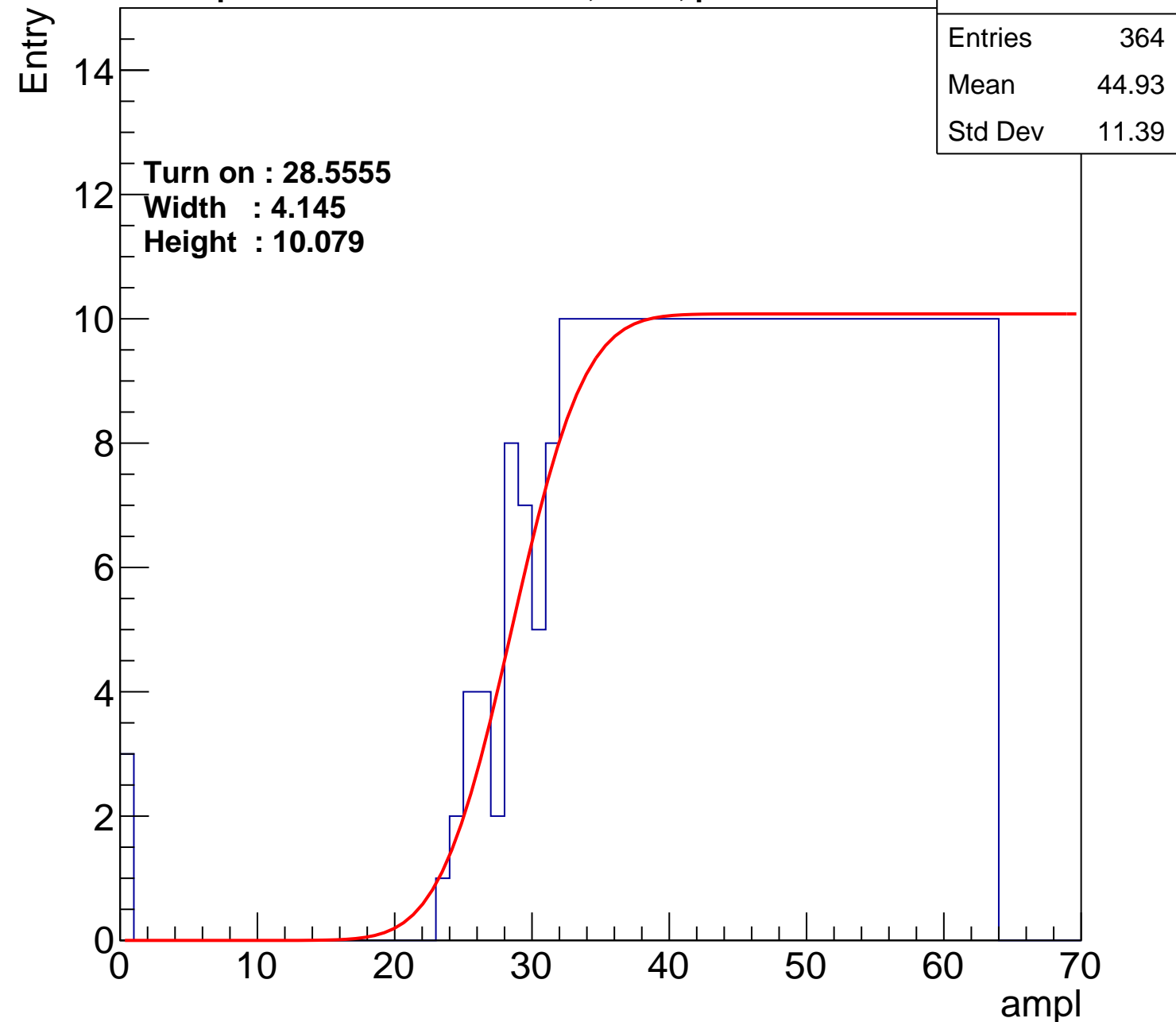
40

50

60

70

ampl



B0L001S, U15-ch118

calib_packv5_042523_0143.root, FC#9, port A1

Entries	382
Mean	43.97
Std Dev	12.09

Turn on : 26.9499

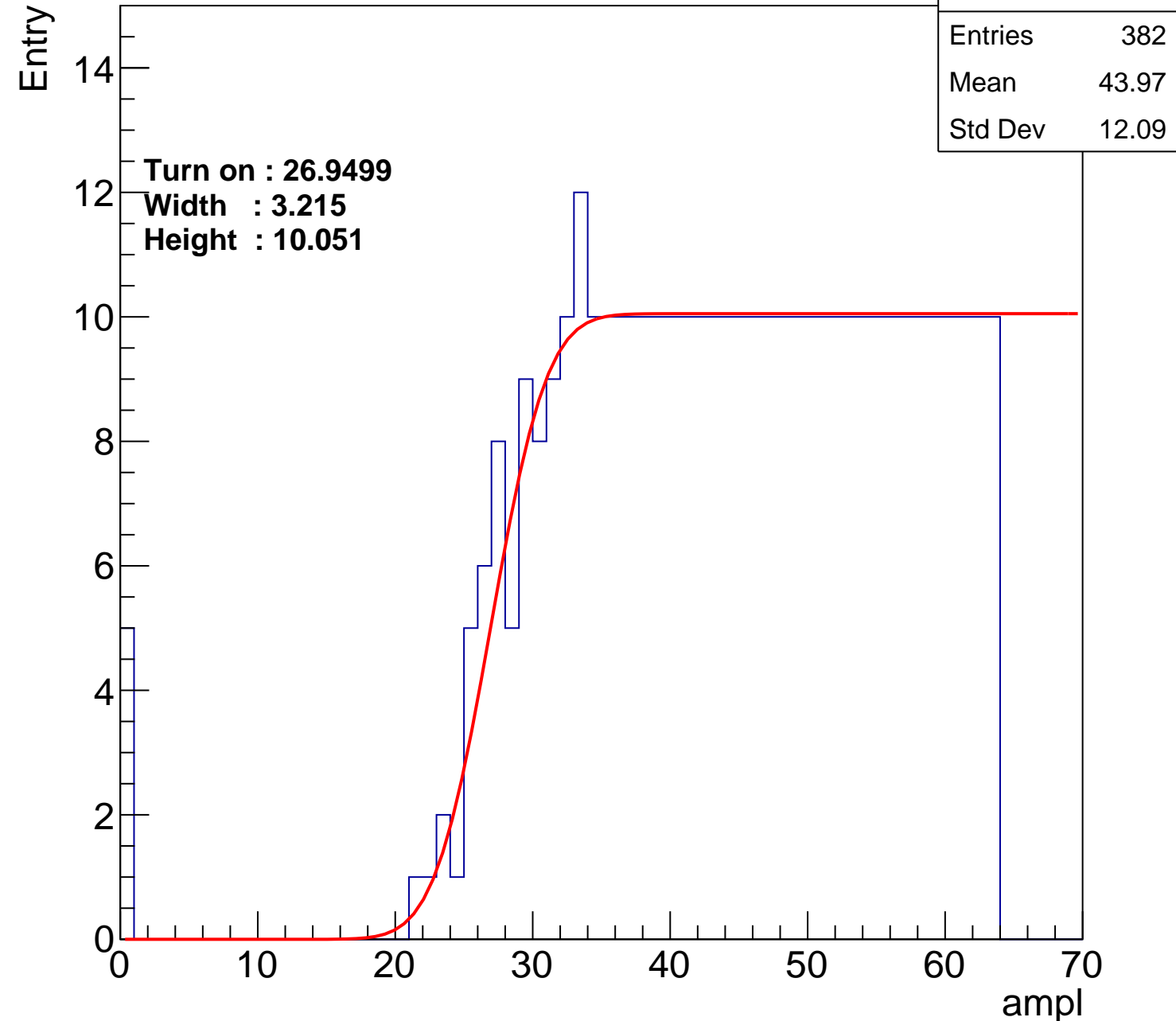
Width : 3.215

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch119

calib_packv5_042523_0143.root, FC#9, port A1

Entries	369
Mean	44.71
Std Dev	11.45

Turn on : 27.5253

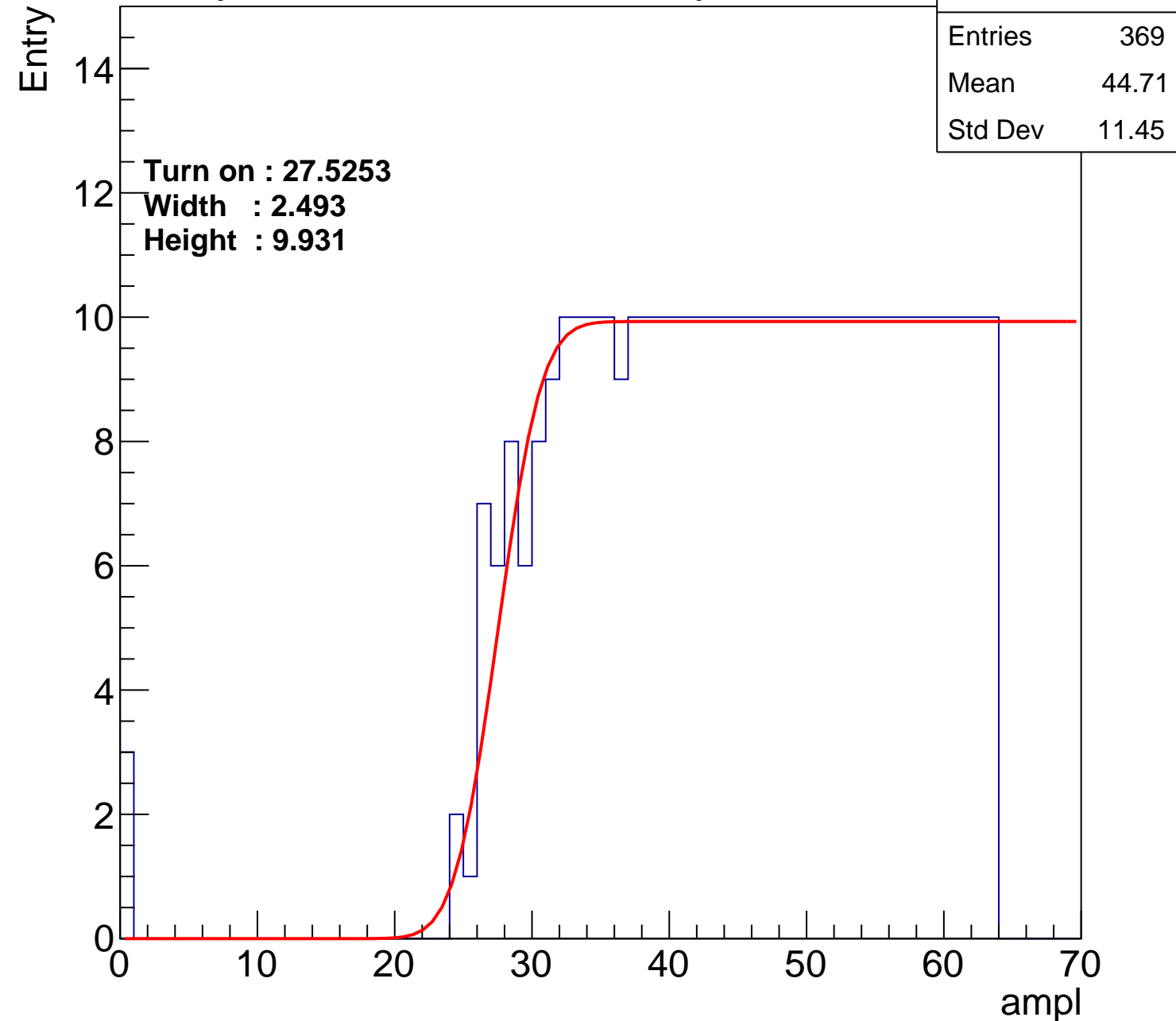
Width : 2.493

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch120

calib_packv5_042523_0143.root, FC#9, port A1

Entries	356
Mean	45.45
Std Dev	10.81

Turn on : 29.4250

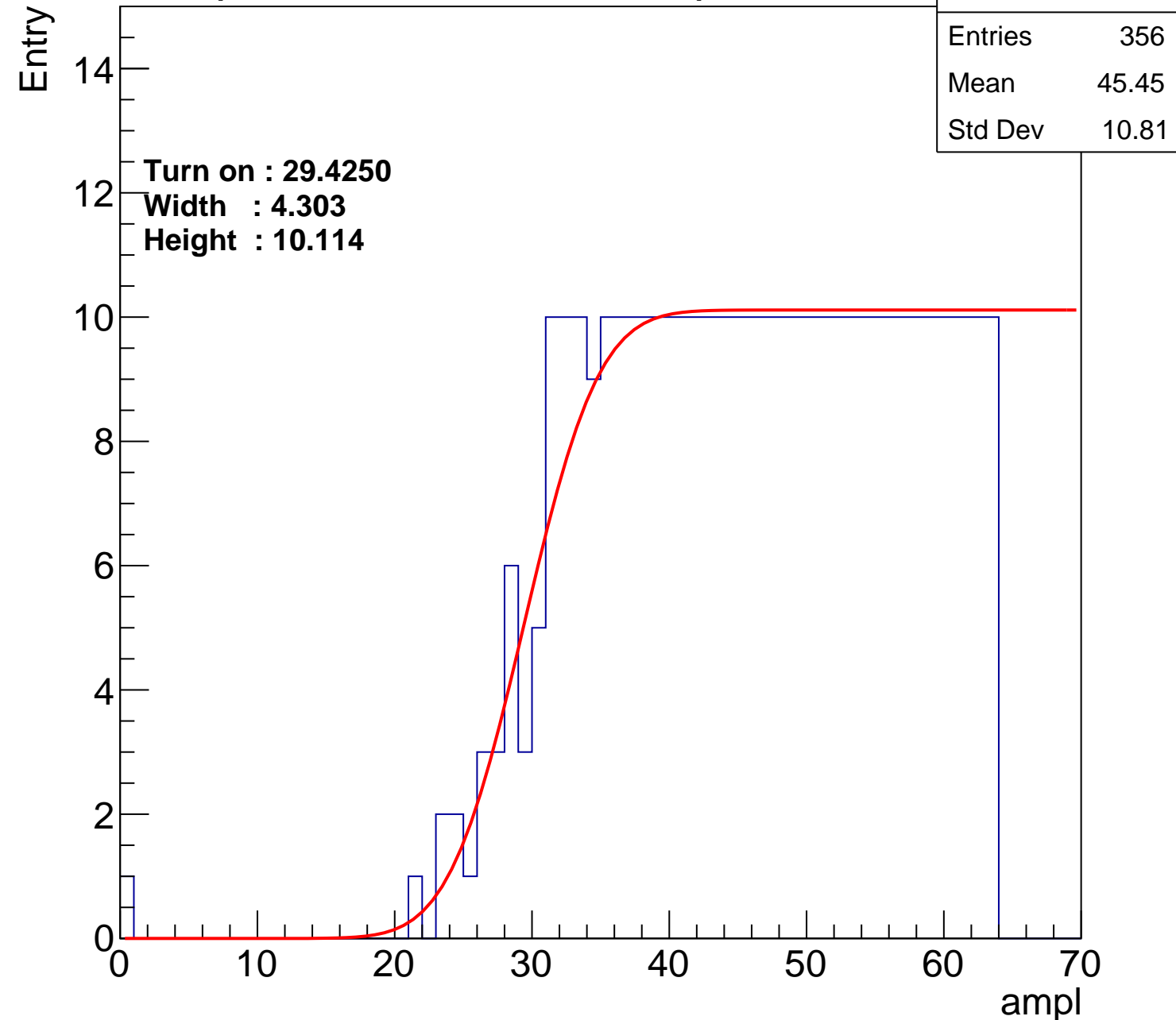
Width : 4.303

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch121

calib_packv5_042523_0143.root, FC#9, port A1

Entries	362
Mean	45.22
Std Dev	10.84

Turn on : 27.9046

Width : 2.554

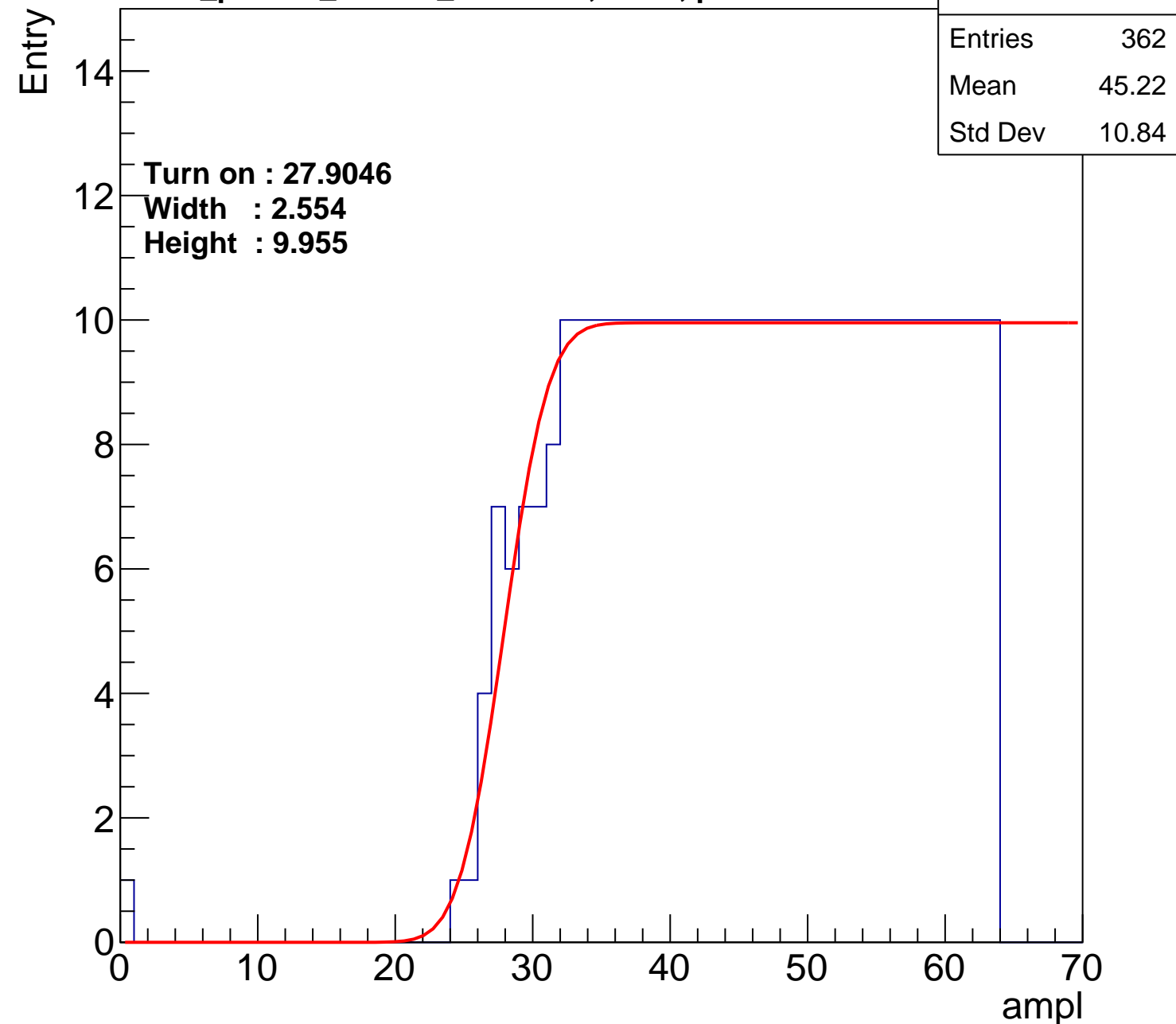
Height : 9.955

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U15-ch122

calib_packv5_042523_0143.root, FC#9, port A1

Entries	364
Mean	44.97
Std Dev	11.32

Turn on : 27.9070

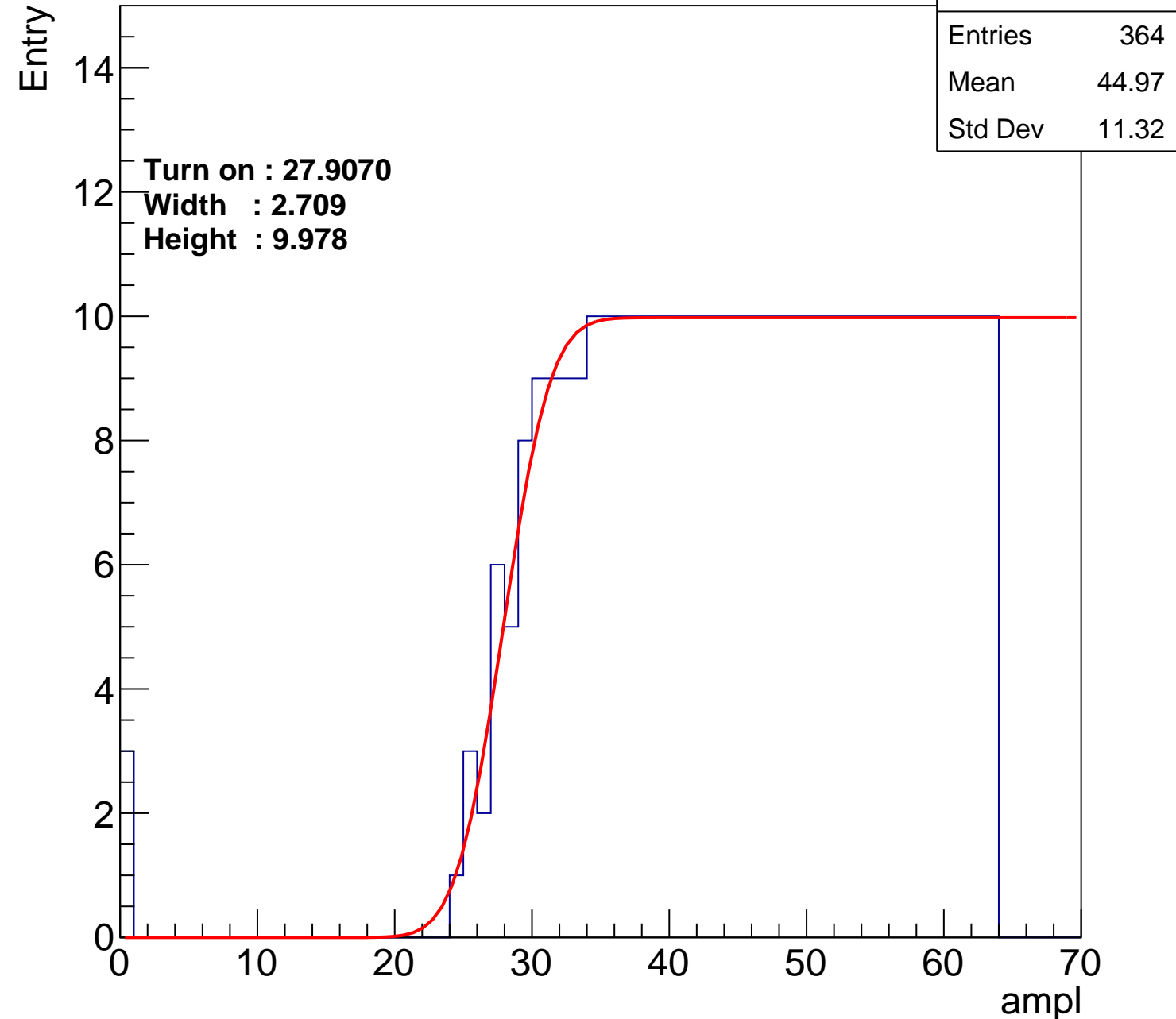
Width : 2.709

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch123

calib_packv5_042523_0143.root, FC#9, port A1

Entries	354
Mean	45.42
Std Dev	11.15

Turn on : 29.5572

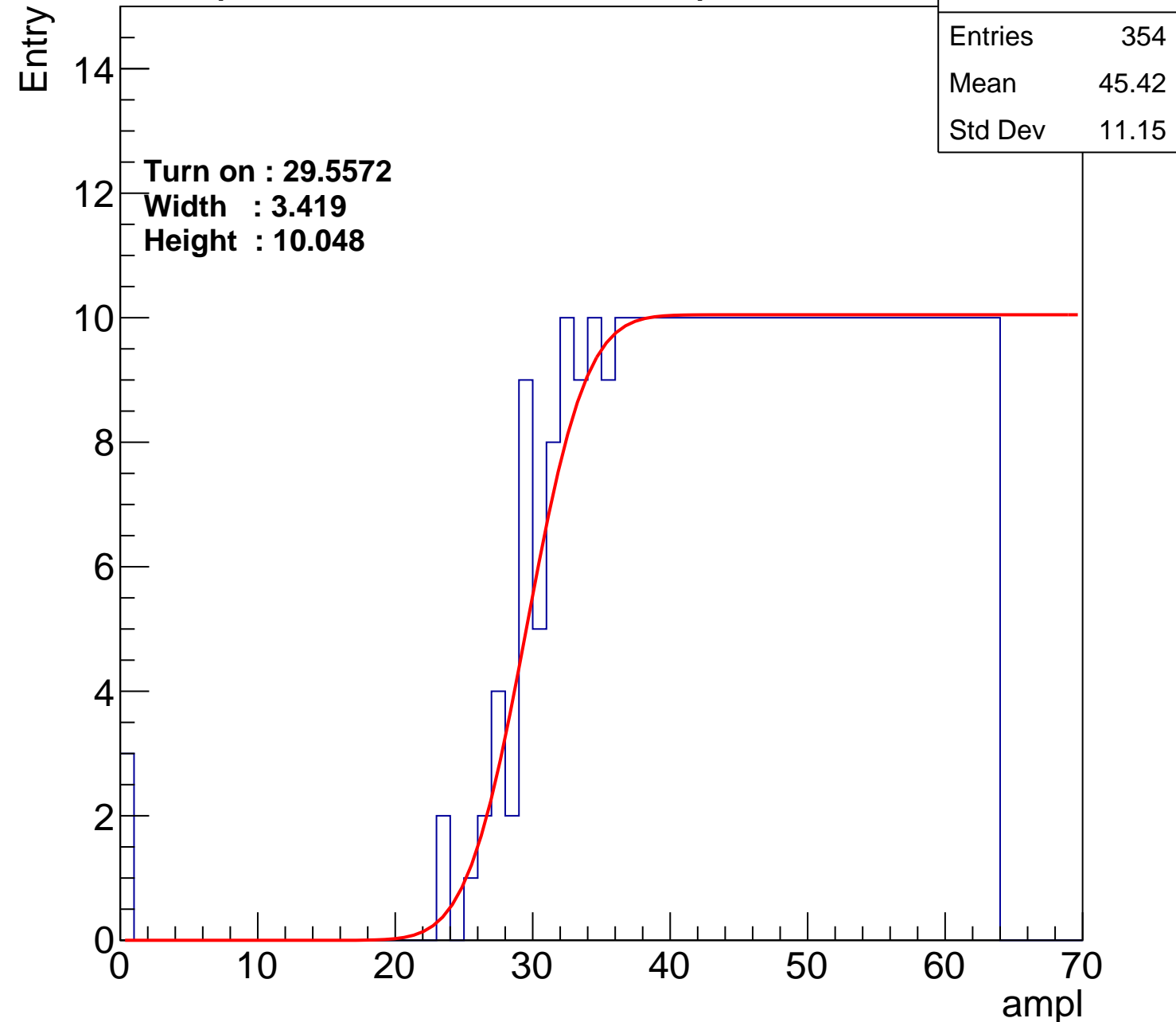
Width : 3.419

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch124

calib_packv5_042523_0143.root, FC#9, port A1

Entries	342
Mean	46.15
Std Dev	10.41

Turn on : 30.2959

Width : 3.778

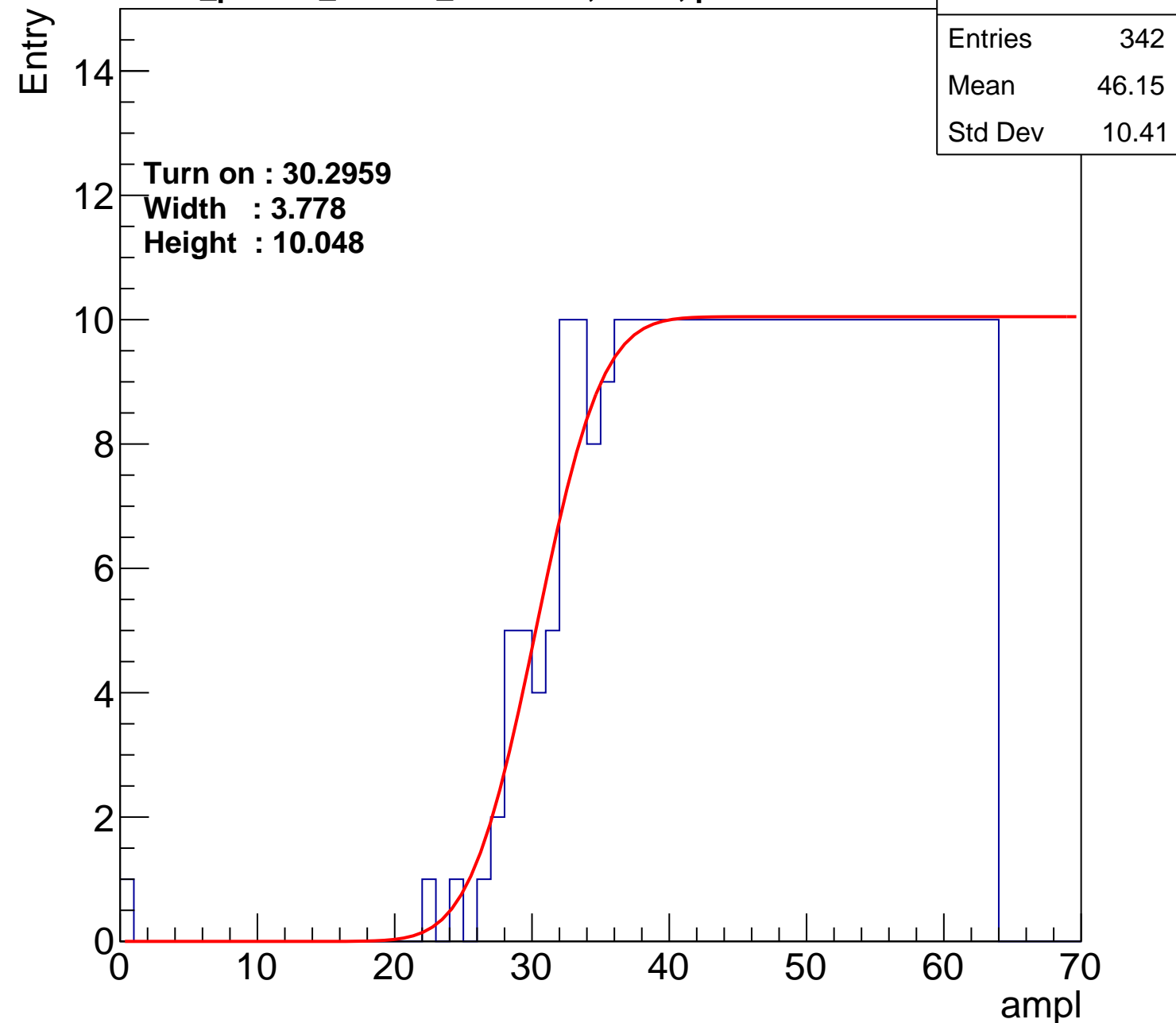
Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U15-ch125

calib_packv5_042523_0143.root, FC#9, port A1

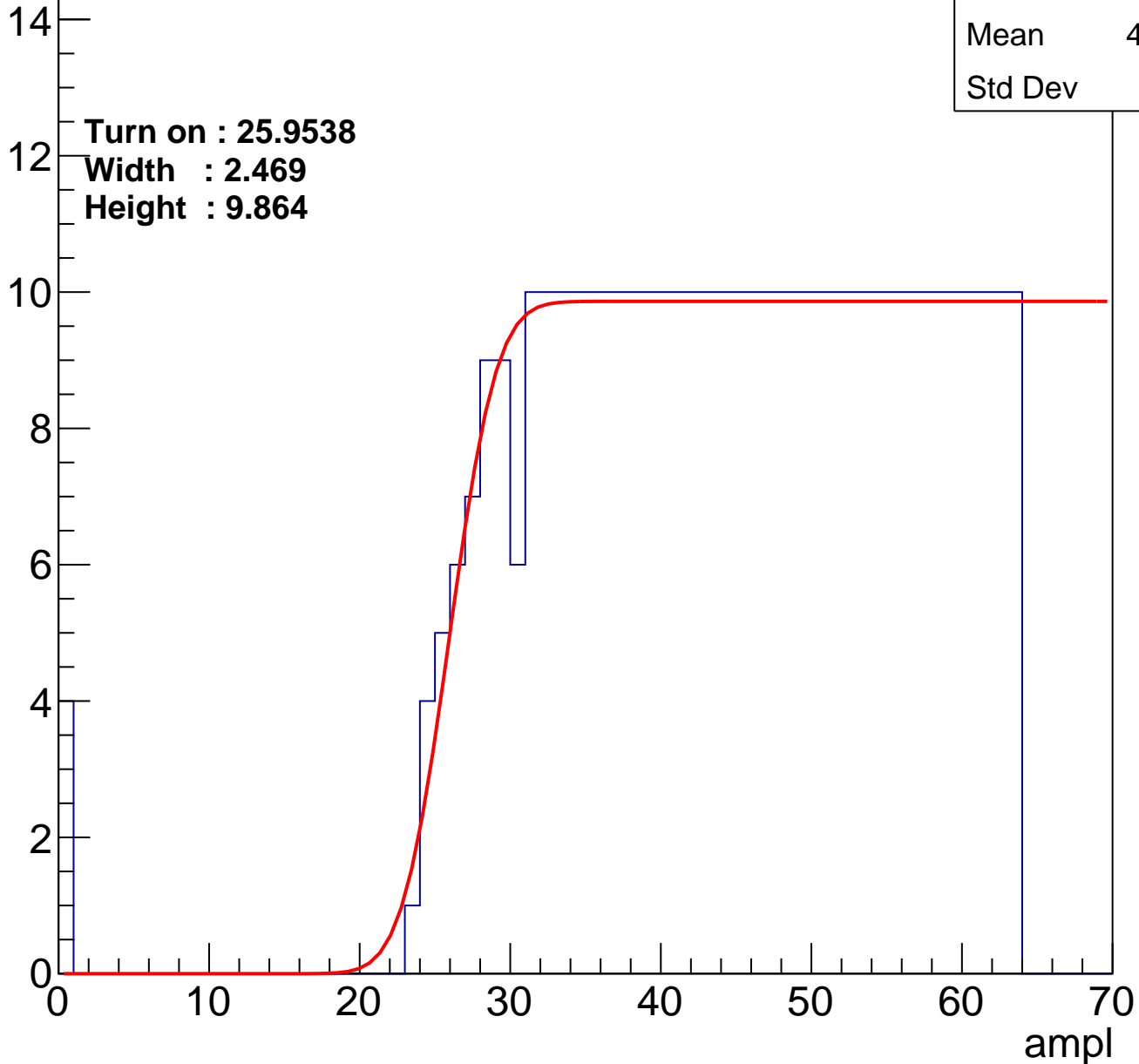
Entries	381
Mean	44.07
Std Dev	11.9

Turn on : 25.9538

Width : 2.469

Height : 9.864

Entry



B0L001S, U15-ch126

calib_packv5_042523_0143.root, FC#9, port A1

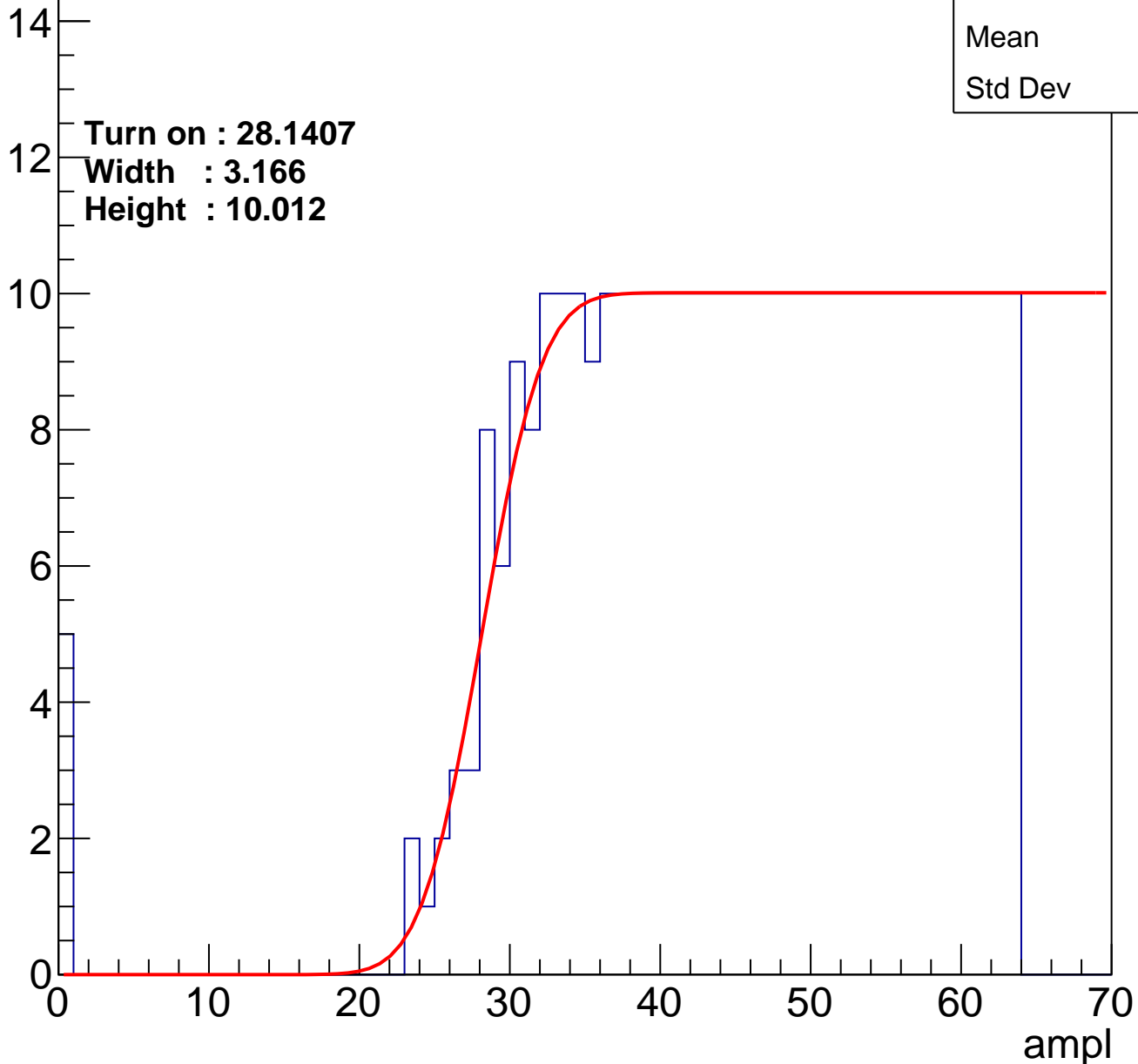
Entries	366
Mean	44.7
Std Dev	11.8

Turn on : 28.1407

Width : 3.166

Height : 10.012

Entry



B0L001S, U15-ch127

calib_packv5_042523_0143.root, FC#9, port A1

Entries	354
Mean	45.5
Std Dev	10.93

Turn on : 28.8856

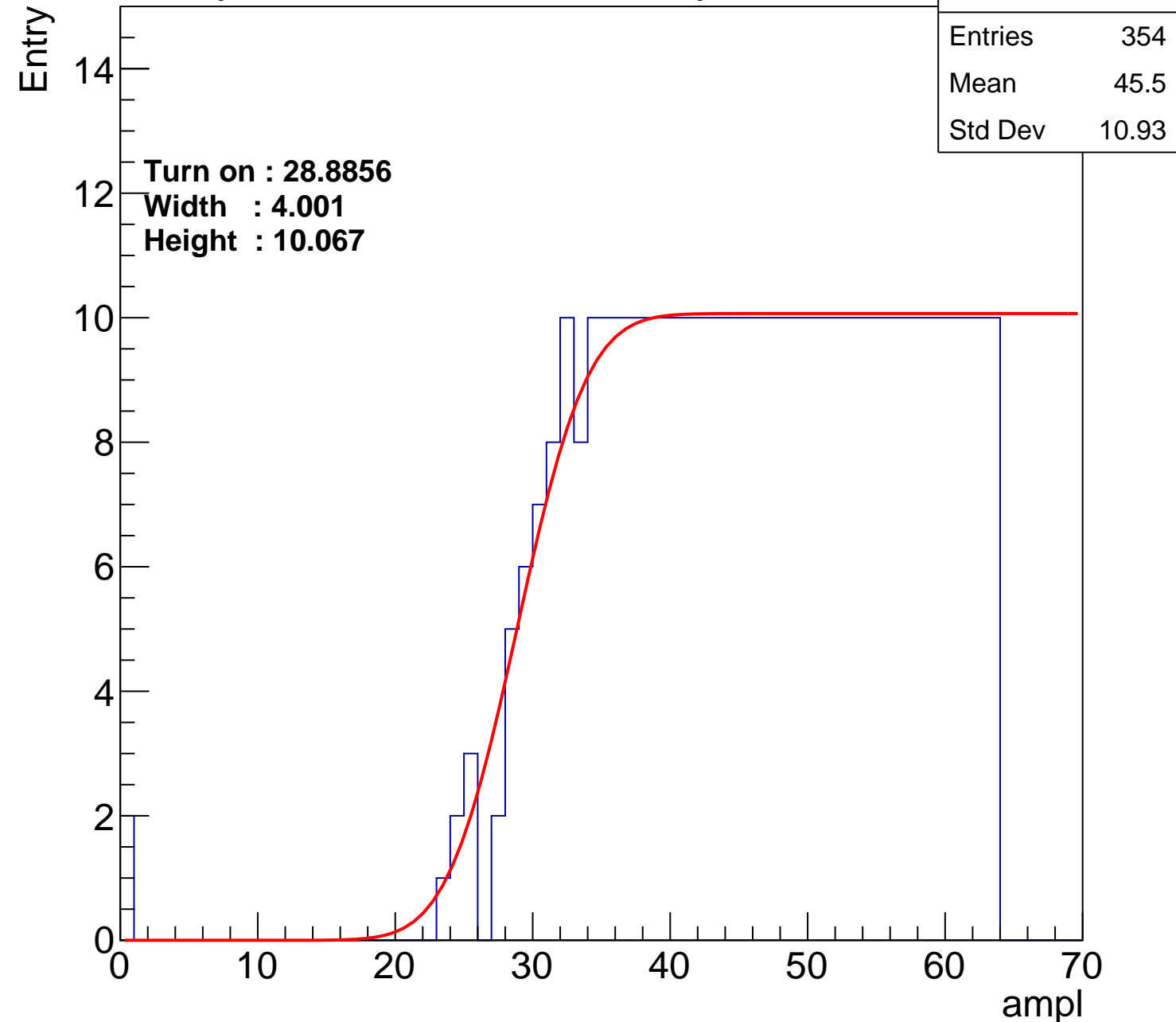
Width : 4.001

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U15-ch127

calib_packv5_042523_0143.root, FC#9, port A1

Entries	354
Mean	45.5
Std Dev	10.93

Turn on : 28.8856

Width : 4.001

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl

