

B0L000S, U21-ch0

calib_packv5_042523_0143.root, FC#5, port B1

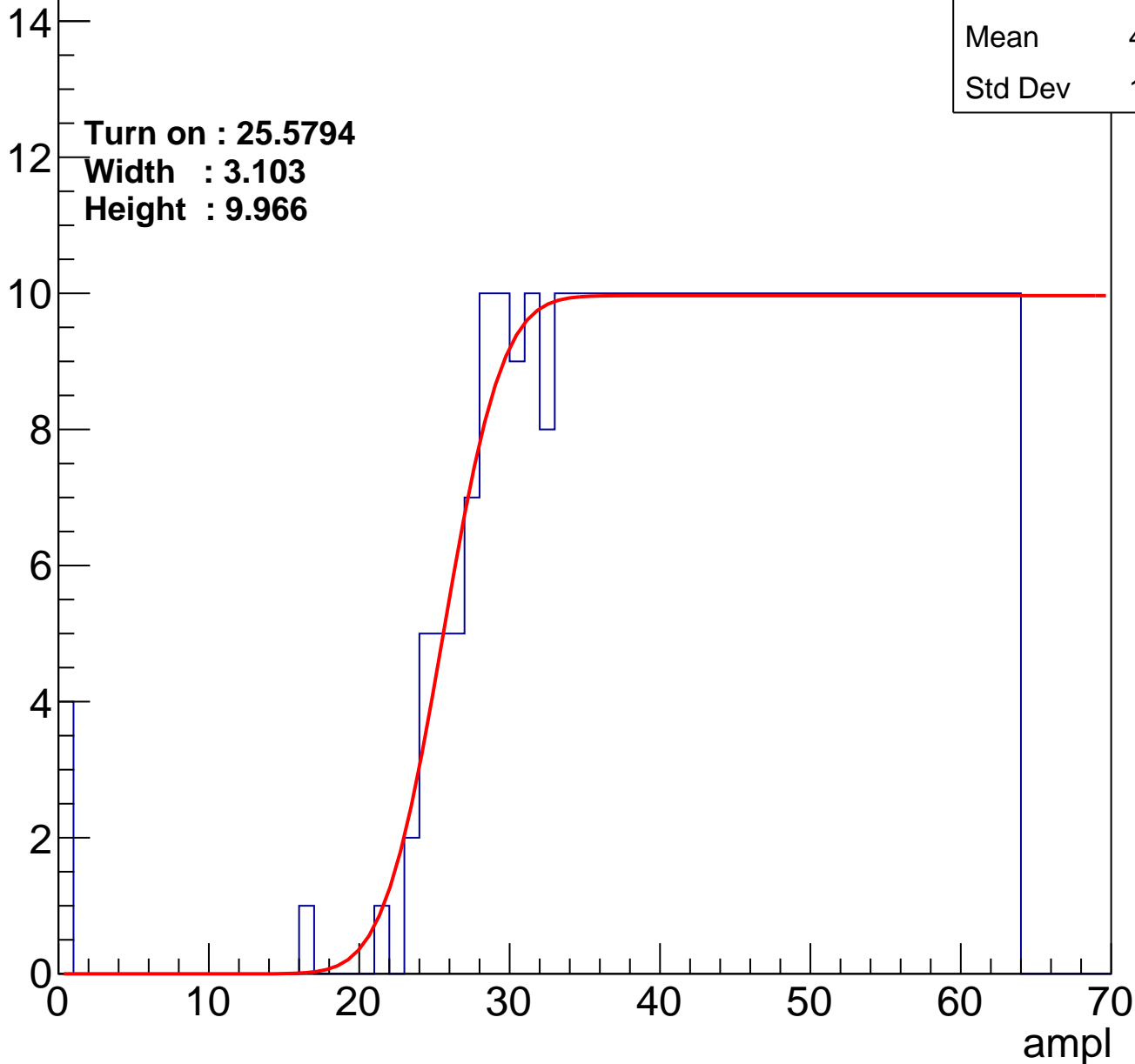
Entry

Entries	387
Mean	43.75
Std Dev	12.09

Turn on : 25.5794

Width : 3.103

Height : 9.966



B0L000S, U21-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	44
Std Dev	11.49

Turn on : 25.6118

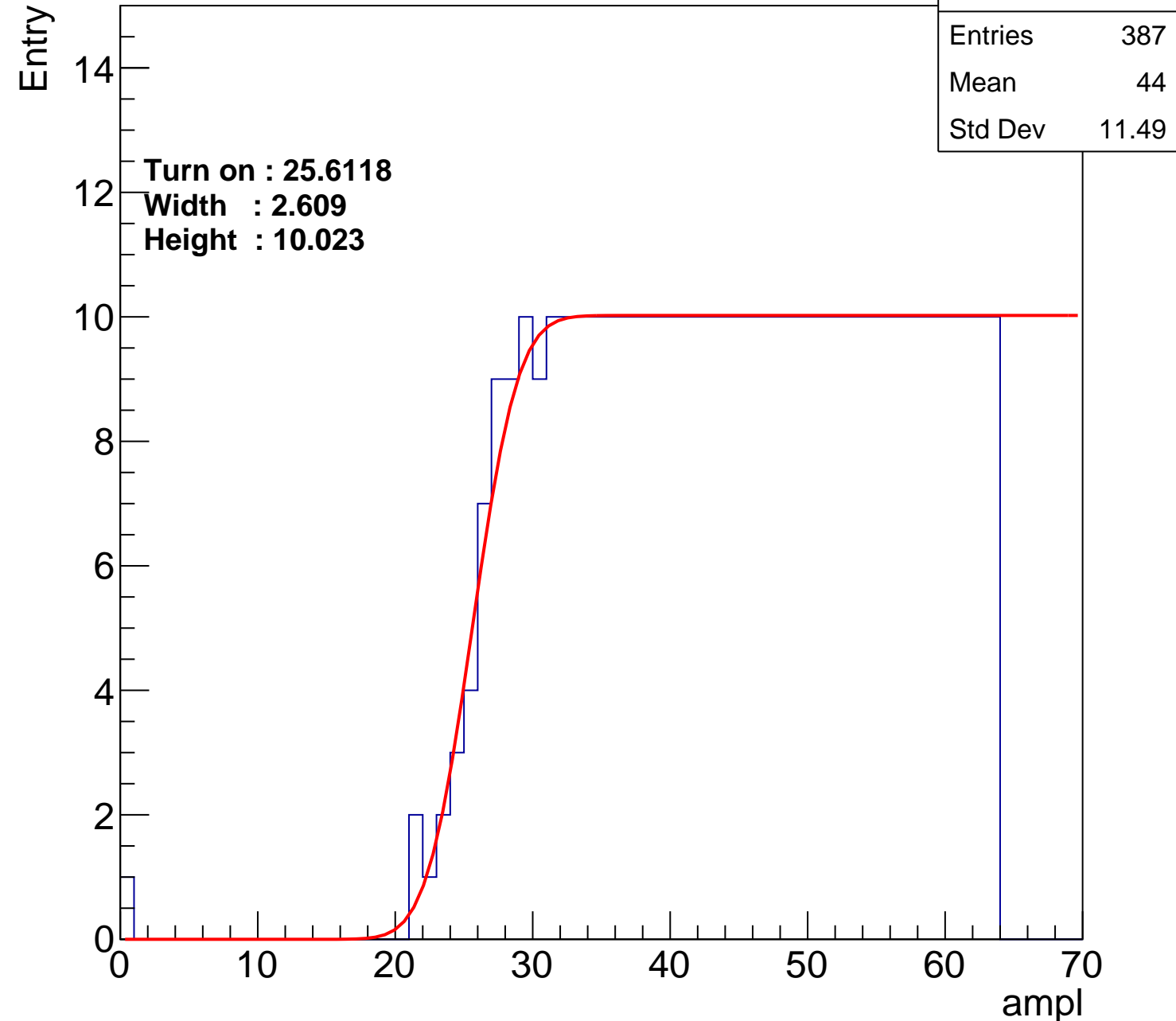
Width : 2.609

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 24.5124

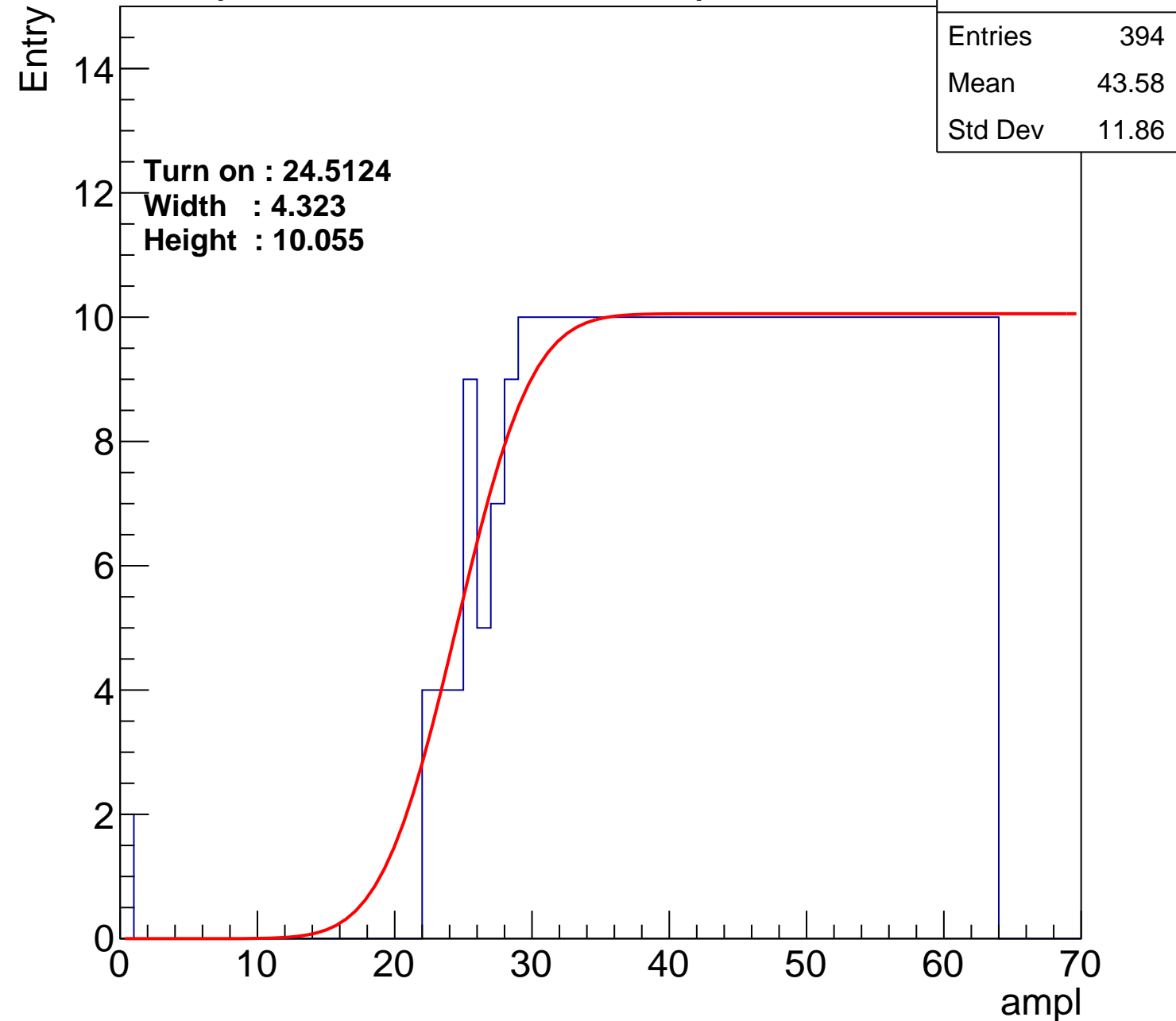
Width : 4.323

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.89
Std Dev	11.56

Turn on : 24.7910

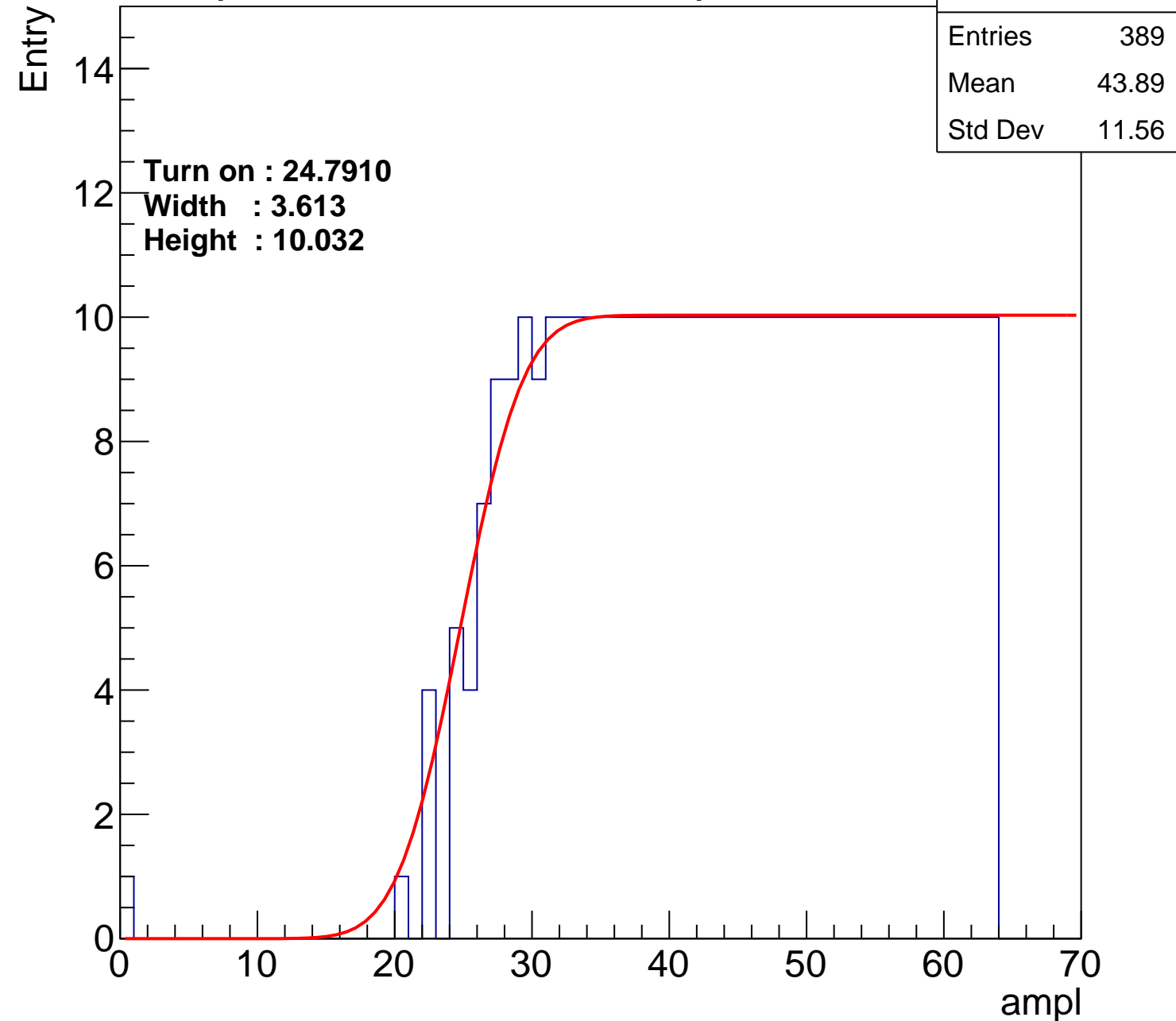
Width : 3.613

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch4

calib_packv5_042523_0143.root, FC#5, port B1

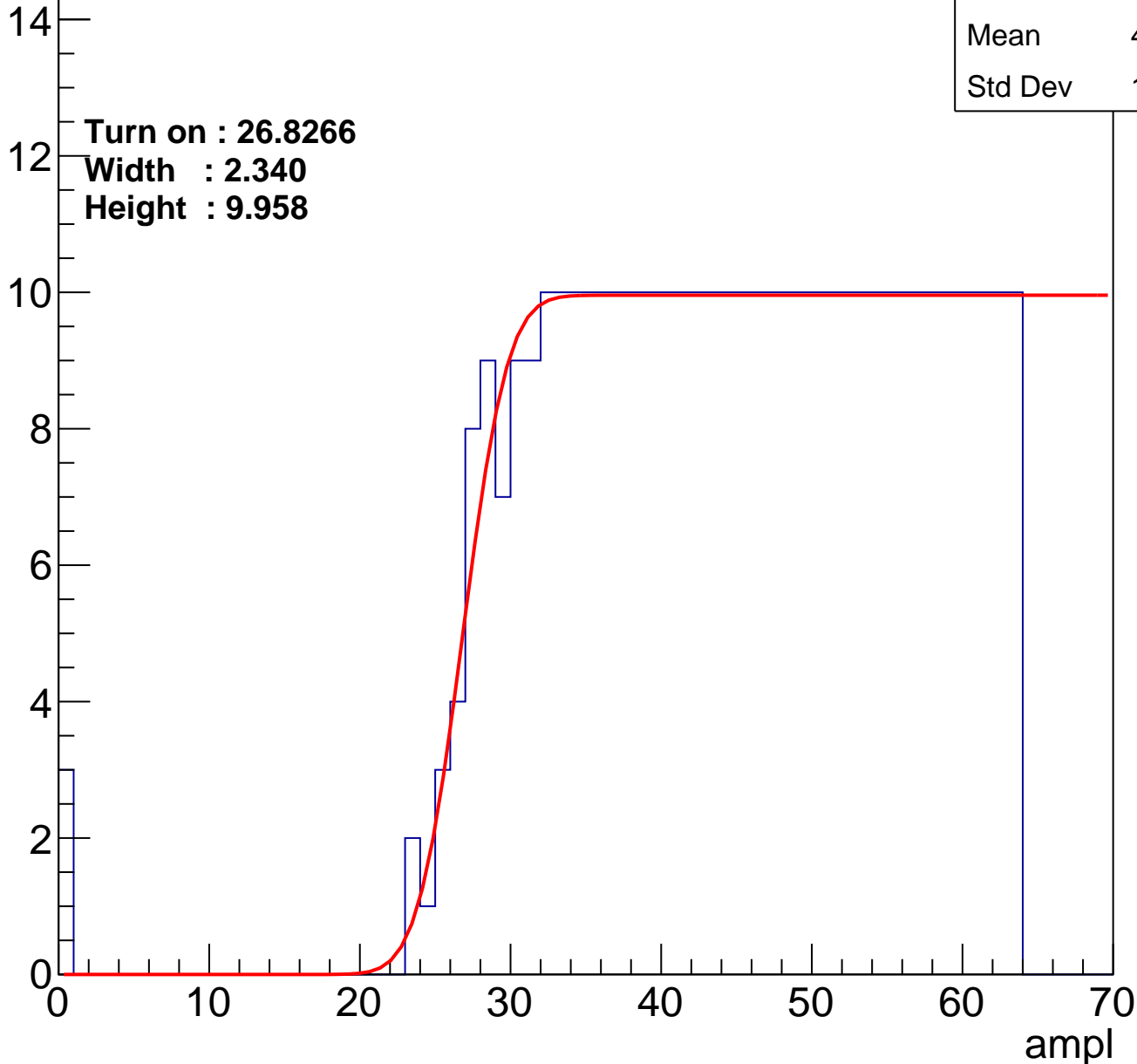
Entry

Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 26.8266

Width : 2.340

Height : 9.958



B0L000S, U21-ch5

calib_packv5_042523_0143.root, FC#5, port B1

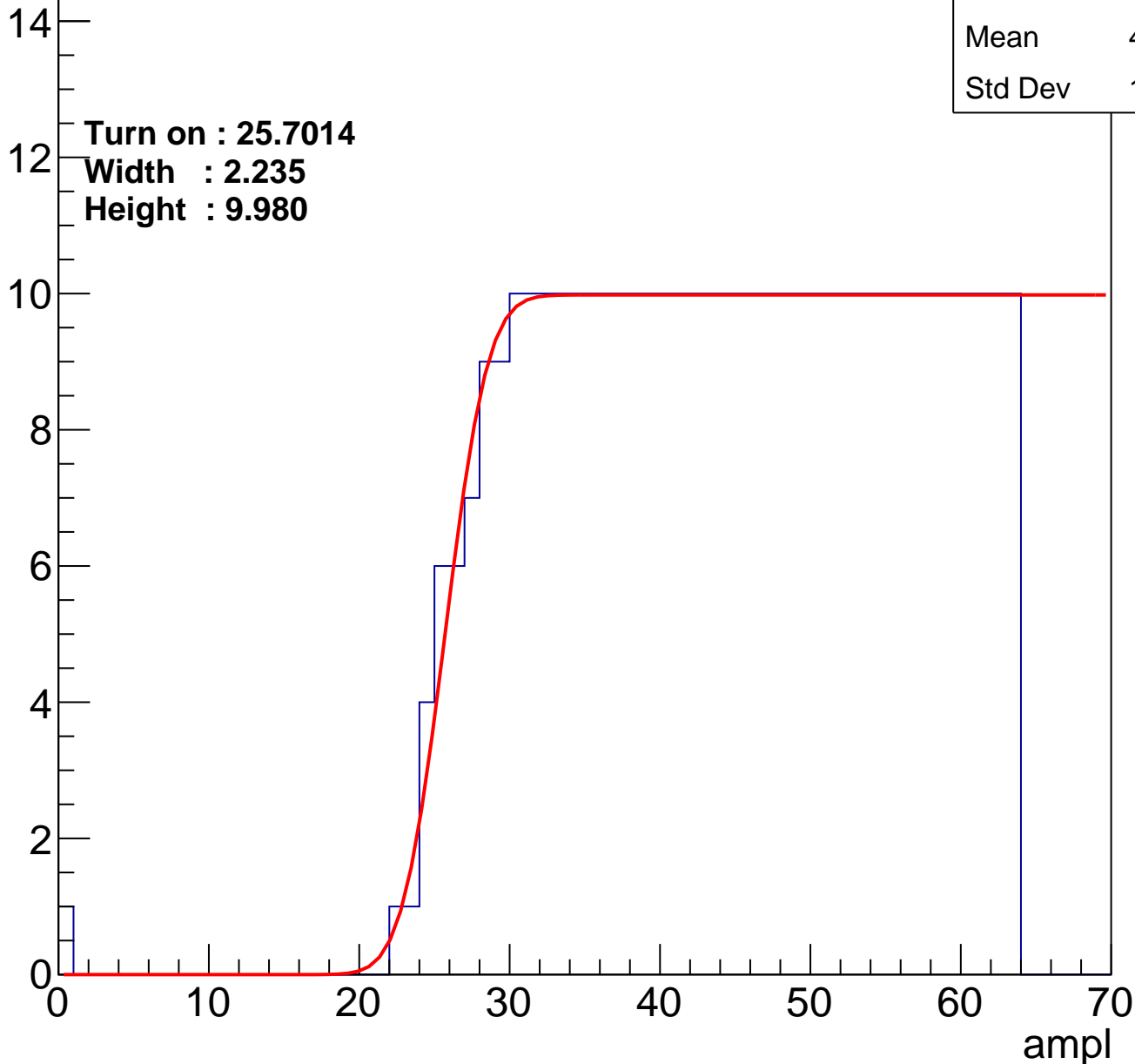
Entry

Entries	384
Mean	44.16
Std Dev	11.38

Turn on : 25.7014

Width : 2.235

Height : 9.980



B0L000S, U21-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.97
Std Dev	10.93

Turn on : 27.1728

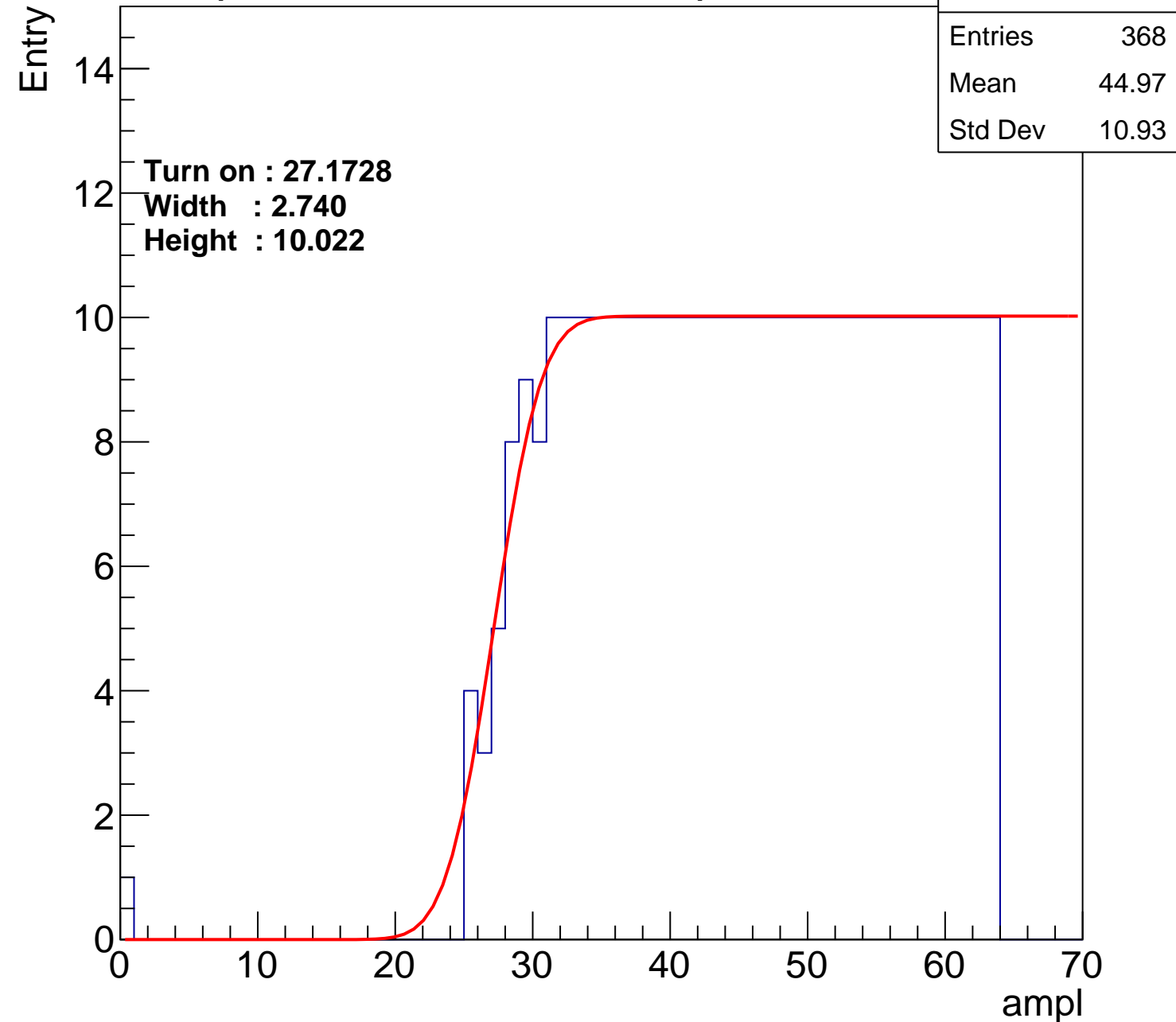
Width : 2.740

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch7

calib_packv5_042523_0143.root, FC#5, port B1

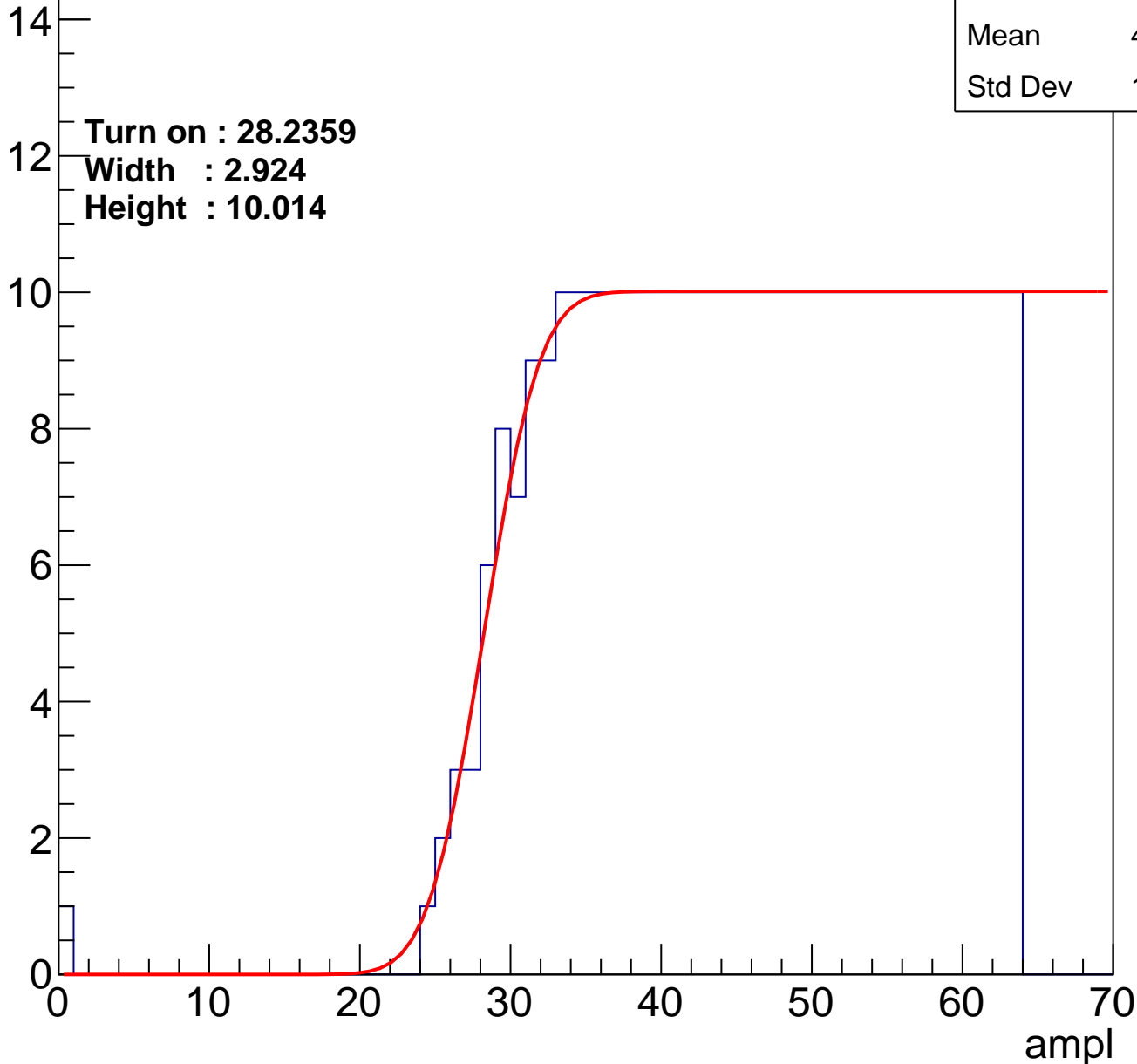
Entry

Entries	359
Mean	45.38
Std Dev	10.75

Turn on : 28.2359

Width : 2.924

Height : 10.014



B0L000S, U21-ch8

calib_packv5_042523_0143.root, FC#5, port B1

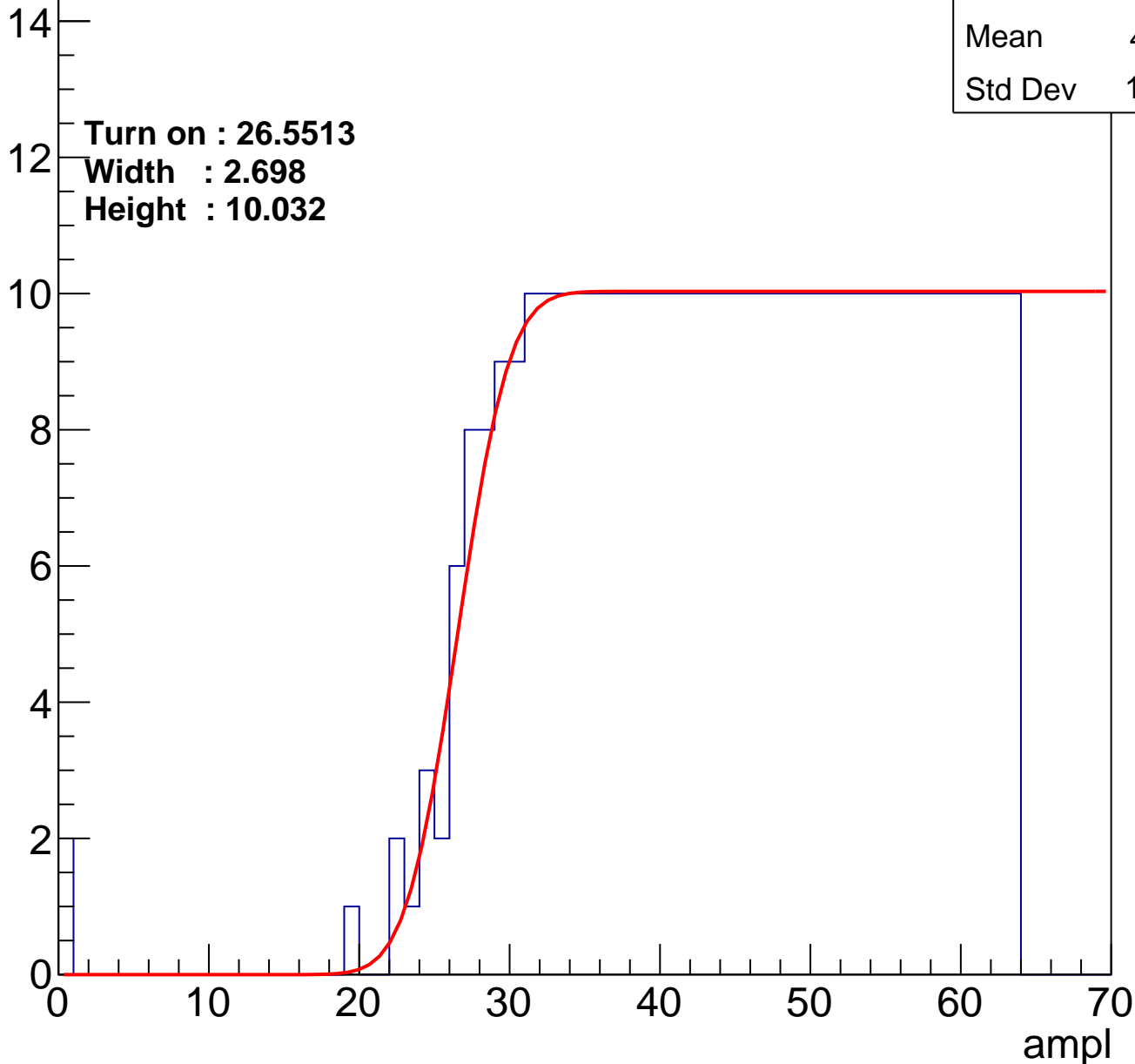
Entry

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.5513

Width : 2.698

Height : 10.032



B0L000S, U21-ch9

calib_packv5_042523_0143.root, FC#5, port B1

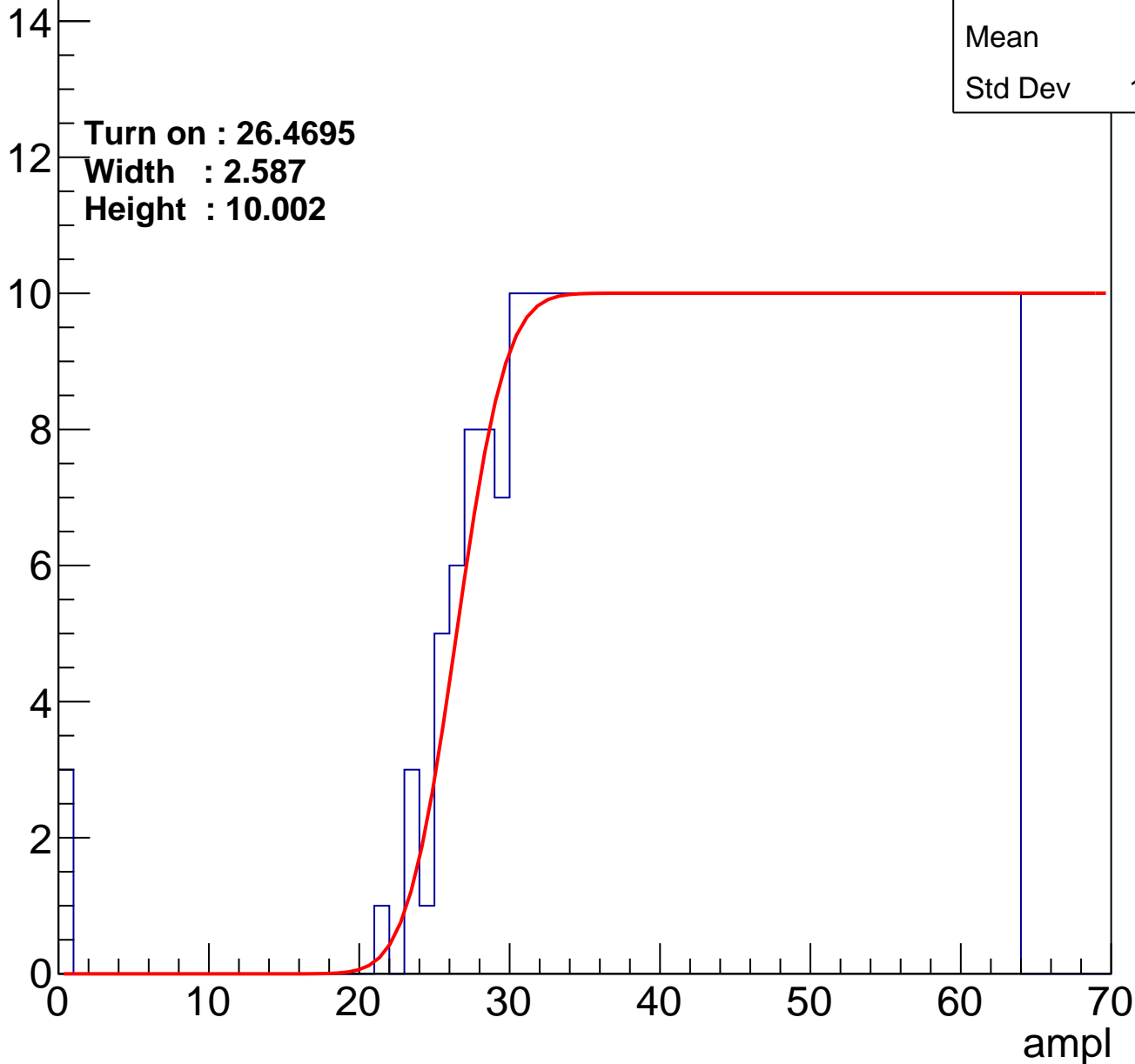
Entry

Entries	382
Mean	44.1
Std Dev	11.73

Turn on : 26.4695

Width : 2.587

Height : 10.002



B0L000S, U21-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.75
Std Dev	12.05

Turn on : 25.9683

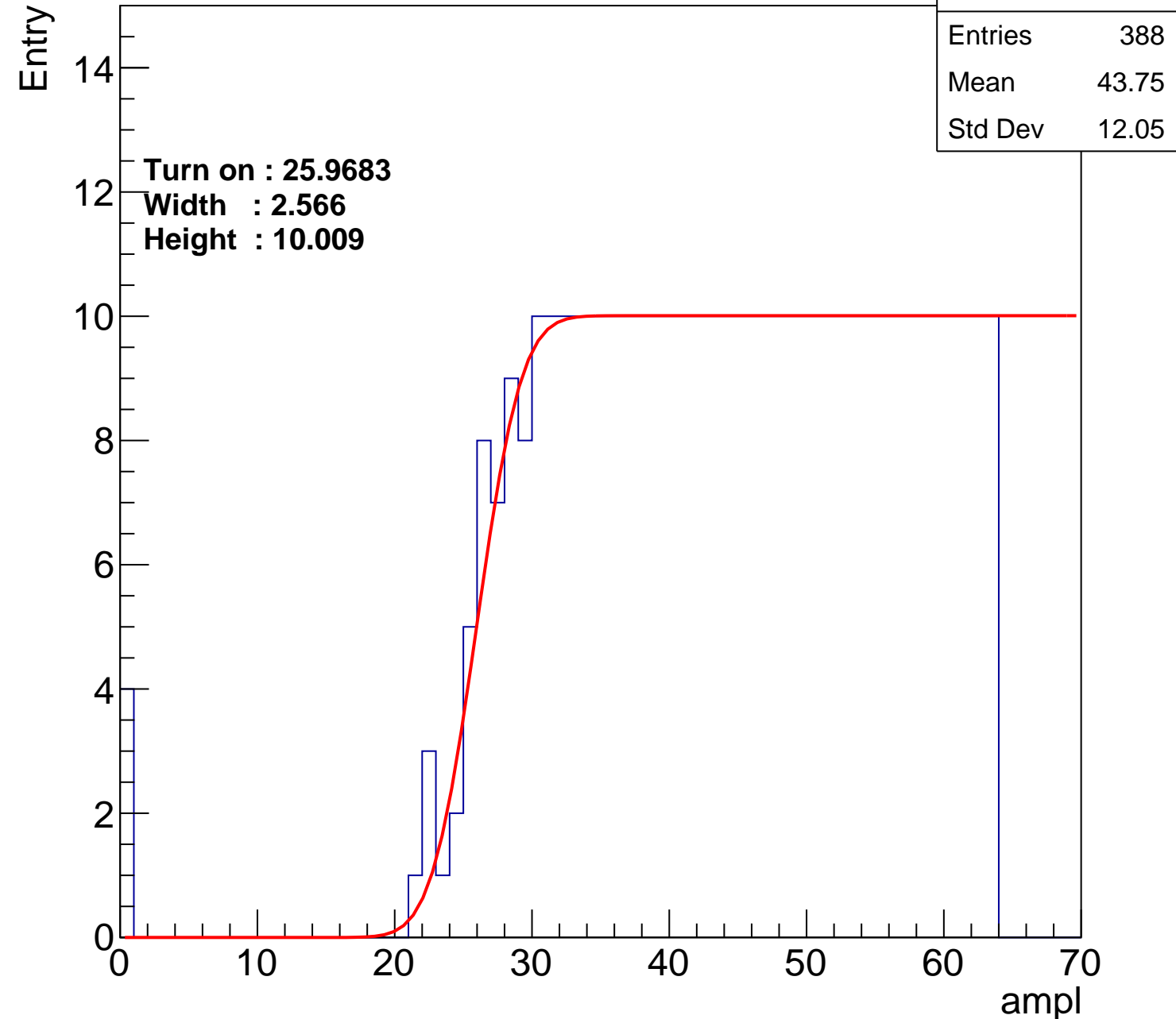
Width : 2.566

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch11

calib_packv5_042523_0143.root, FC#5, port B1

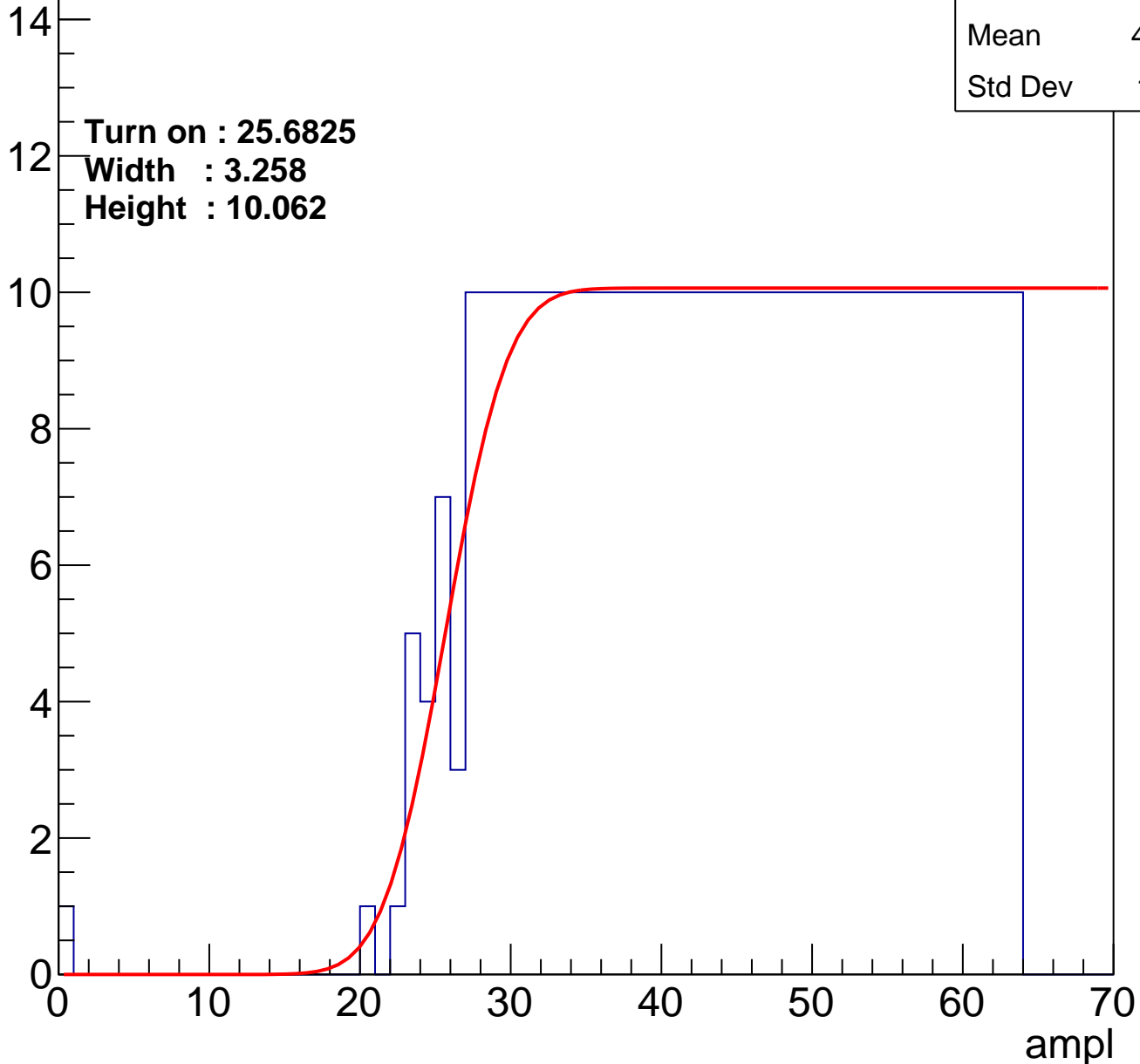
Entries	392
Mean	43.77
Std Dev	11.61

Turn on : 25.6825

Width : 3.258

Height : 10.062

Entry



B0L000S, U21-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.39

Turn on : 27.1378

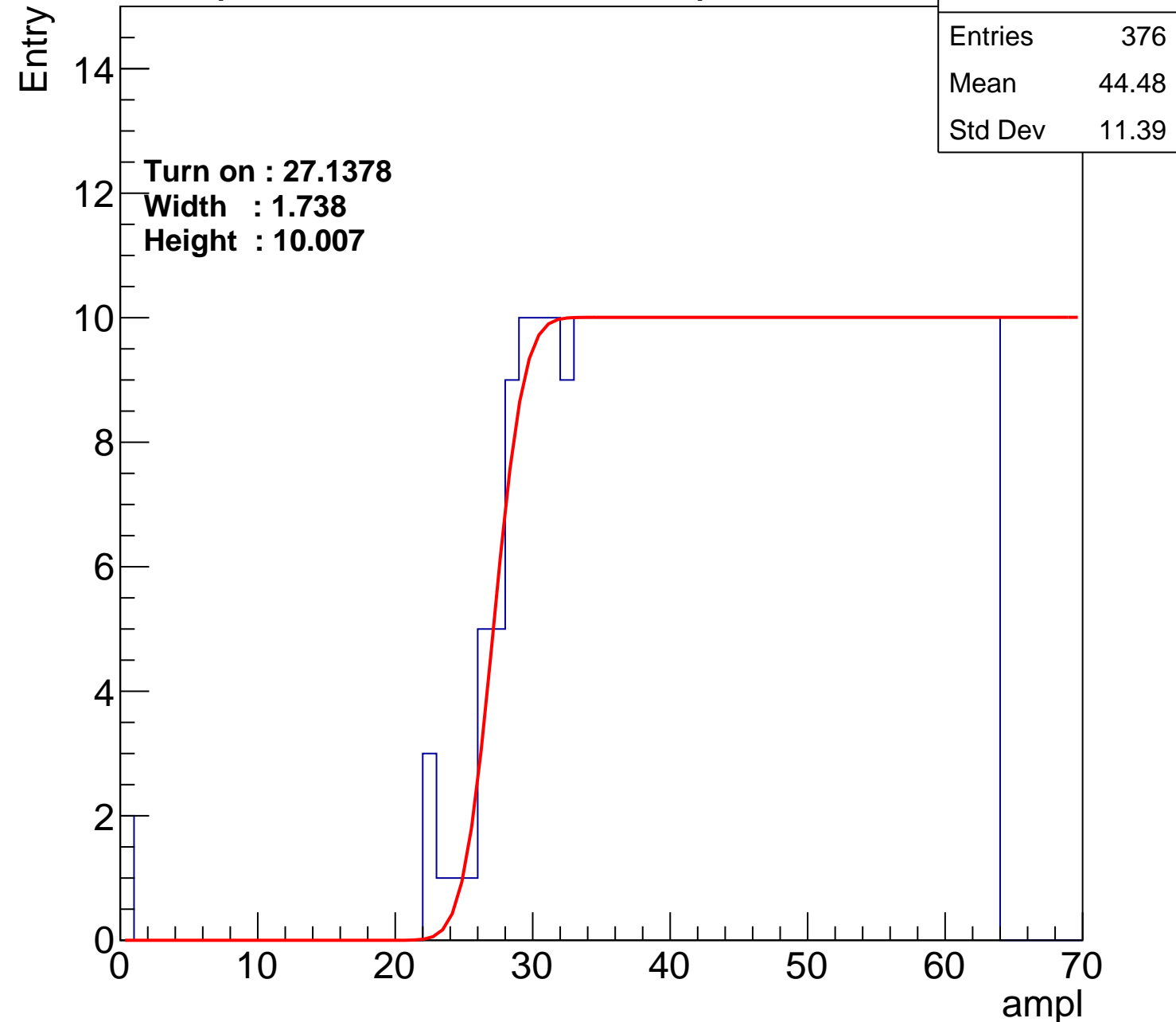
Width : 1.738

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.93
Std Dev	10.99

Turn on : 27.2071

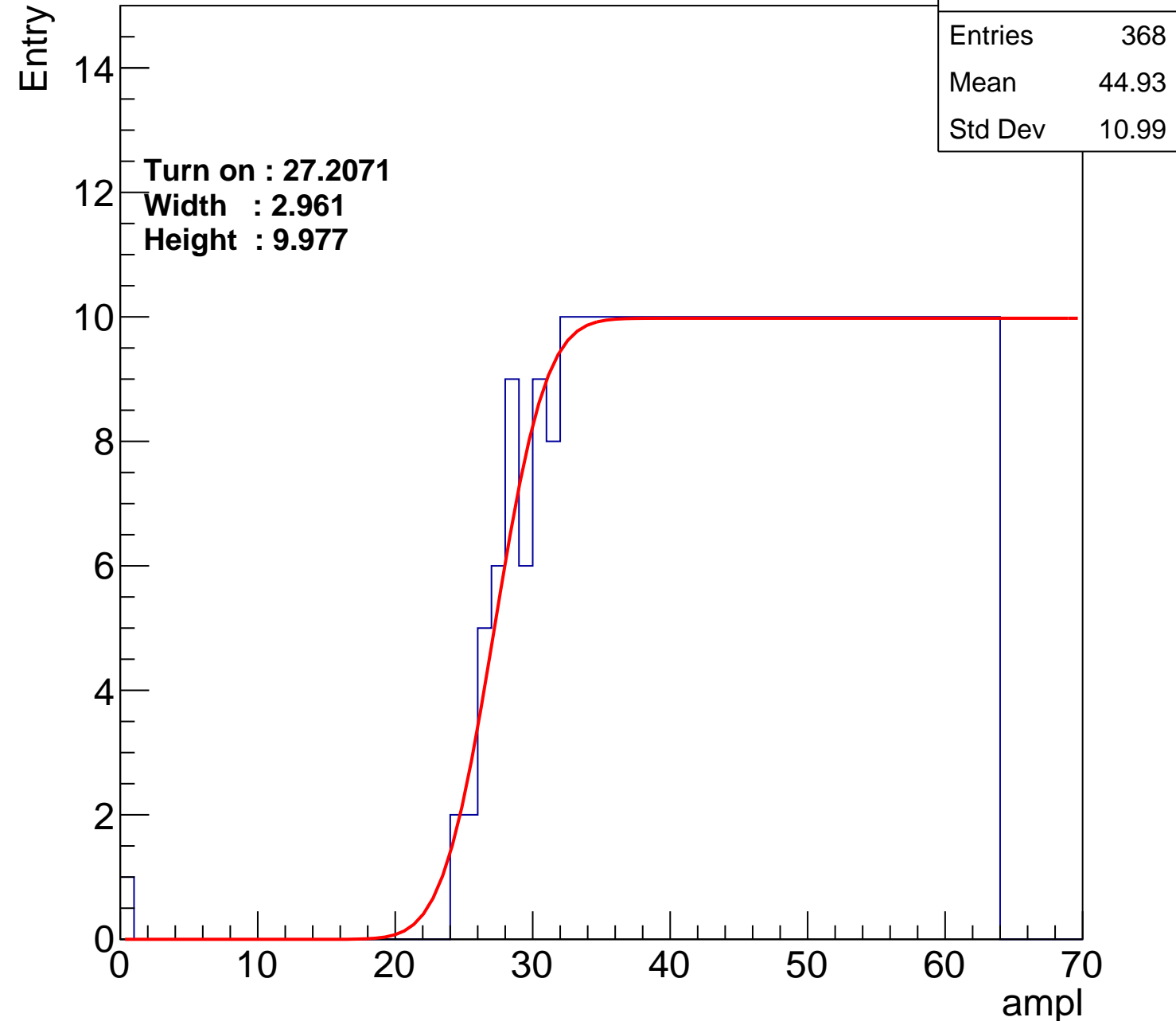
Width : 2.961

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch14

calib_packv5_042523_0143.root, FC#5, port B1

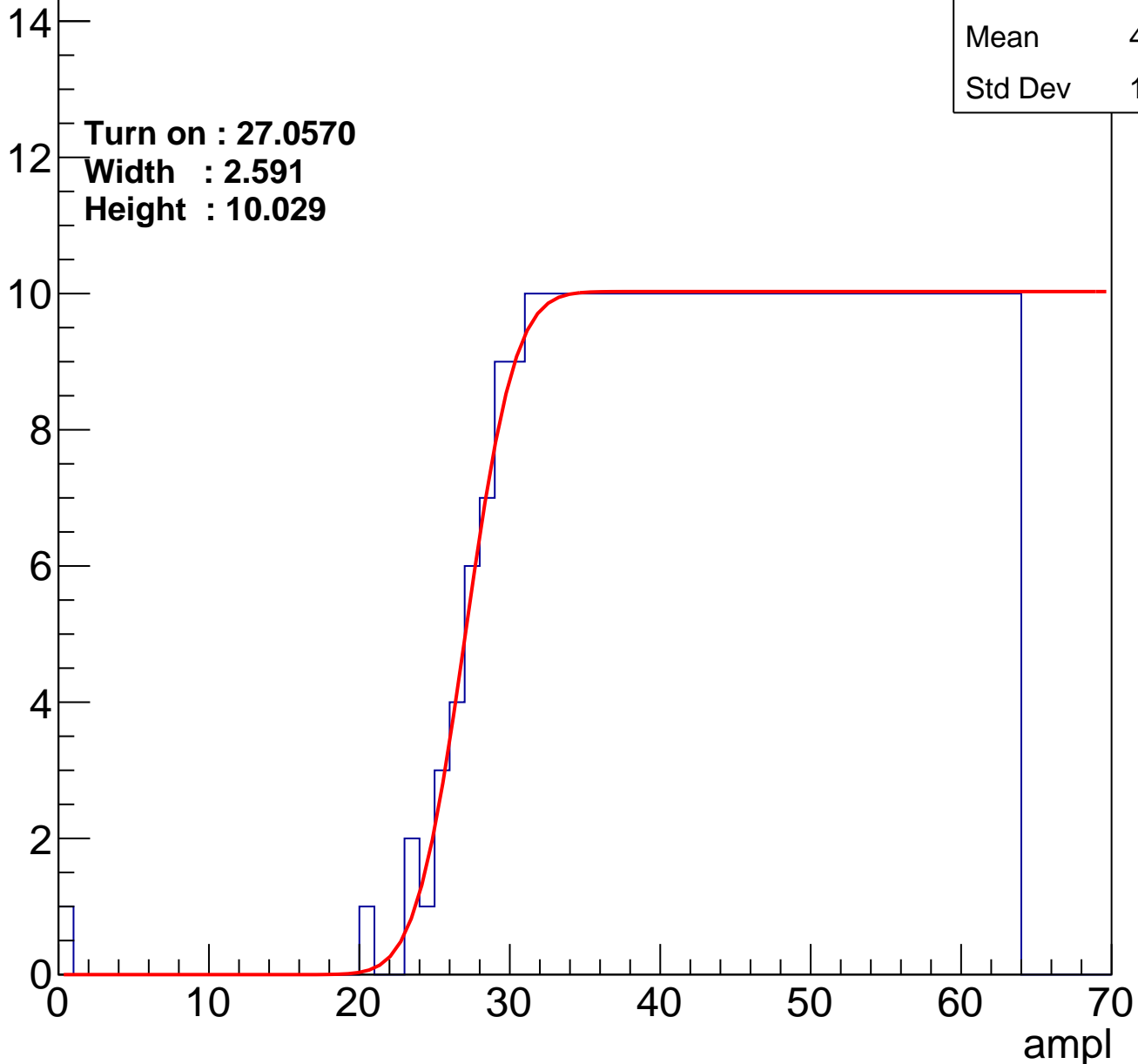
Entries	373
Mean	44.69
Std Dev	11.13

Turn on : 27.0570

Width : 2.591

Height : 10.029

Entry



B0L000S, U21-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 26.2887

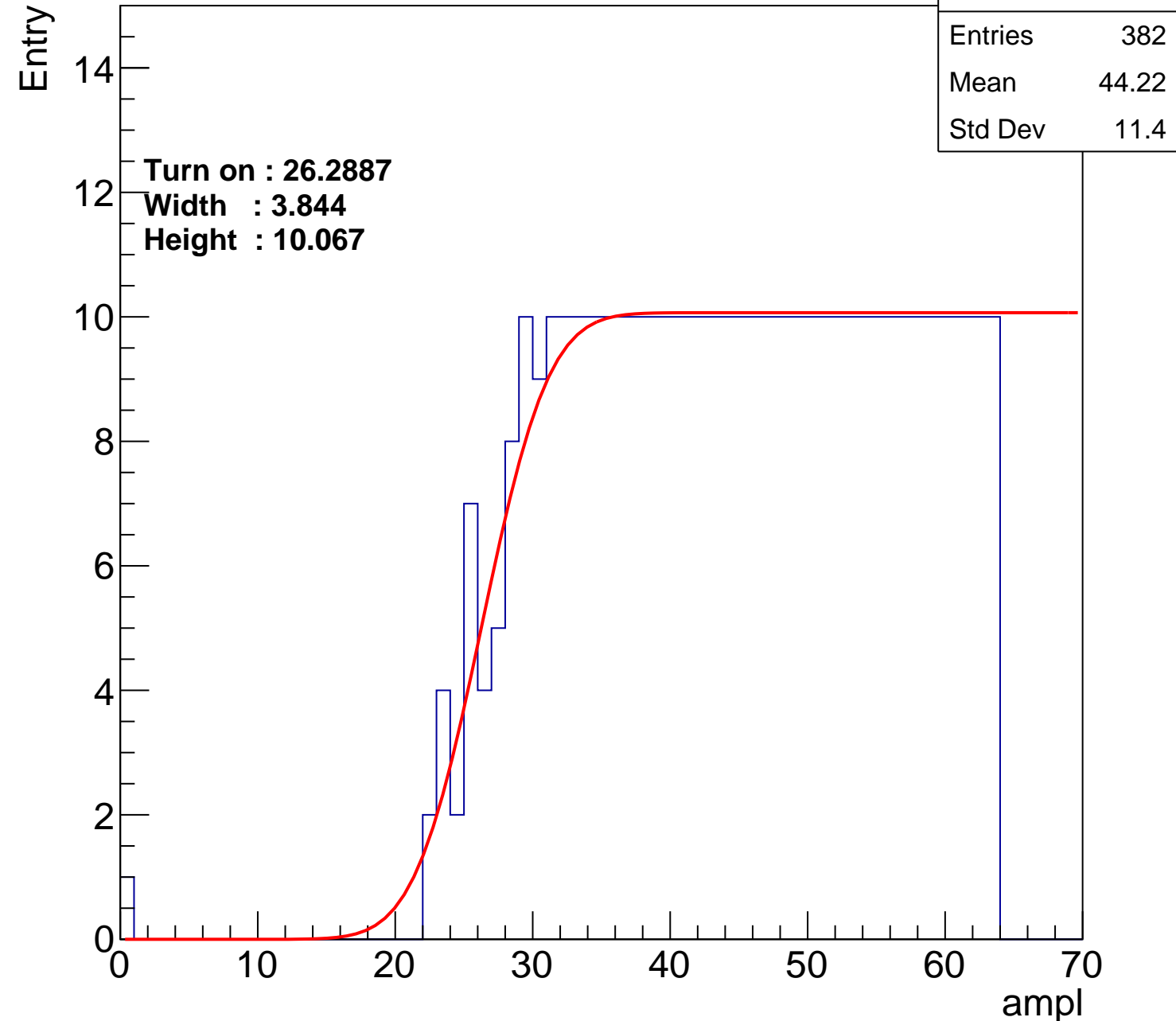
Width : 3.844

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.59
Std Dev	12.54

Turn on : 26.2679

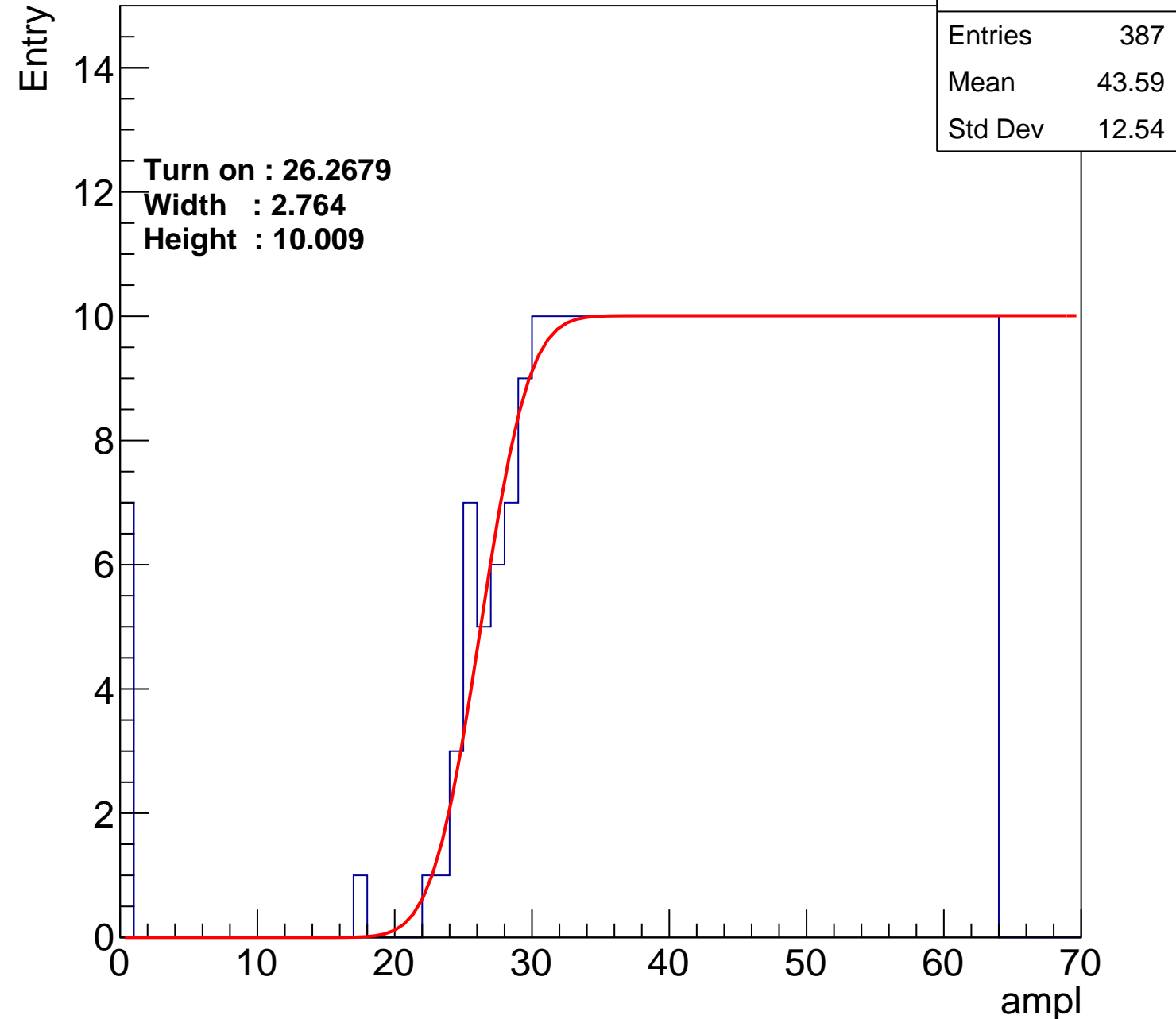
Width : 2.764

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch17

calib_packv5_042523_0143.root, FC#5, port B1

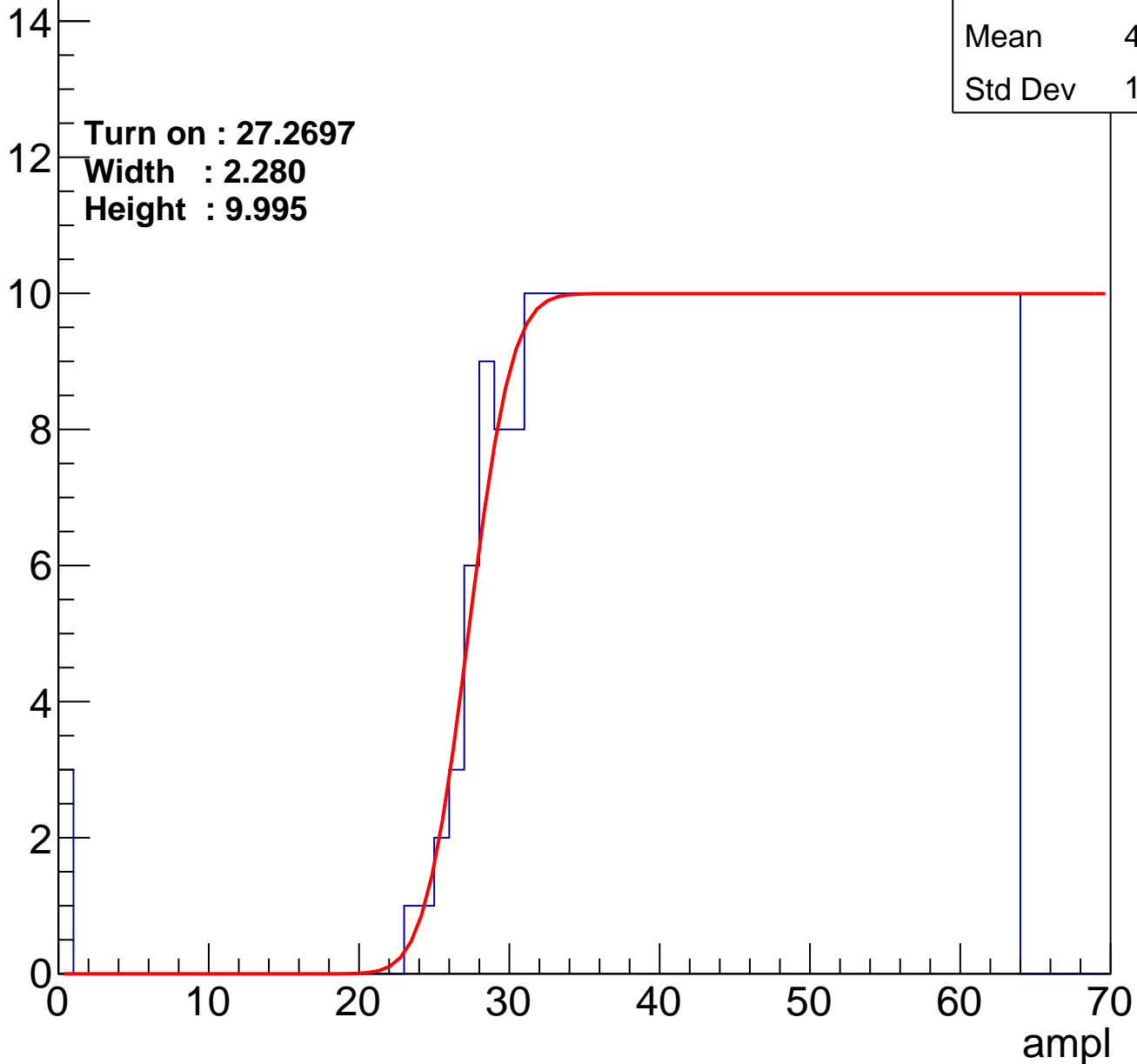
Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.2697

Width : 2.280

Height : 9.995

Entry



B0L000S, U21-ch18

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.5999

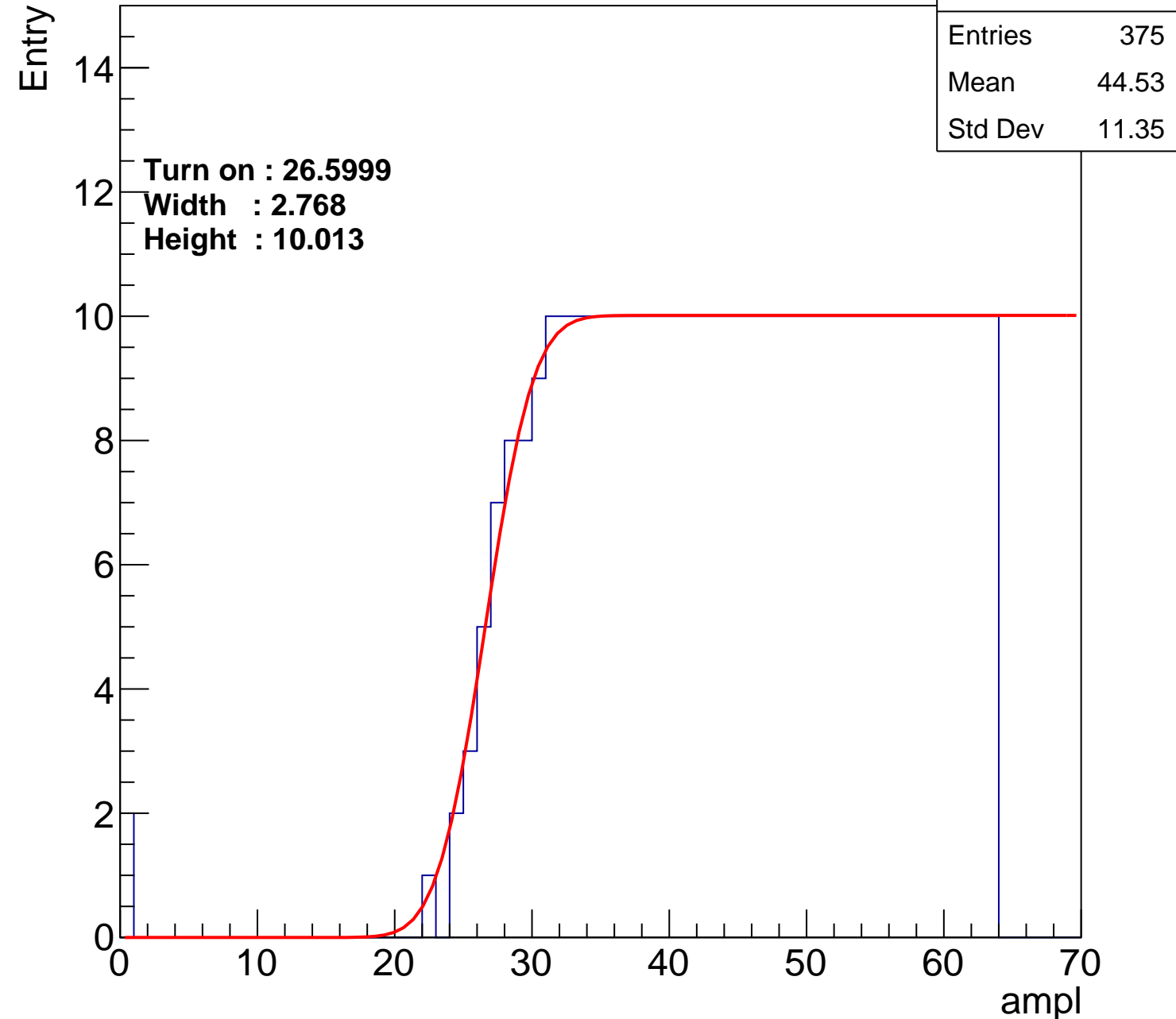
Width : 2.768

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	400
Mean	43.37
Std Dev	11.83

Turn on : 23.8308

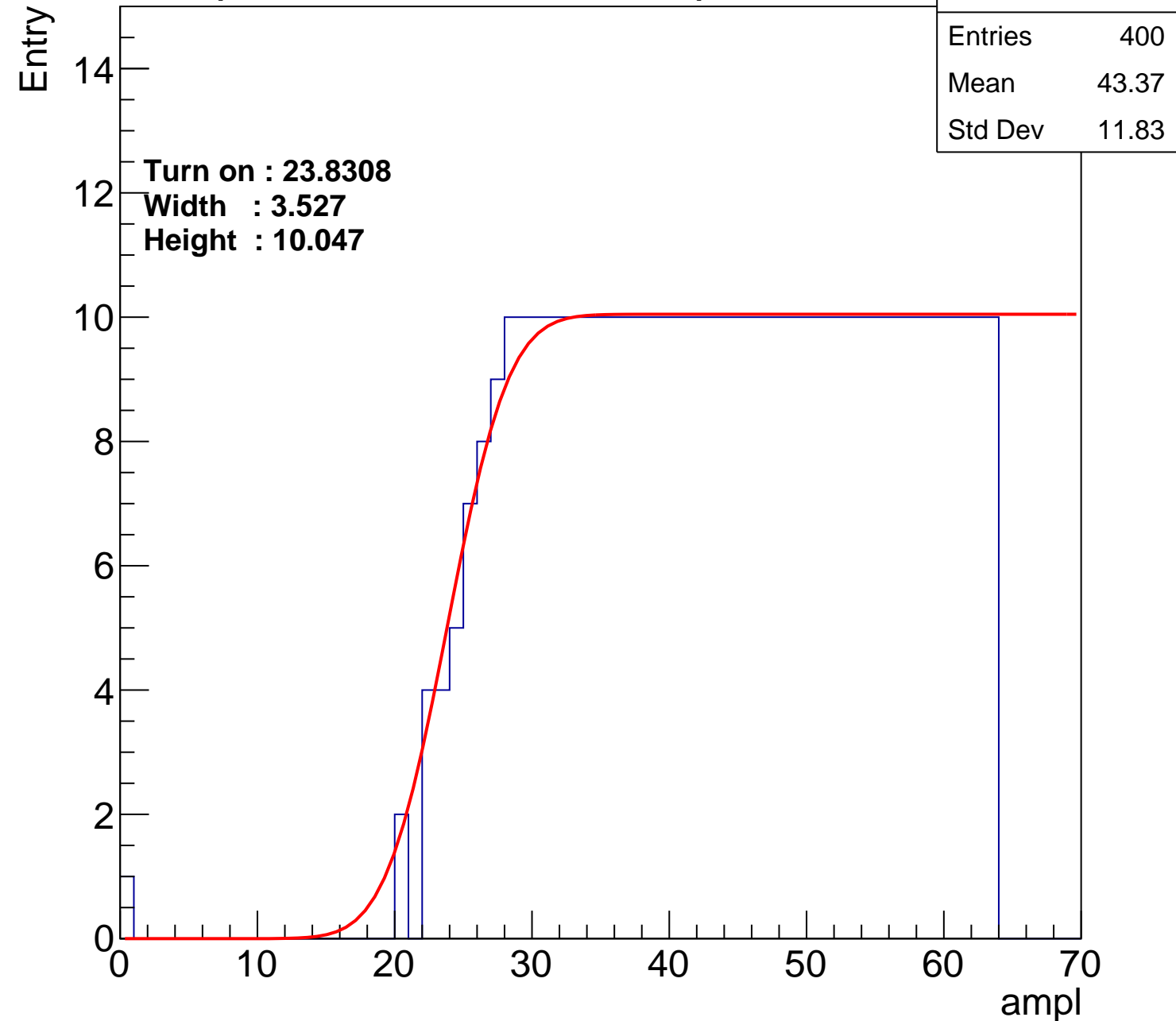
Width : 3.527

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	395
Mean	43.48
Std Dev	12.04

Turn on : 24.7669

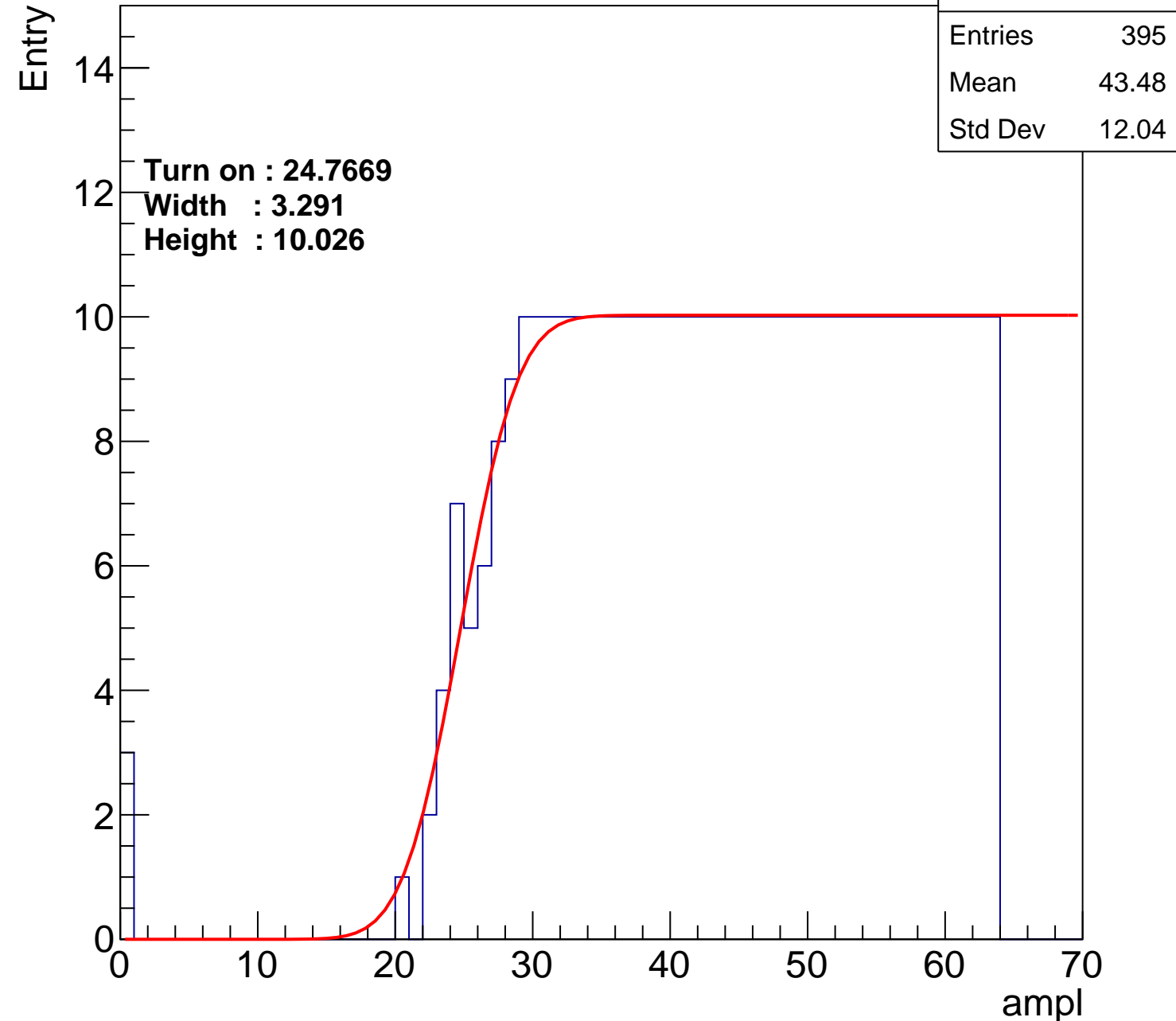
Width : 3.291

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 28.3024

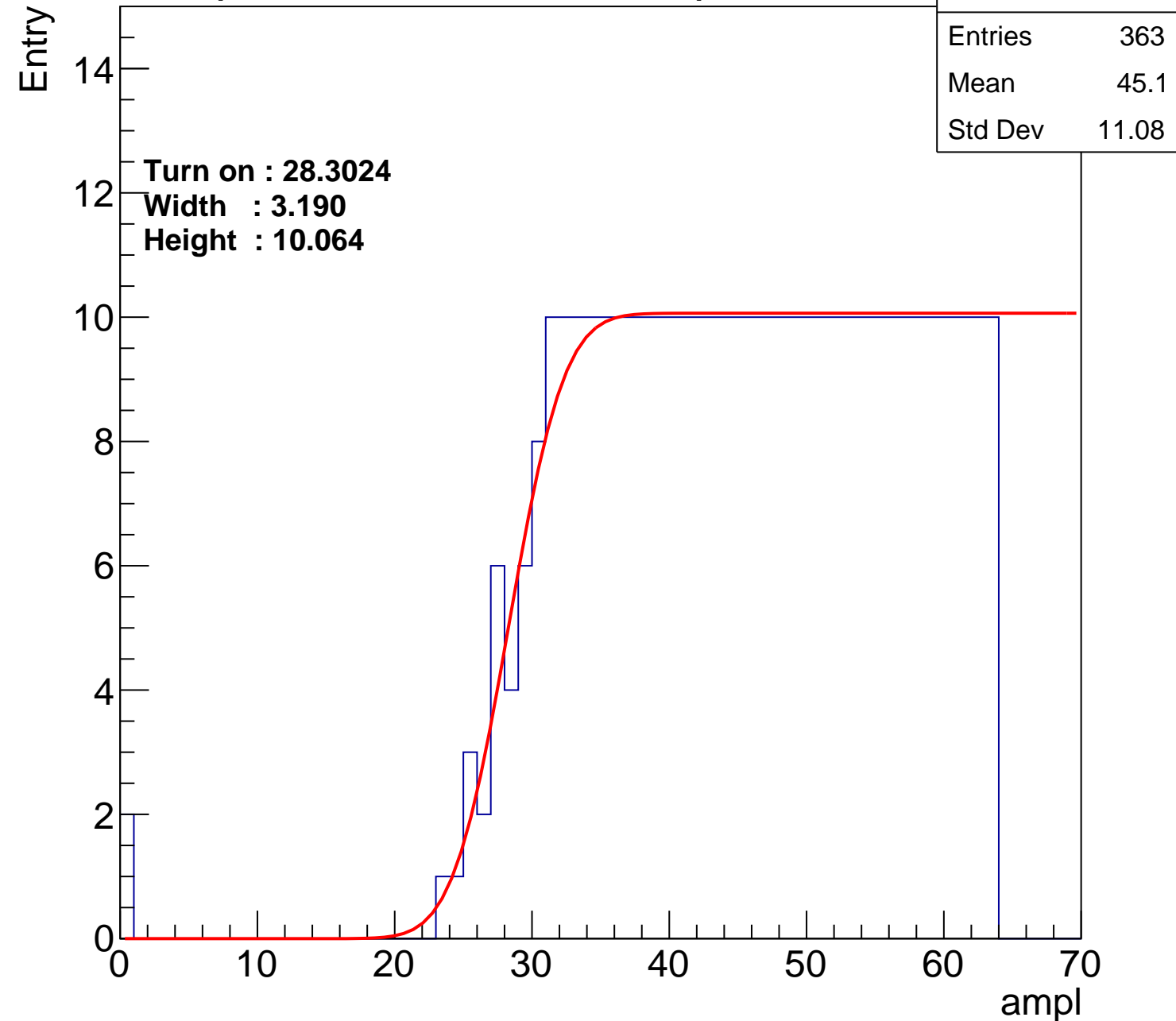
Width : 3.190

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.9985

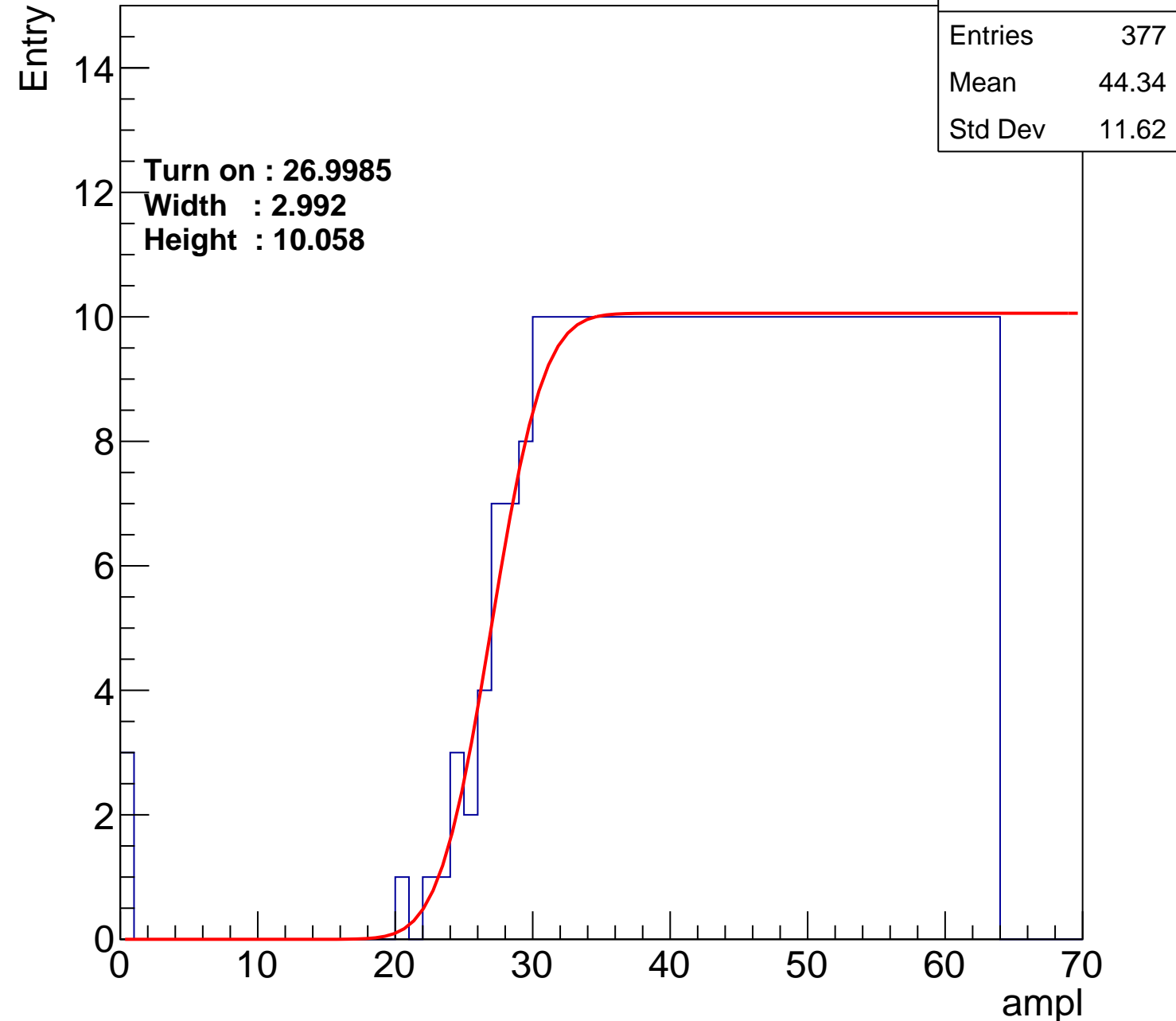
Width : 2.992

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.11
Std Dev	12.03

Turn on : 26.6479

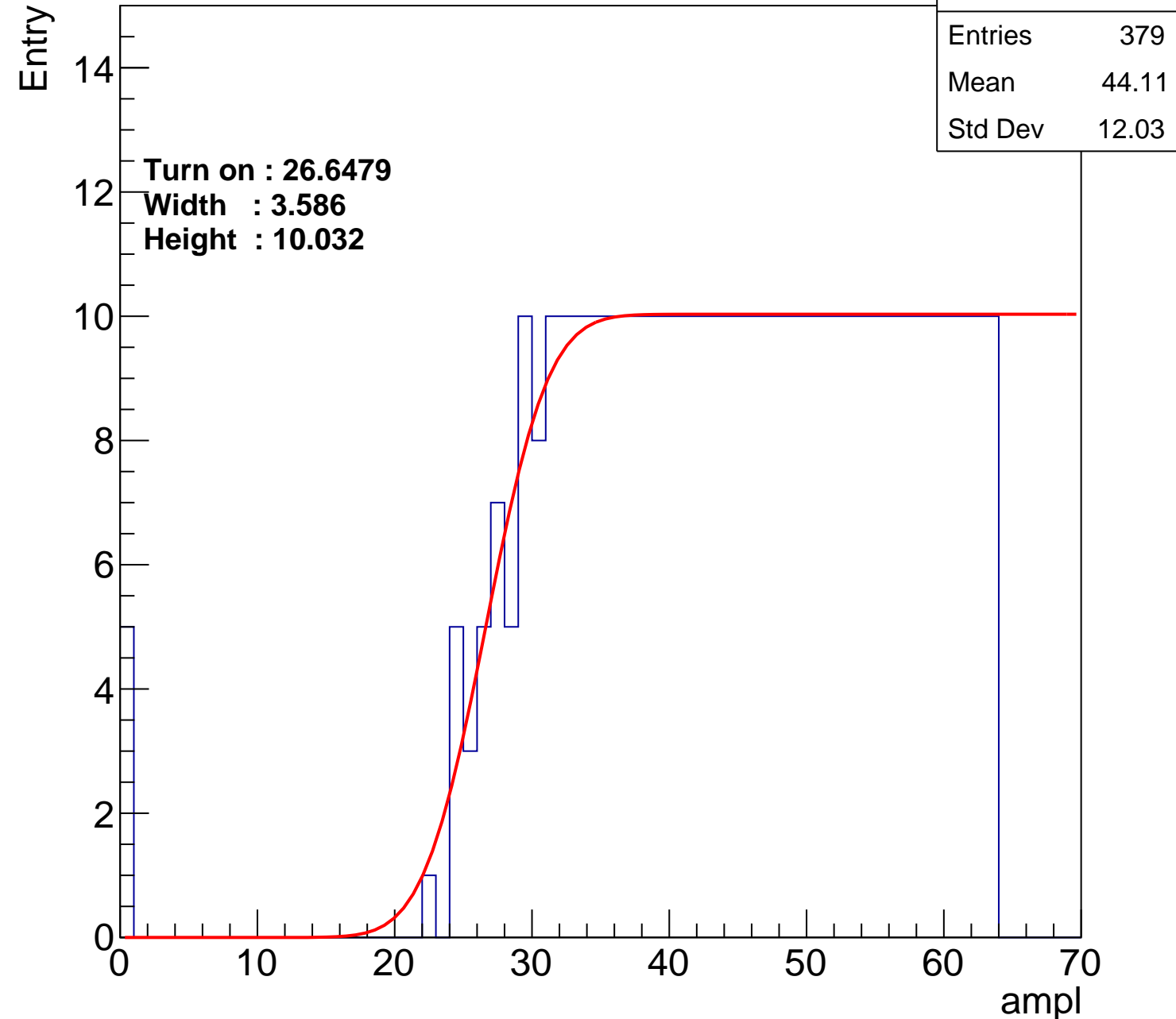
Width : 3.586

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch24

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.9
Std Dev	11.78

Turn on : 25.5771

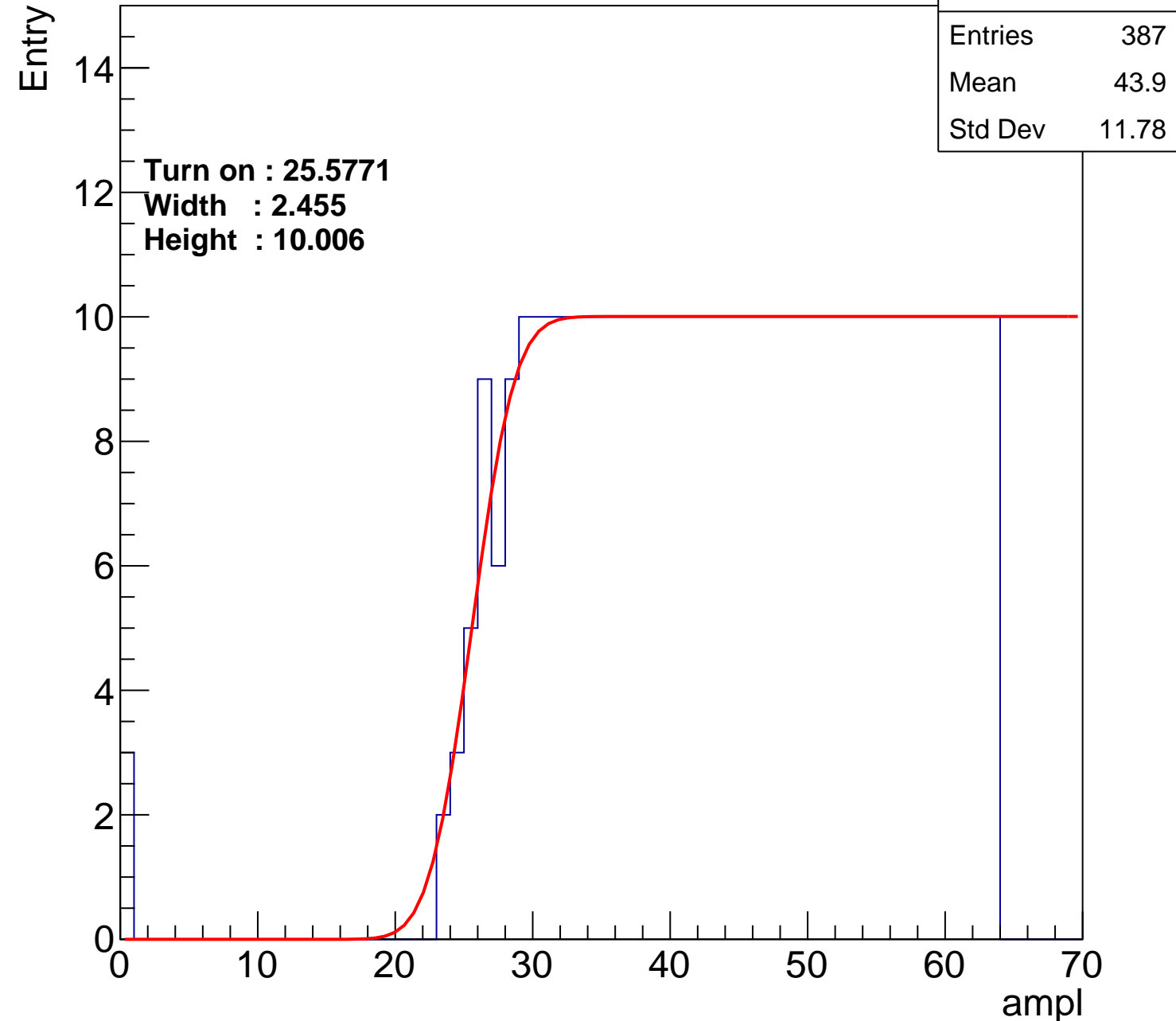
Width : 2.455

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.68
Std Dev	10.8

Turn on : 29.3962

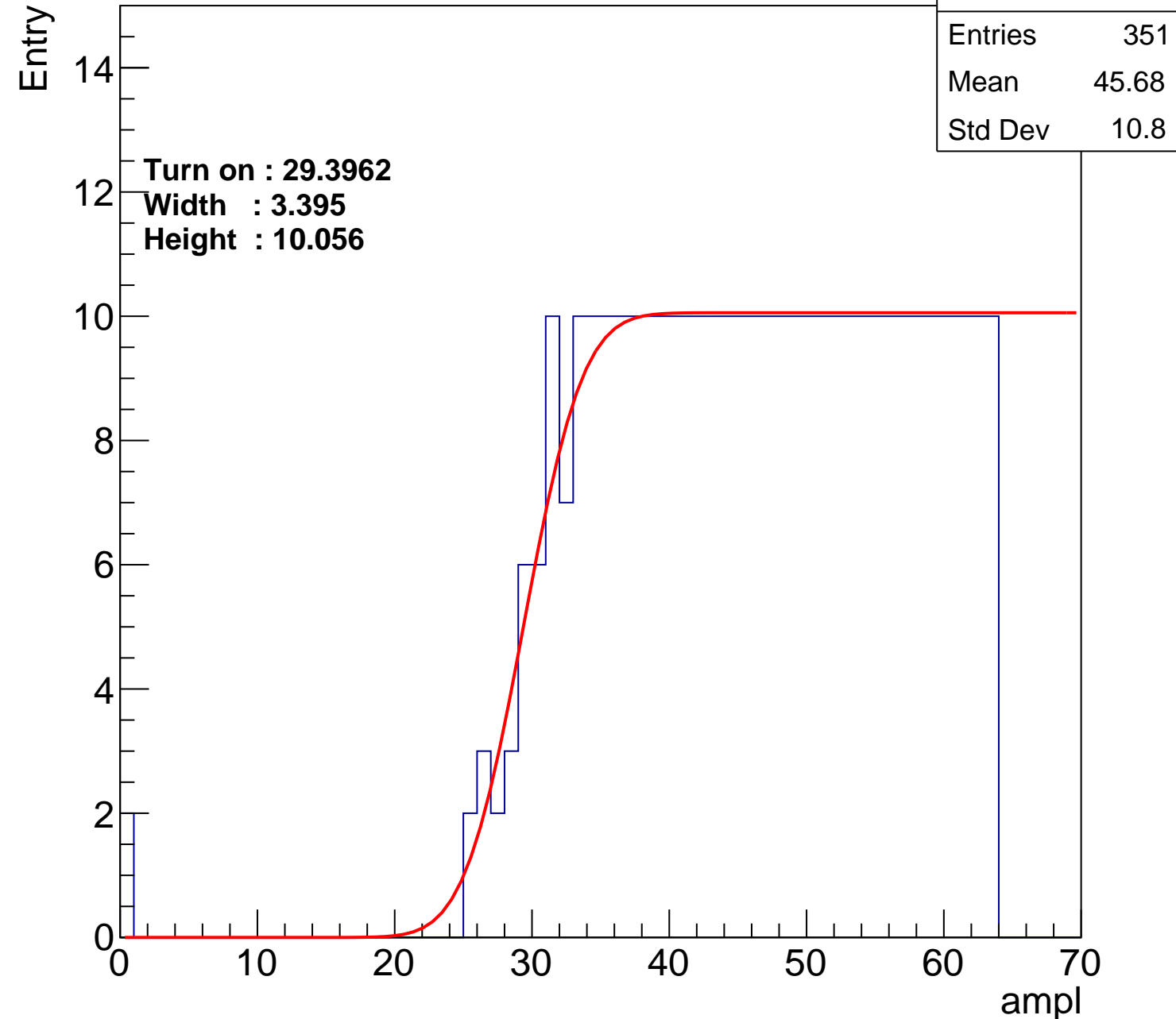
Width : 3.395

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch26

calib_packv5_042523_0143.root, FC#5, port B1

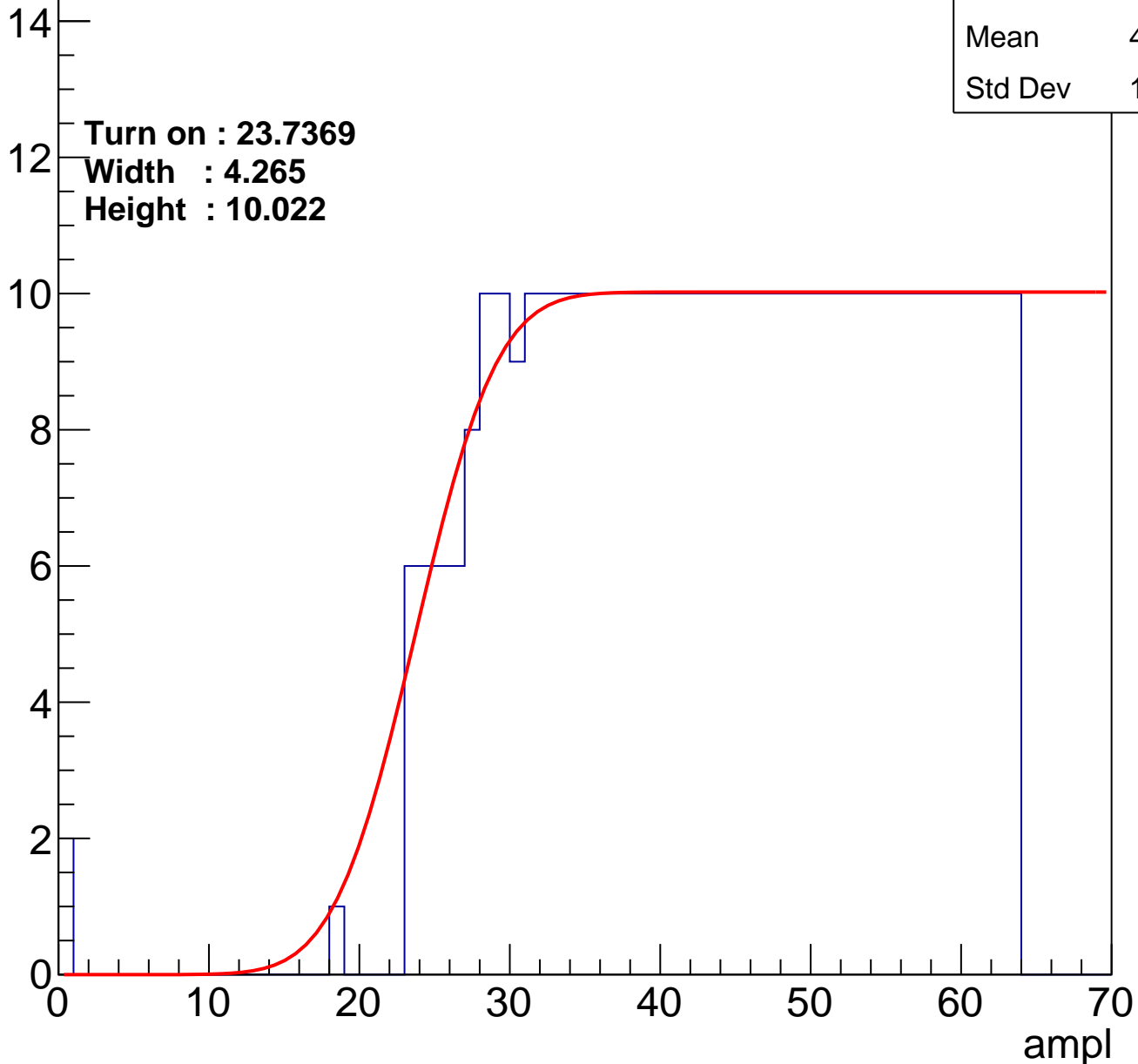
Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 23.7369

Width : 4.265

Height : 10.022

Entry



B0L000S, U21-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	402
Mean	43.19
Std Dev	12.07

Turn on : 24.1556

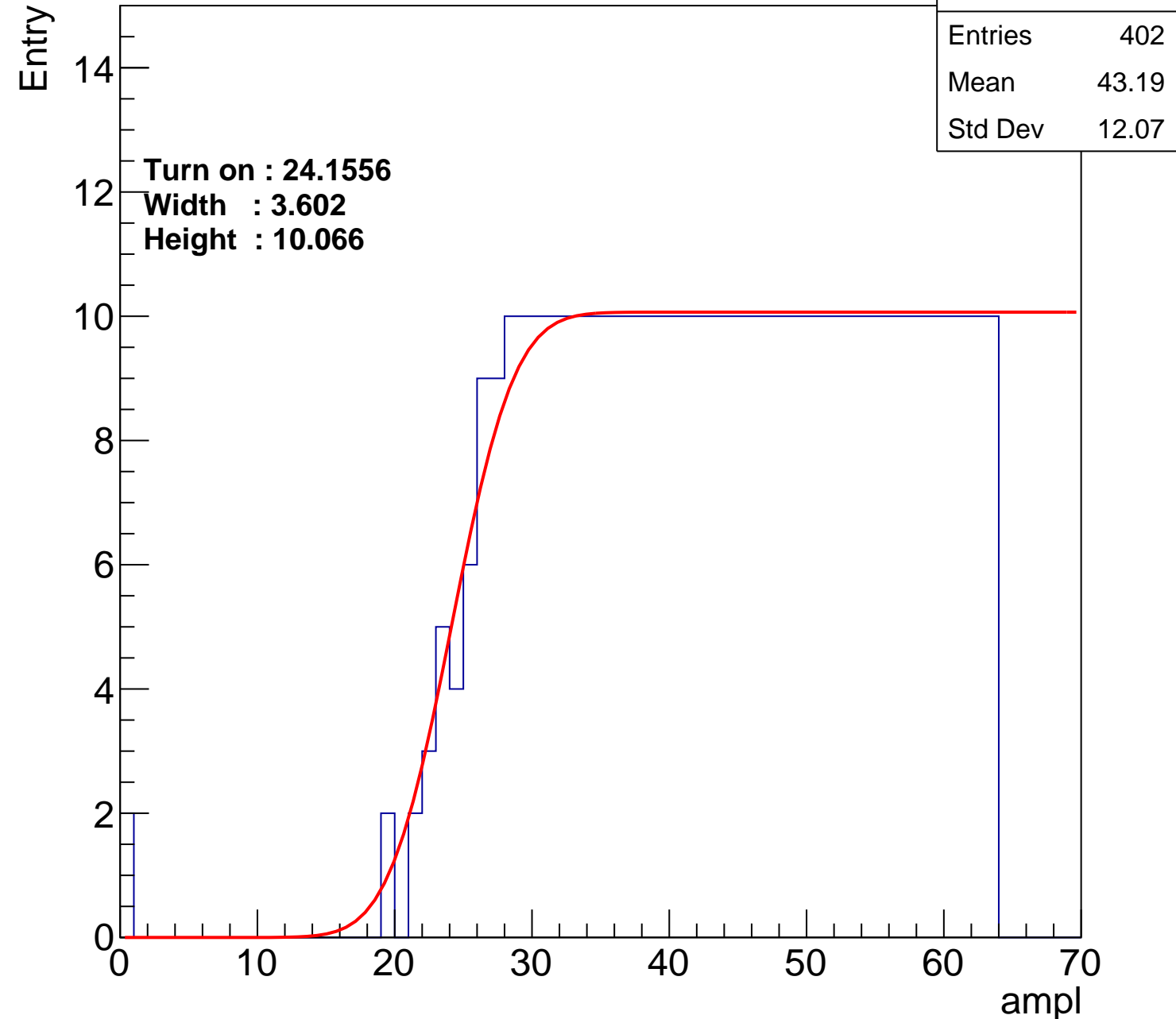
Width : 3.602

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.28
Std Dev	11.61

Turn on : 26.0921

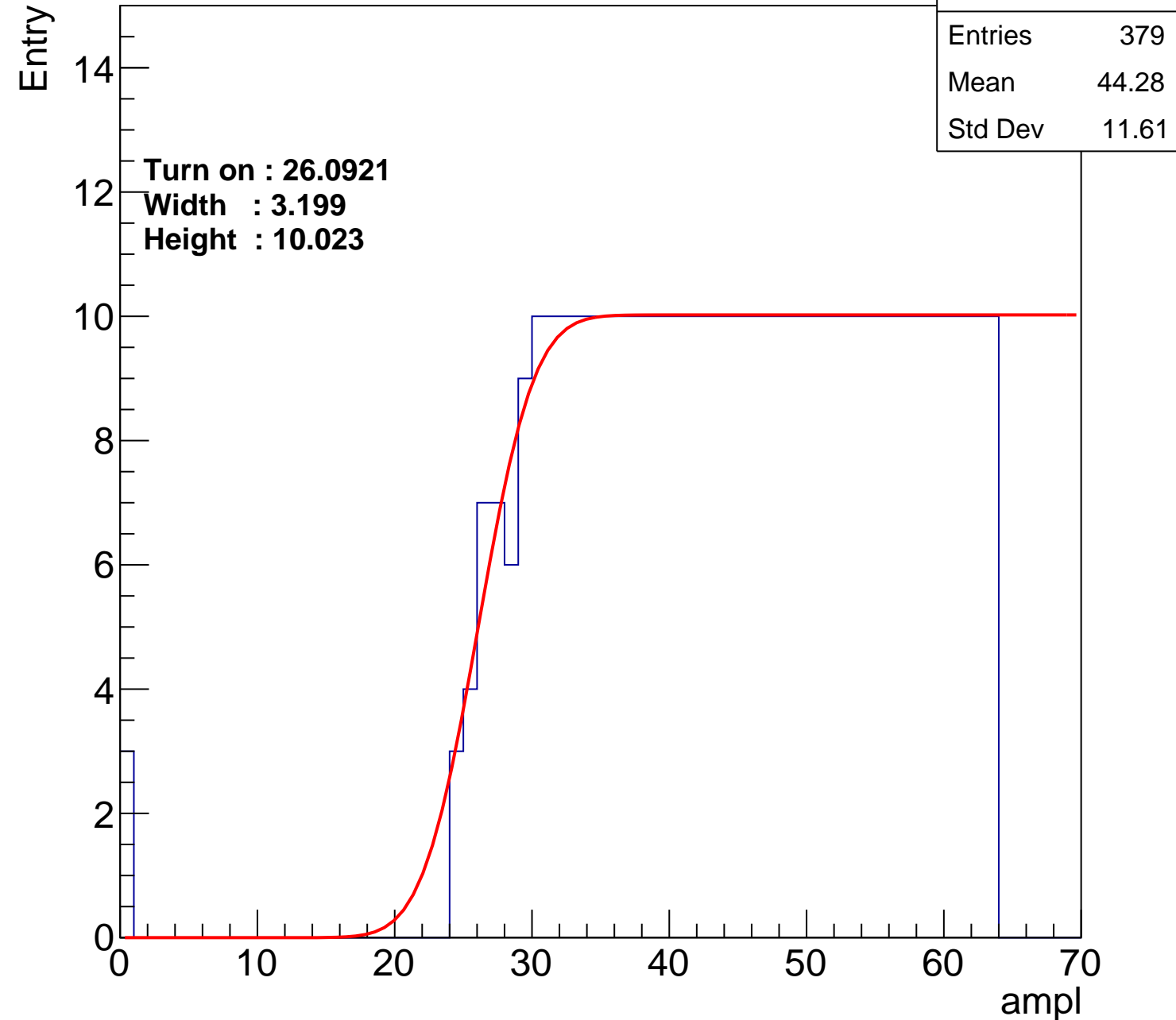
Width : 3.199

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch29

calib_packv5_042523_0143.root, FC#5, port B1

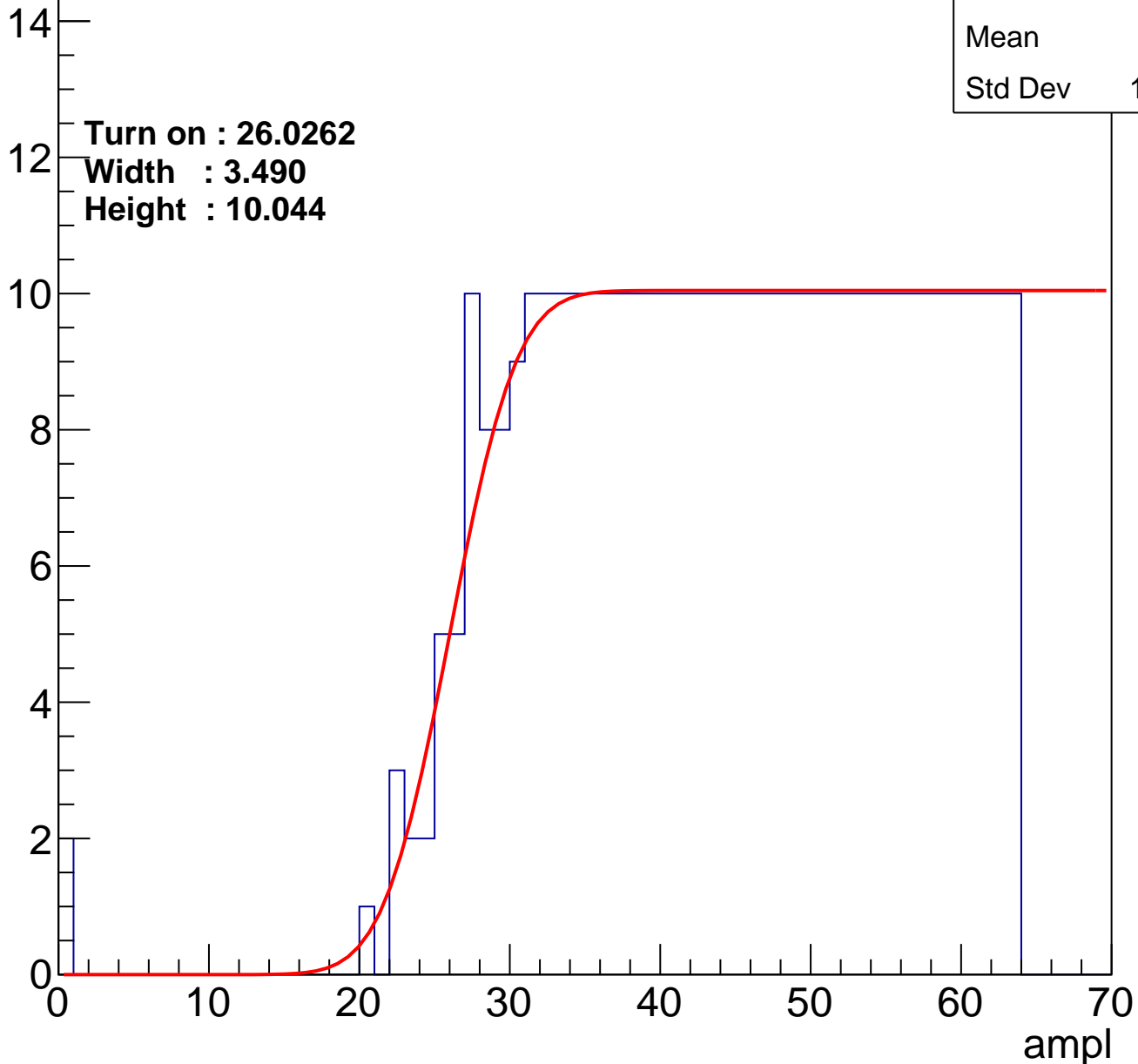
Entries	385
Mean	44
Std Dev	11.67

Turn on : 26.0262

Width : 3.490

Height : 10.044

Entry



B0L000S, U21-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.11
Std Dev	11.26

Turn on : 27.8523

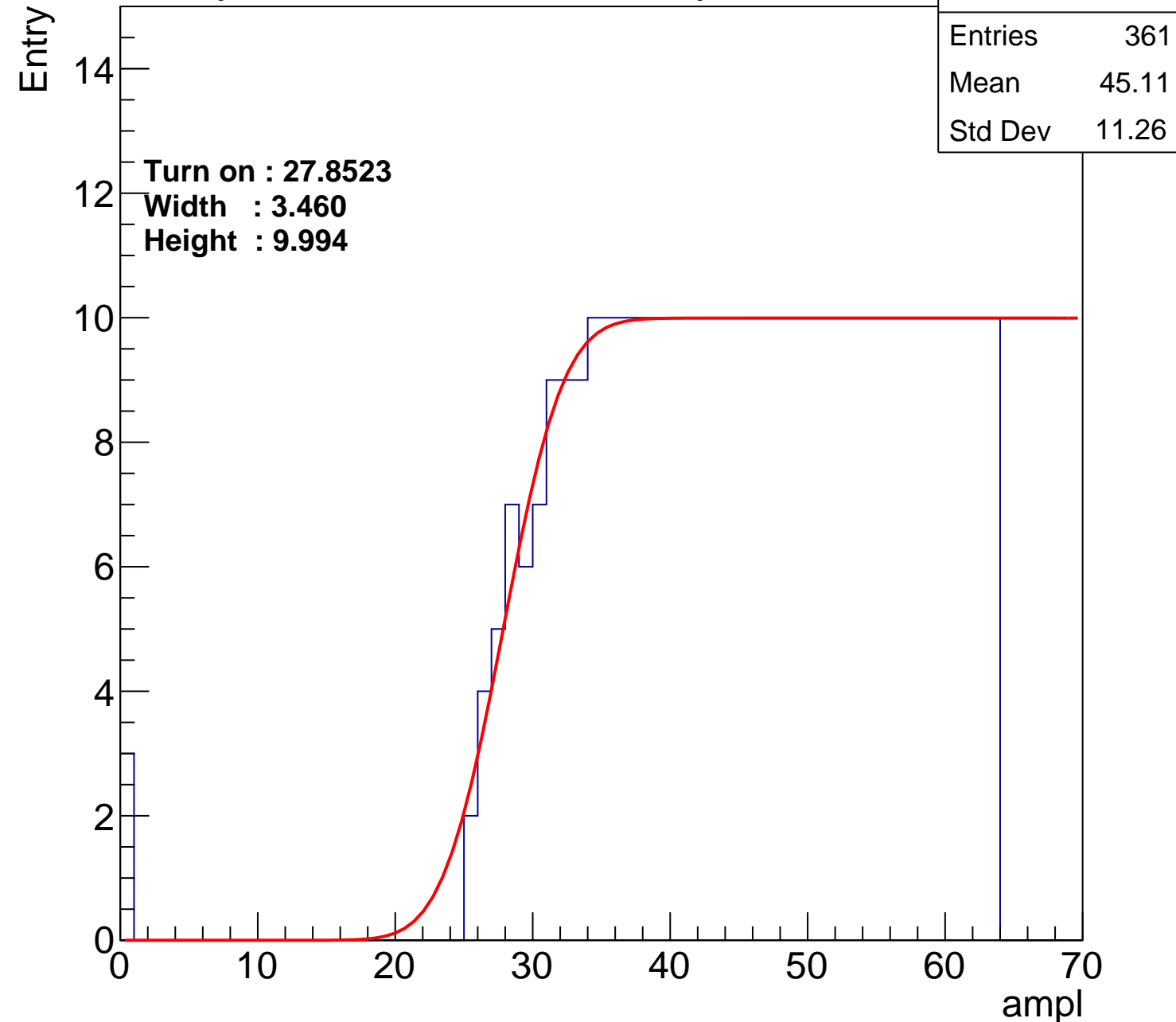
Width : 3.460

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch31

calib_packv5_042523_0143.root, FC#5, port B1

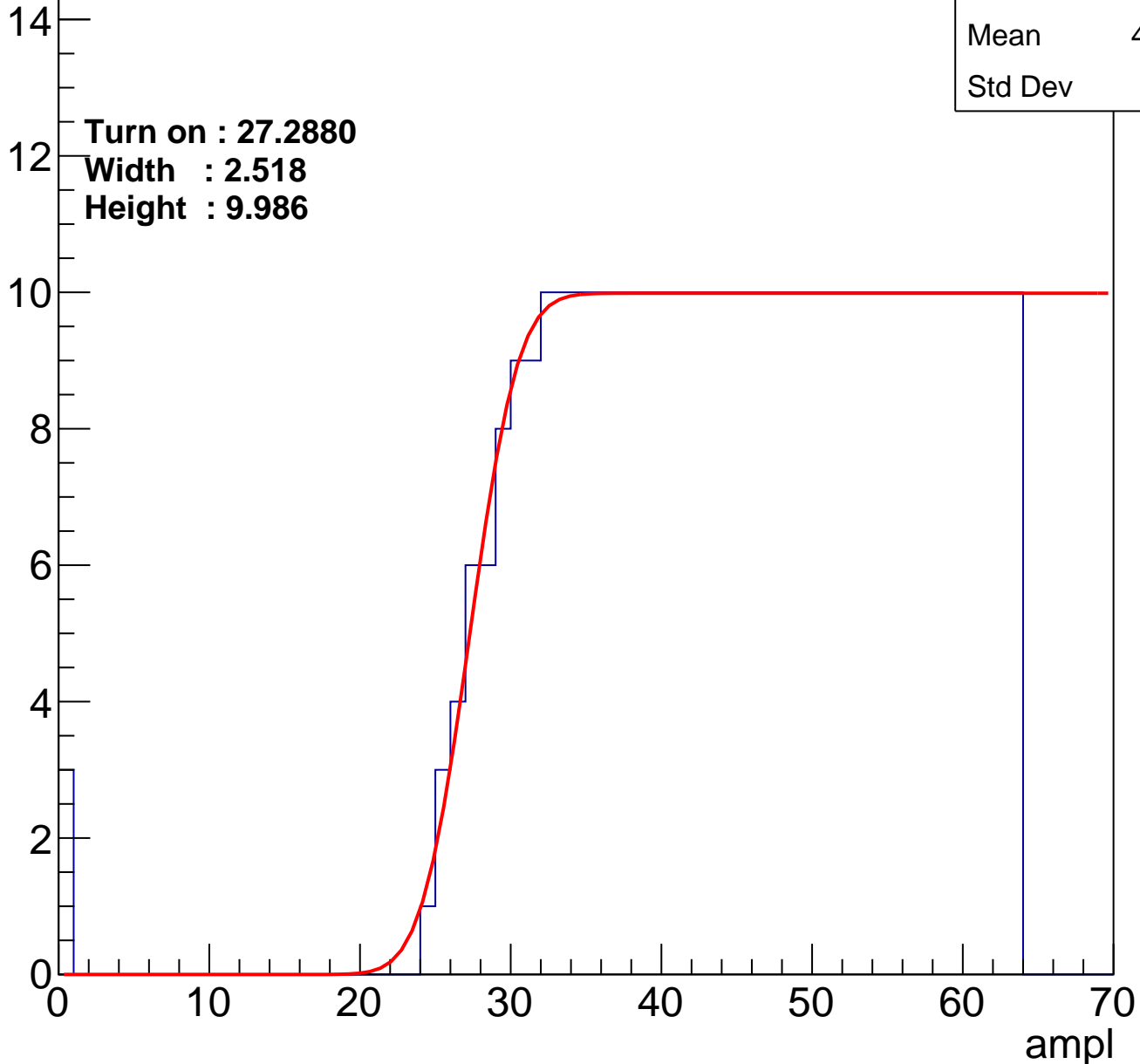
Entries	369
Mean	44.75
Std Dev	11.4

Turn on : 27.2880

Width : 2.518

Height : 9.986

Entry



B0L000S, U21-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.58
Std Dev	11.14

Turn on : 26.3907

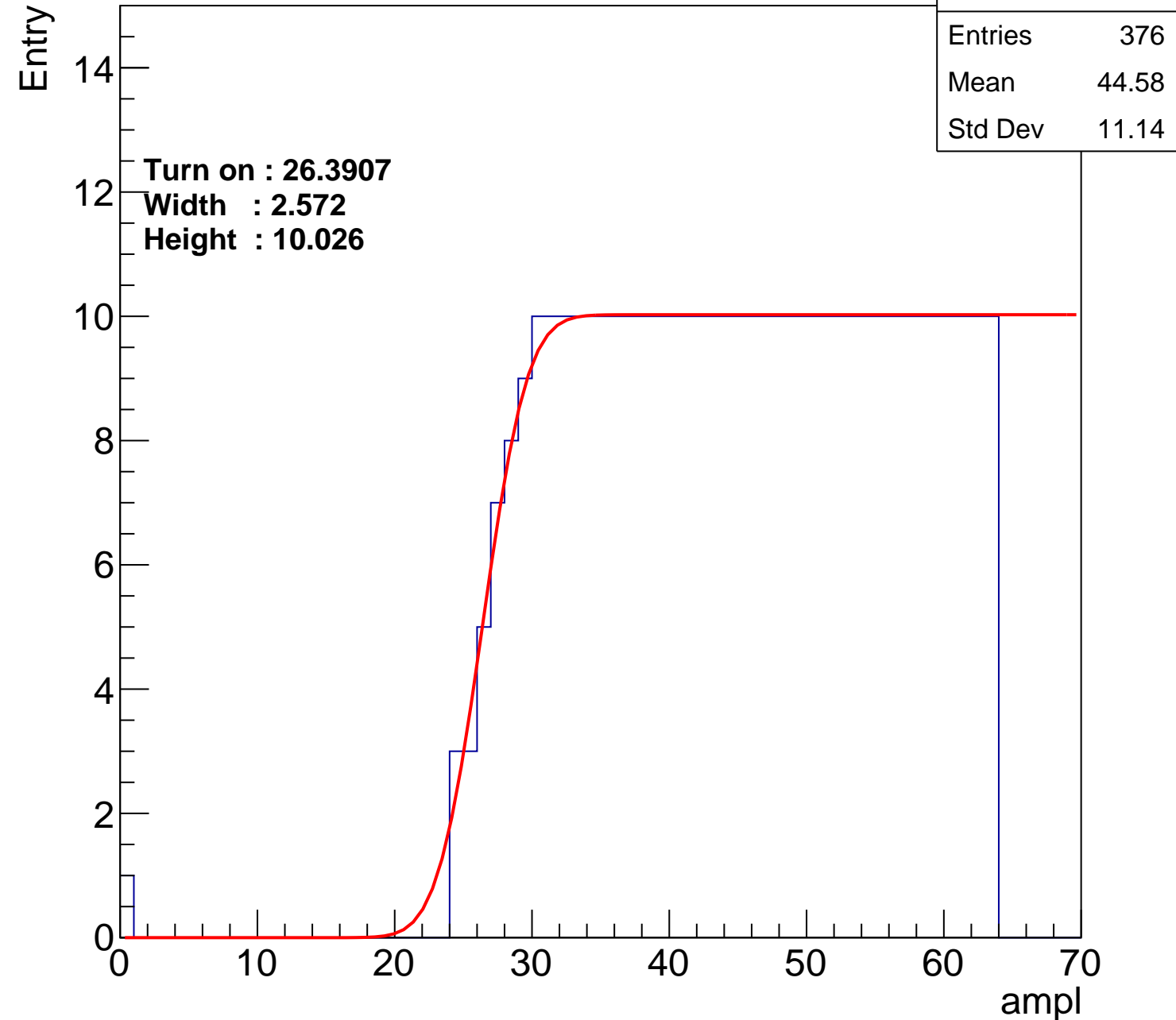
Width : 2.572

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch33

calib_packv5_042523_0143.root, FC#5, port B1

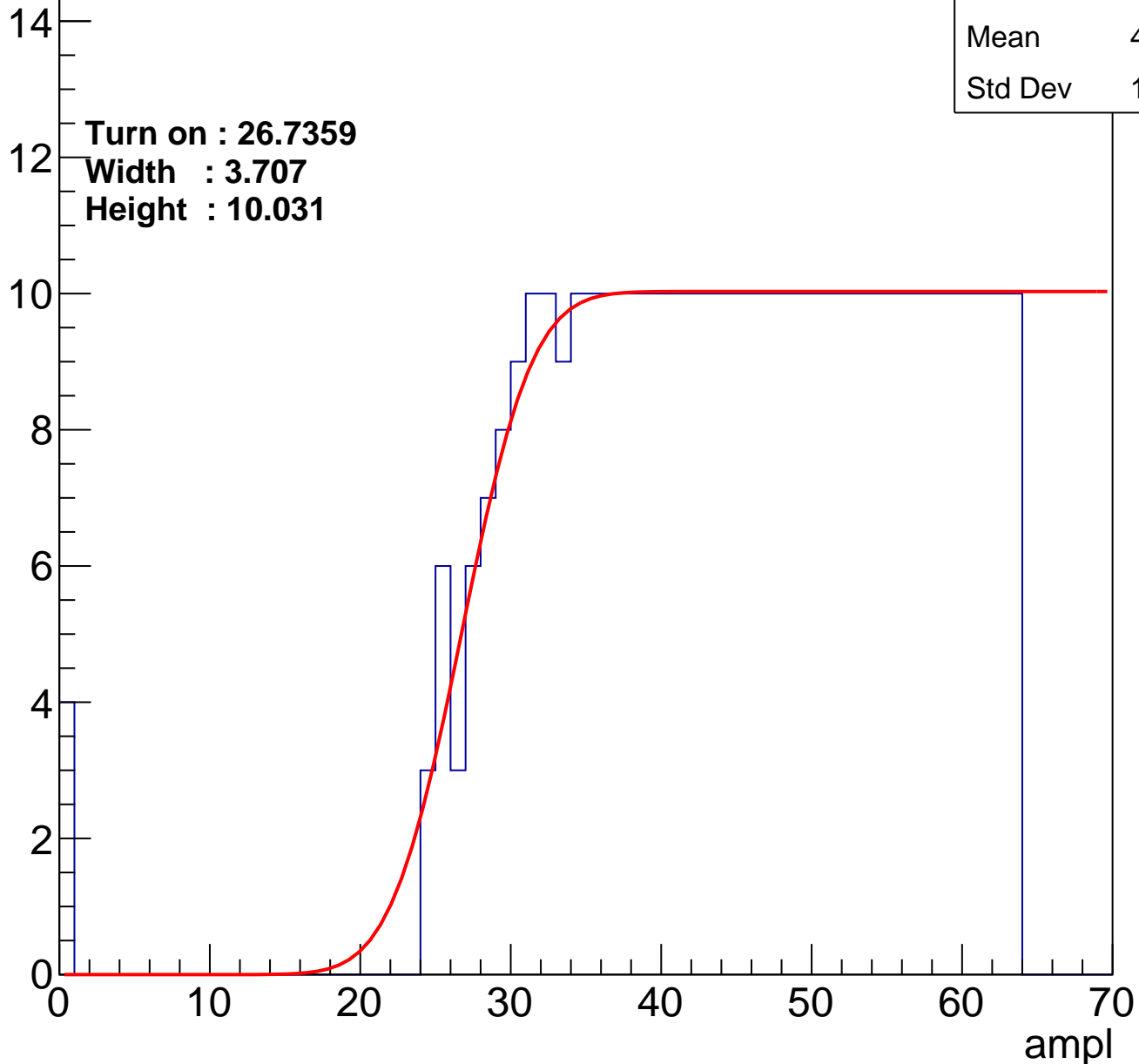
Entries	375
Mean	44.37
Std Dev	11.76

Turn on : 26.7359

Width : 3.707

Height : 10.031

Entry



B0L000S, U21-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 28.1765

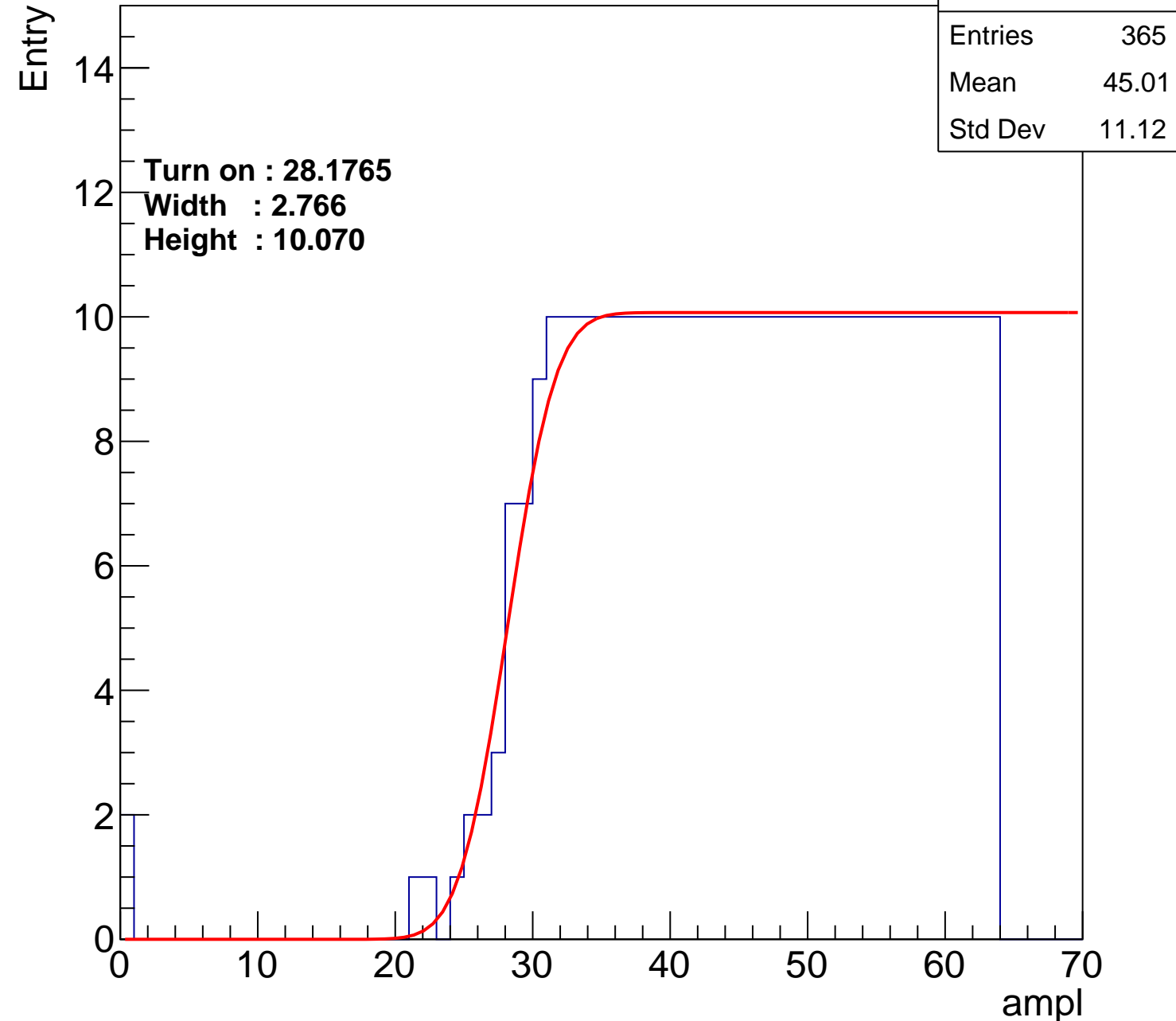
Width : 2.766

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch35

calib_packv5_042523_0143.root, FC#5, port B1

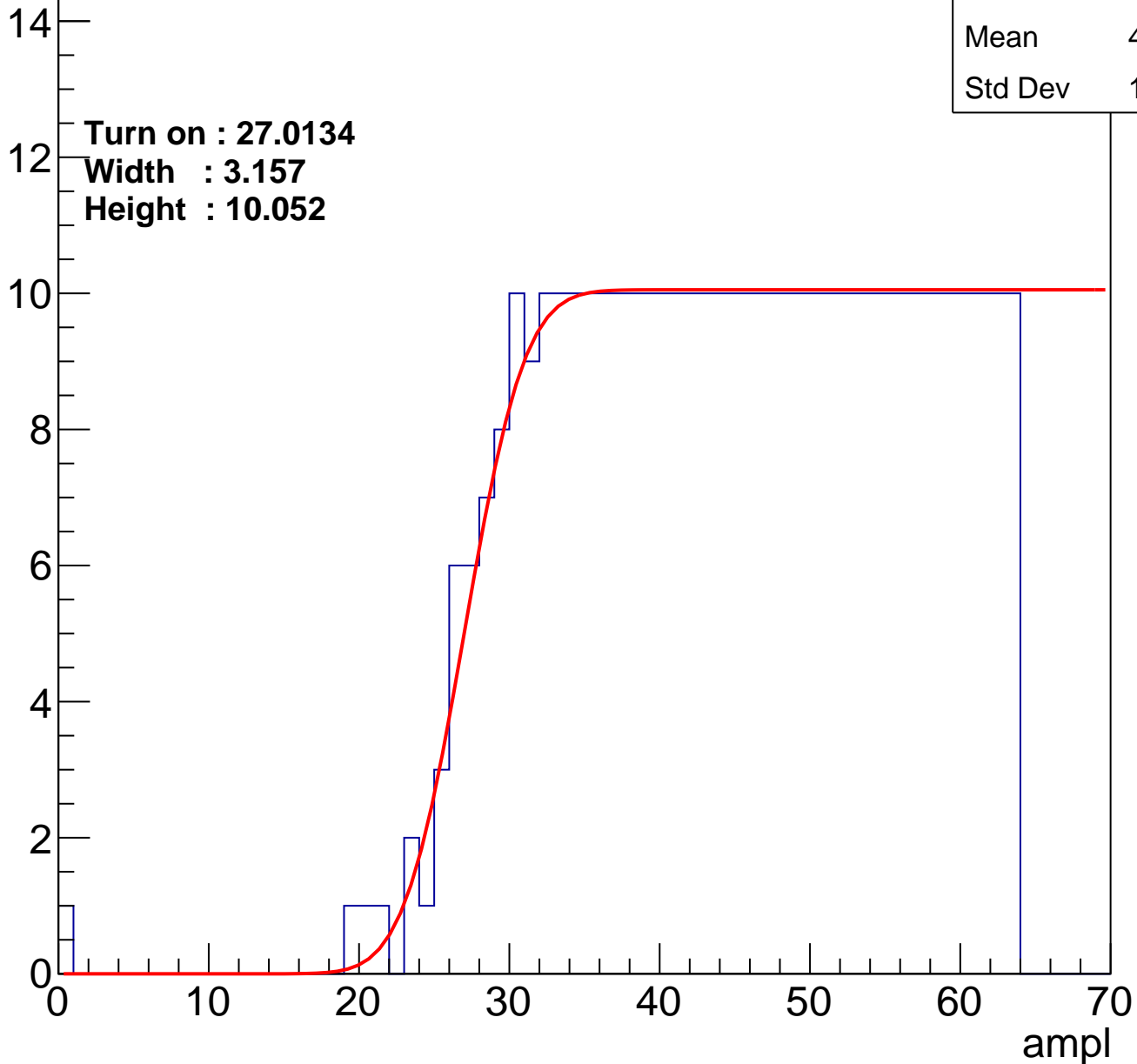
Entries	376
Mean	44.49
Std Dev	11.29

Turn on : 27.0134

Width : 3.157

Height : 10.052

Entry



B0L000S, U21-ch36

calib_packv5_042523_0143.root, FC#5, port B1

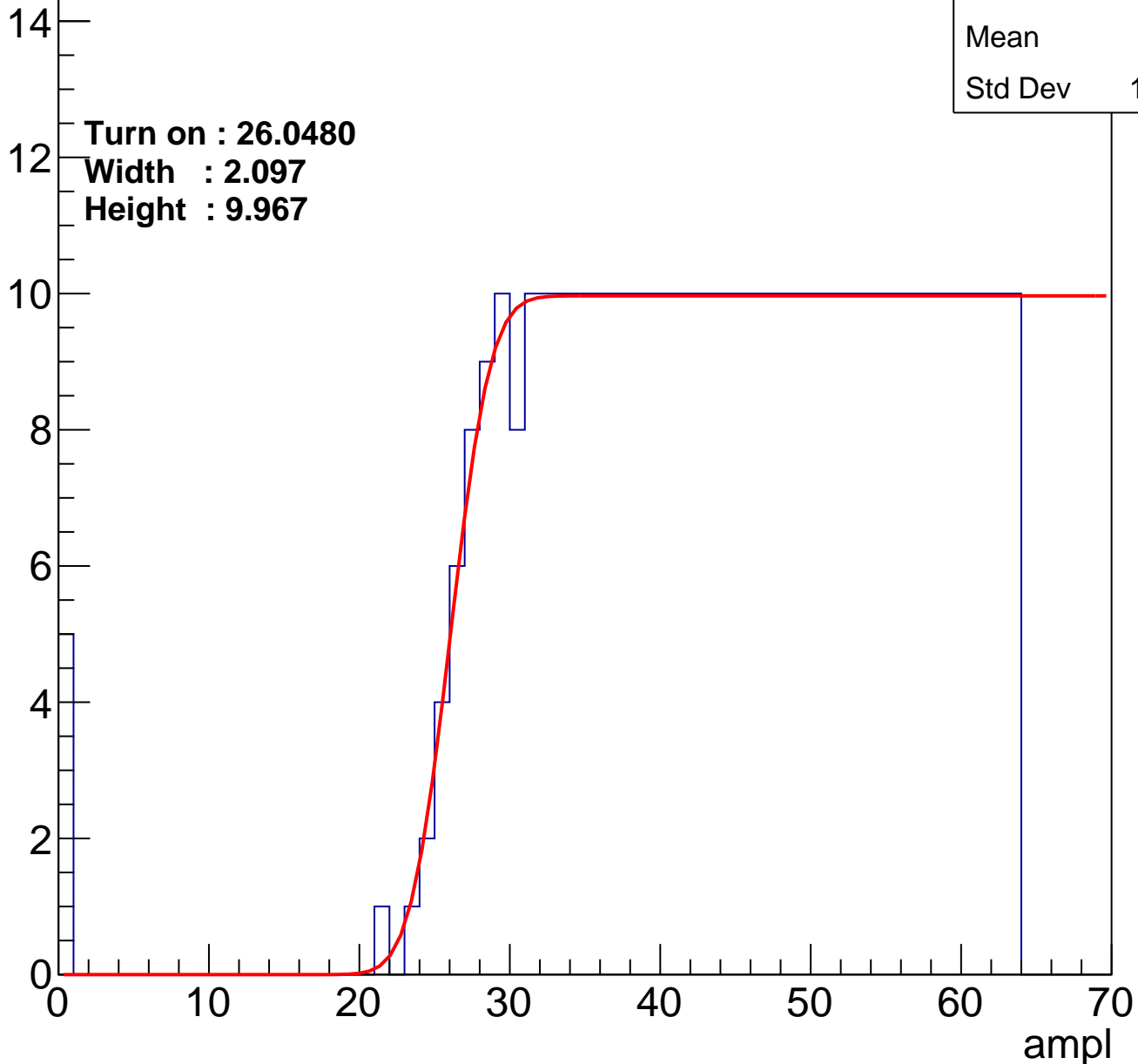
Entries	384
Mean	43.9
Std Dev	12.09

Turn on : 26.0480

Width : 2.097

Height : 9.967

Entry



B0L000S, U21-ch37

calib_packv5_042523_0143.root, FC#5, port B1

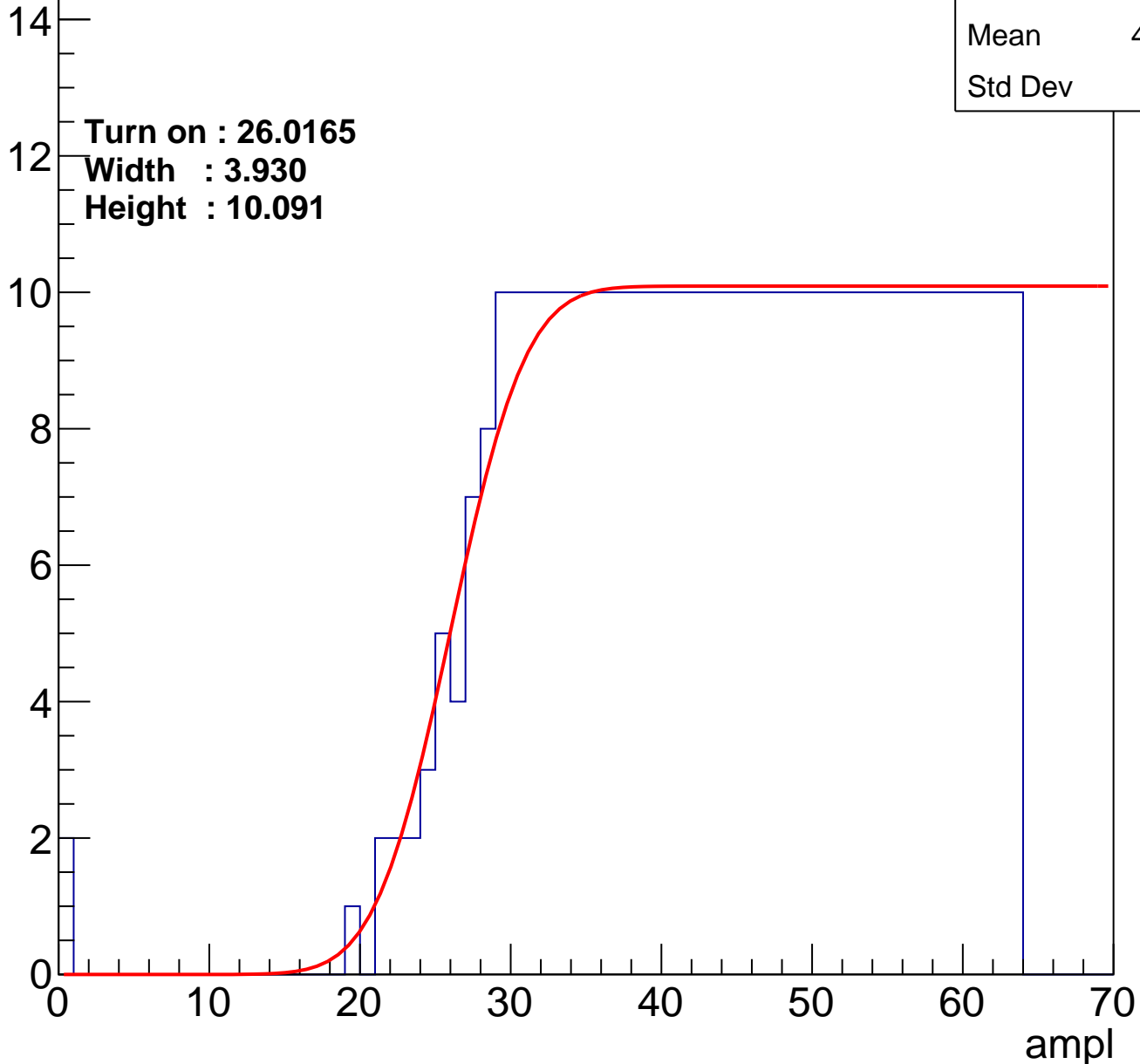
Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 26.0165

Width : 3.930

Height : 10.091

Entry



B0L000S, U21-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.0847

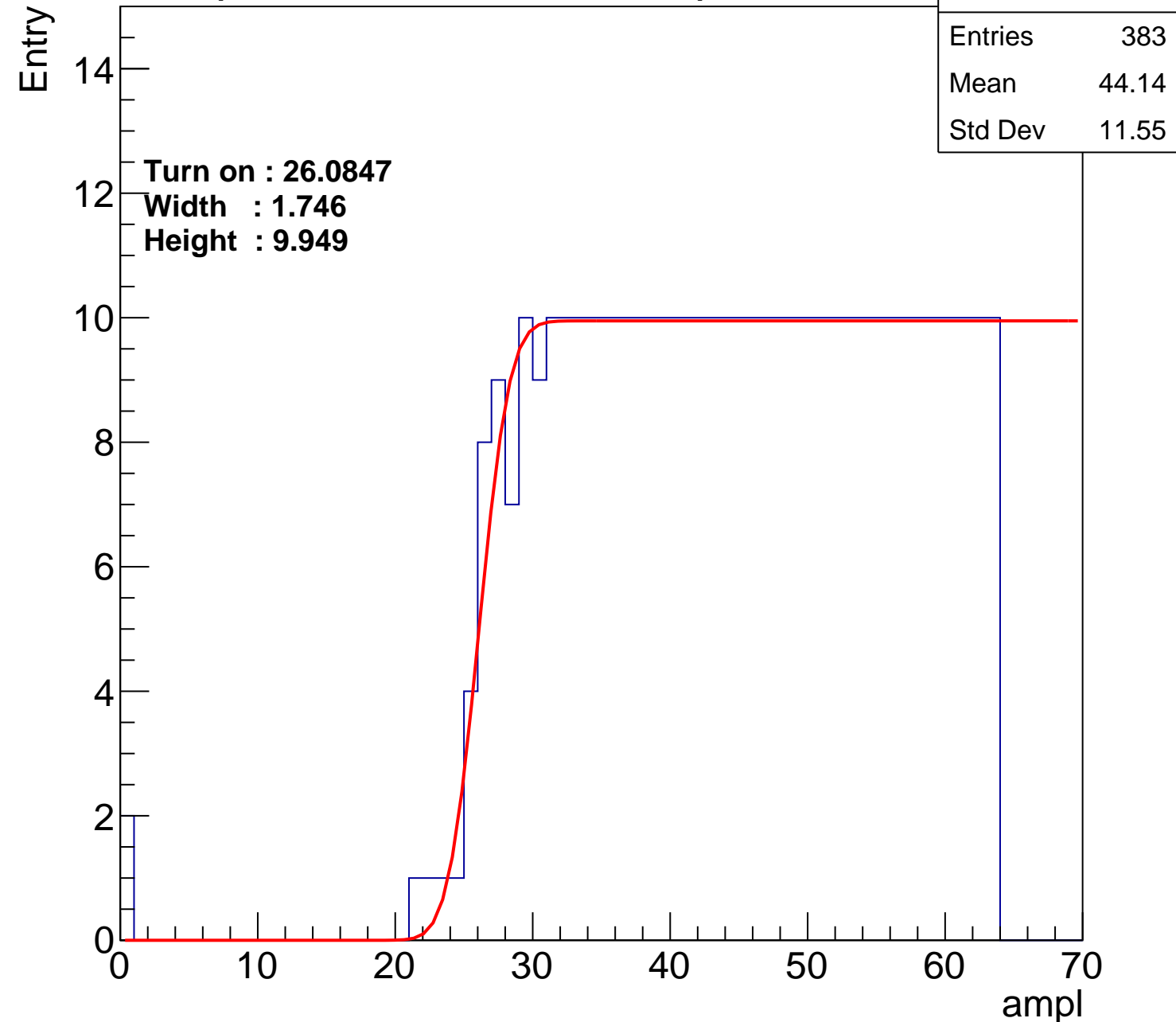
Width : 1.746

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch39

calib_packv5_042523_0143.root, FC#5, port B1

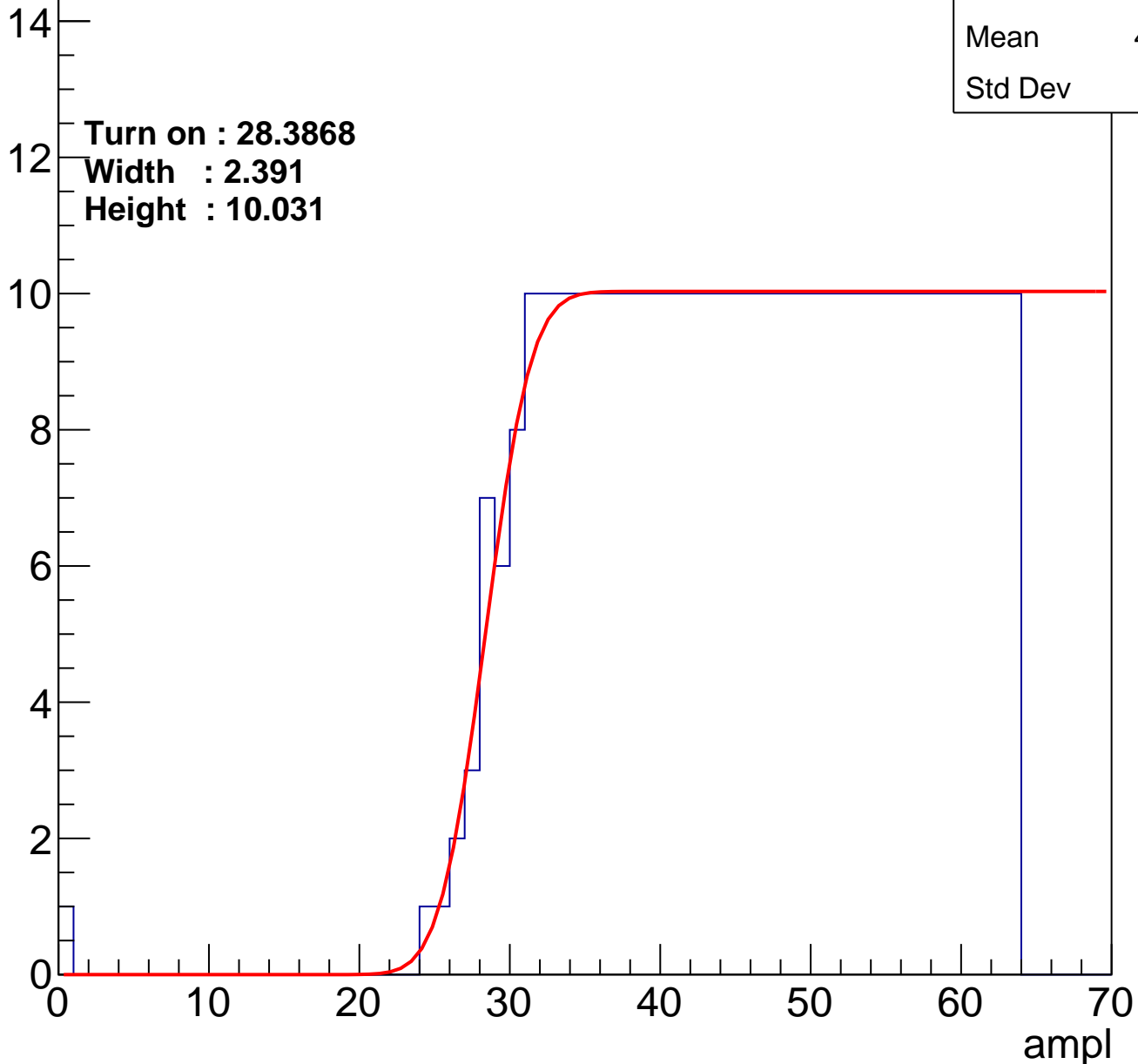
Entries	359
Mean	45.41
Std Dev	10.7

Turn on : 28.3868

Width : 2.391

Height : 10.031

Entry



B0L000S, U21-ch40

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.76
Std Dev	11.24

Turn on : 26.9082

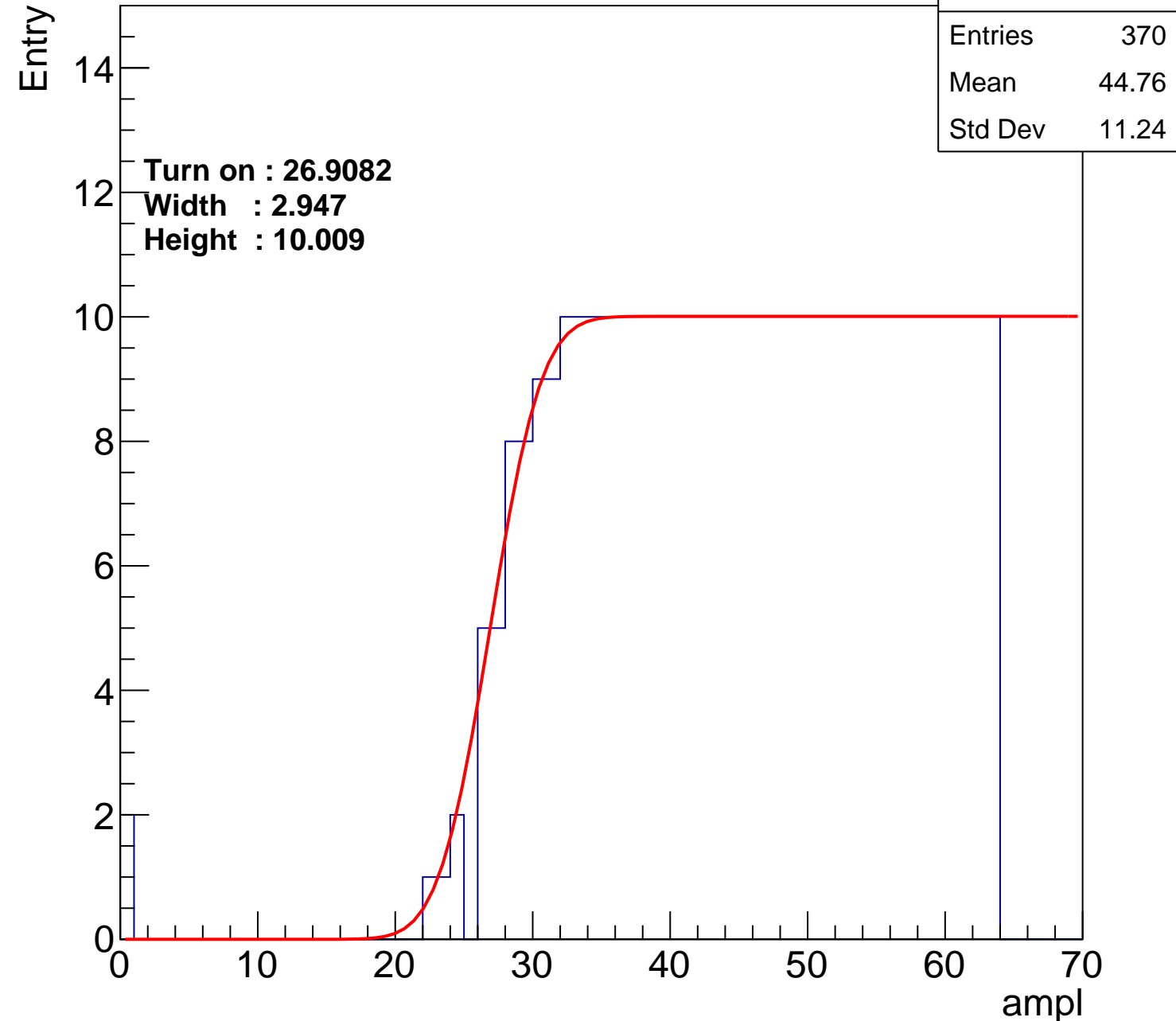
Width : 2.947

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch41

calib_packv5_042523_0143.root, FC#5, port B1

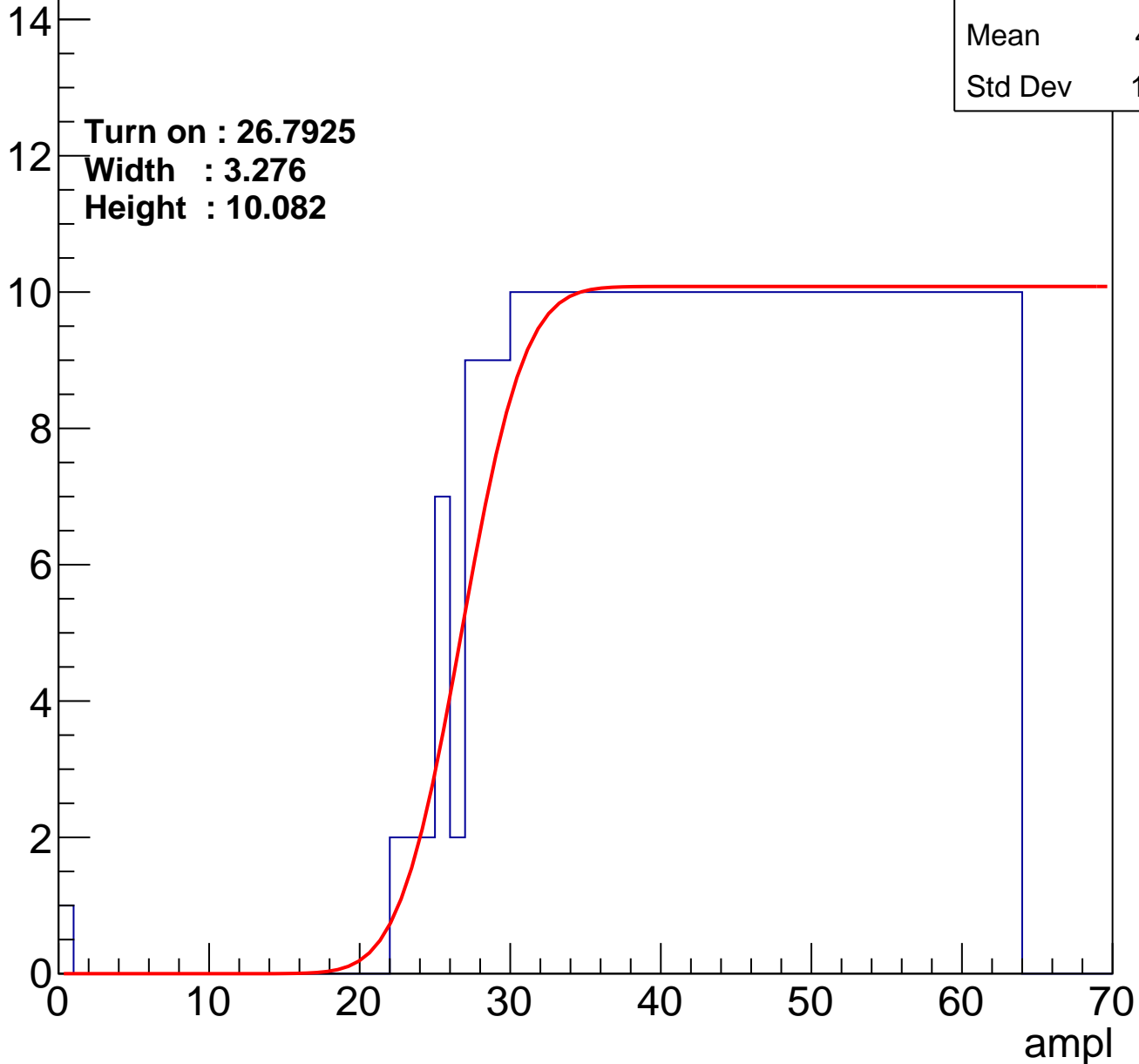
Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.7925

Width : 3.276

Height : 10.082

Entry



B0L000S, U21-ch42

calib_packv5_042523_0143.root, FC#5, port B1

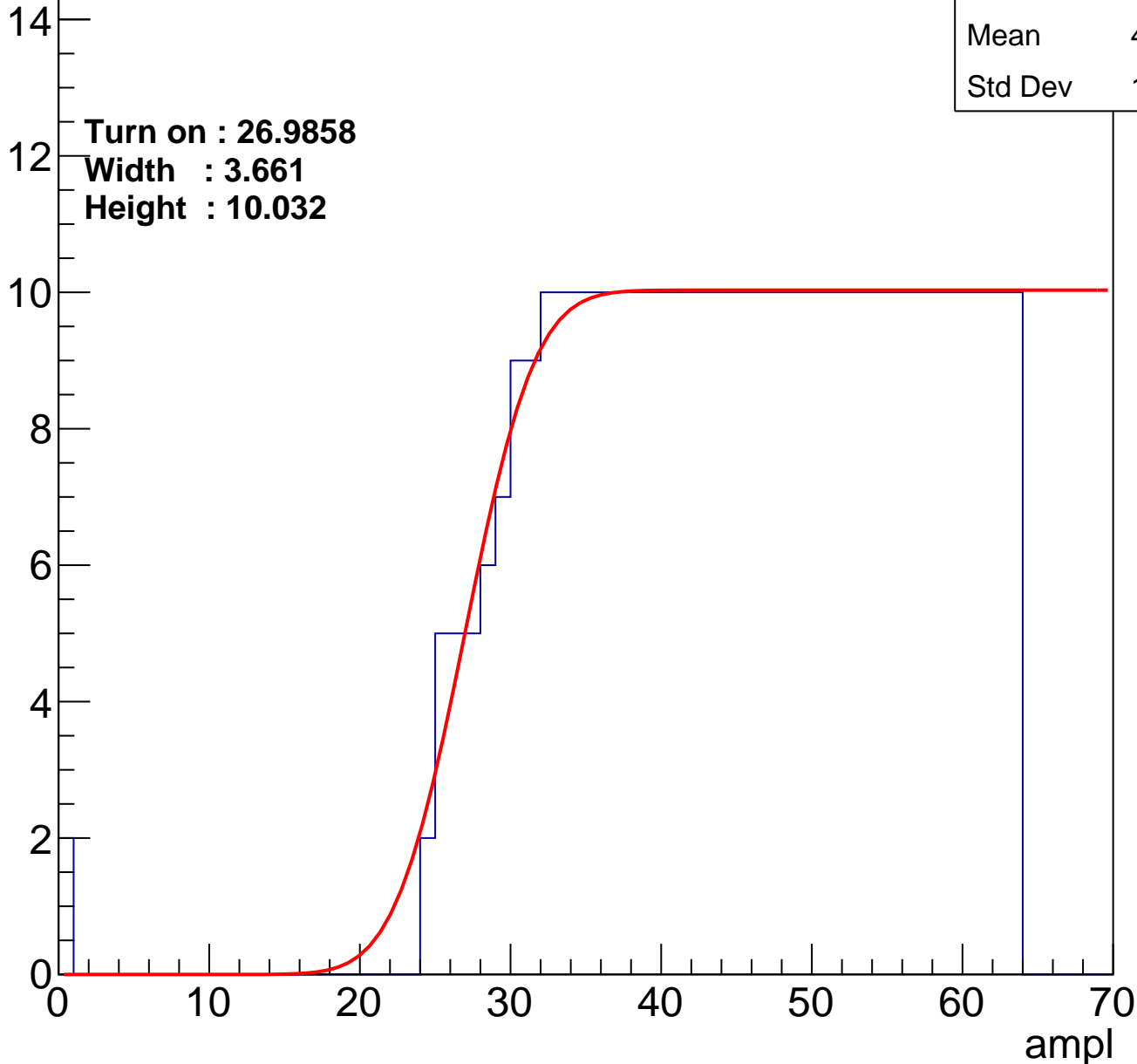
Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 26.9858

Width : 3.661

Height : 10.032

Entry



B0L000S, U21-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.6023

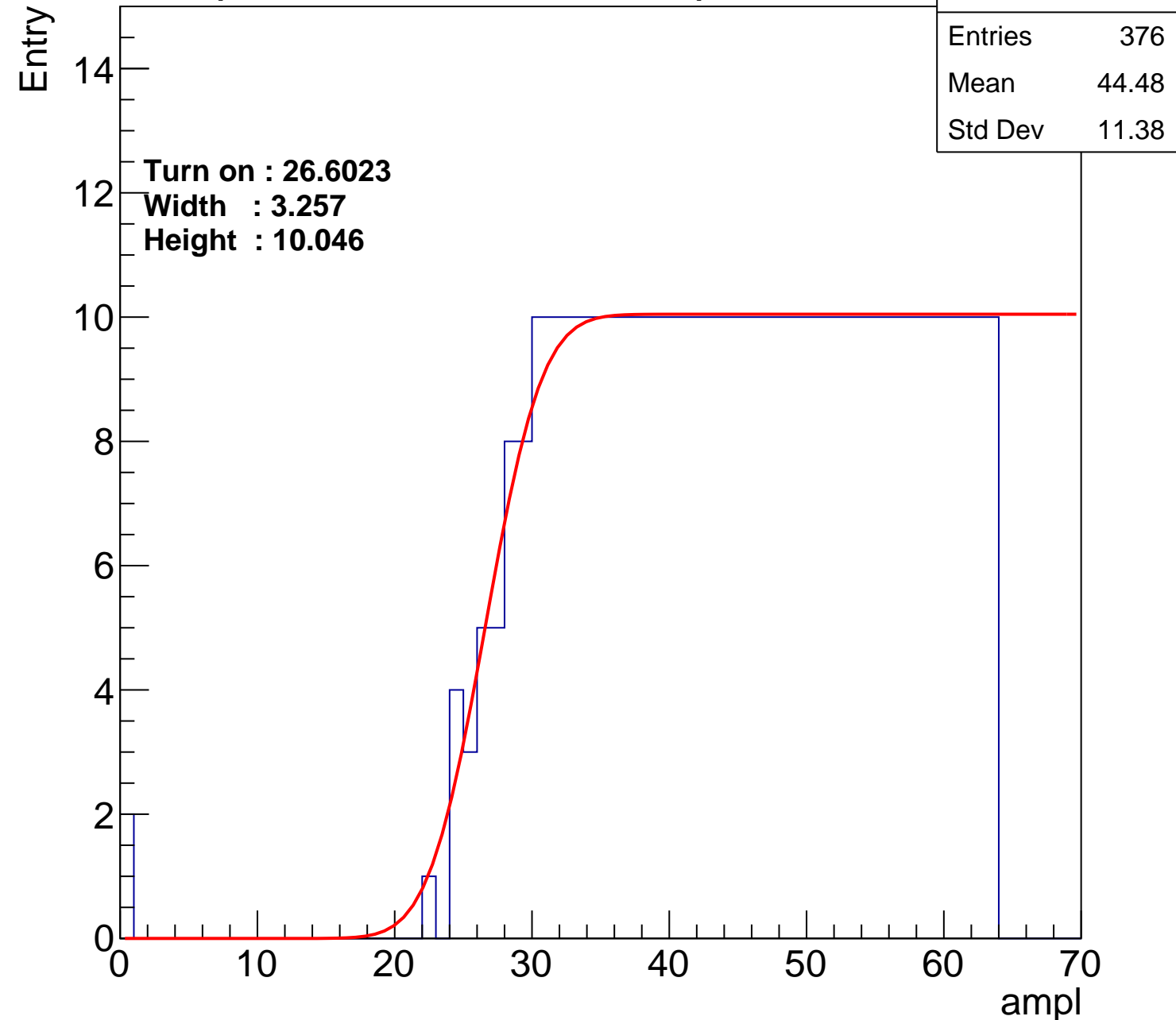
Width : 3.257

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.79
Std Dev	12.16

Turn on : 26.2395

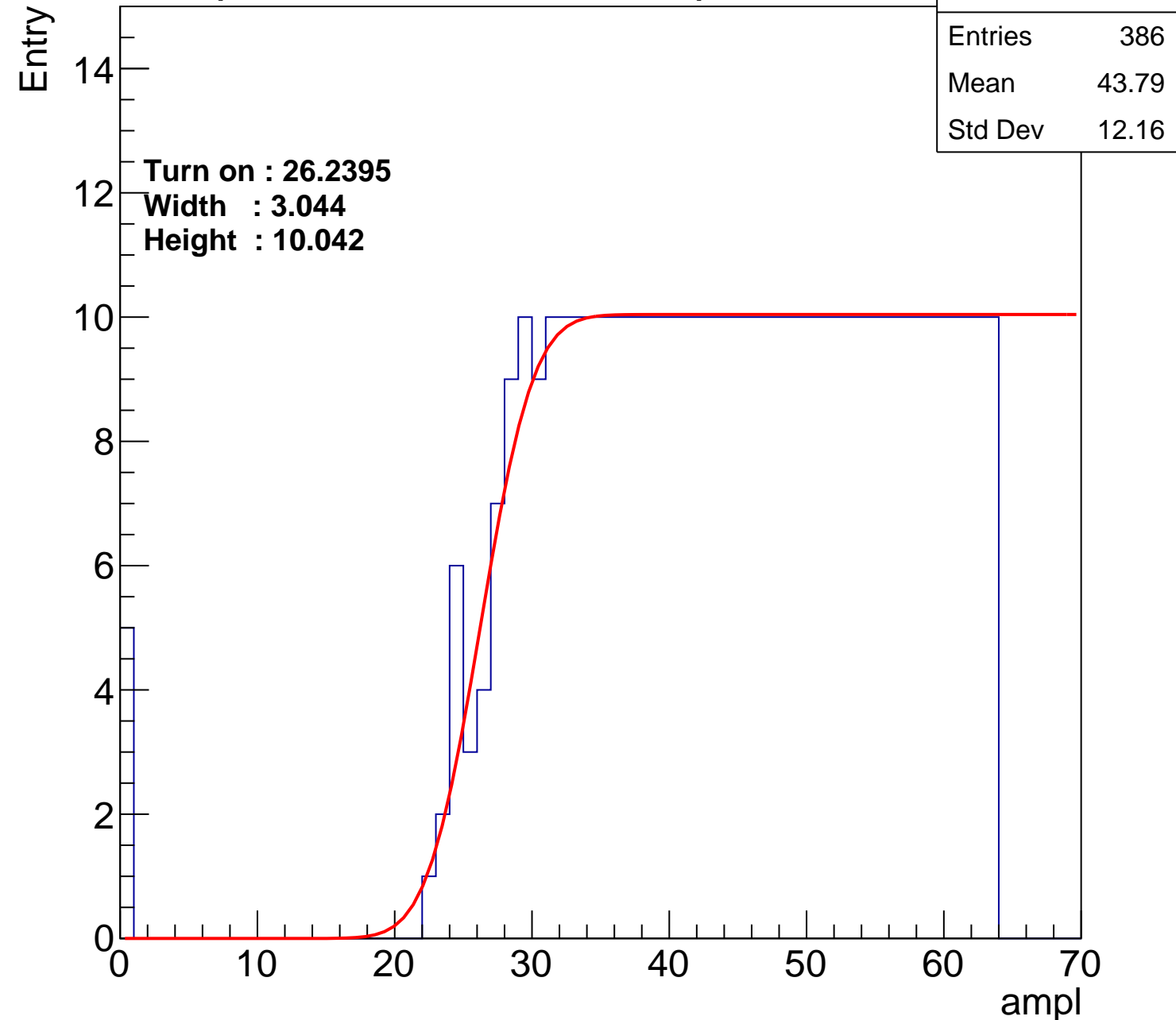
Width : 3.044

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.2256

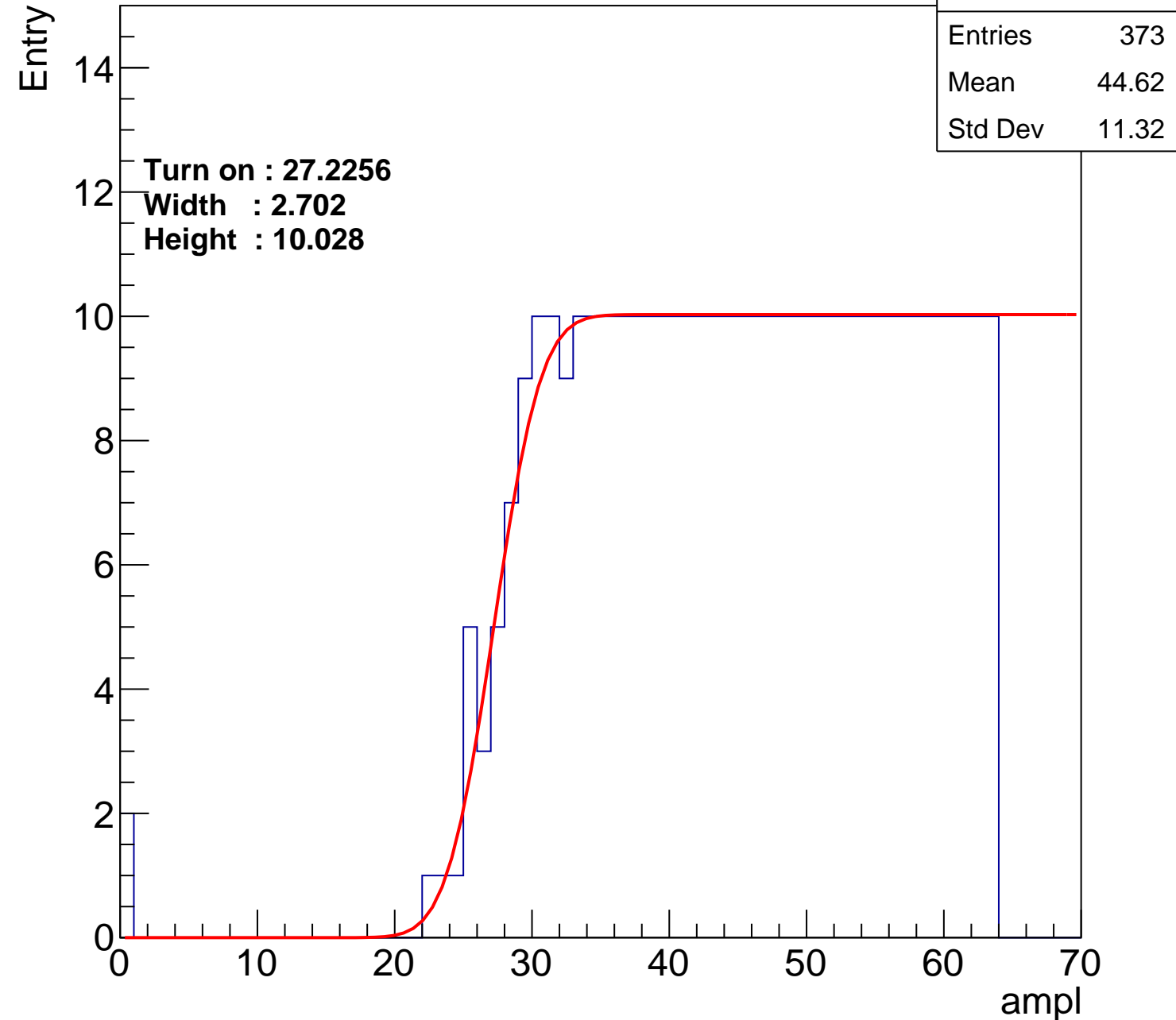
Width : 2.702

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch46

calib_packv5_042523_0143.root, FC#5, port B1

Entries	404
Mean	42.94
Std Dev	12.47

Turn on : 24.6593

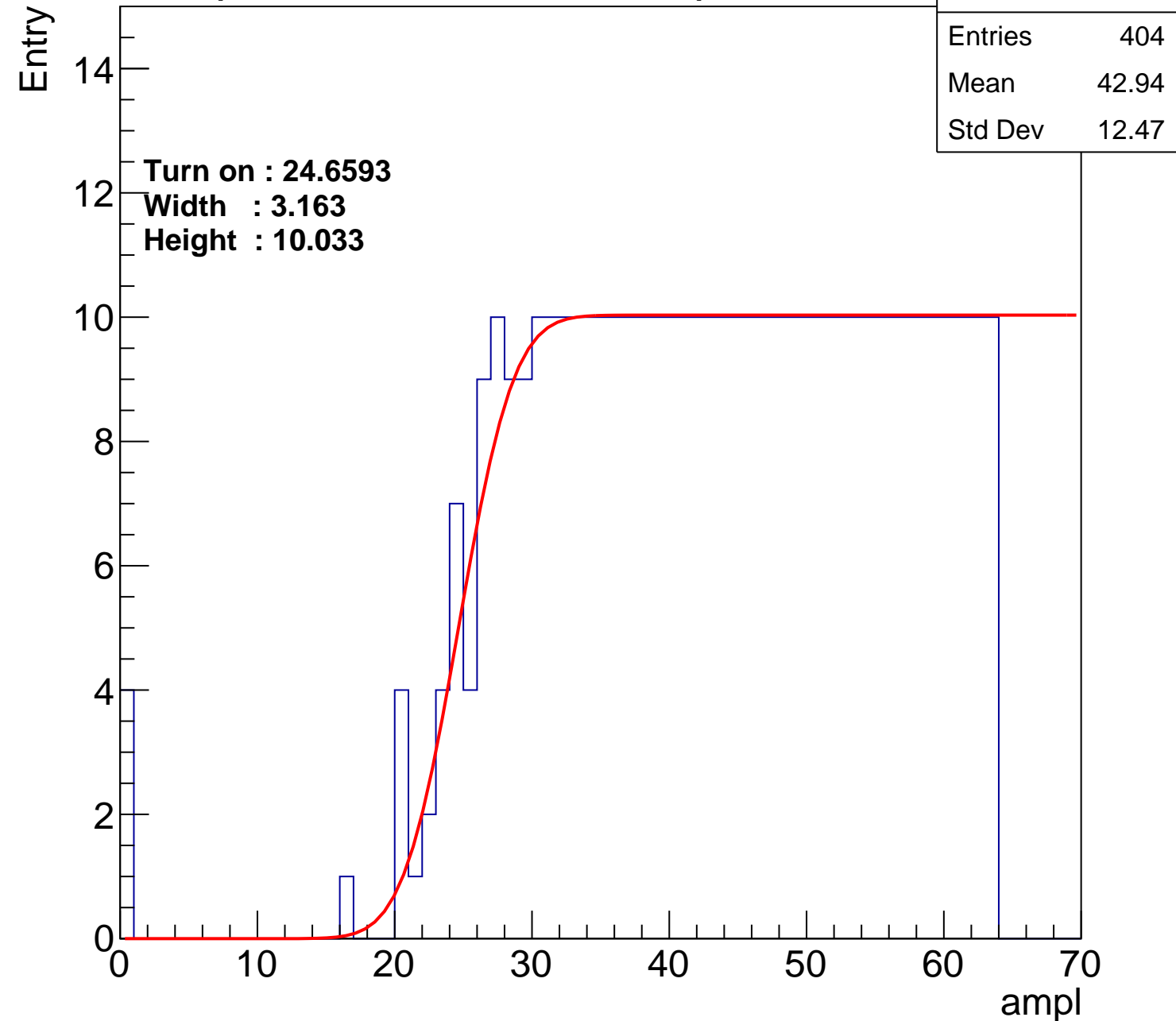
Width : 3.163

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch47

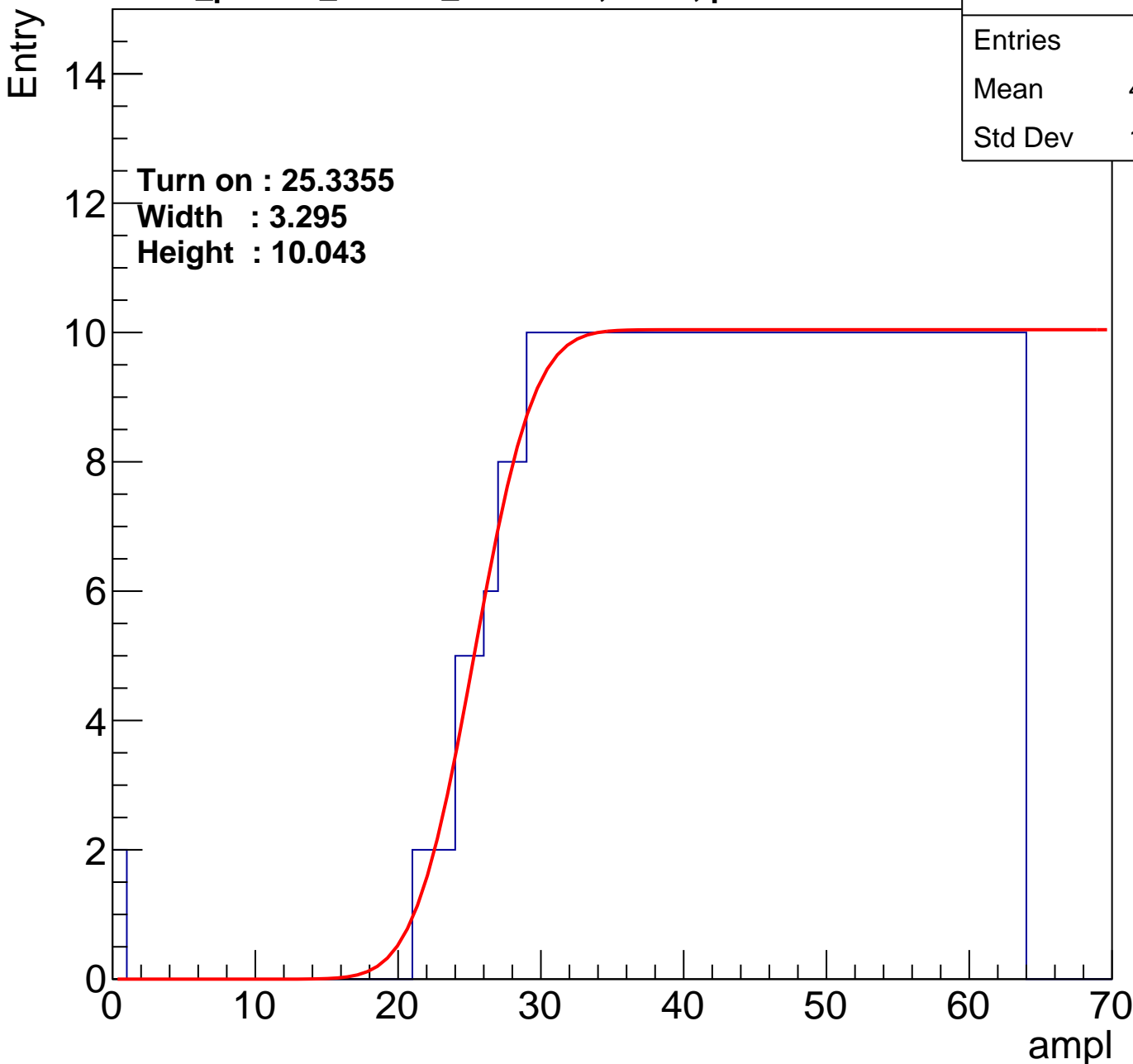
calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.3355

Width : 3.295

Height : 10.043



B0L000S, U21-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.1783

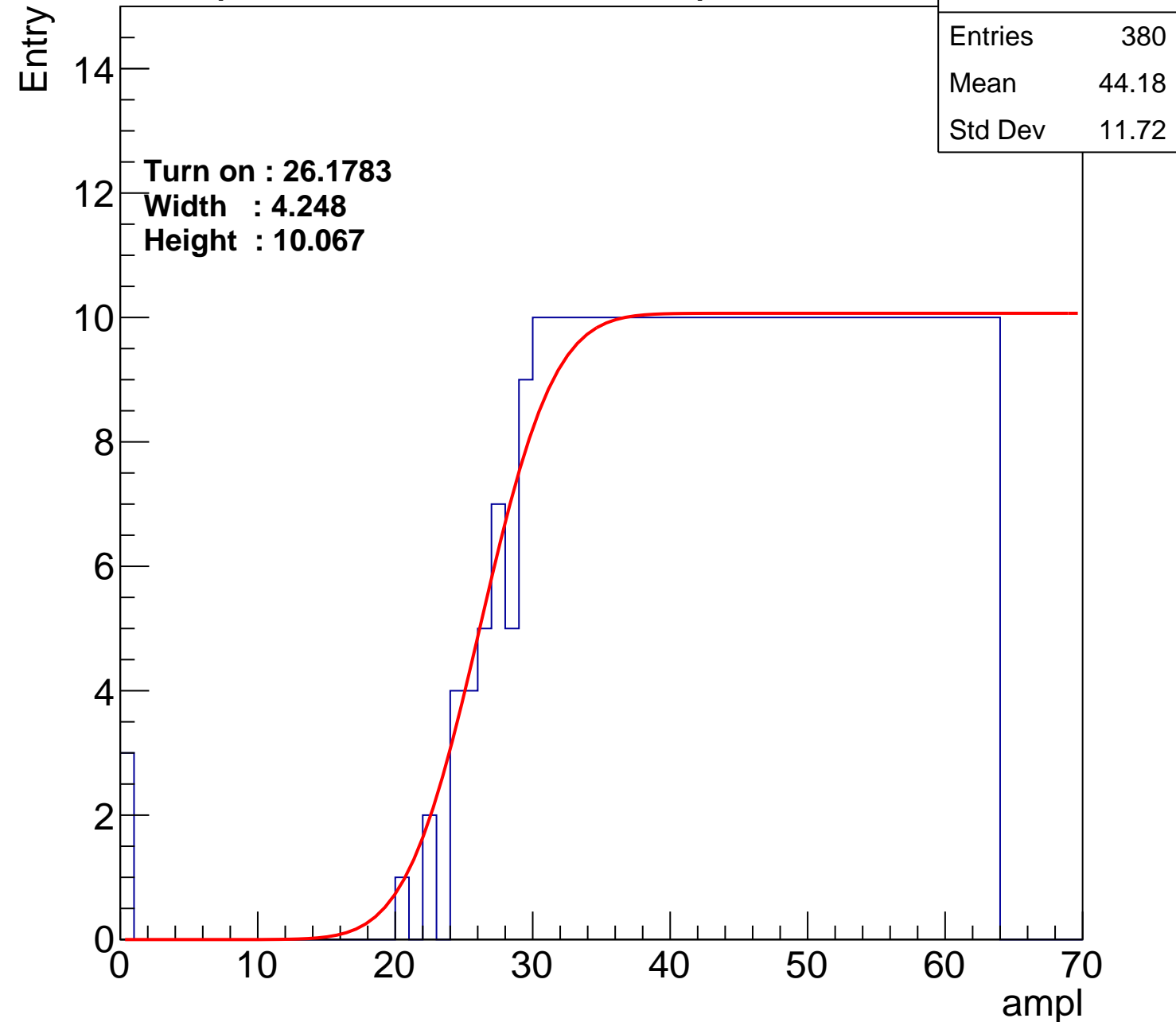
Width : 4.248

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 27.1179

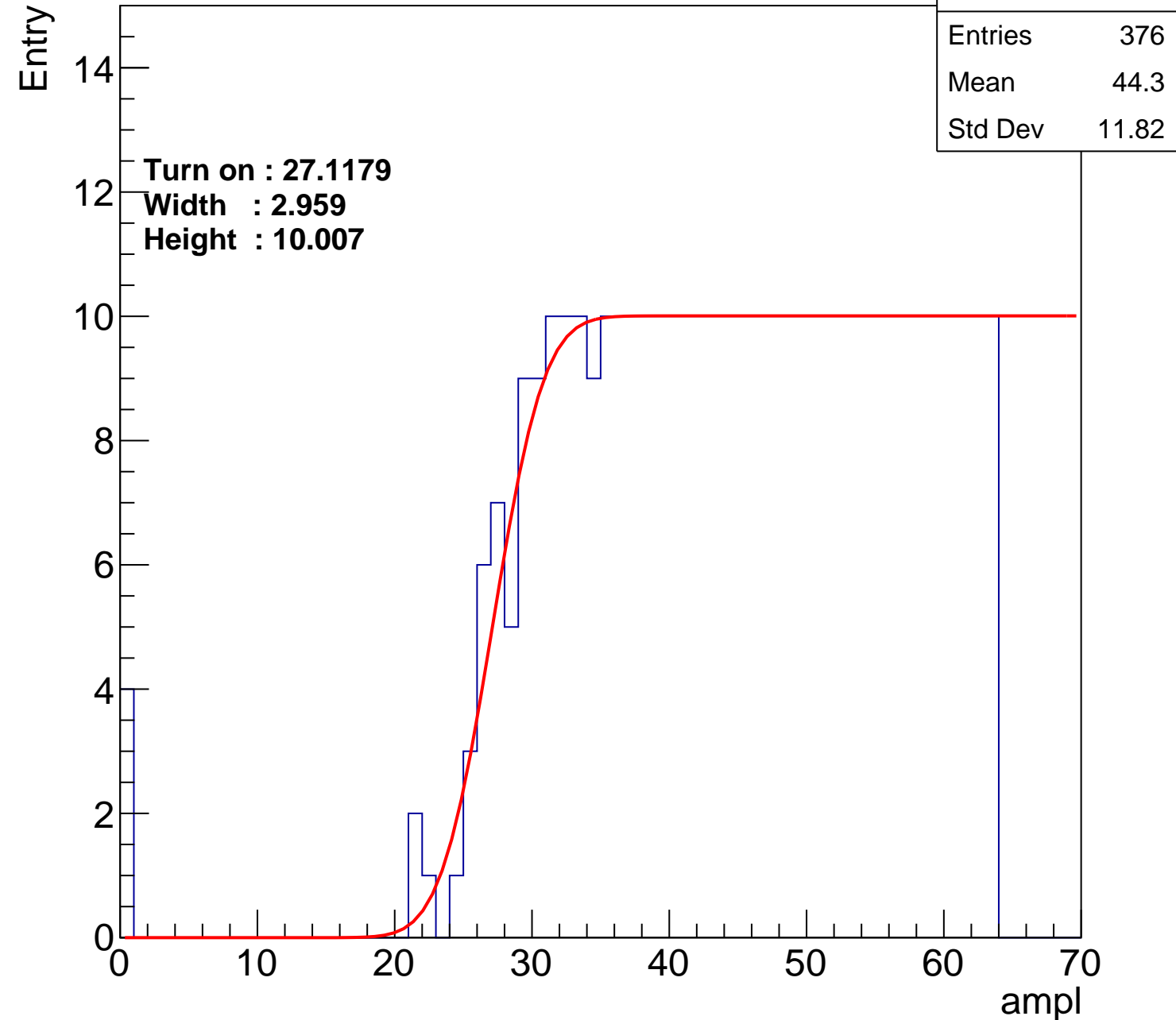
Width : 2.959

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	398
Mean	43.4
Std Dev	11.95

Turn on : 24.8435

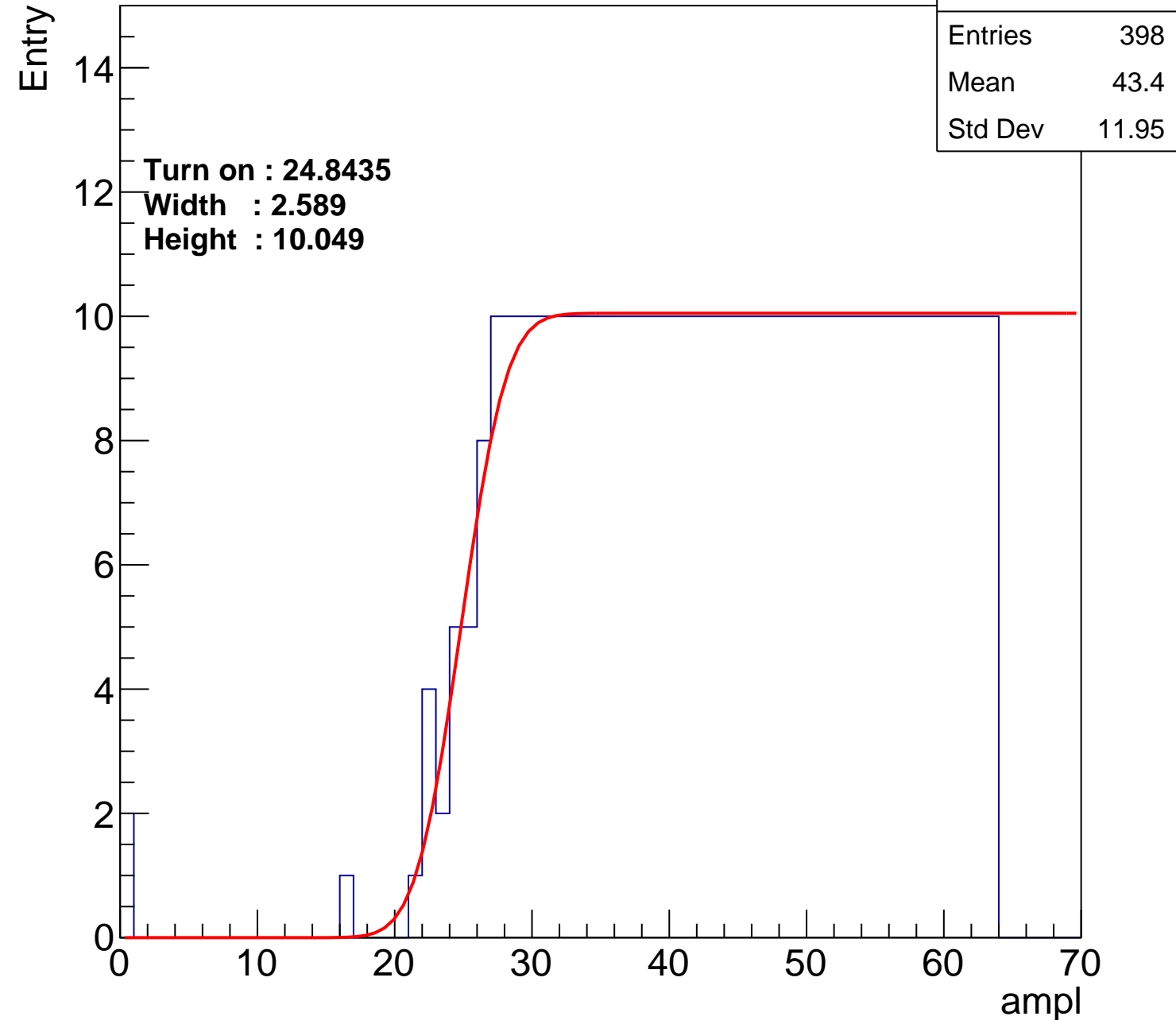
Width : 2.589

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch51

calib_packv5_042523_0143.root, FC#5, port B1

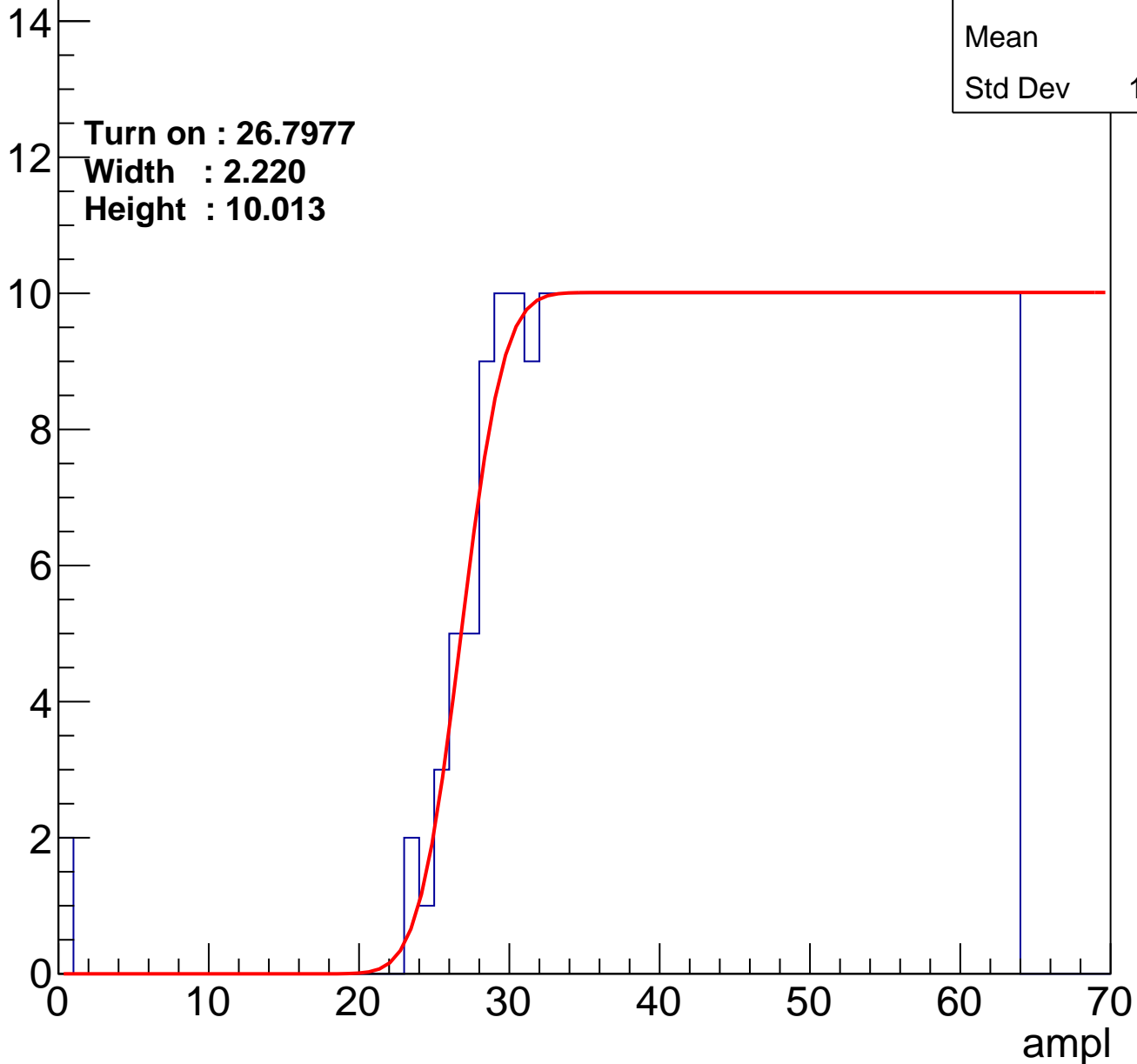
Entries	376
Mean	44.5
Std Dev	11.35

Turn on : 26.7977

Width : 2.220

Height : 10.013

Entry



B0L000S, U21-ch52

calib_packv5_042523_0143.root, FC#5, port B1

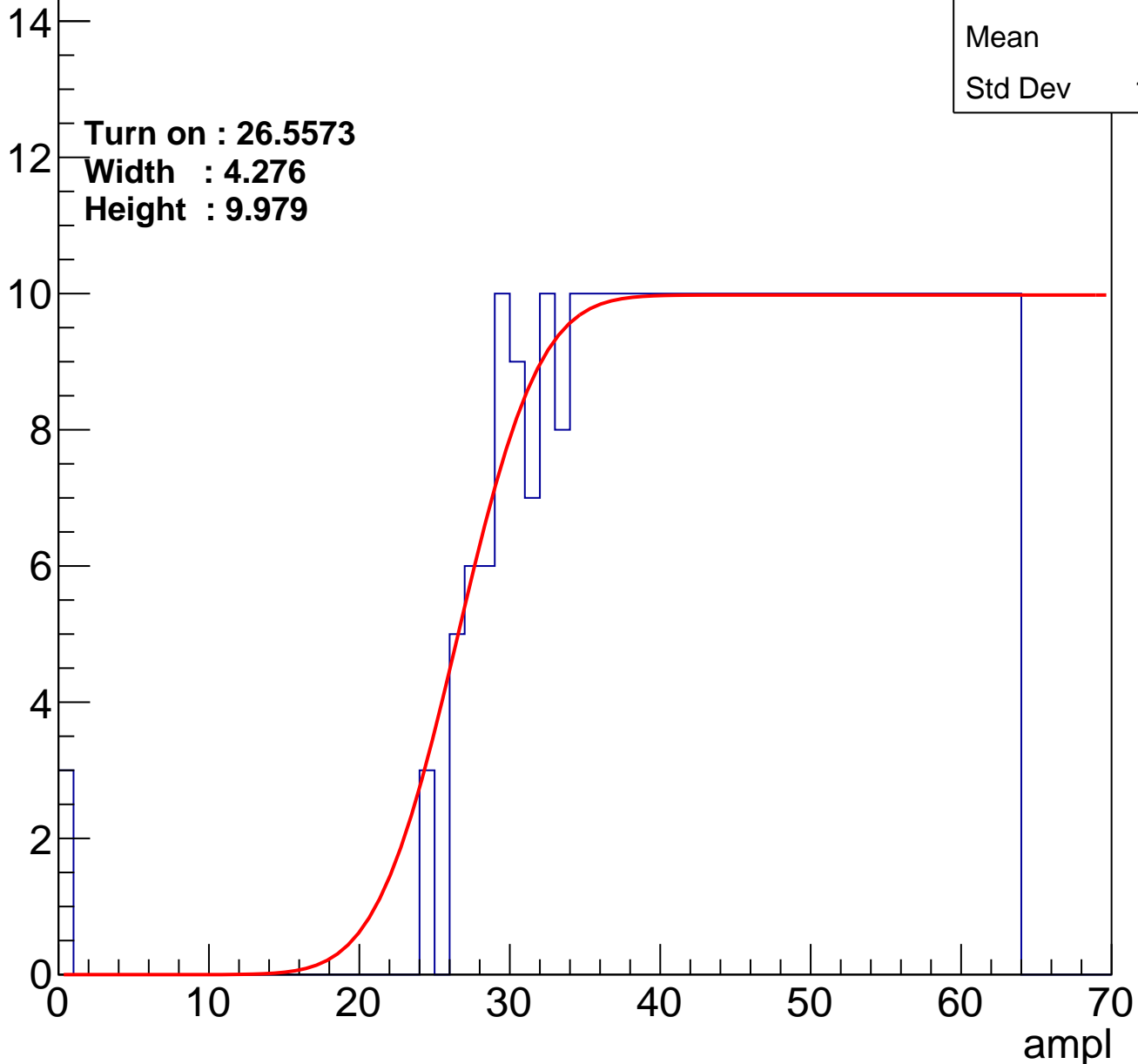
Entries	367
Mean	44.8
Std Dev	11.41

Turn on : 26.5573

Width : 4.276

Height : 9.979

Entry

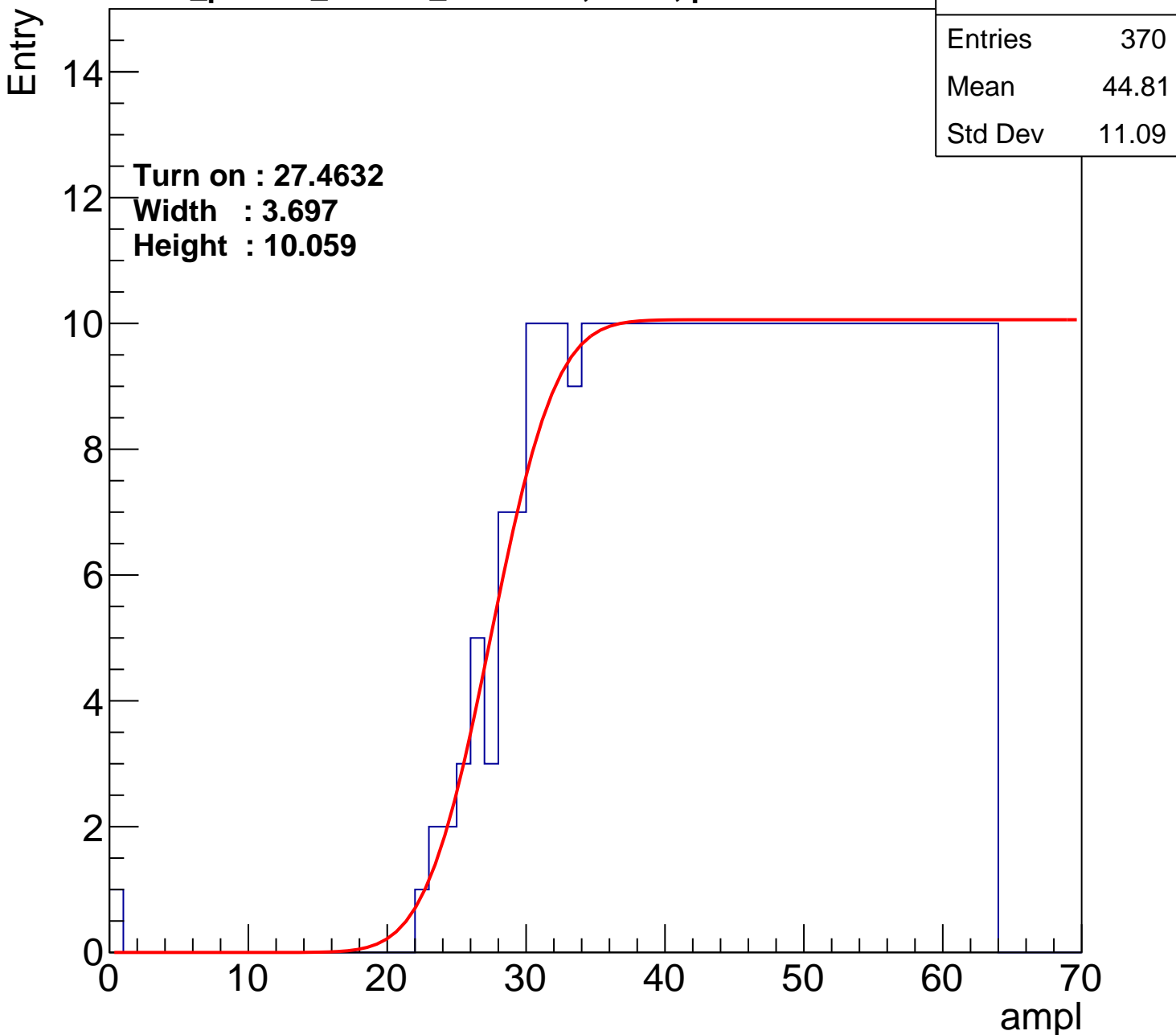


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.81
Std Dev	11.09

Height : 10.059



B0L000S, U21-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.1
Std Dev	11.07

Turn on : 28.3264

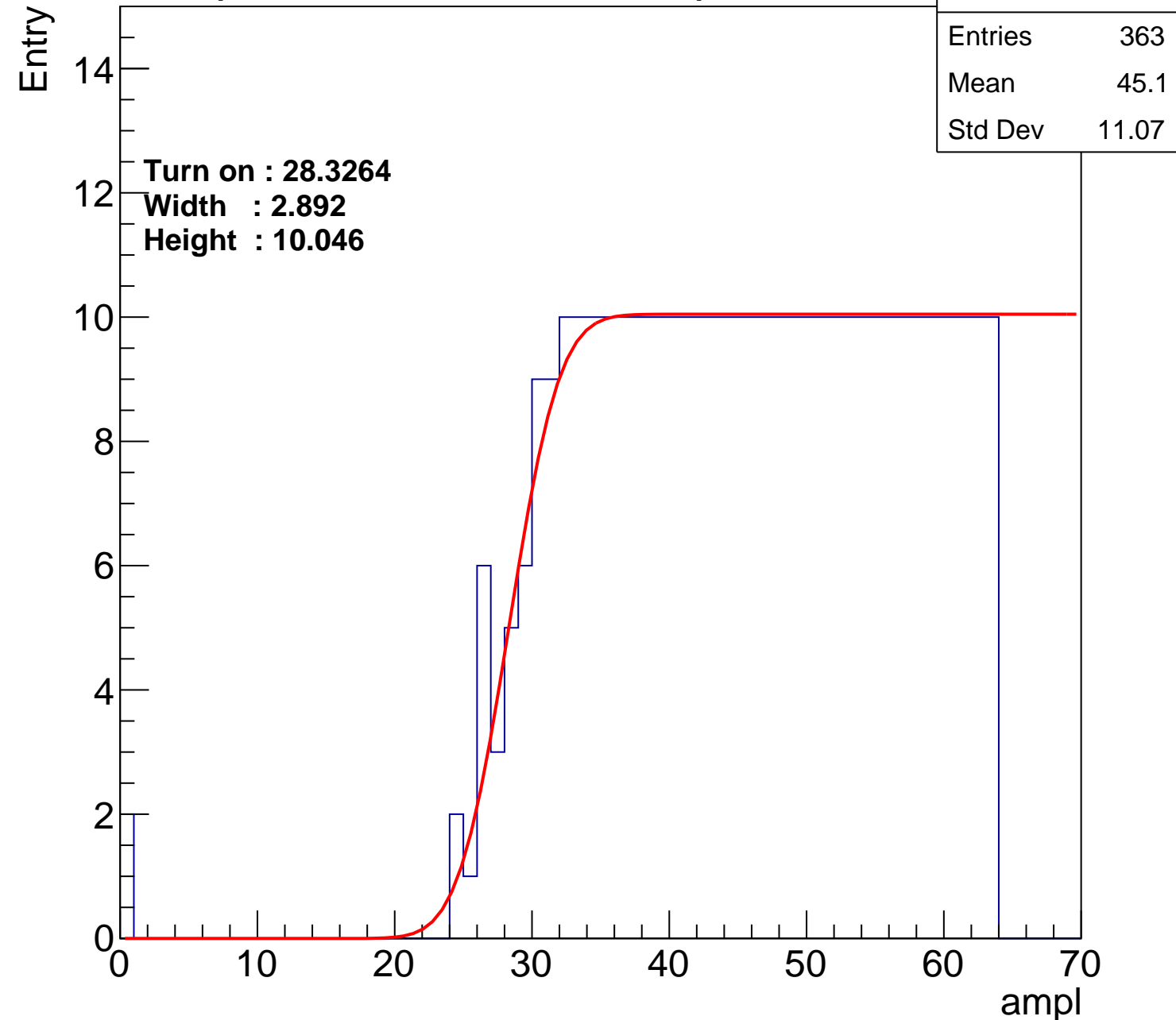
Width : 2.892

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.4077

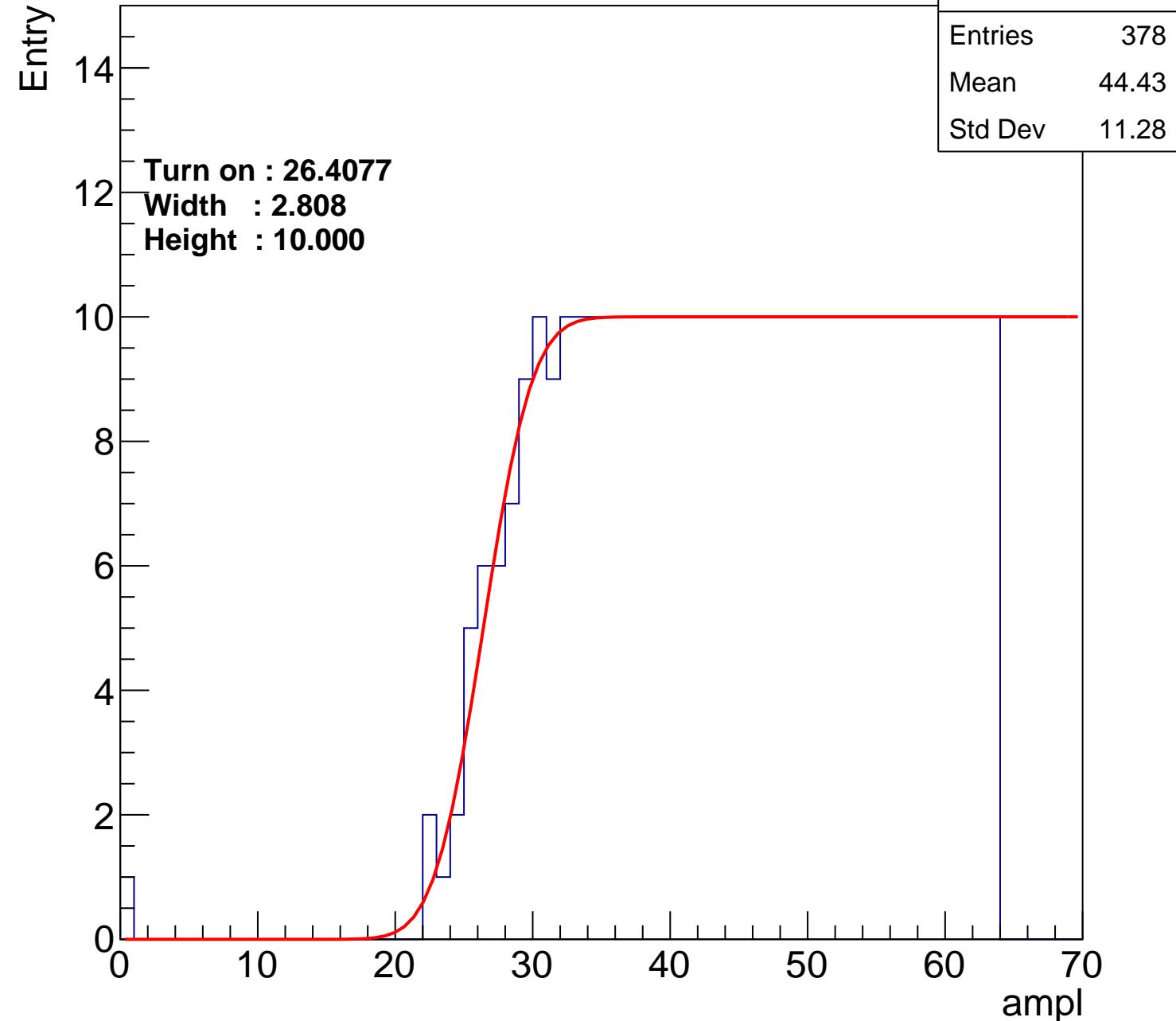
Width : 2.808

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch56

calib_packv5_042523_0143.root, FC#5, port B1

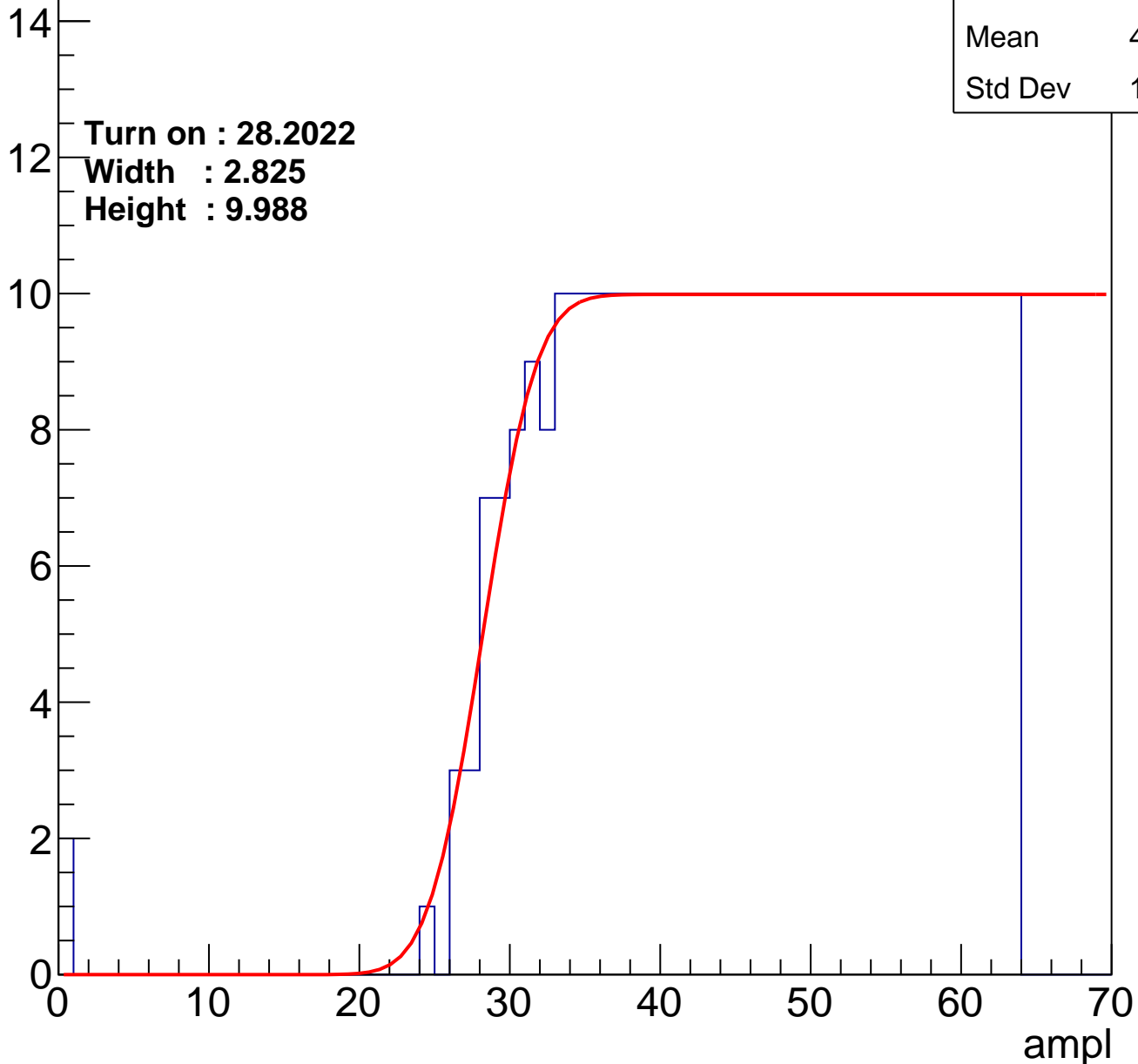
Entries	358
Mean	45.35
Std Dev	10.94

Turn on : 28.2022

Width : 2.825

Height : 9.988

Entry



B0L000S, U21-ch57

calib_packv5_042523_0143.root, FC#5, port B1

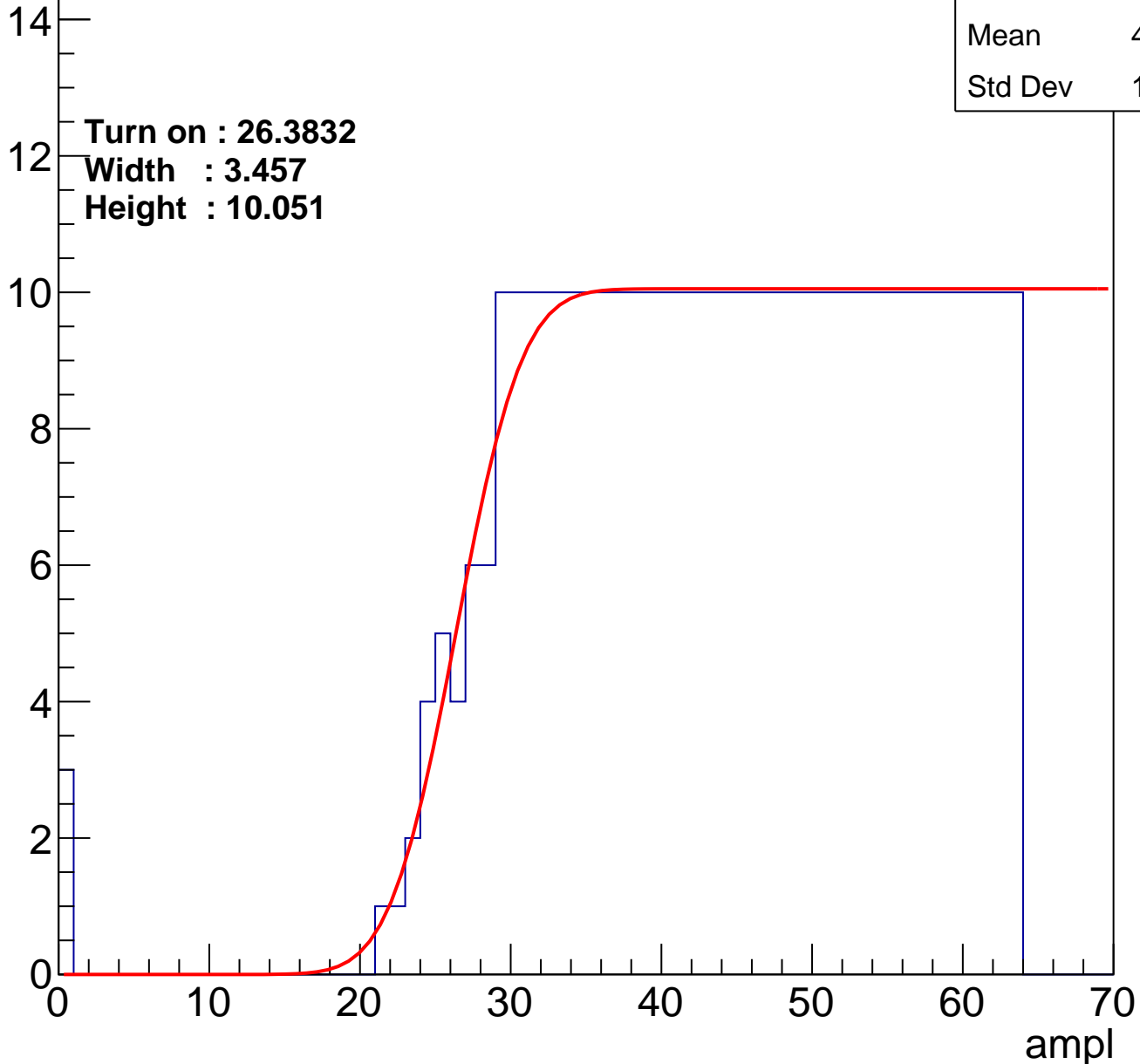
Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 26.3832

Width : 3.457

Height : 10.051

Entry



B0L000S, U21-ch58

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	44.02
Std Dev	11.47

Turn on : 25.8916

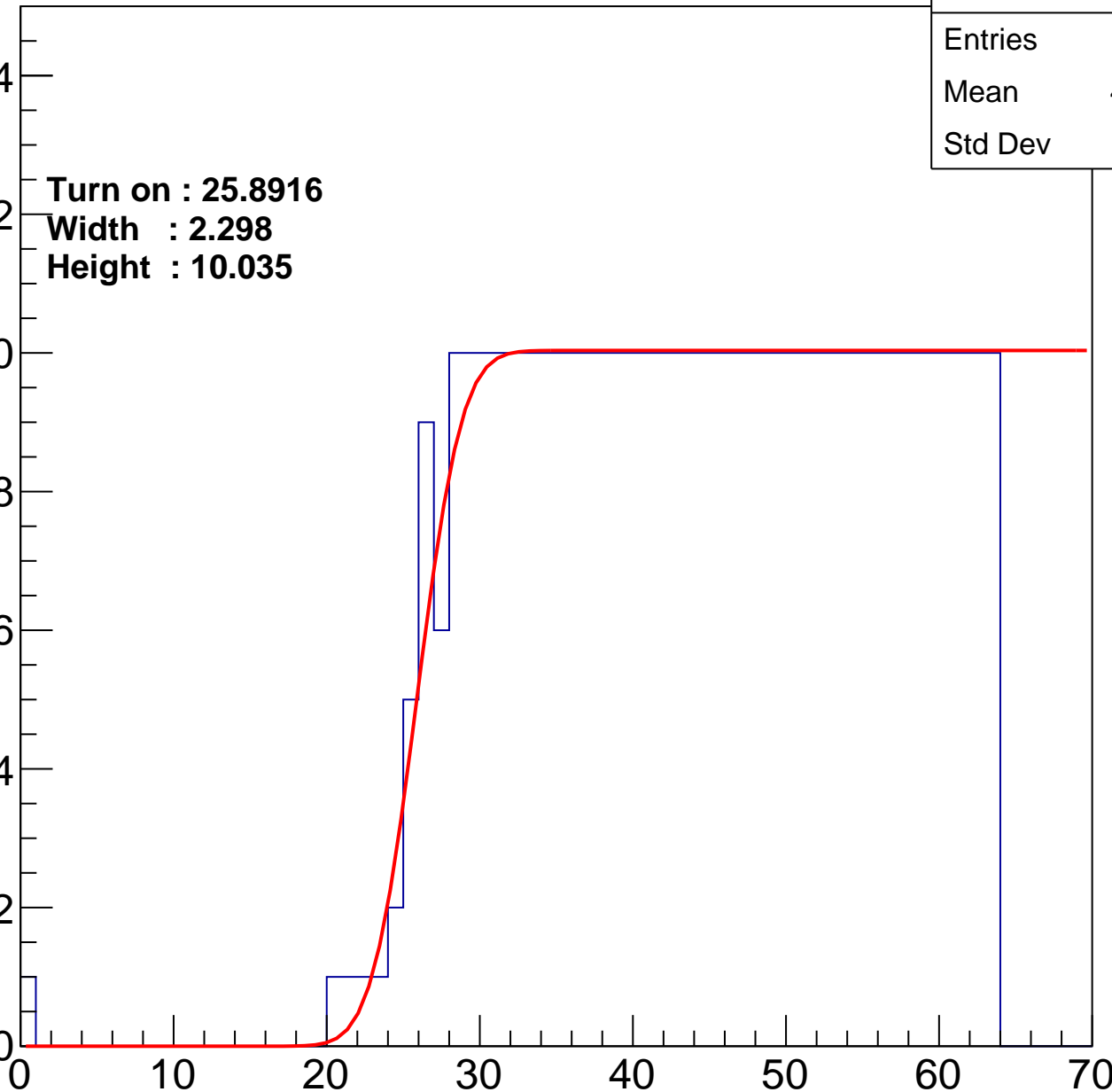
Width : 2.298

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.44
Std Dev	11.38

Turn on : 26.5823

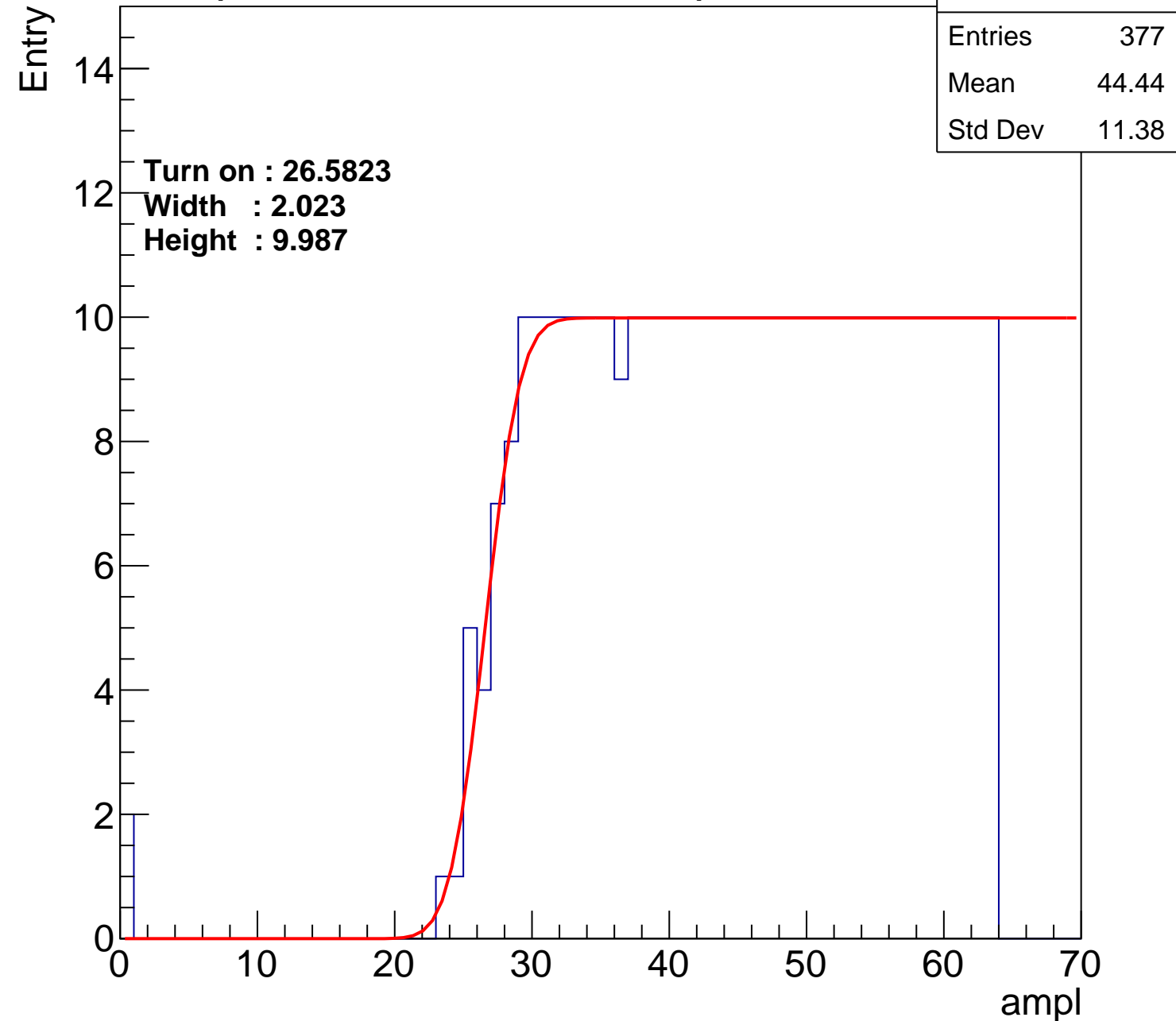
Width : 2.023

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.91
Std Dev	11.1

Turn on : 27.9838

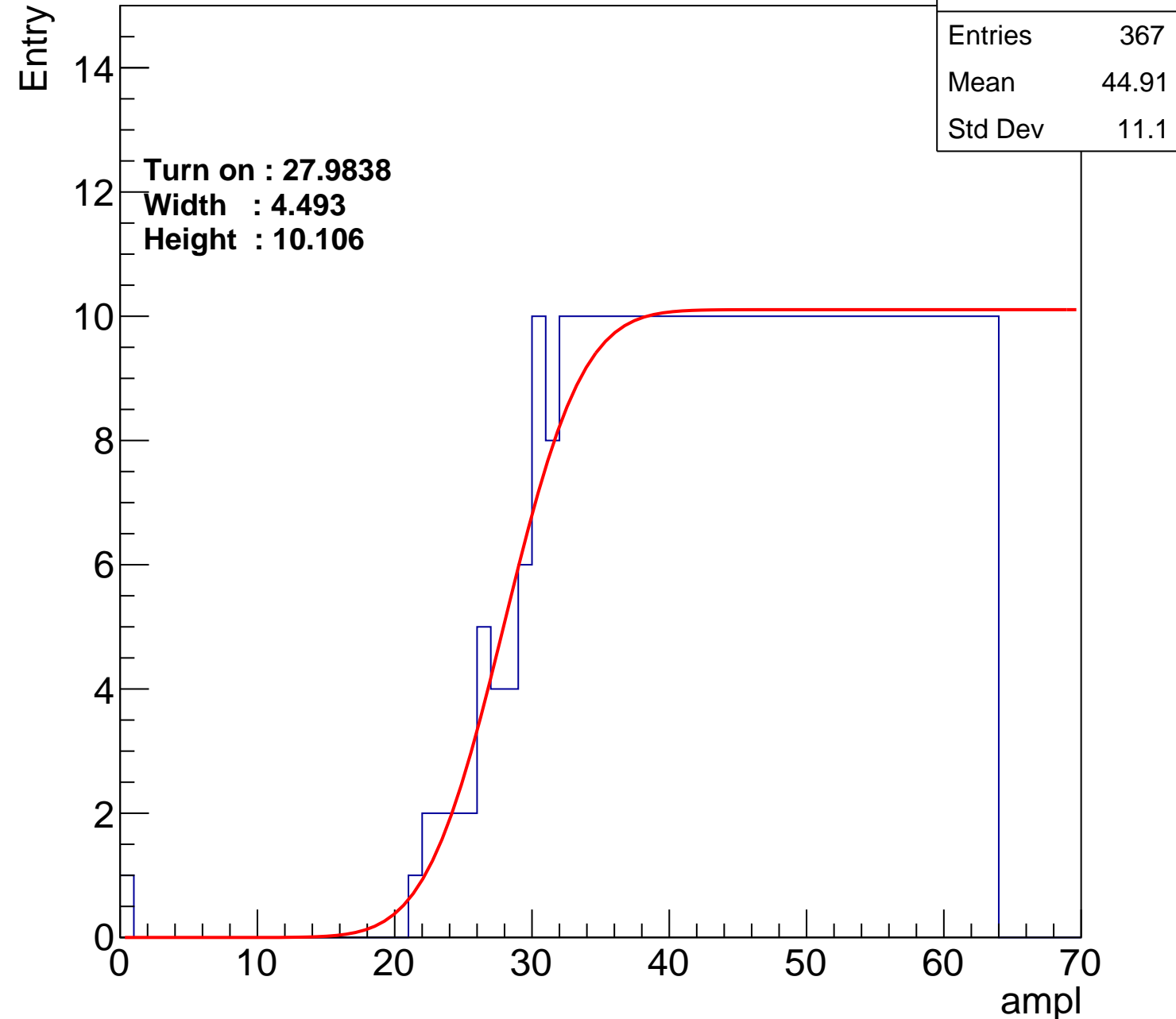
Width : 4.493

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch61

calib_packv5_042523_0143.root, FC#5, port B1

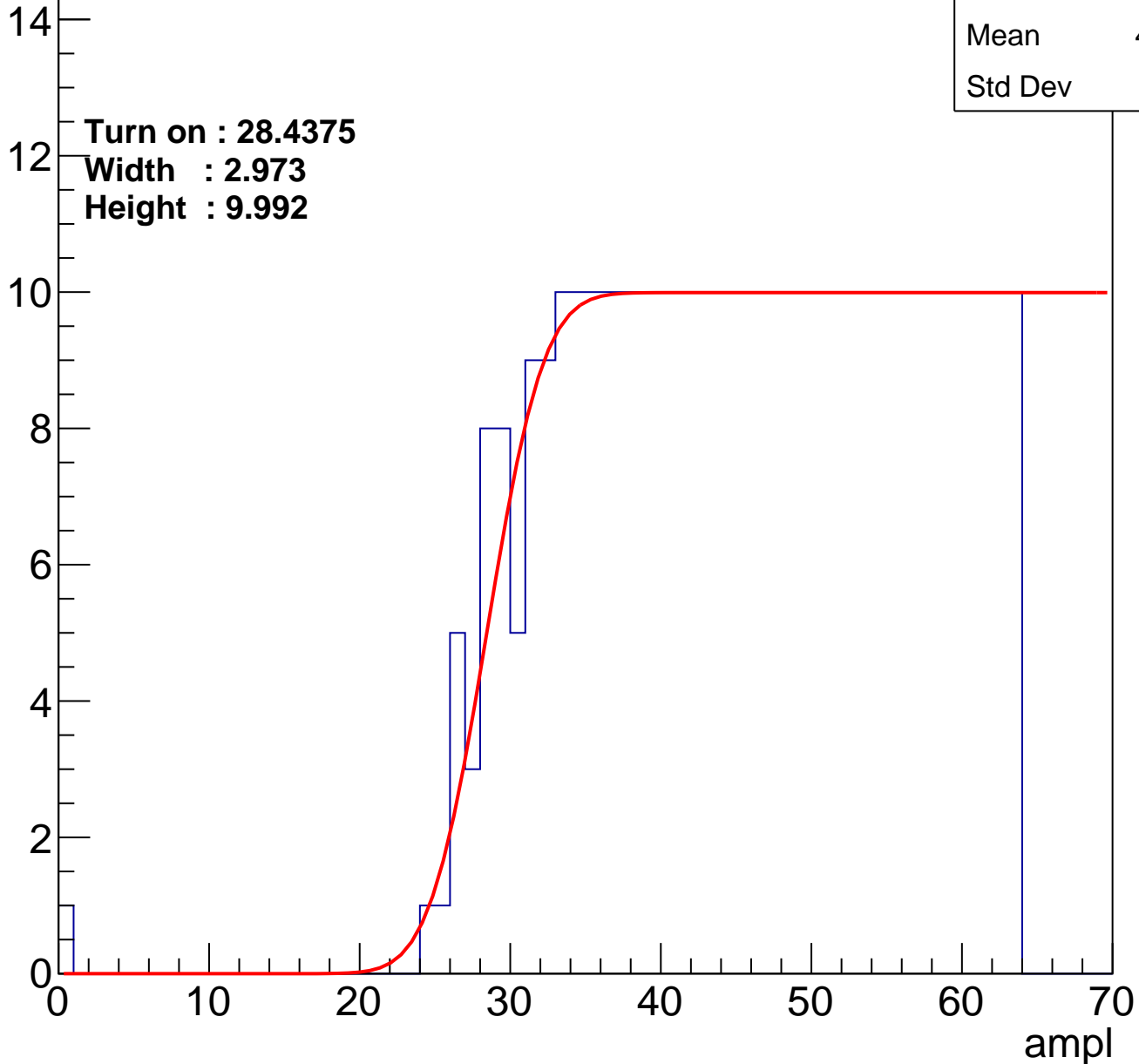
Entries	360
Mean	45.31
Std Dev	10.8

Turn on : 28.4375

Width : 2.973

Height : 9.992

Entry



B0L000S, U21-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.54
Std Dev	11.93

Turn on : 24.7348

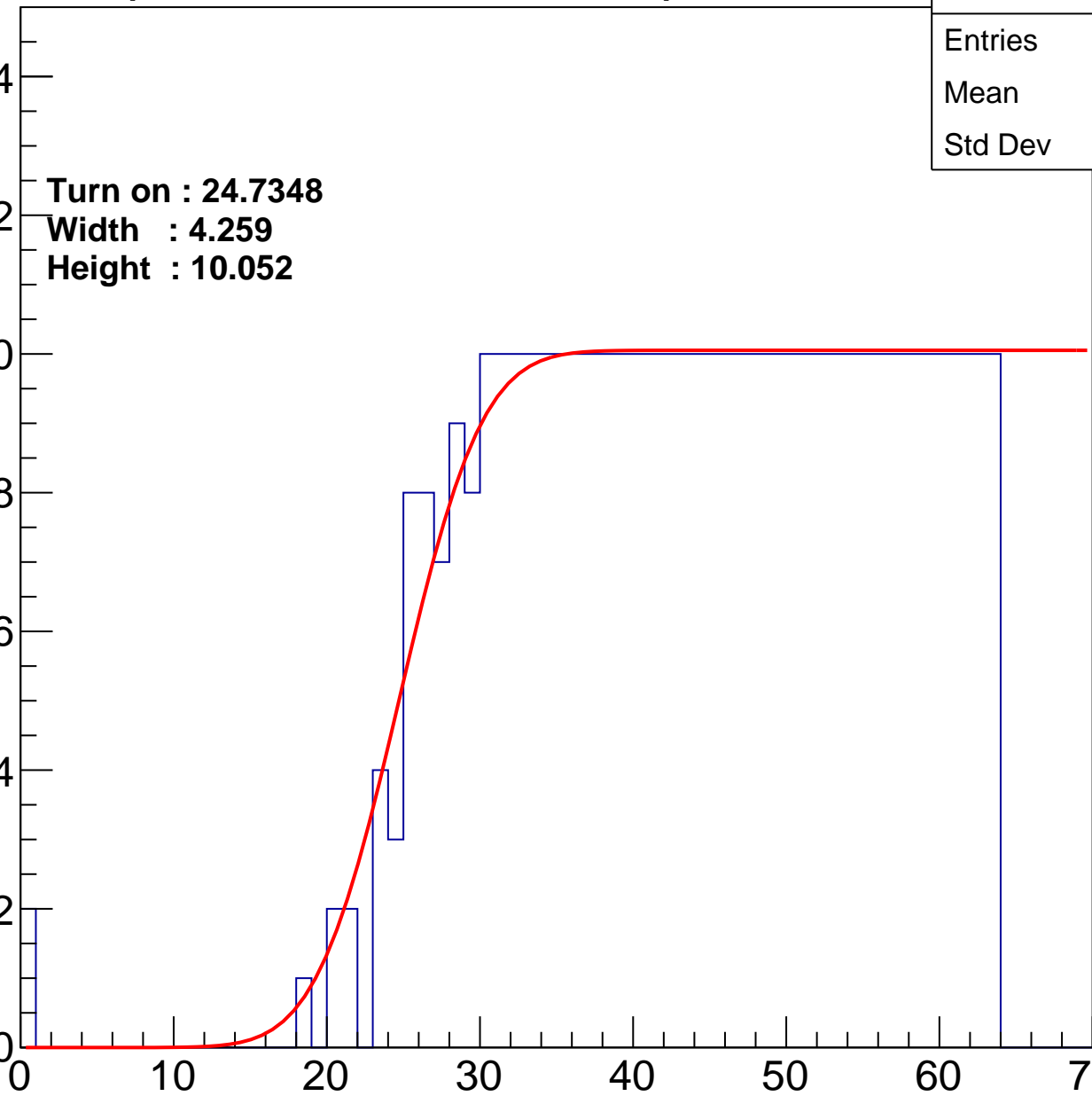
Width : 4.259

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch63

calib_packv5_042523_0143.root, FC#5, port B1

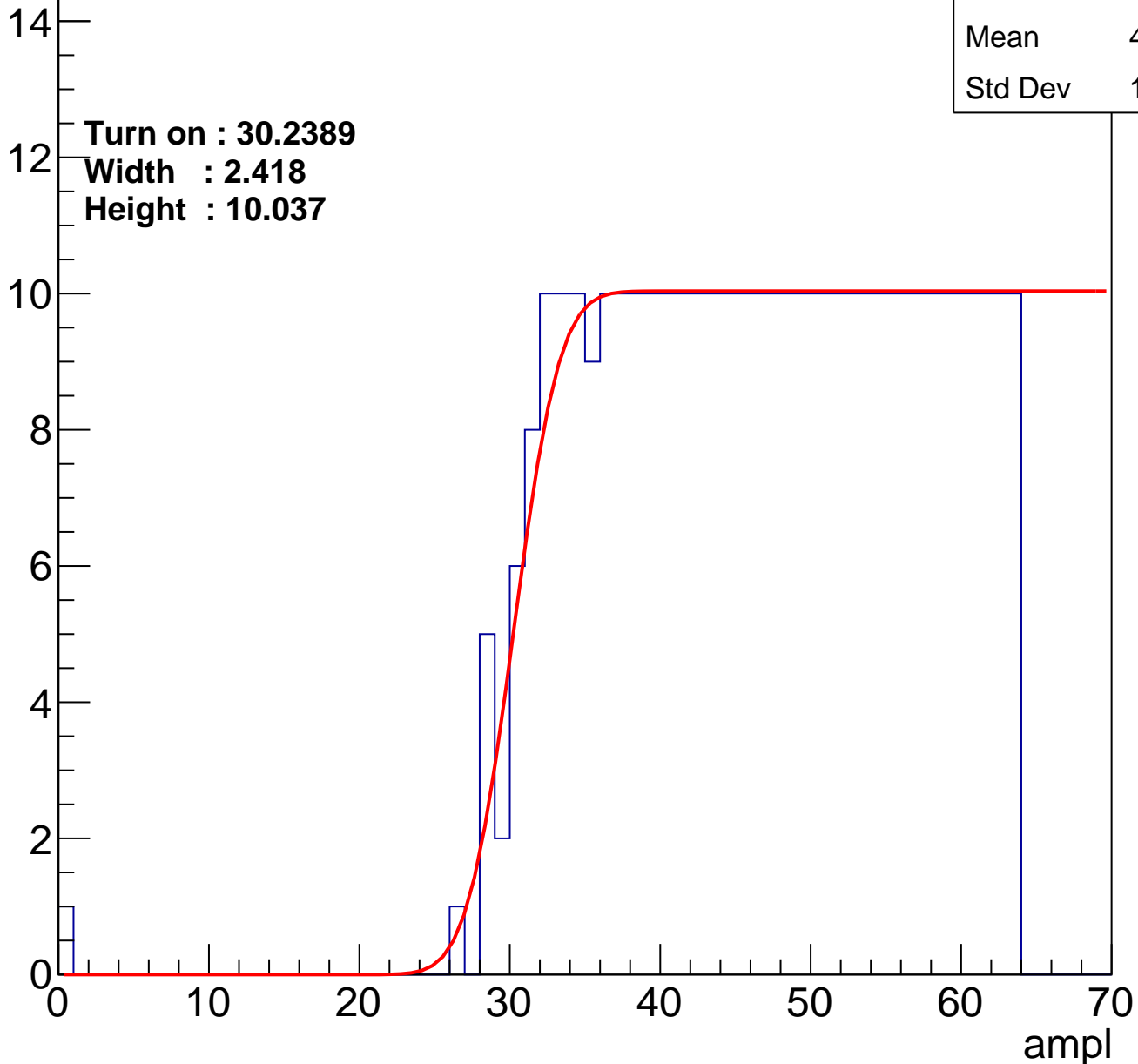
Entries	342
Mean	46.25
Std Dev	10.25

Turn on : 30.2389

Width : 2.418

Height : 10.037

Entry



B0L000S, U21-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.6169

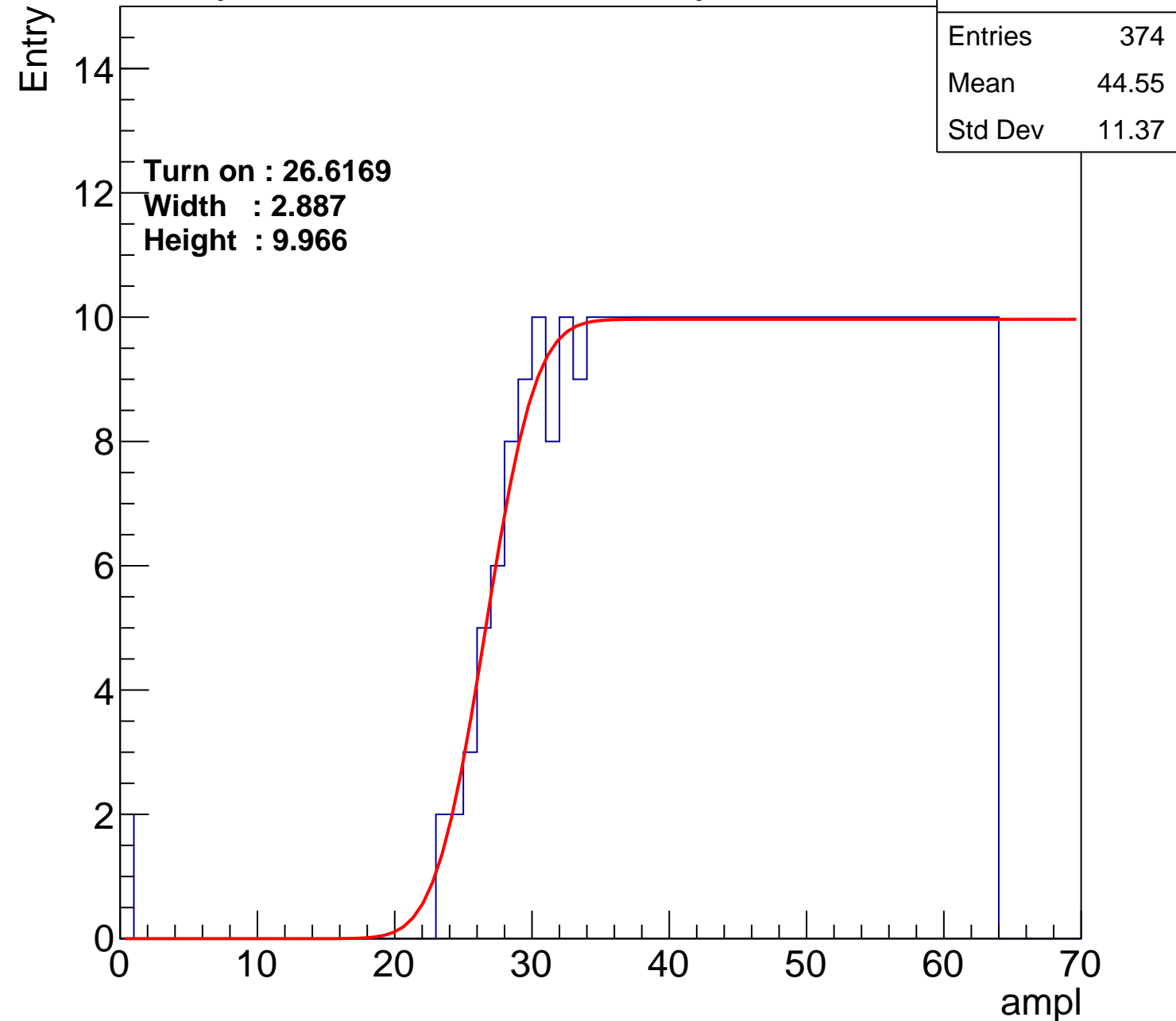
Width : 2.887

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.97
Std Dev	11.12

Width : 2.702

Height : 10.029



14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

B0L000S, U21-ch66

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.85
Std Dev	11.16

Turn on : 27.3053

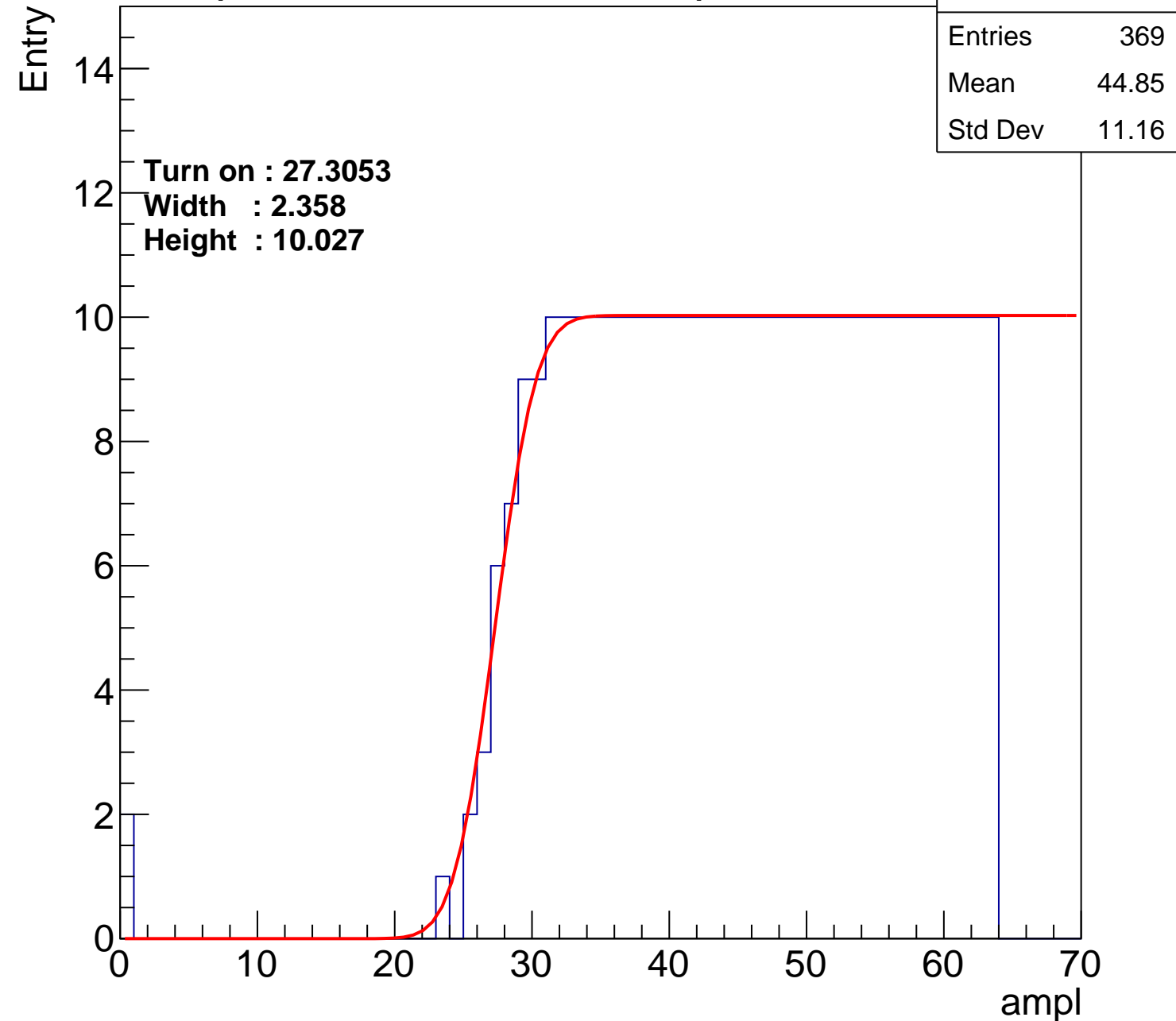
Width : 2.358

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.01
Std Dev	11.11

Turn on : 27.7273

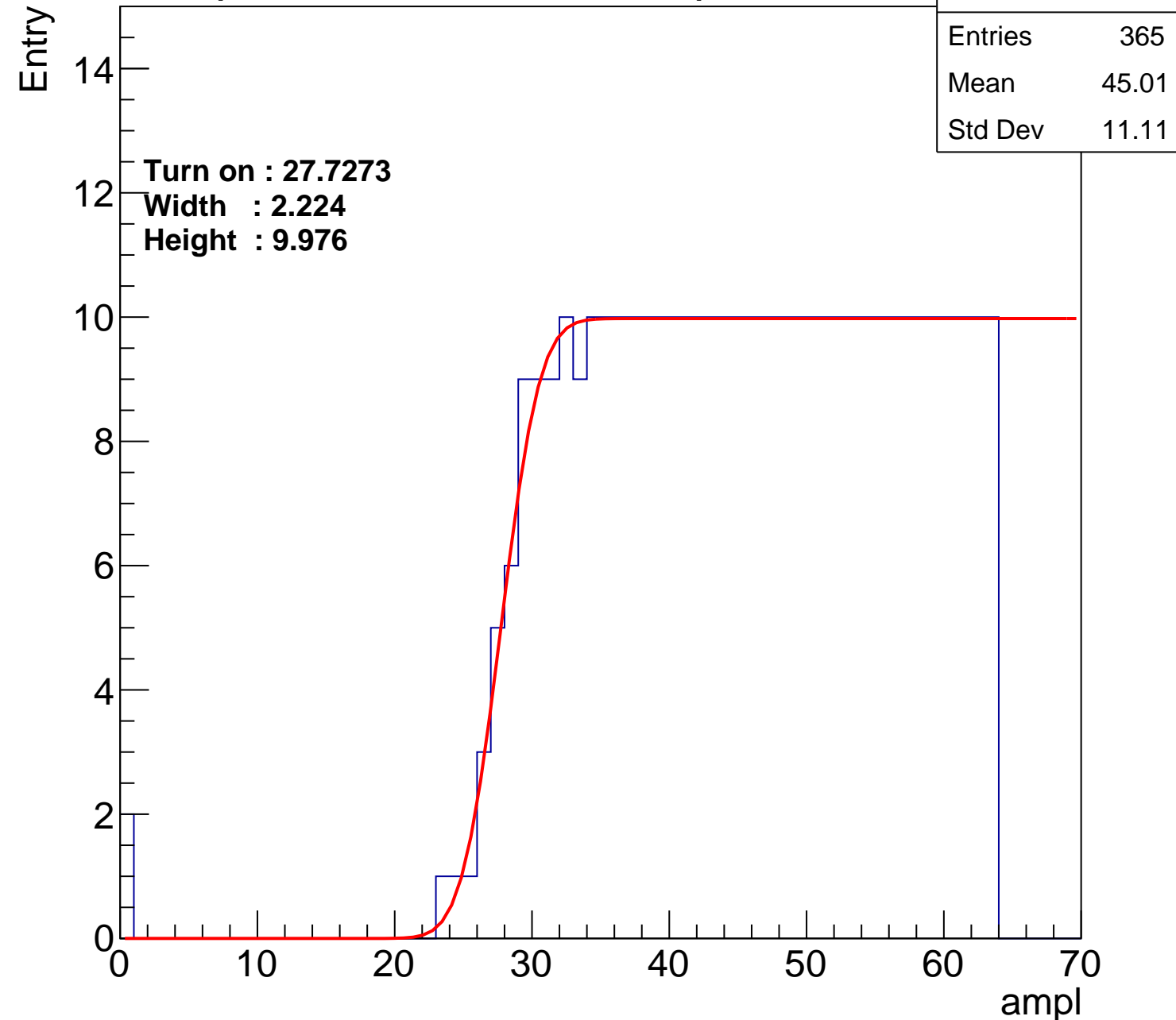
Width : 2.224

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.2166

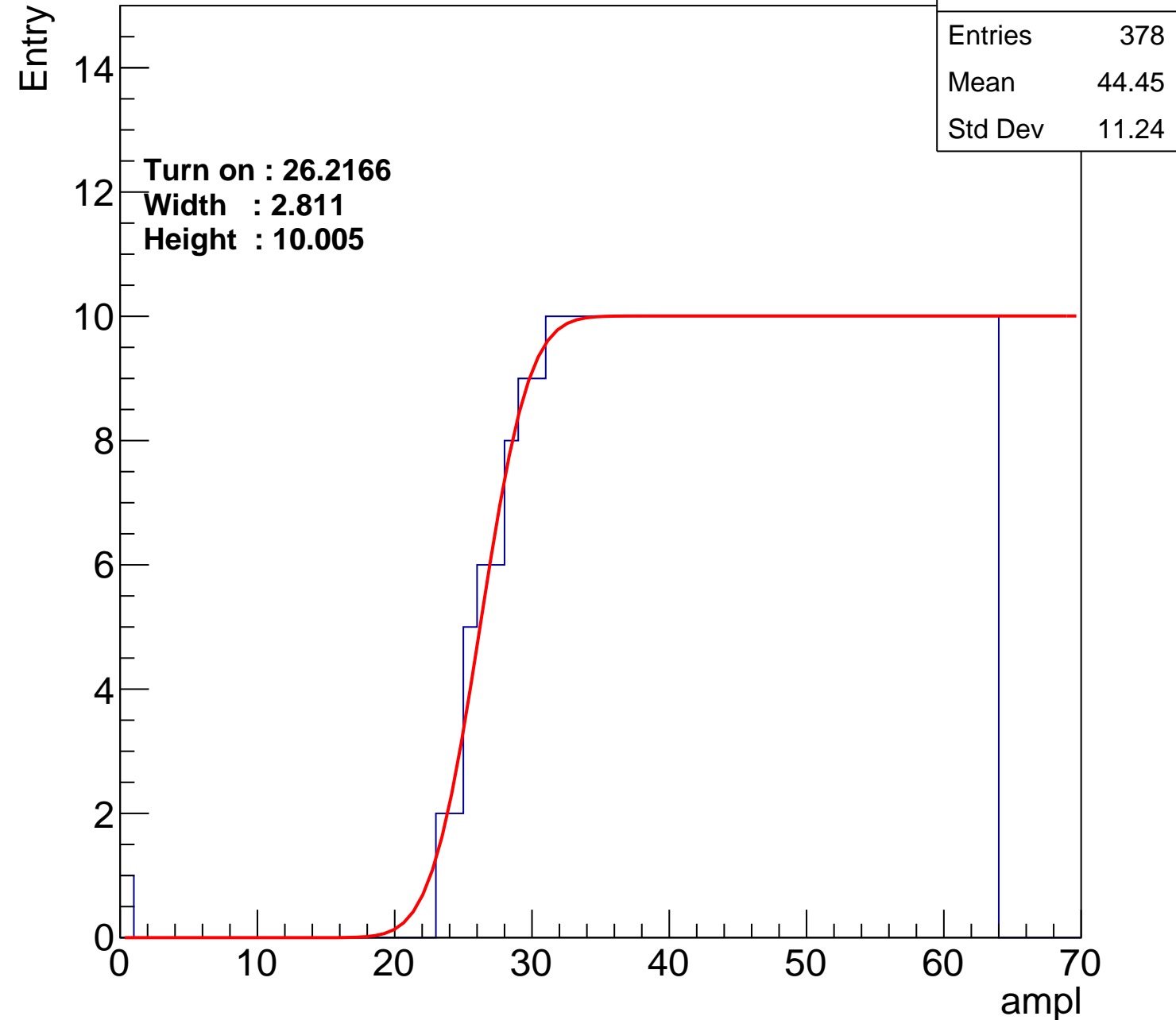
Width : 2.811

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.63
Std Dev	11.95

Turn on : 25.4517

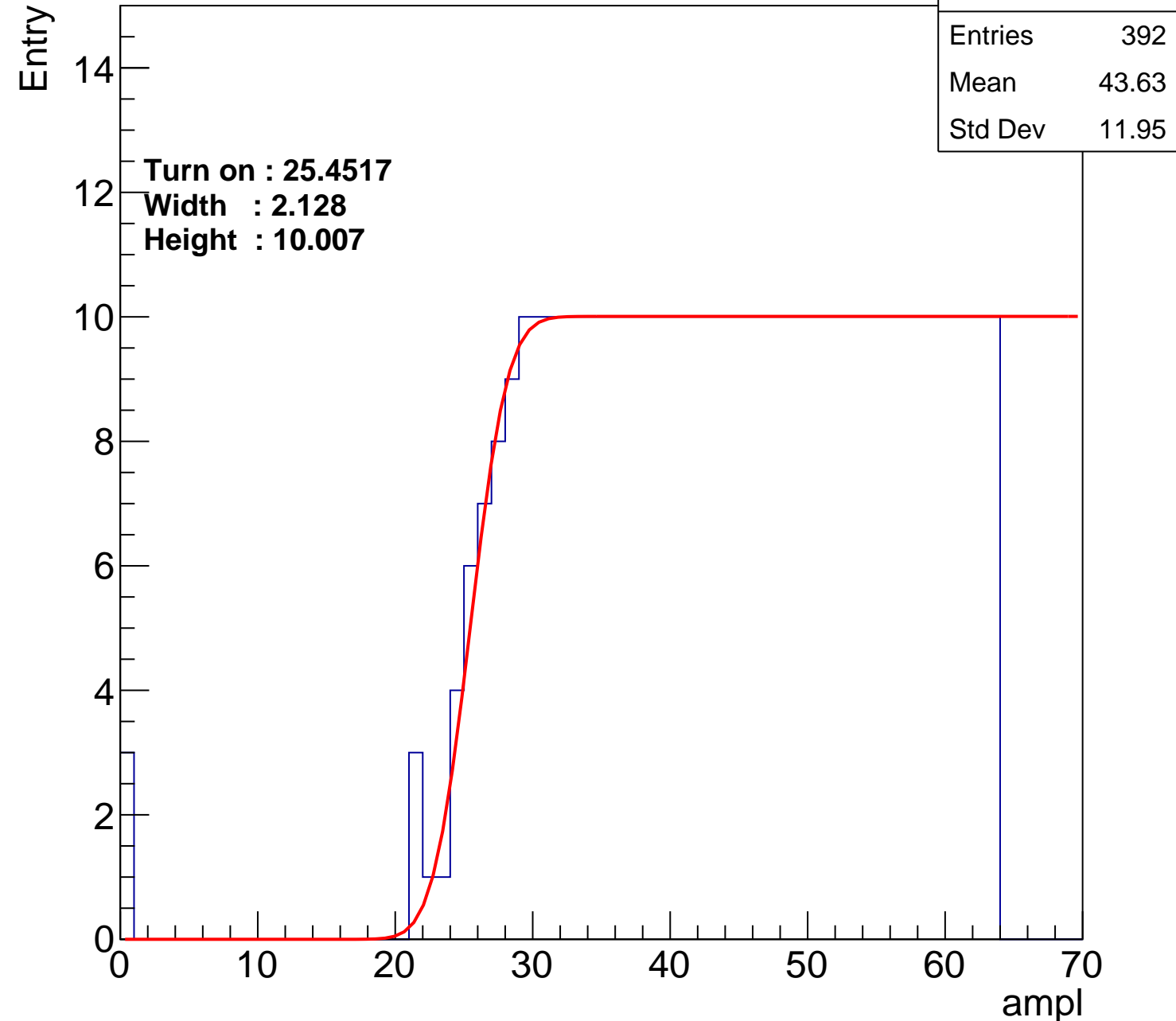
Width : 2.128

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch70

calib_packv5_042523_0143.root, FC#5, port B1

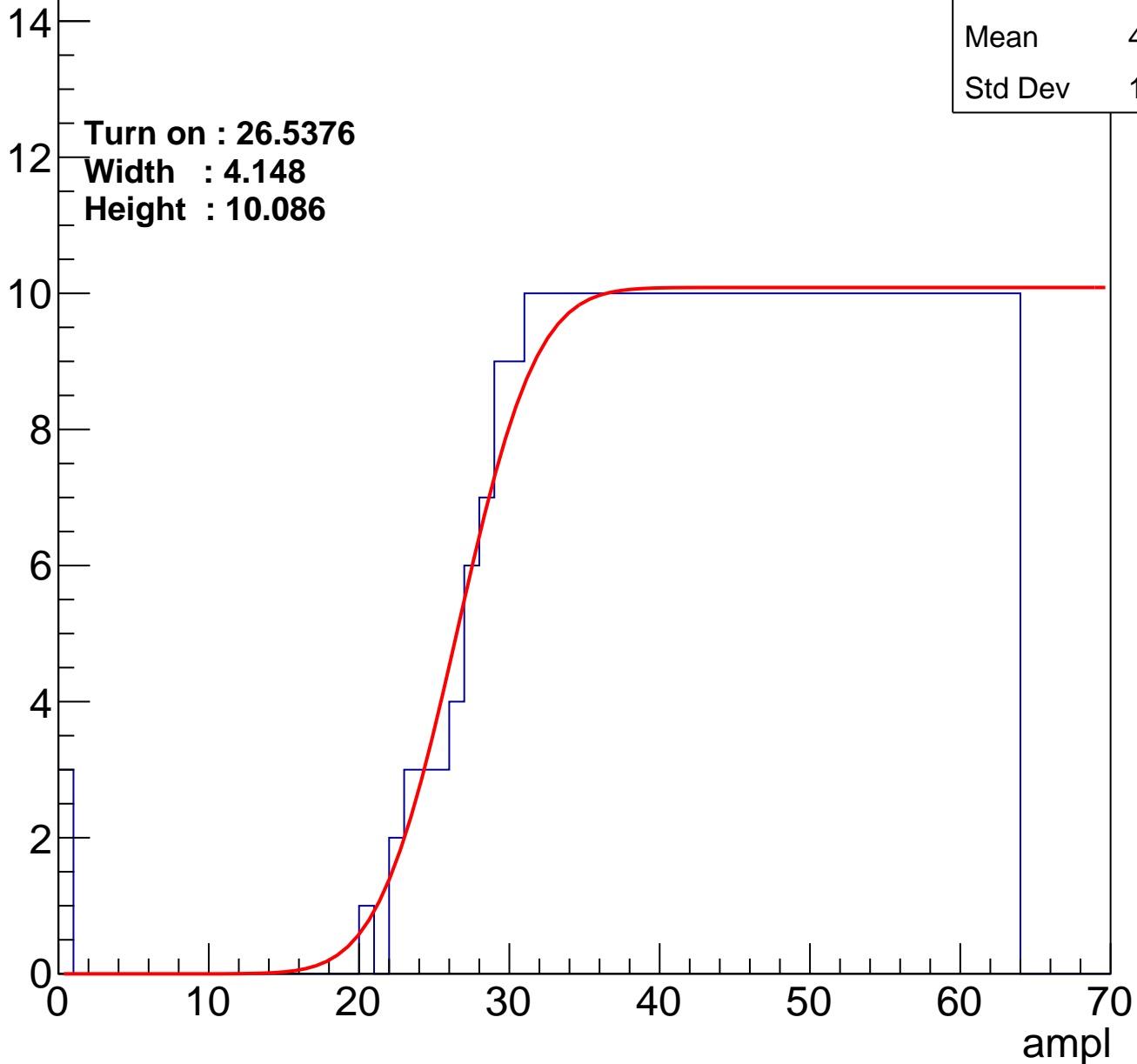
Entries	380
Mean	44.17
Std Dev	11.75

Turn on : 26.5376

Width : 4.148

Height : 10.086

Entry



B0L000S, U21-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.27
Std Dev	10.77

Turn on : 27.9894

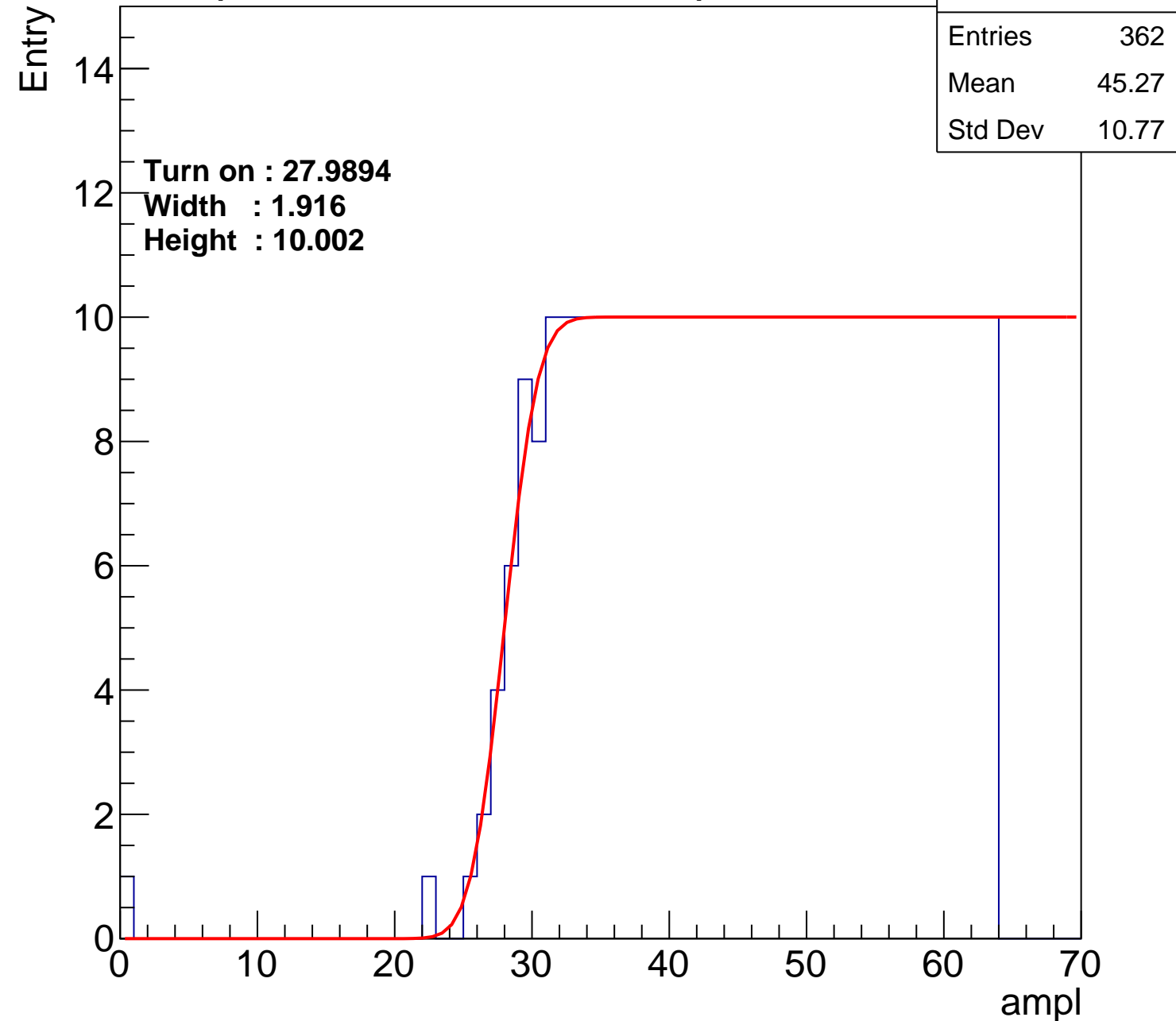
Width : 1.916

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch72

calib_packv5_042523_0143.root, FC#5, port B1

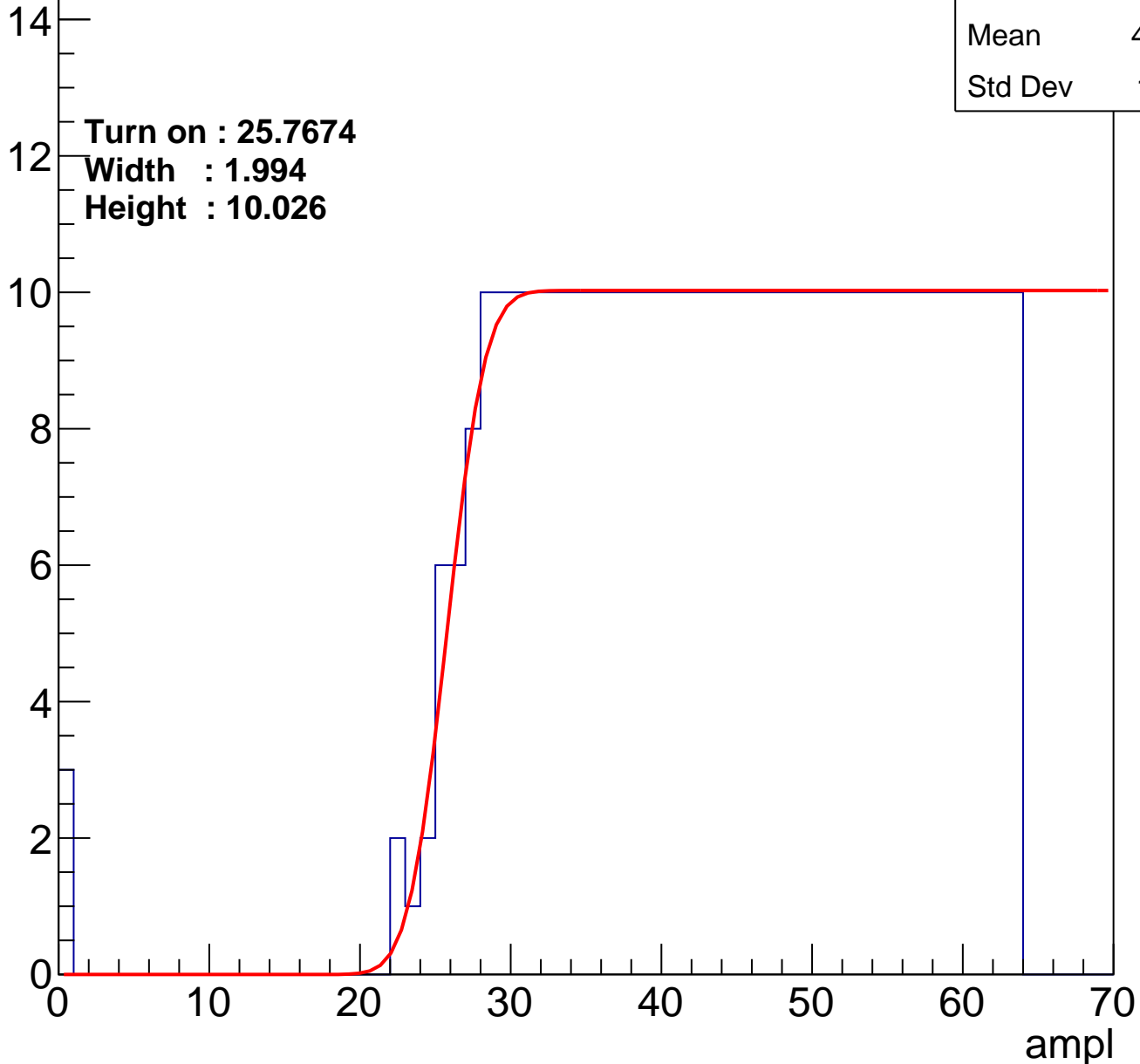
Entries	388
Mean	43.86
Std Dev	11.81

Turn on : 25.7674

Width : 1.994

Height : 10.026

Entry



B0L000S, U21-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.6904

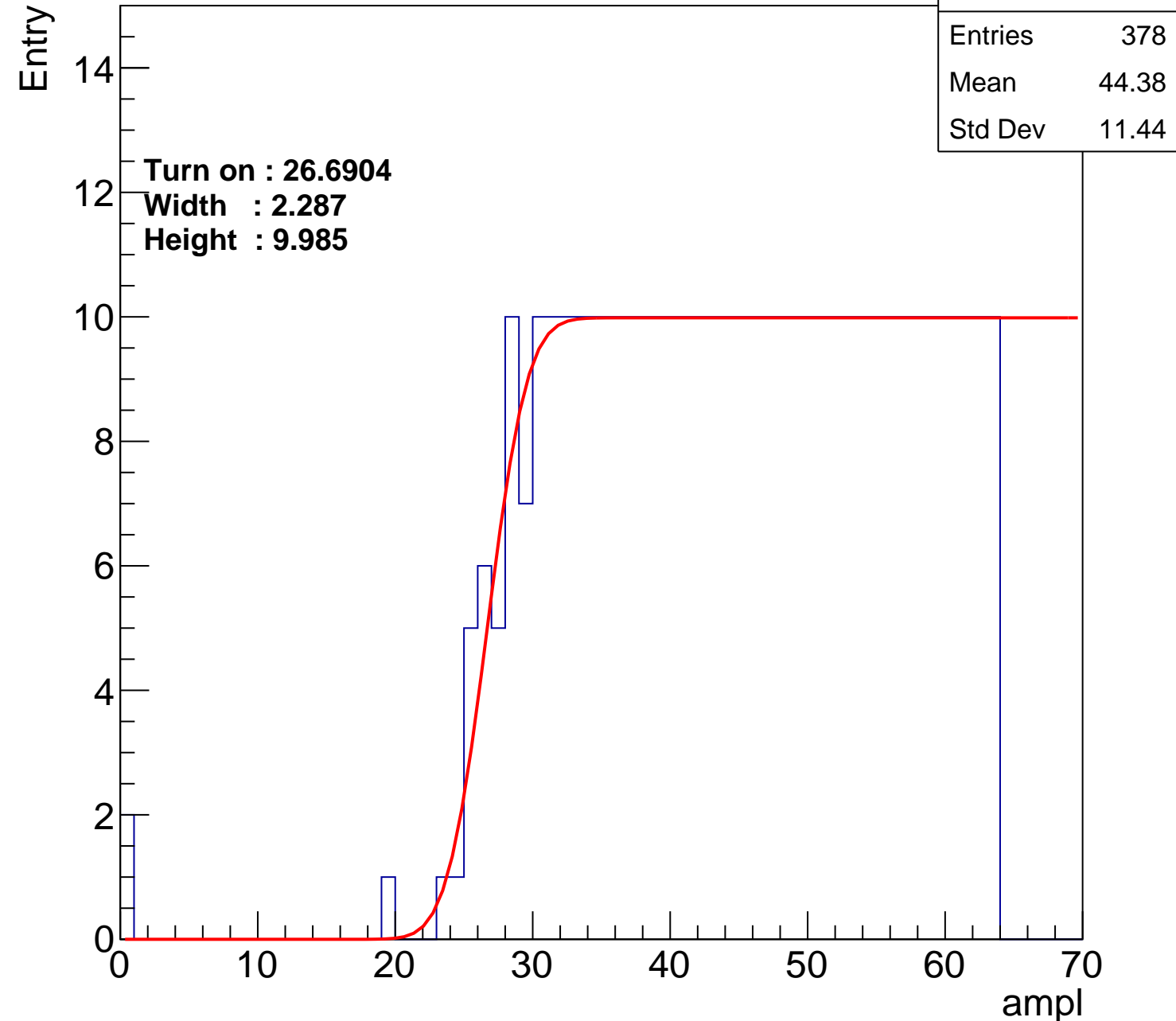
Width : 2.287

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 28.0607

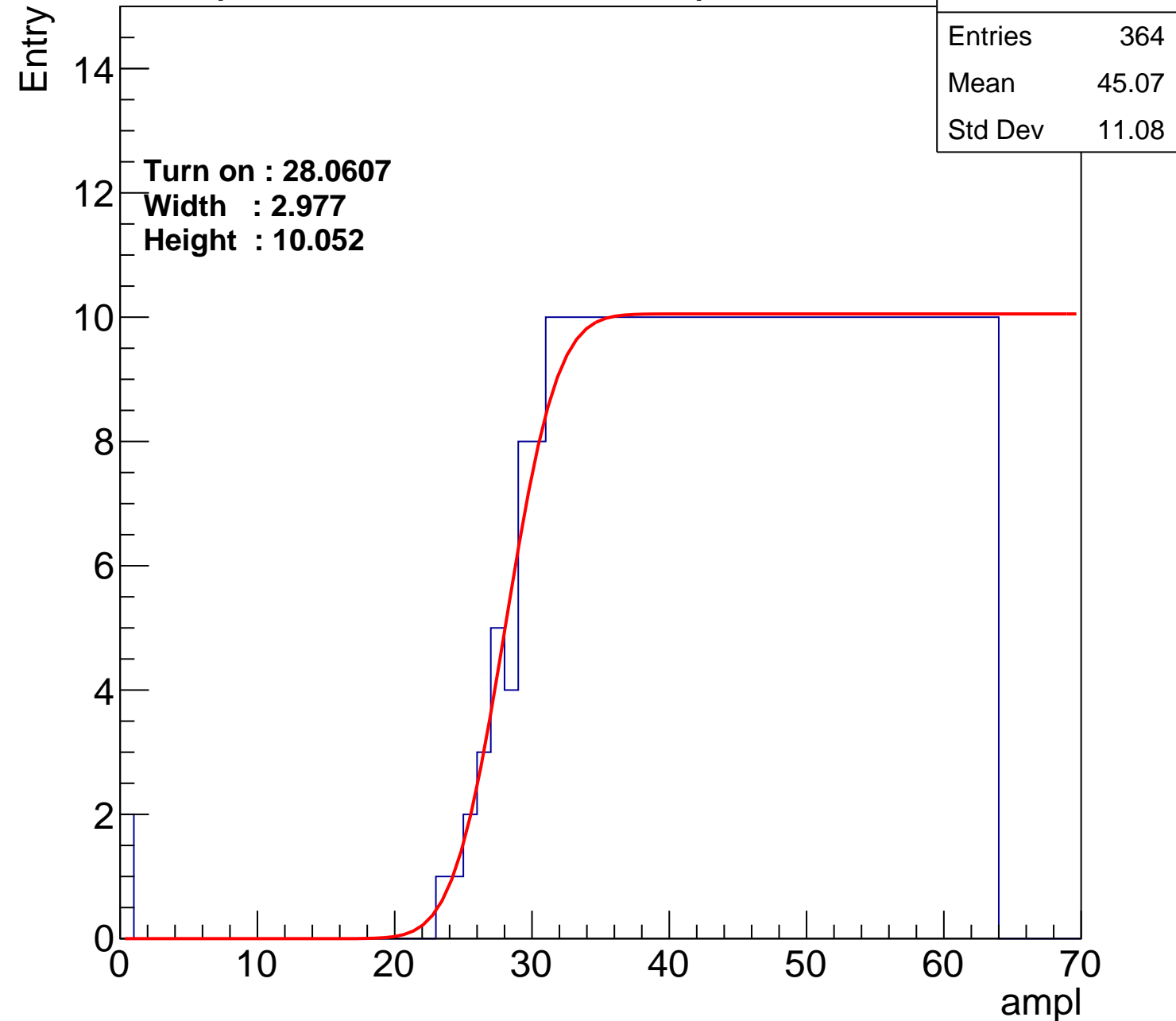
Width : 2.977

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 26.7145

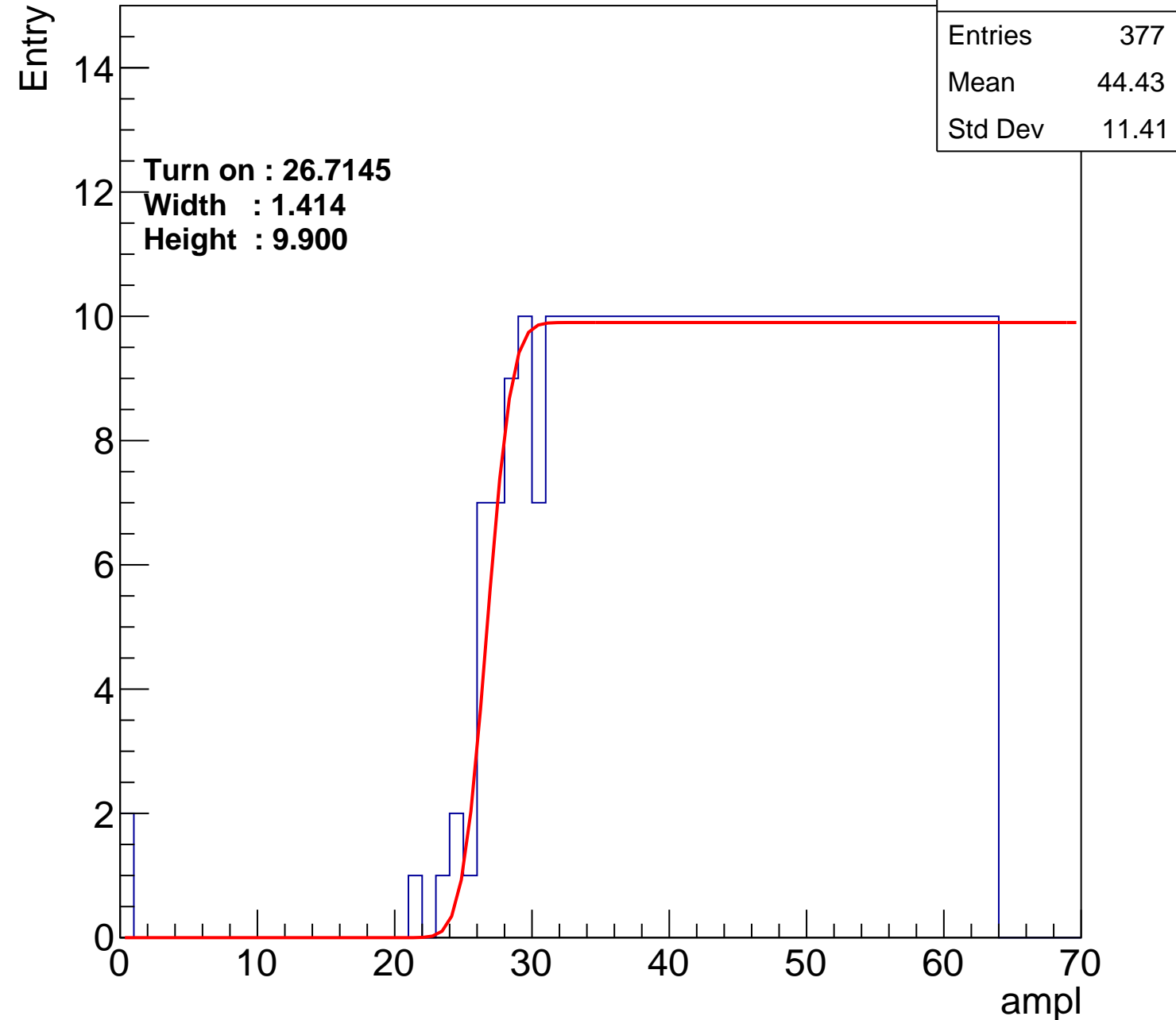
Width : 1.414

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch76

calib_packv5_042523_0143.root, FC#5, port B1

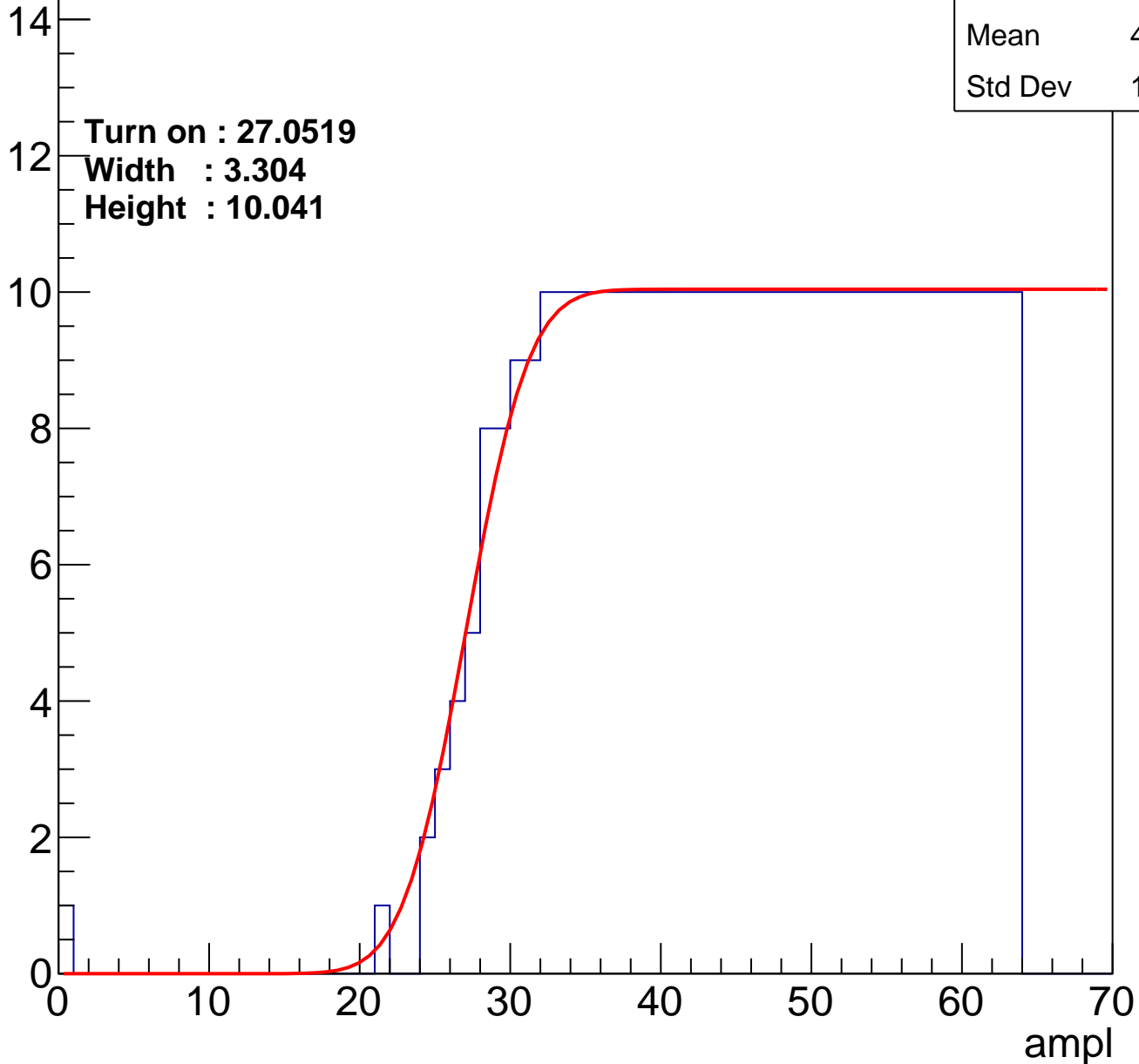
Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 27.0519

Width : 3.304

Height : 10.041

Entry



B0L000S, U21-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.36
Std Dev	10.99

Turn on : 29.1661

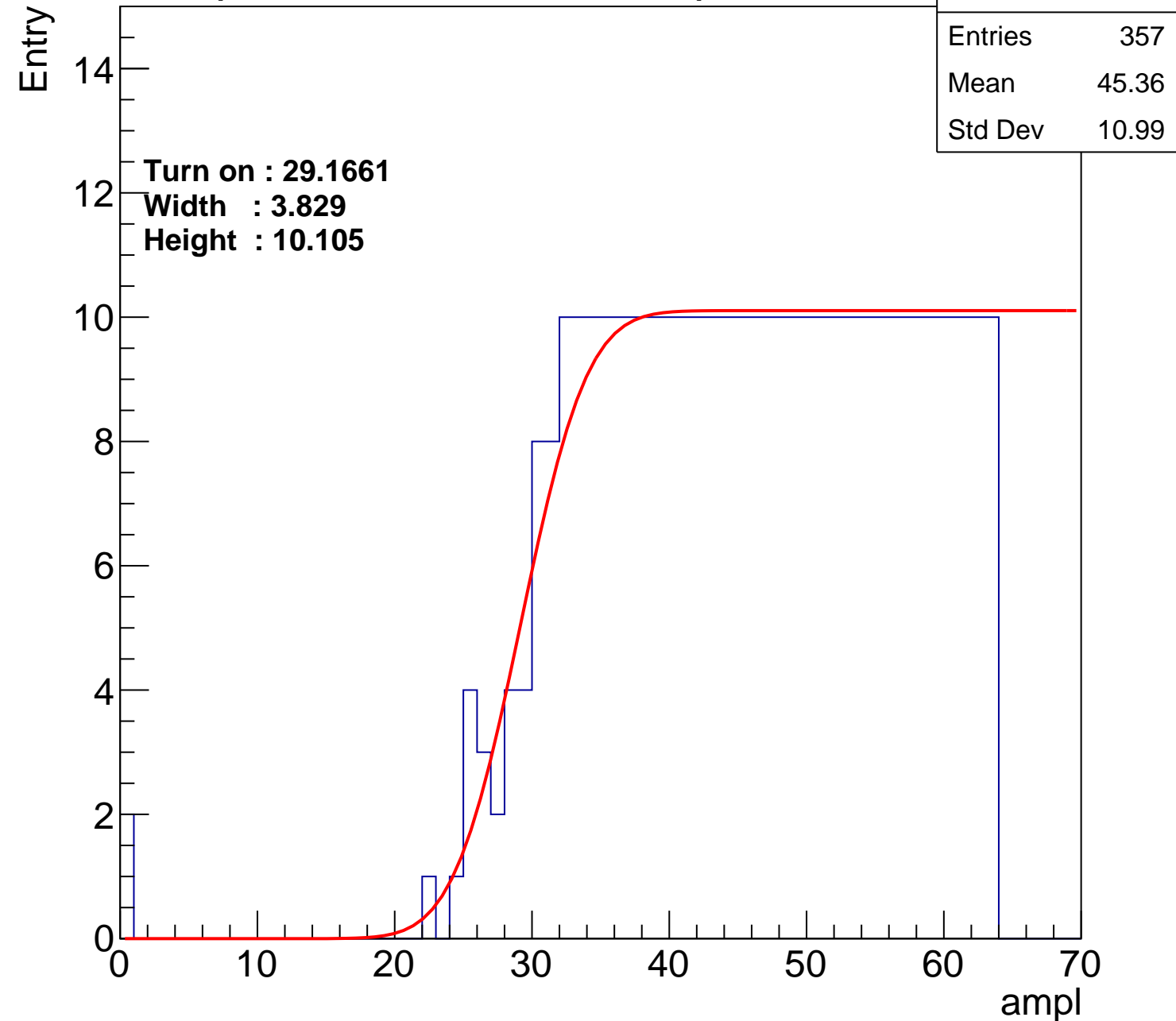
Width : 3.829

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.96
Std Dev	11.55

Turn on : 25.1052

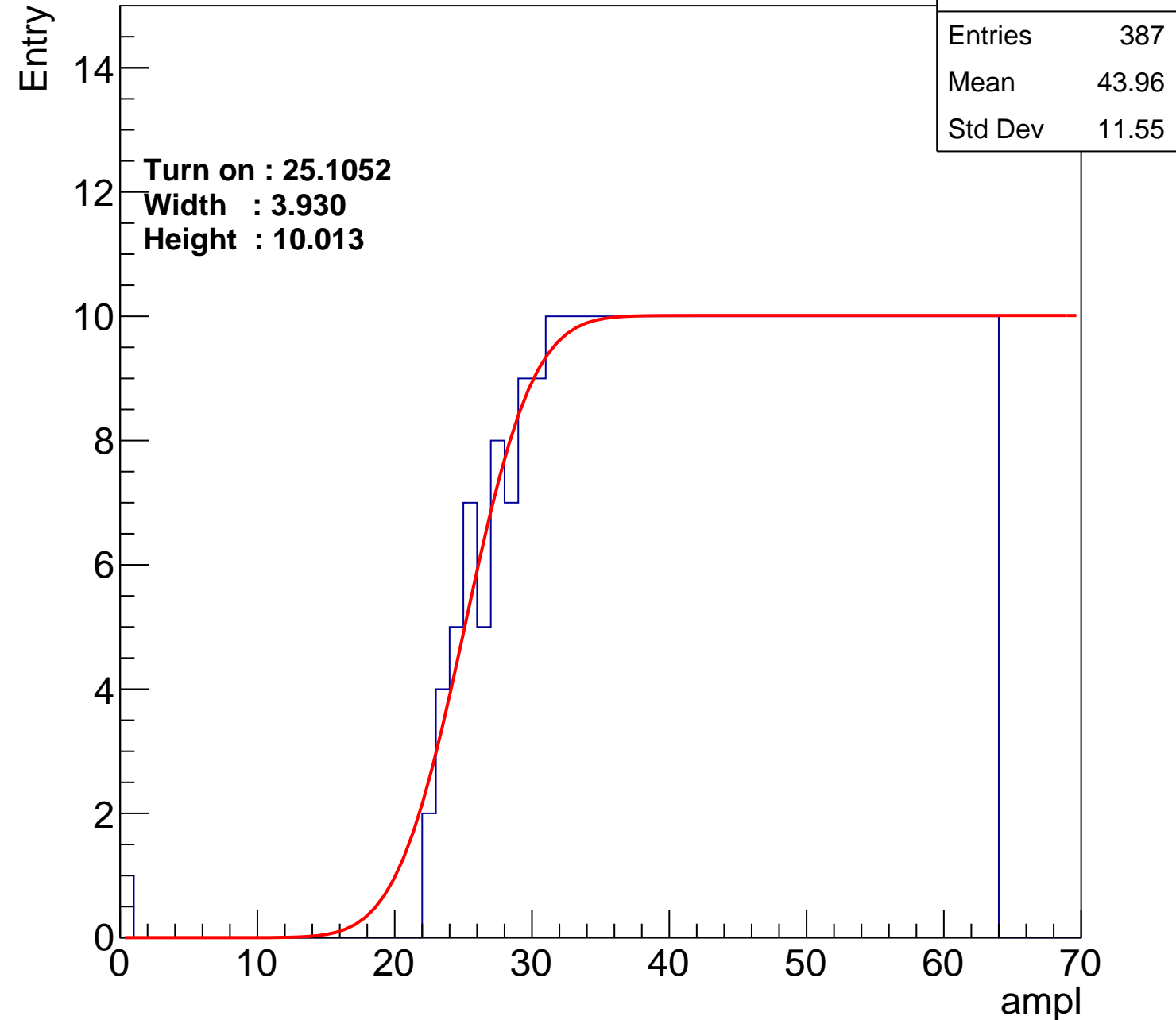
Width : 3.930

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 26.5520

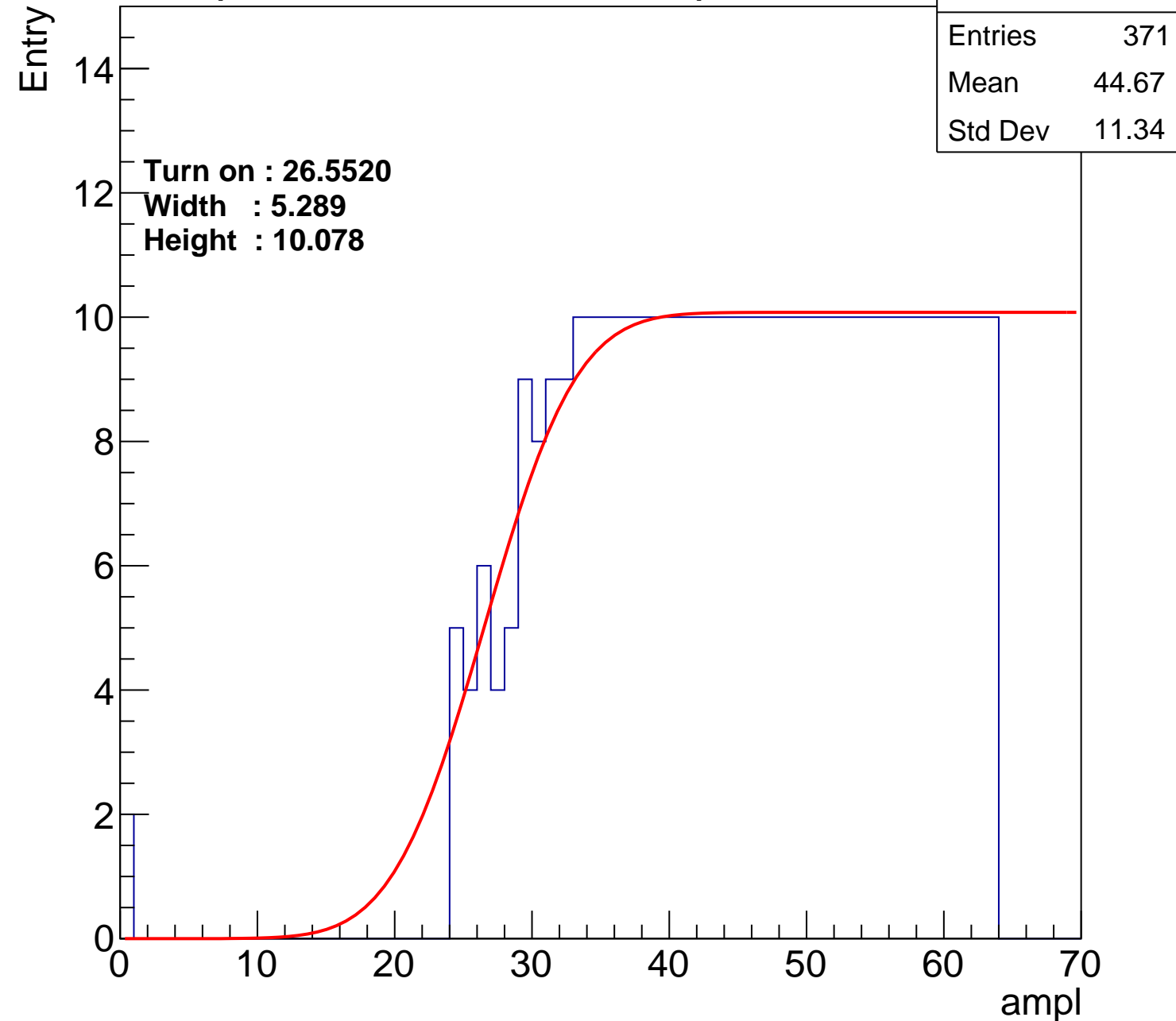
Width : 5.289

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch80

calib_packv5_042523_0143.root, FC#5, port B1

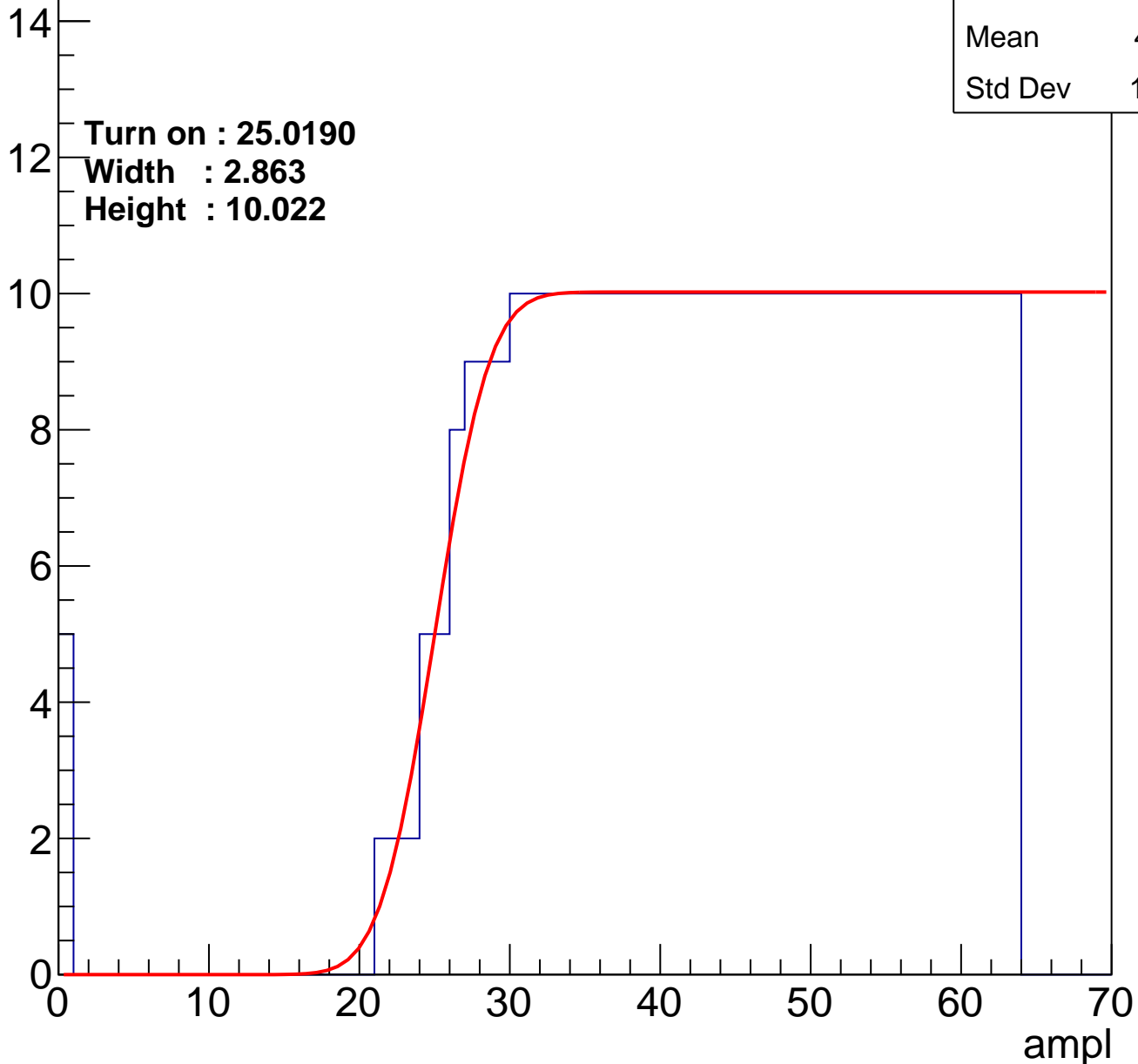
Entries	396
Mean	43.31
Std Dev	12.37

Turn on : 25.0190

Width : 2.863

Height : 10.022

Entry



B0L000S, U21-ch81

calib_packv5_042523_0143.root, FC#5, port B1

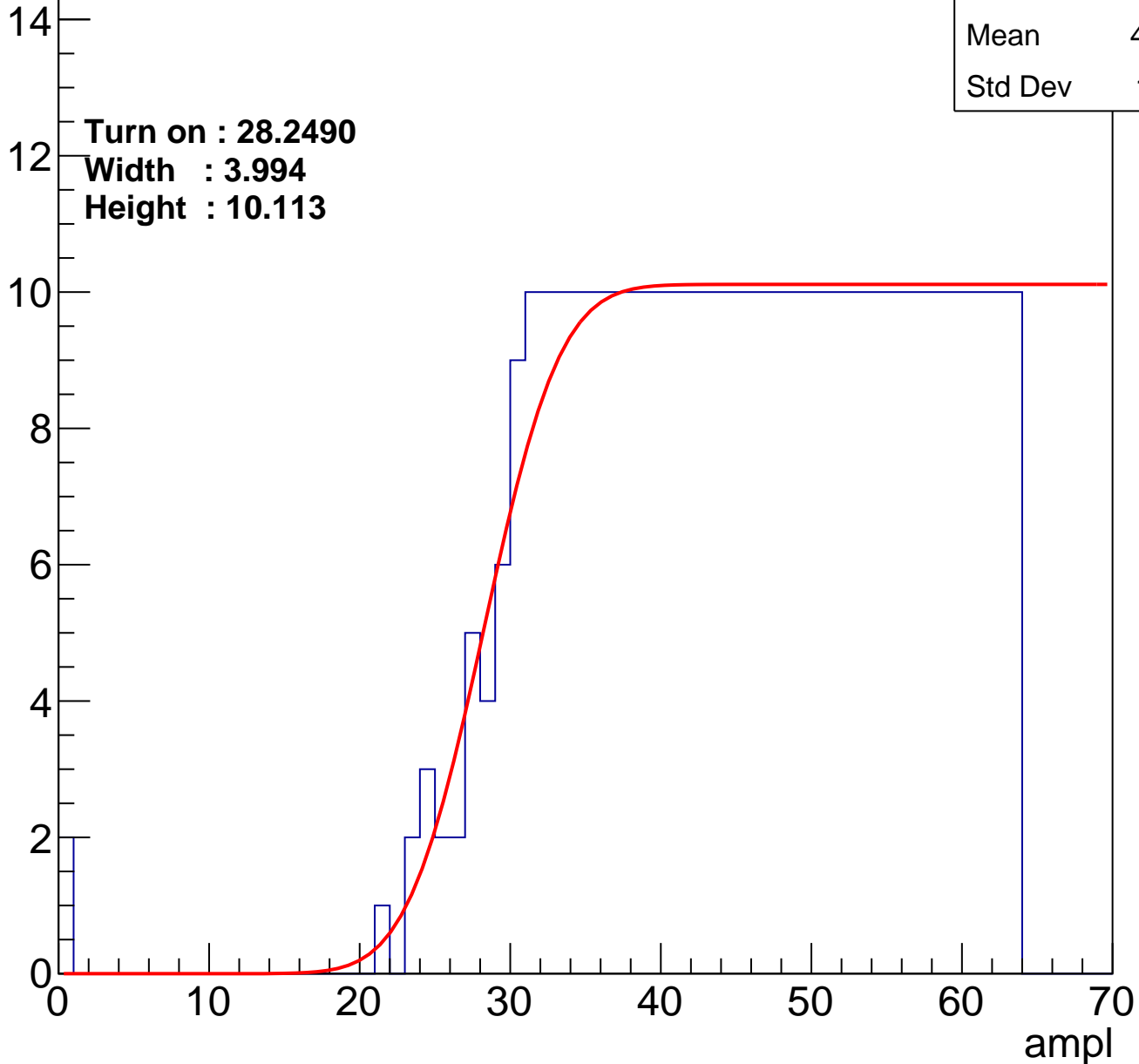
Entries	366
Mean	44.92
Std Dev	11.21

Turn on : 28.2490

Width : 3.994

Height : 10.113

Entry



B0L000S, U21-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.81
Std Dev	12

Turn on : 25.6372

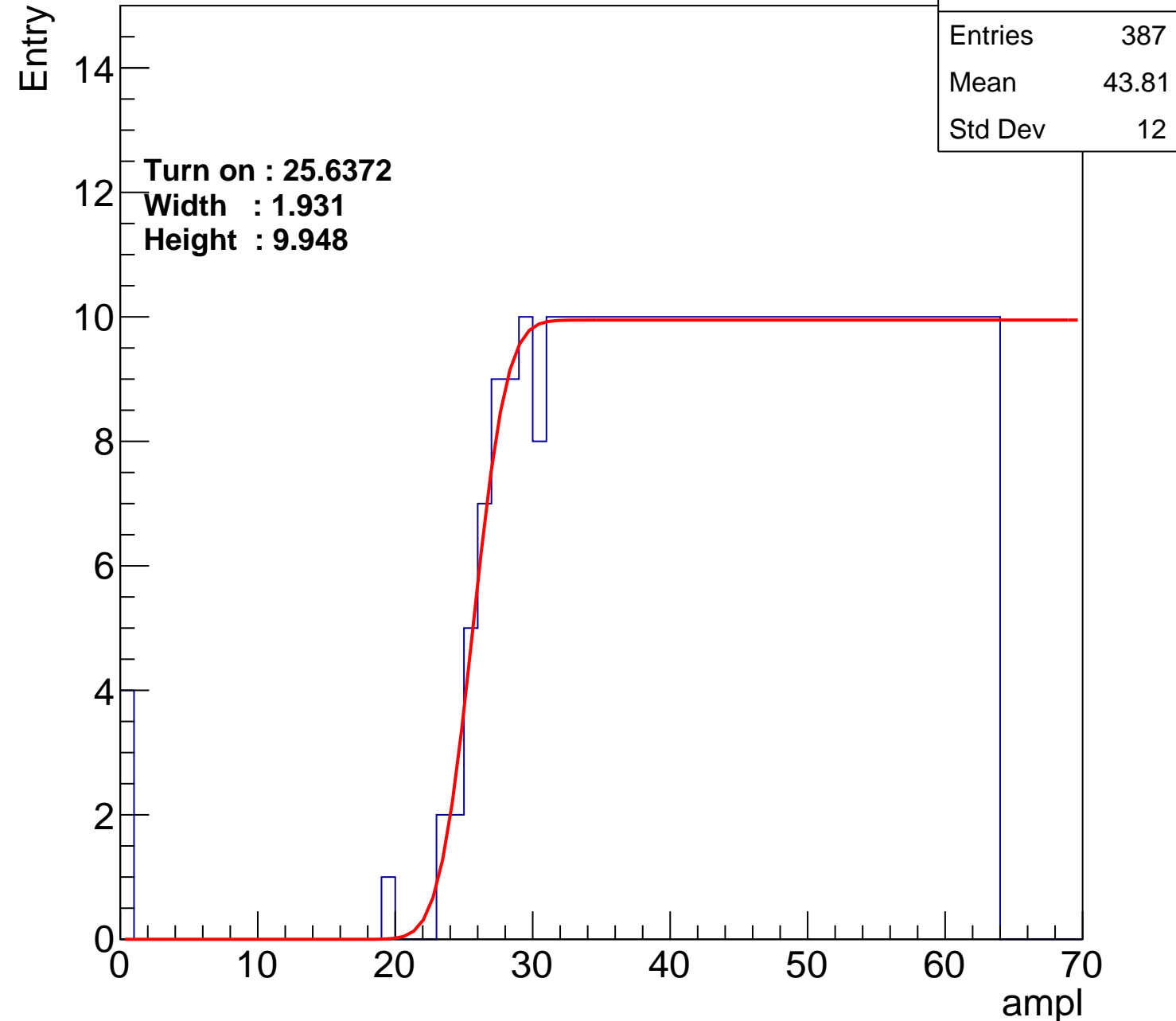
Width : 1.931

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch83

calib_packv5_042523_0143.root, FC#5, port B1

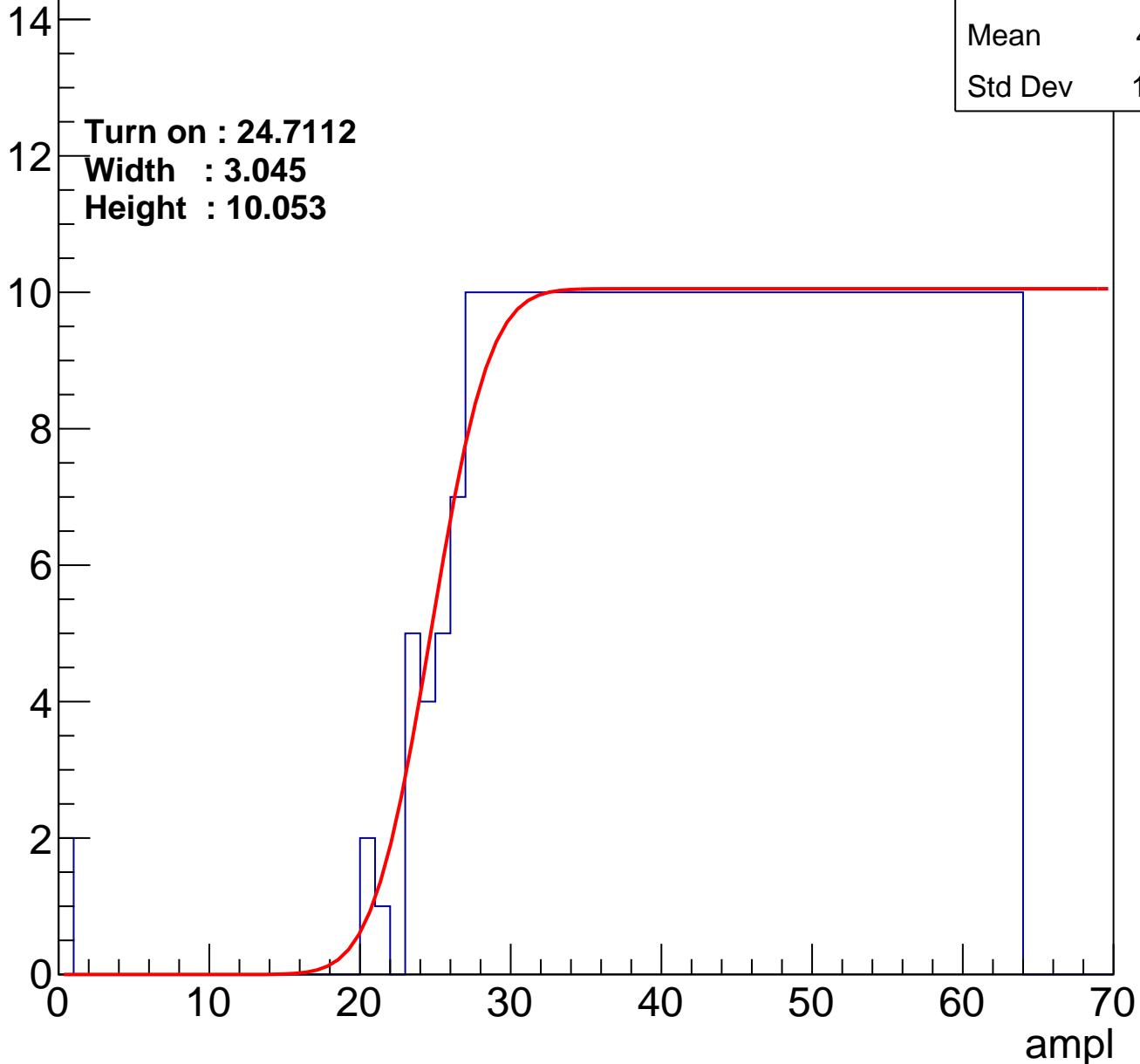
Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.7112

Width : 3.045

Height : 10.053

Entry



B0L000S, U21-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.12
Std Dev	11.47

Turn on : 26.2386

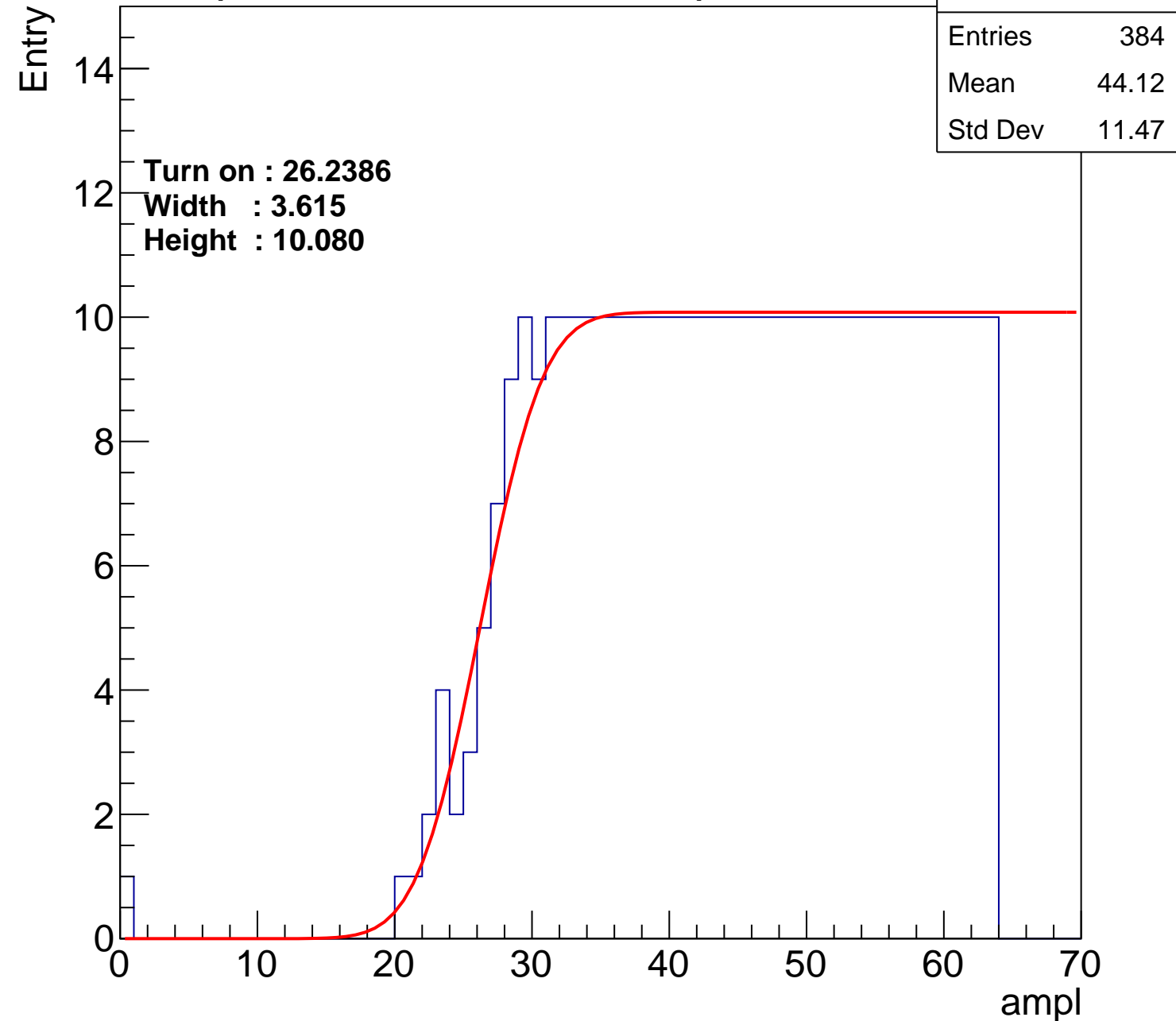
Width : 3.615

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 28.1168

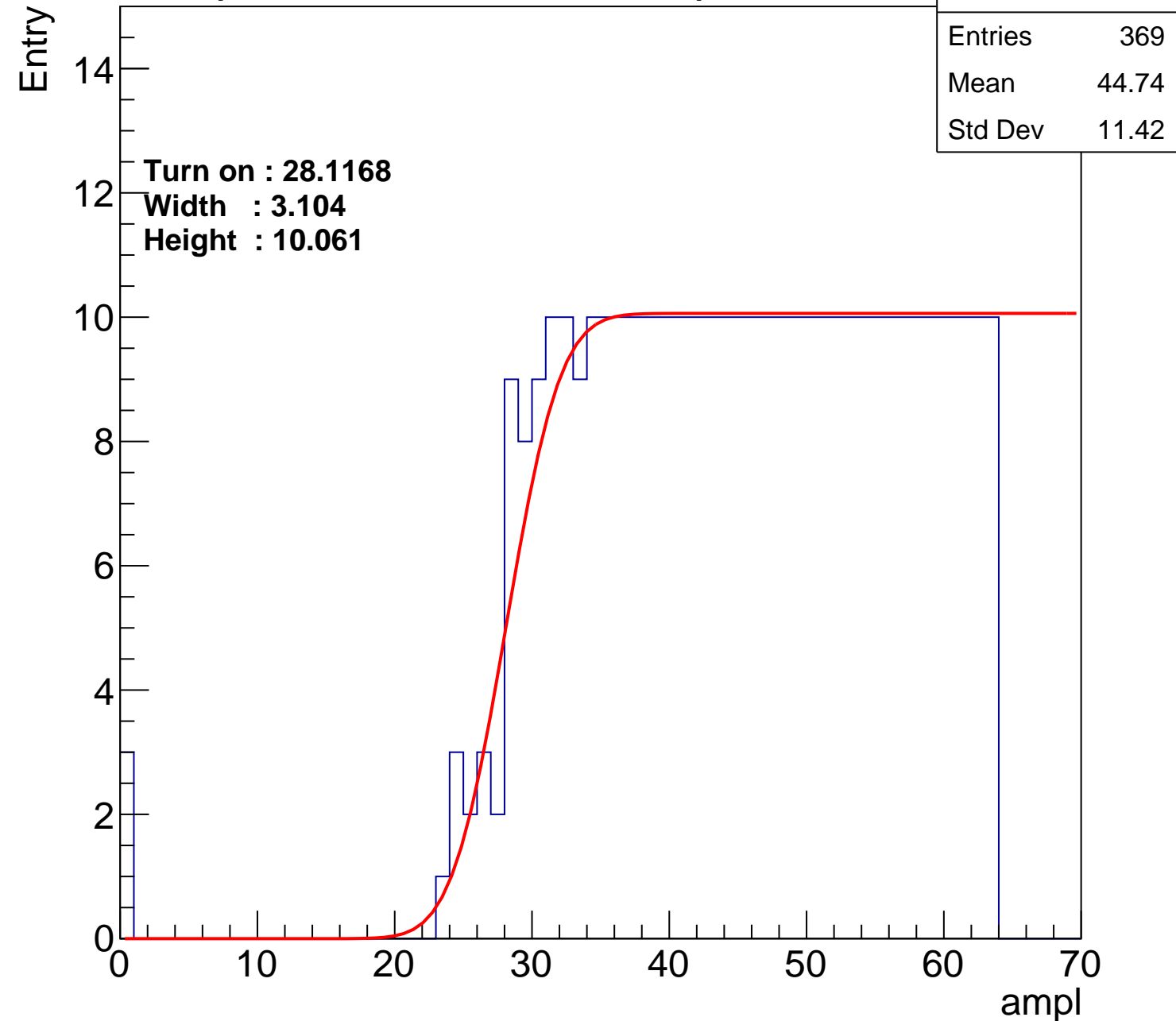
Width : 3.104

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.71
Std Dev	11.82

Turn on : 25.3655

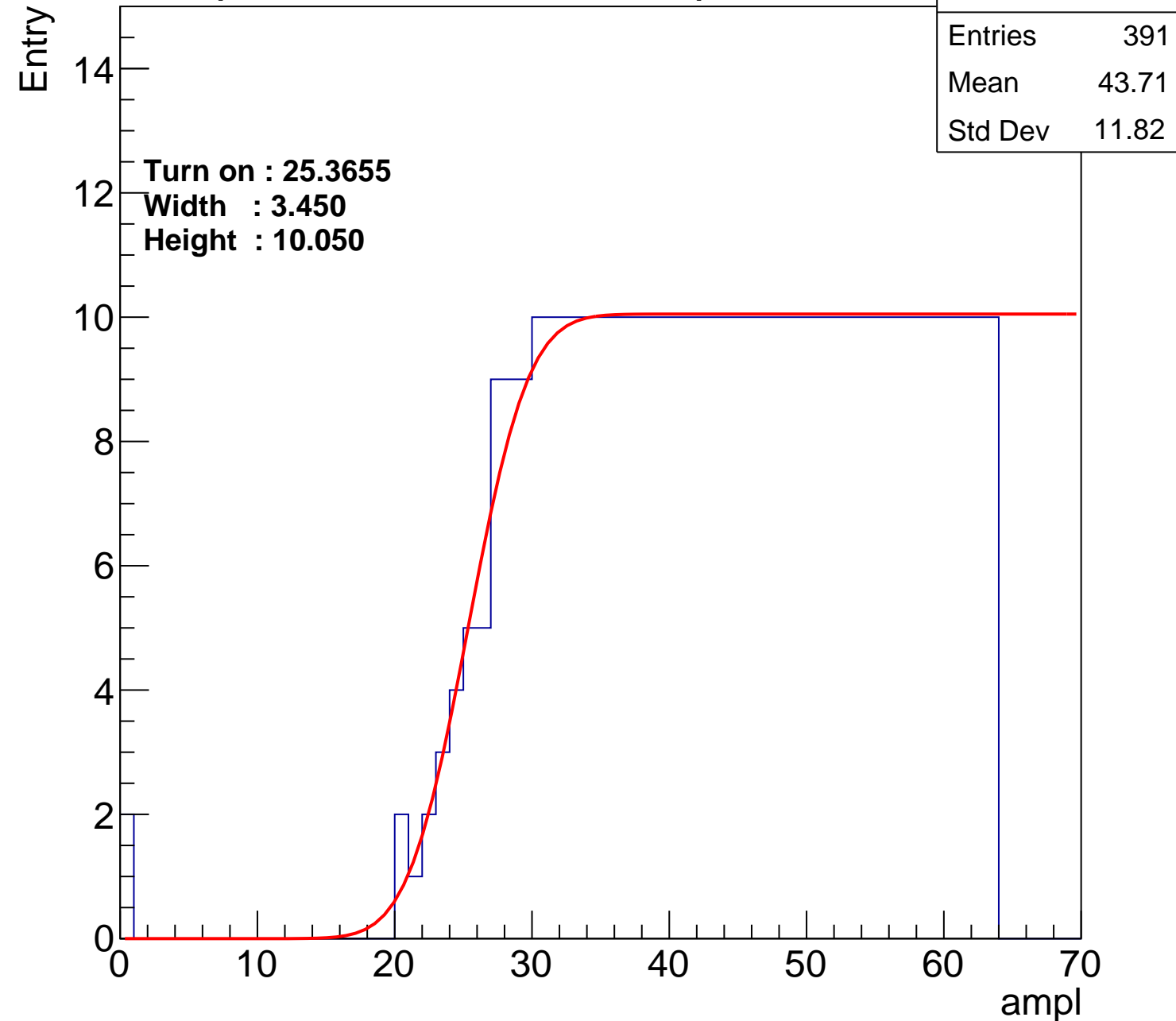
Width : 3.450

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.37

Turn on : 27.1186

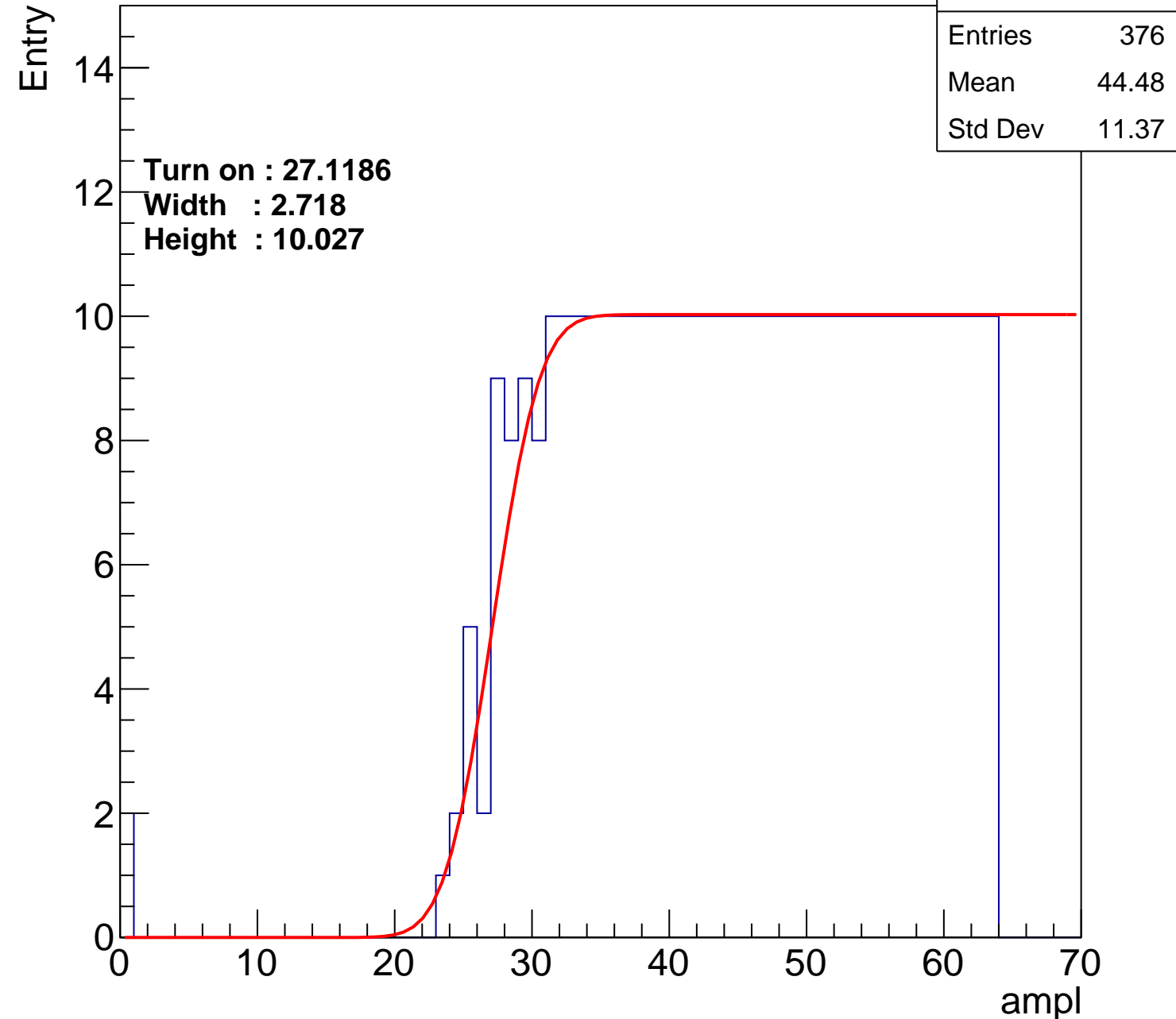
Width : 2.718

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch88

calib_packv5_042523_0143.root, FC#5, port B1

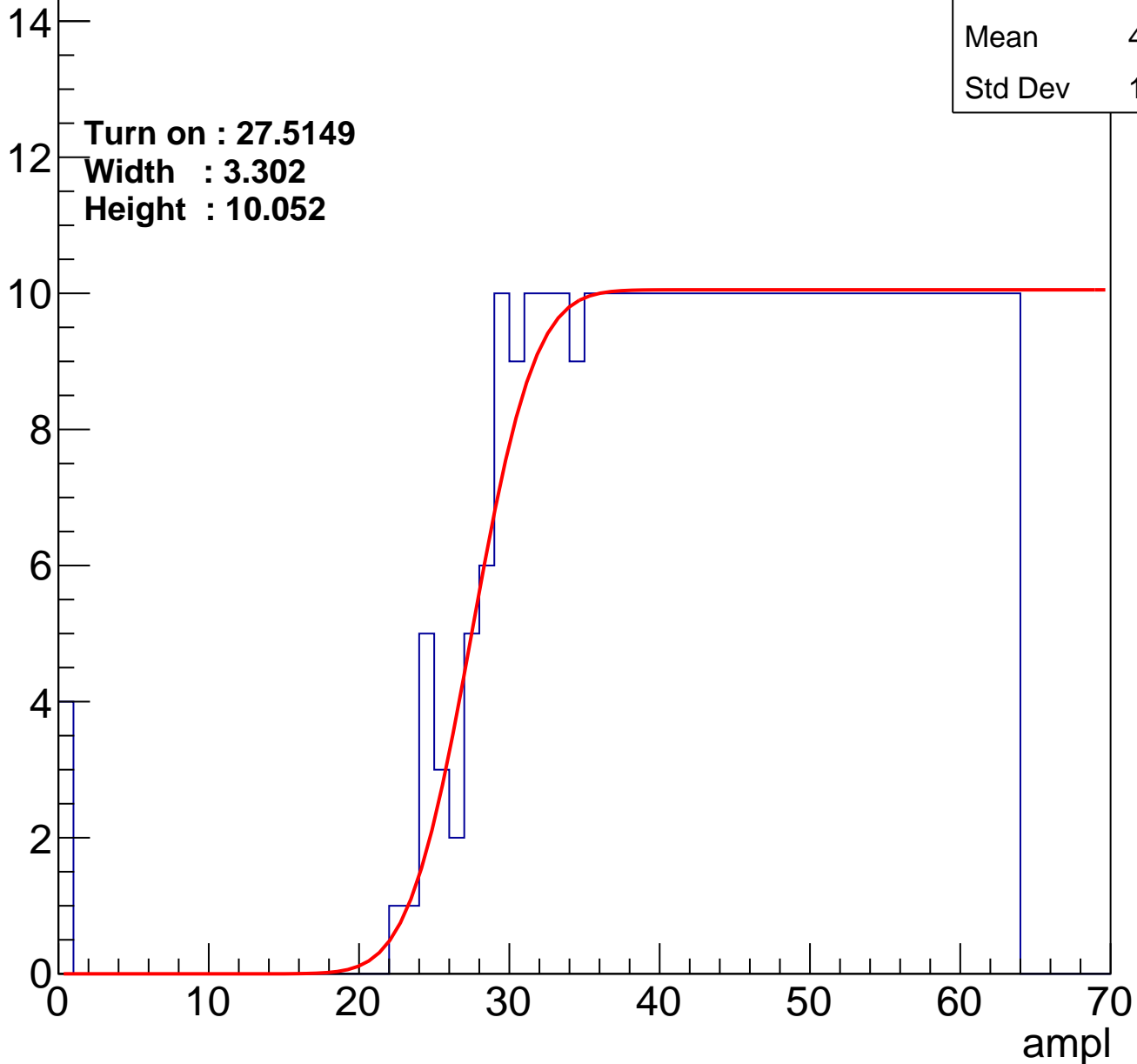
Entries	375
Mean	44.35
Std Dev	11.79

Turn on : 27.5149

Width : 3.302

Height : 10.052

Entry



B0L000S, U21-ch89

calib_packv5_042523_0143.root, FC#5, port B1

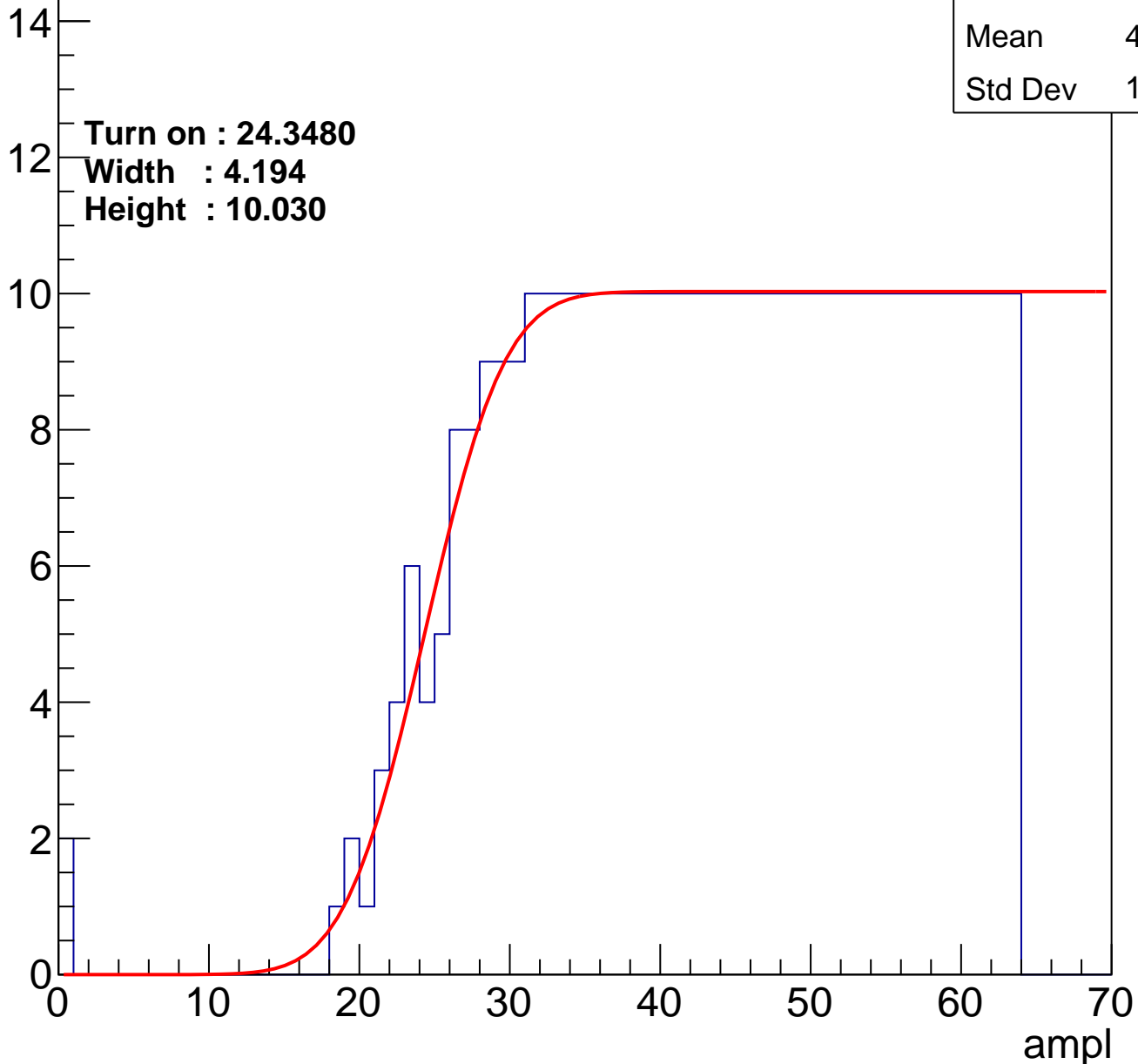
Entries	401
Mean	43.15
Std Dev	12.19

Turn on : 24.3480

Width : 4.194

Height : 10.030

Entry



B0L000S, U21-ch90

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 25.9796

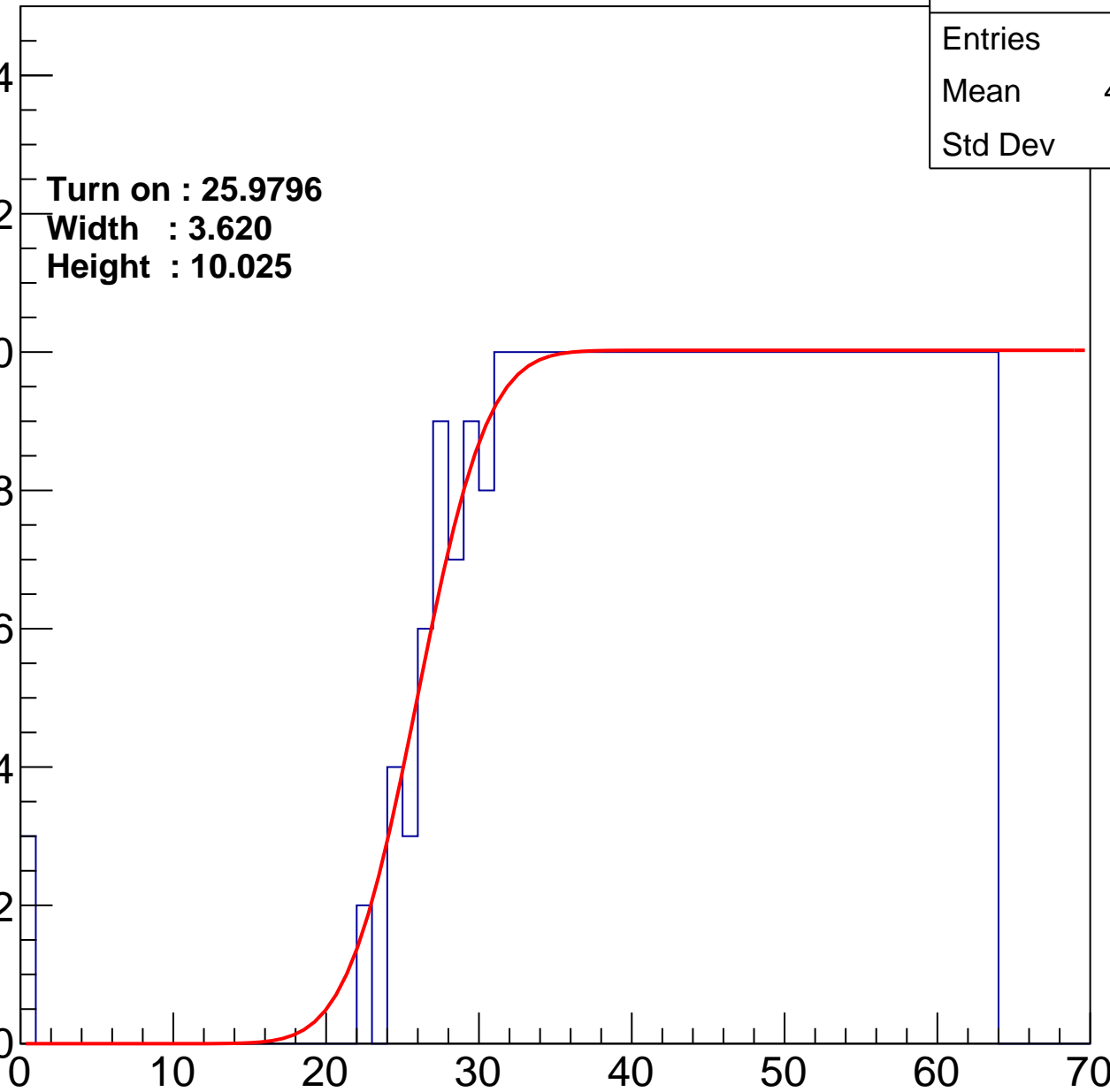
Width : 3.620

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 25.3039

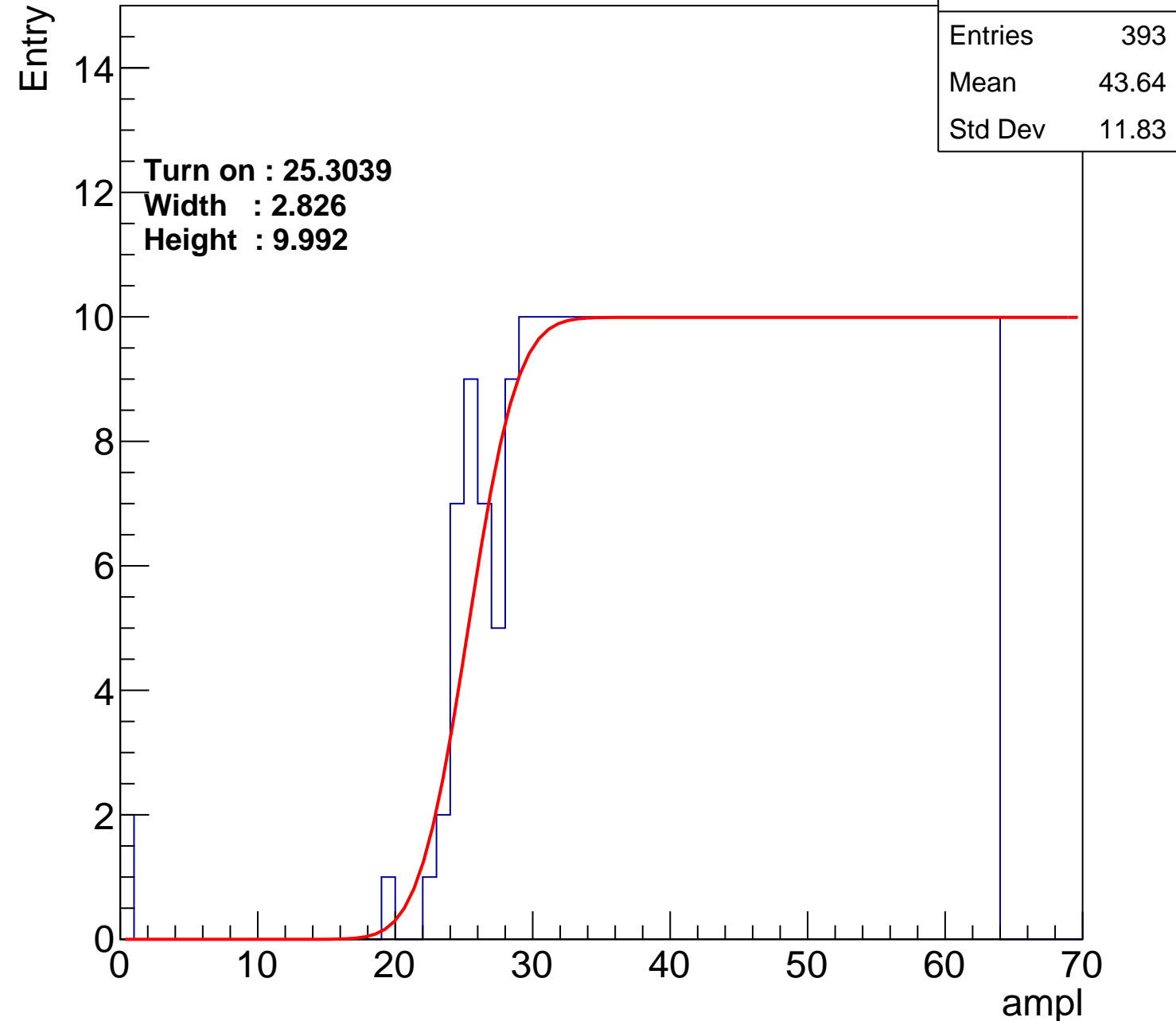
Width : 2.826

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.24
Std Dev	11.39

Turn on : 26.4616

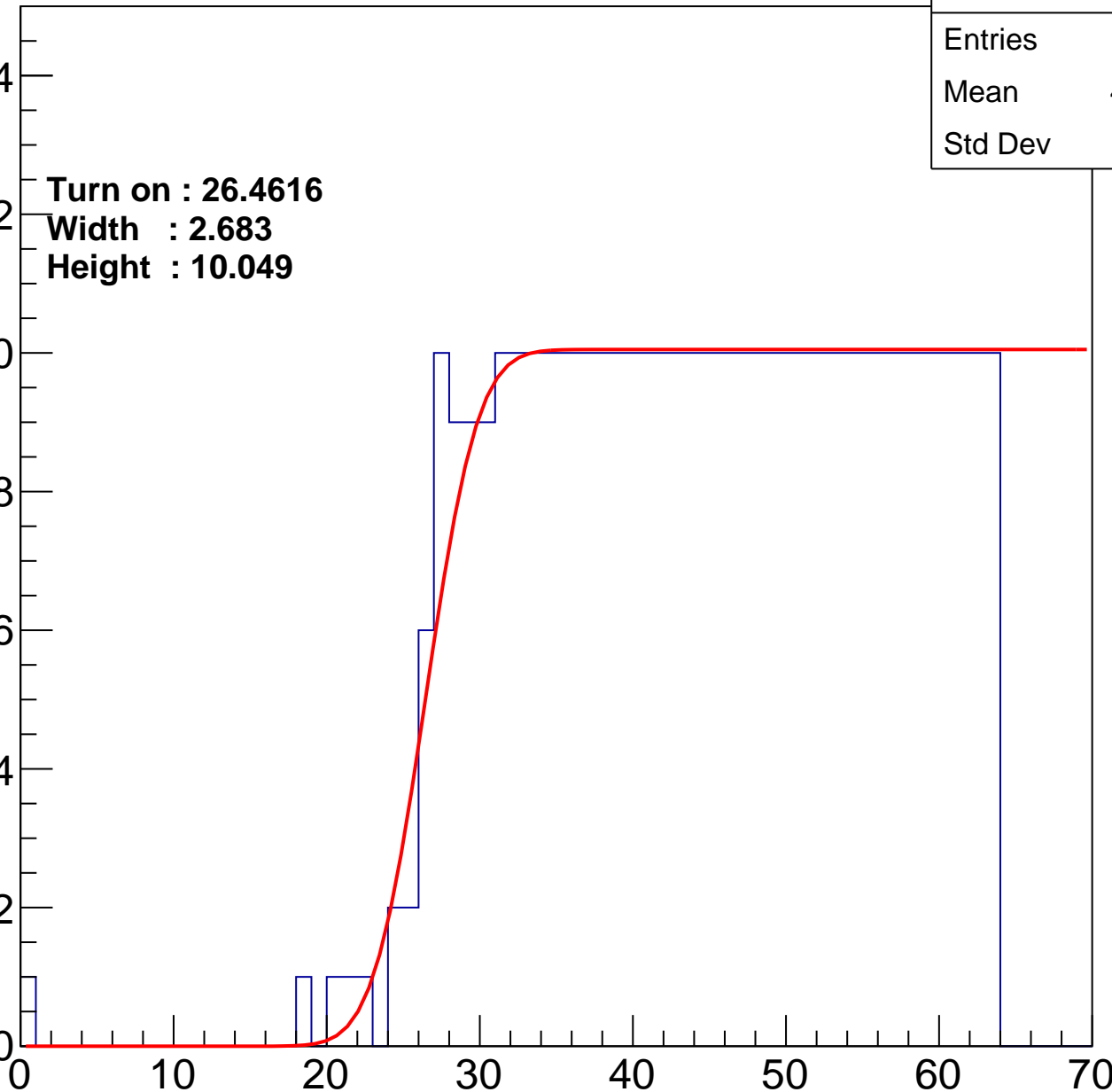
Width : 2.683

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch93

calib_packv5_042523_0143.root, FC#5, port B1

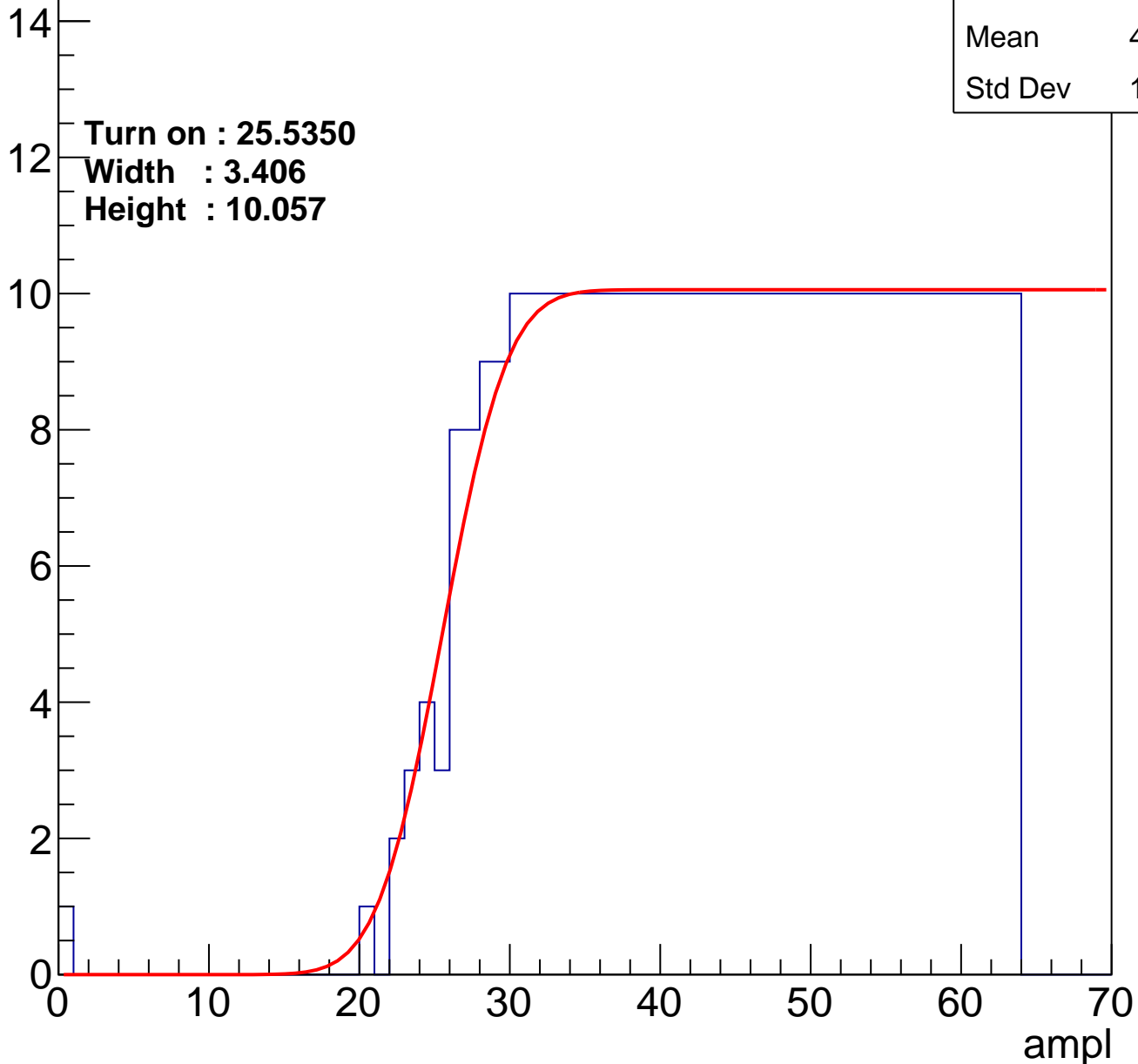
Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 25.5350

Width : 3.406

Height : 10.057

Entry



B0L000S, U21-ch94

calib_packv5_042523_0143.root, FC#5, port B1

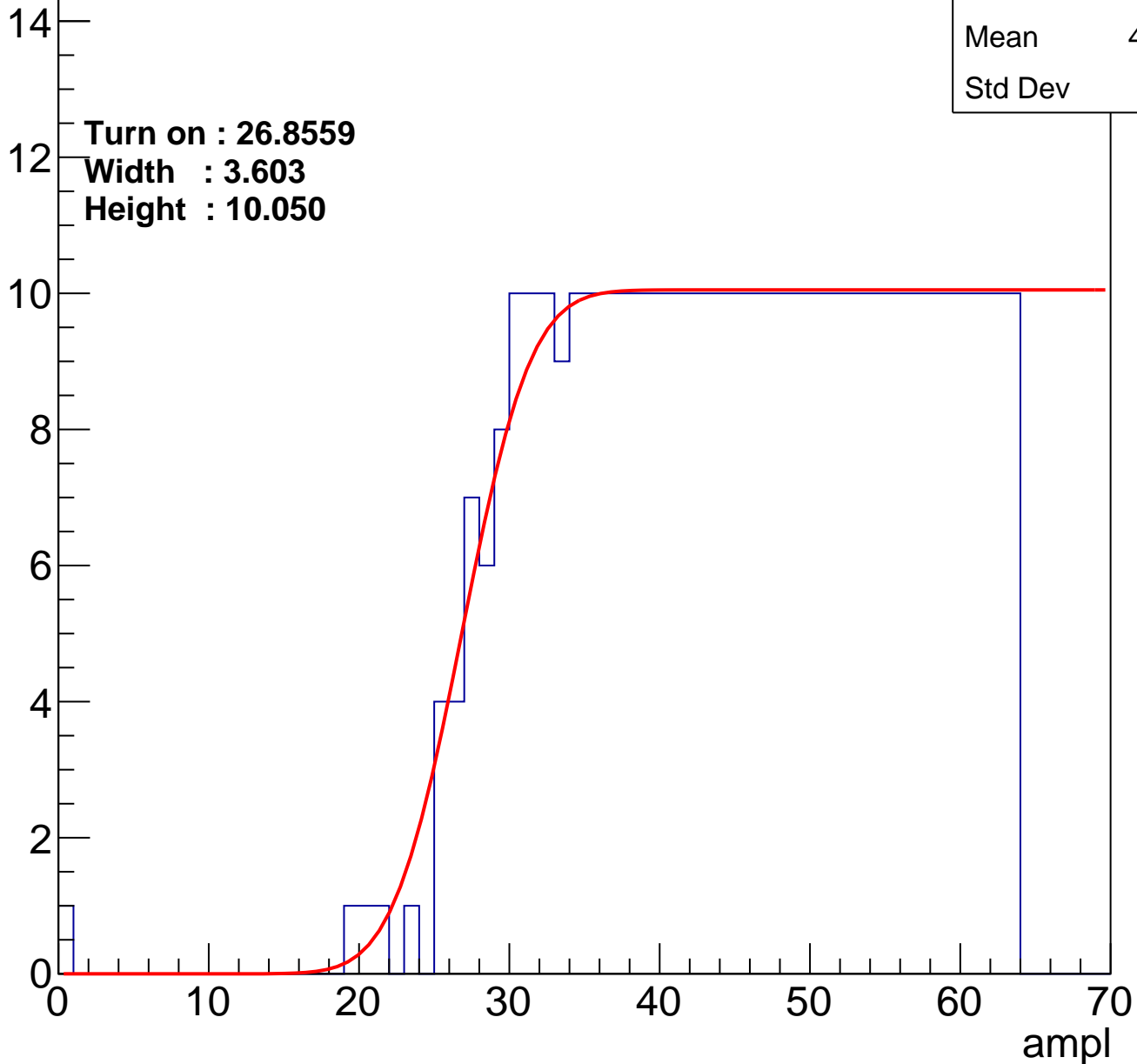
Entries	373
Mean	44.65
Std Dev	11.2

Turn on : 26.8559

Width : 3.603

Height : 10.050

Entry



B0L000S, U21-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.23
Std Dev	11.69

Turn on : 26.2677

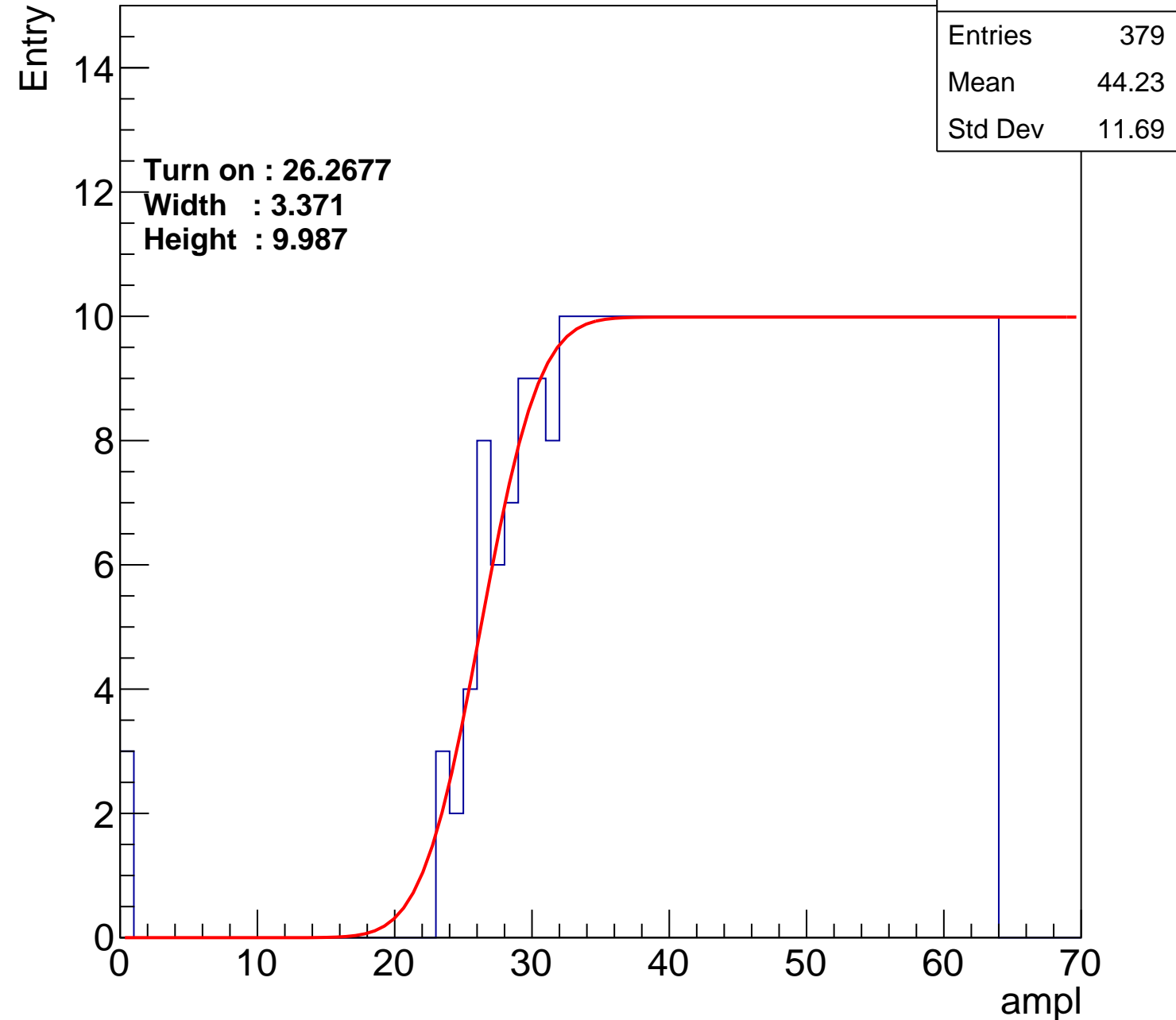
Width : 3.371

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.28
Std Dev	11.53

Turn on : 25.8099

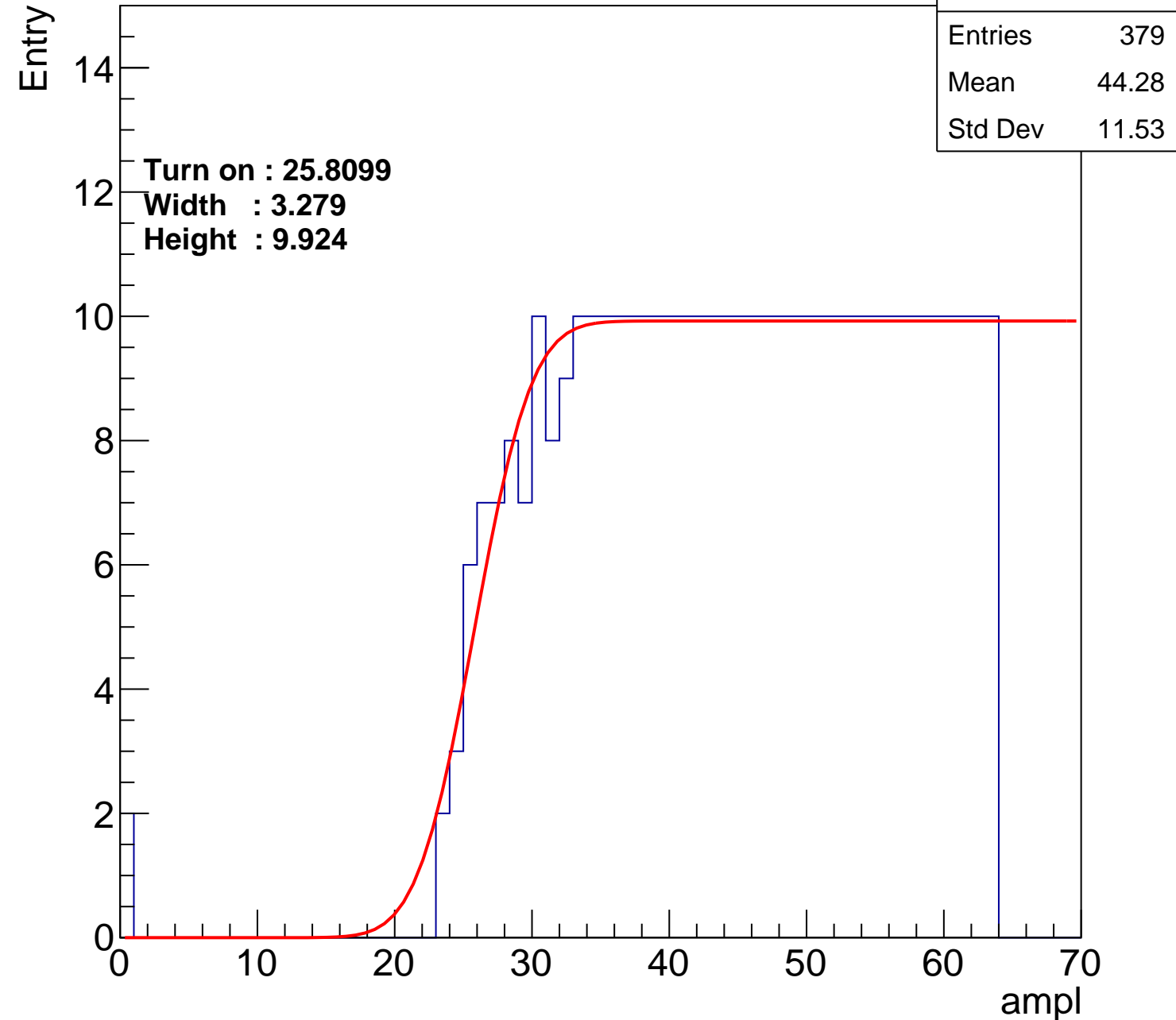
Width : 3.279

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.7
Std Dev	12.19

Turn on : 25.7990

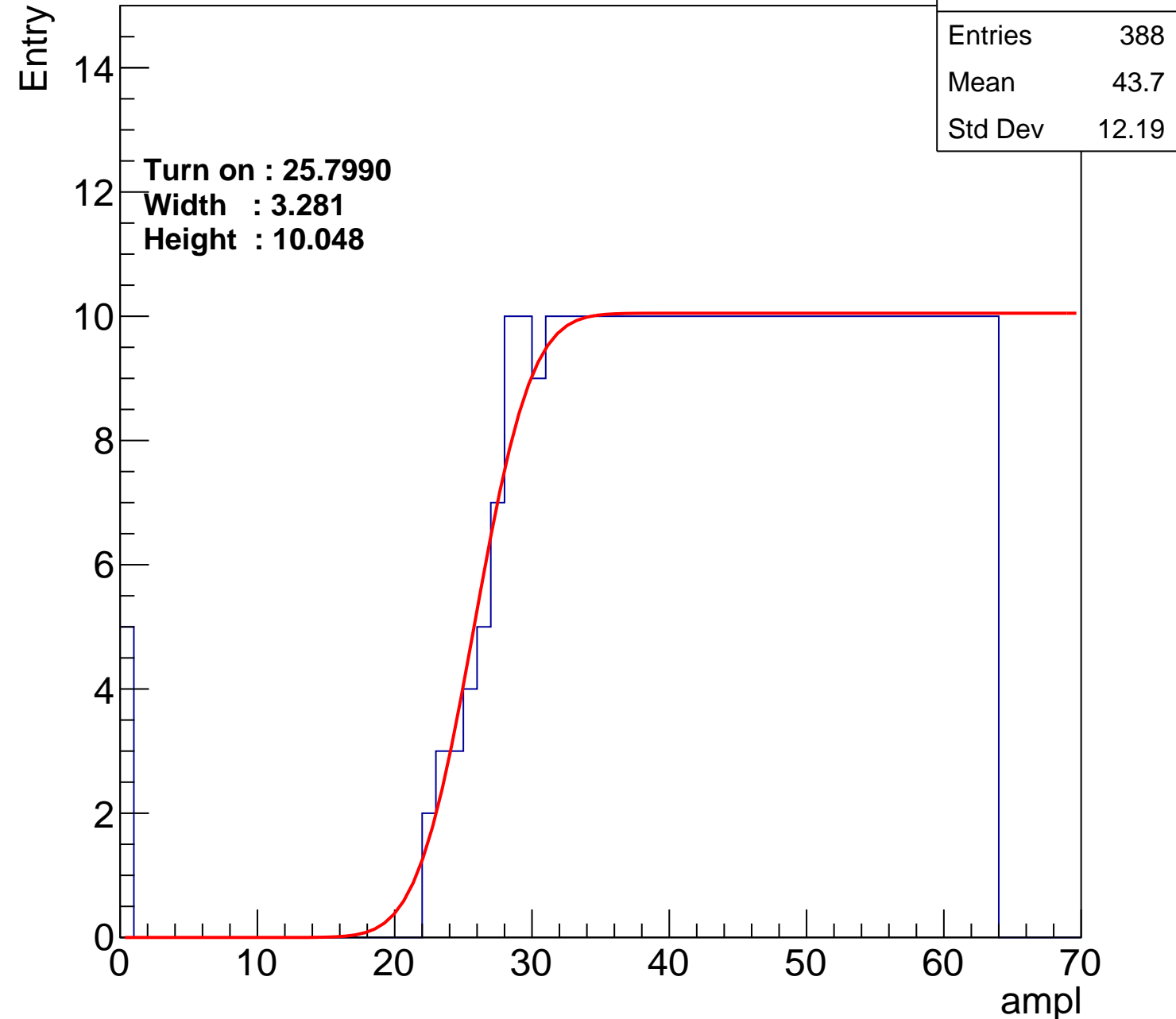
Width : 3.281

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.87
Std Dev	11.39

Turn on : 28.4477

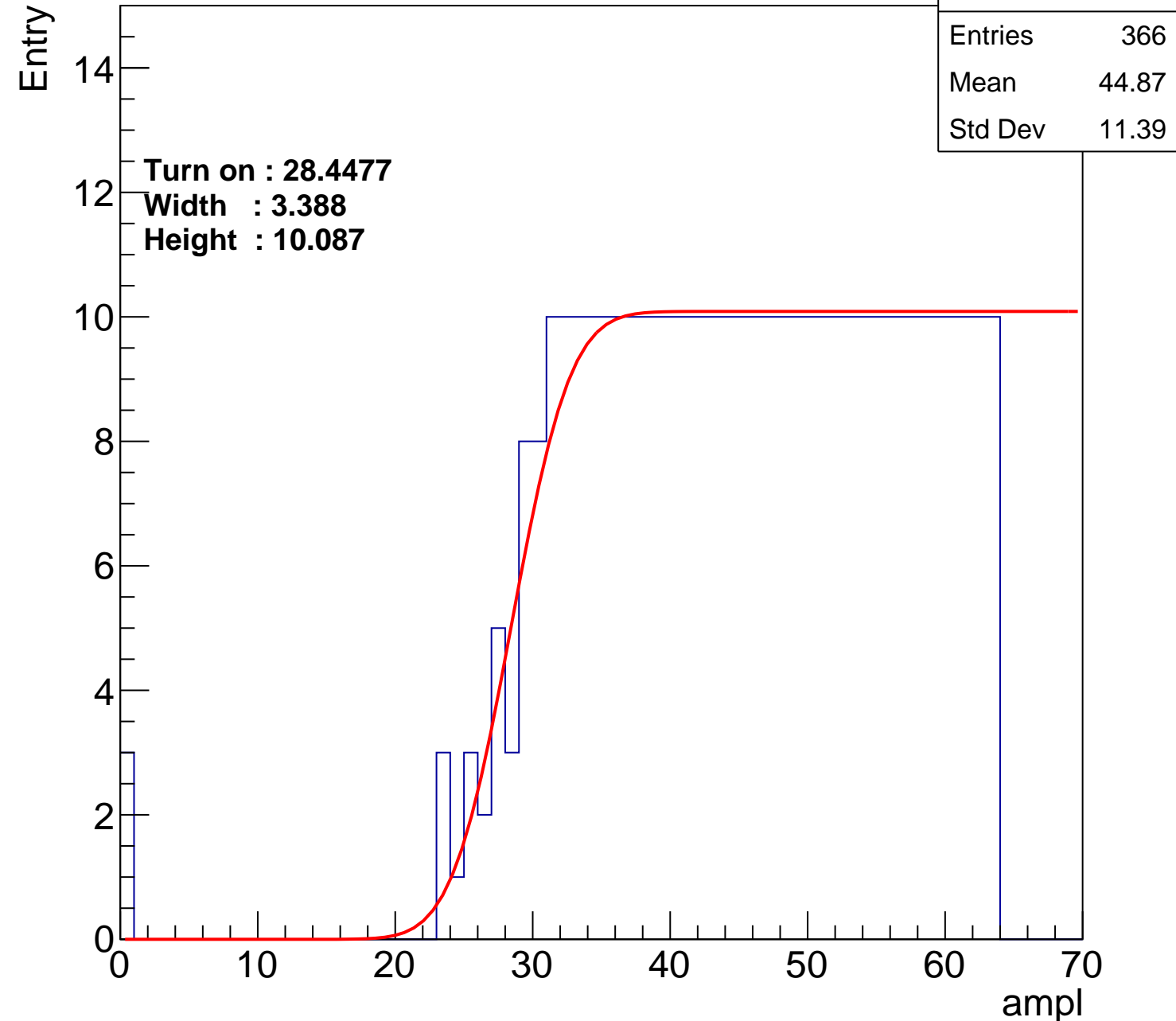
Width : 3.388

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.66
Std Dev	10.78

Turn on : 29.1138

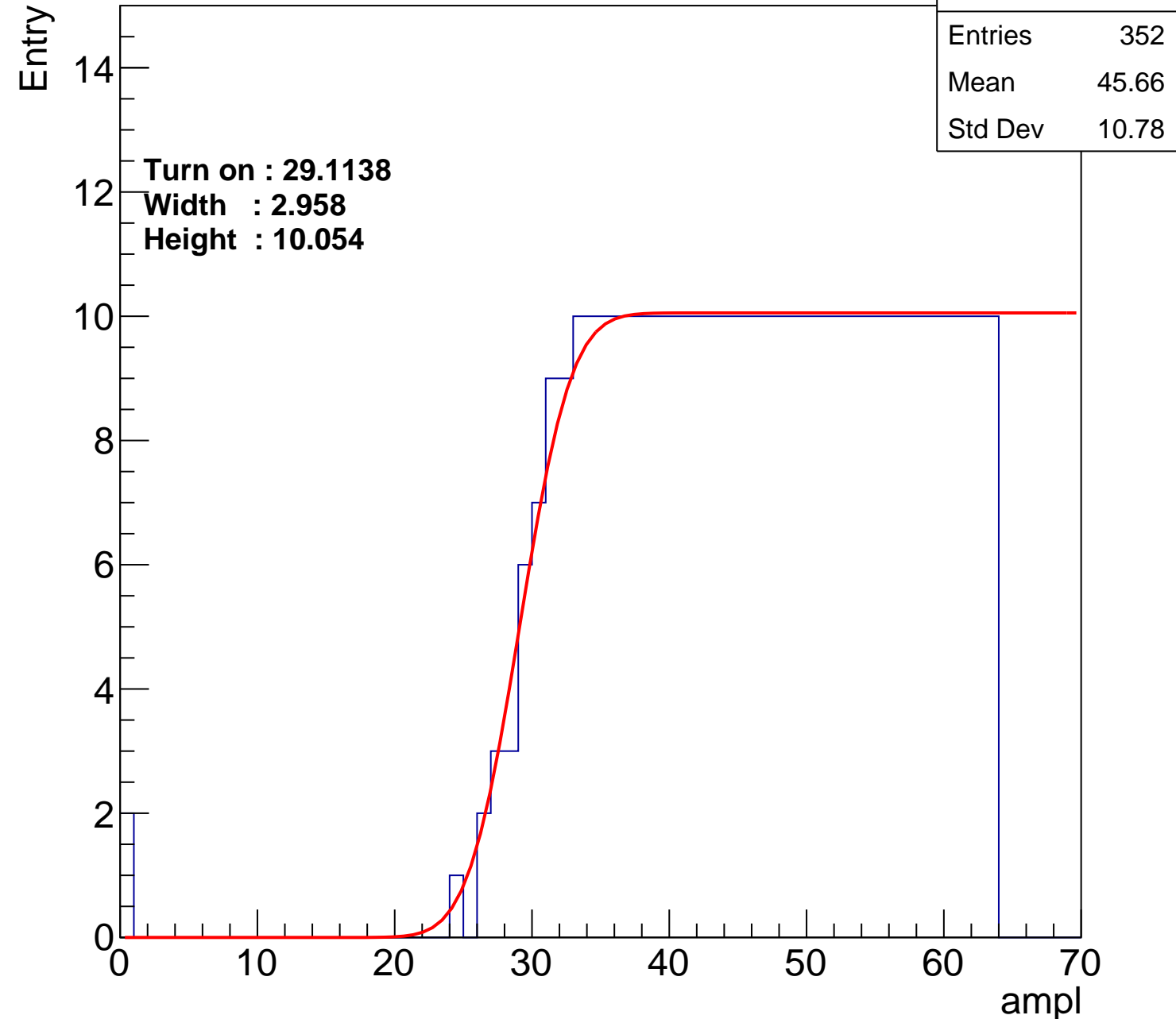
Width : 2.958

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch100

calib_packv5_042523_0143.root, FC#5, port B1

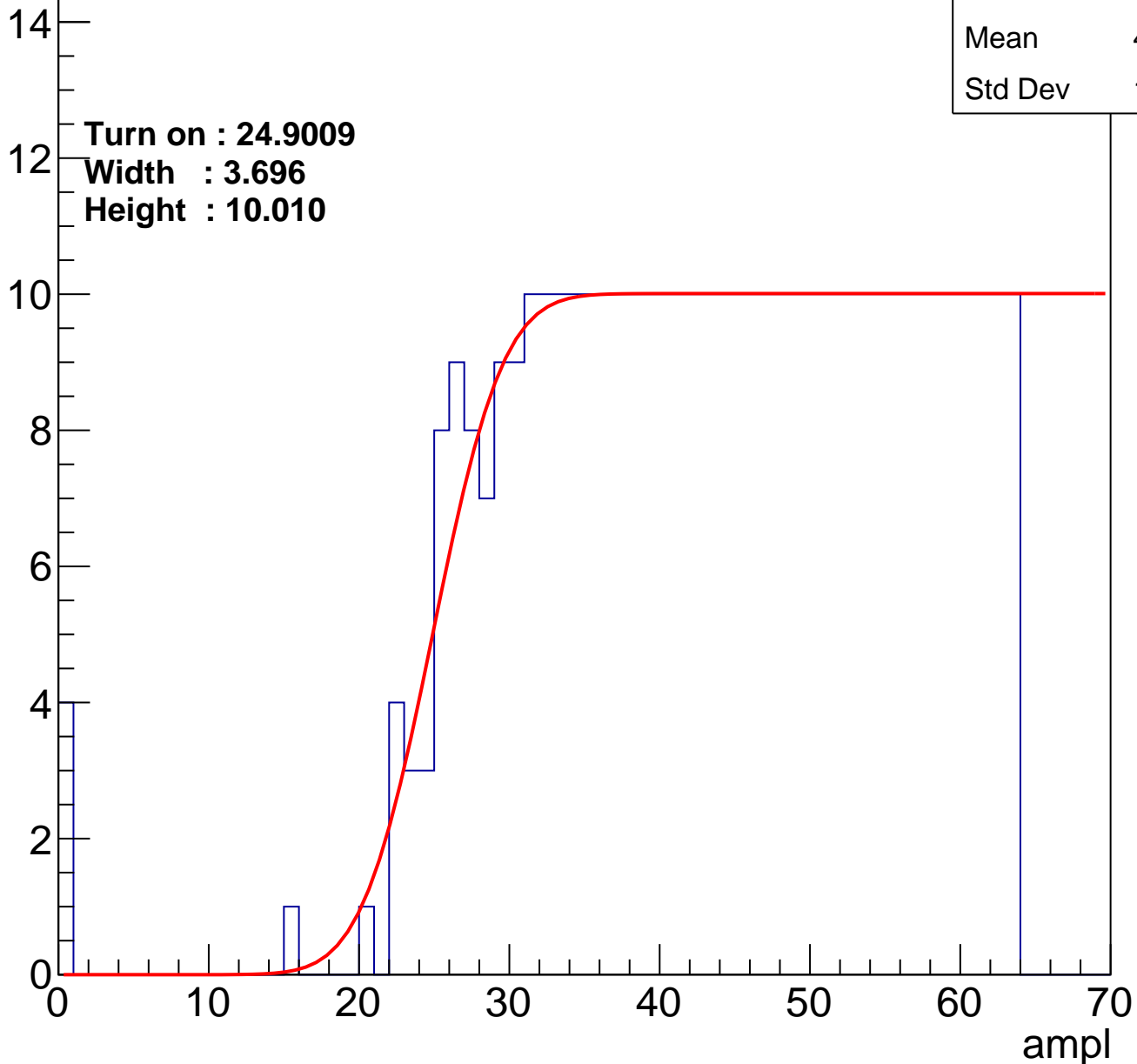
Entries	396
Mean	43.31
Std Dev	12.31

Turn on : 24.9009

Width : 3.696

Height : 10.010

Entry



B0L000S, U21-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	43.76
Std Dev	12.36

Turn on : 26.5017

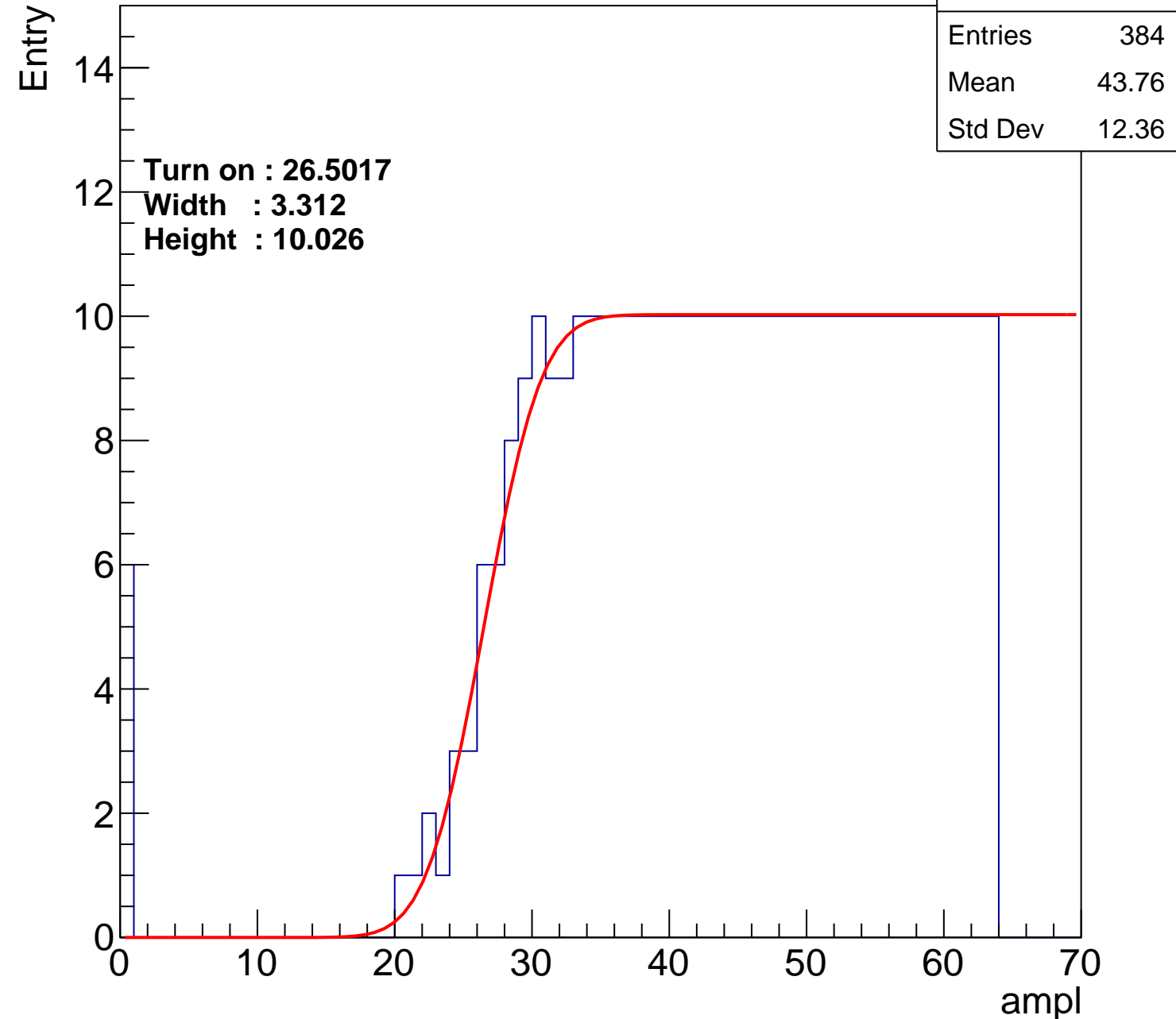
Width : 3.312

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.41
Std Dev	11.24

Turn on : 26.1427

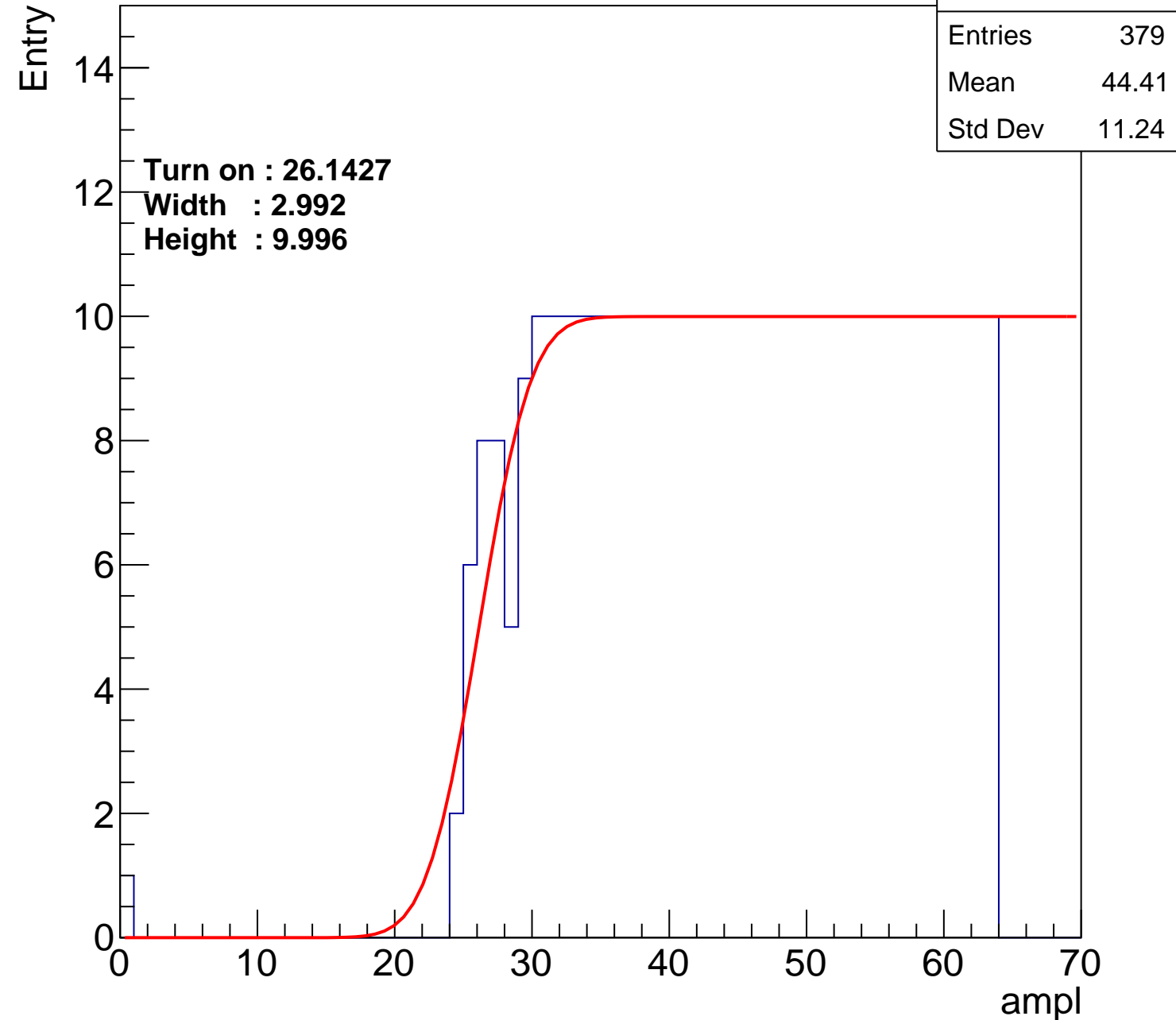
Width : 2.992

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.01
Std Dev	11.77

Turn on : 26.3992

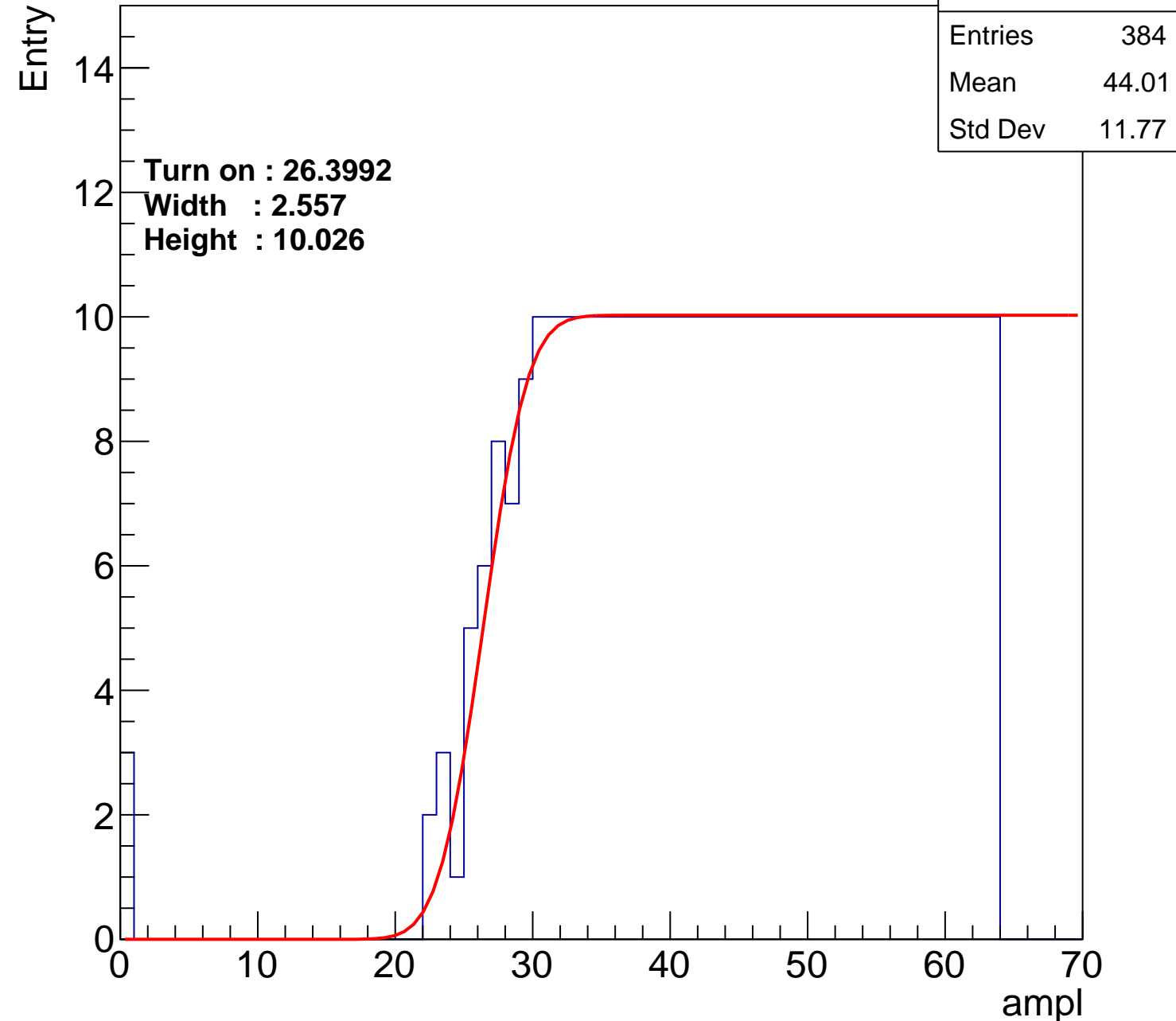
Width : 2.557

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.9565

Width : 2.796

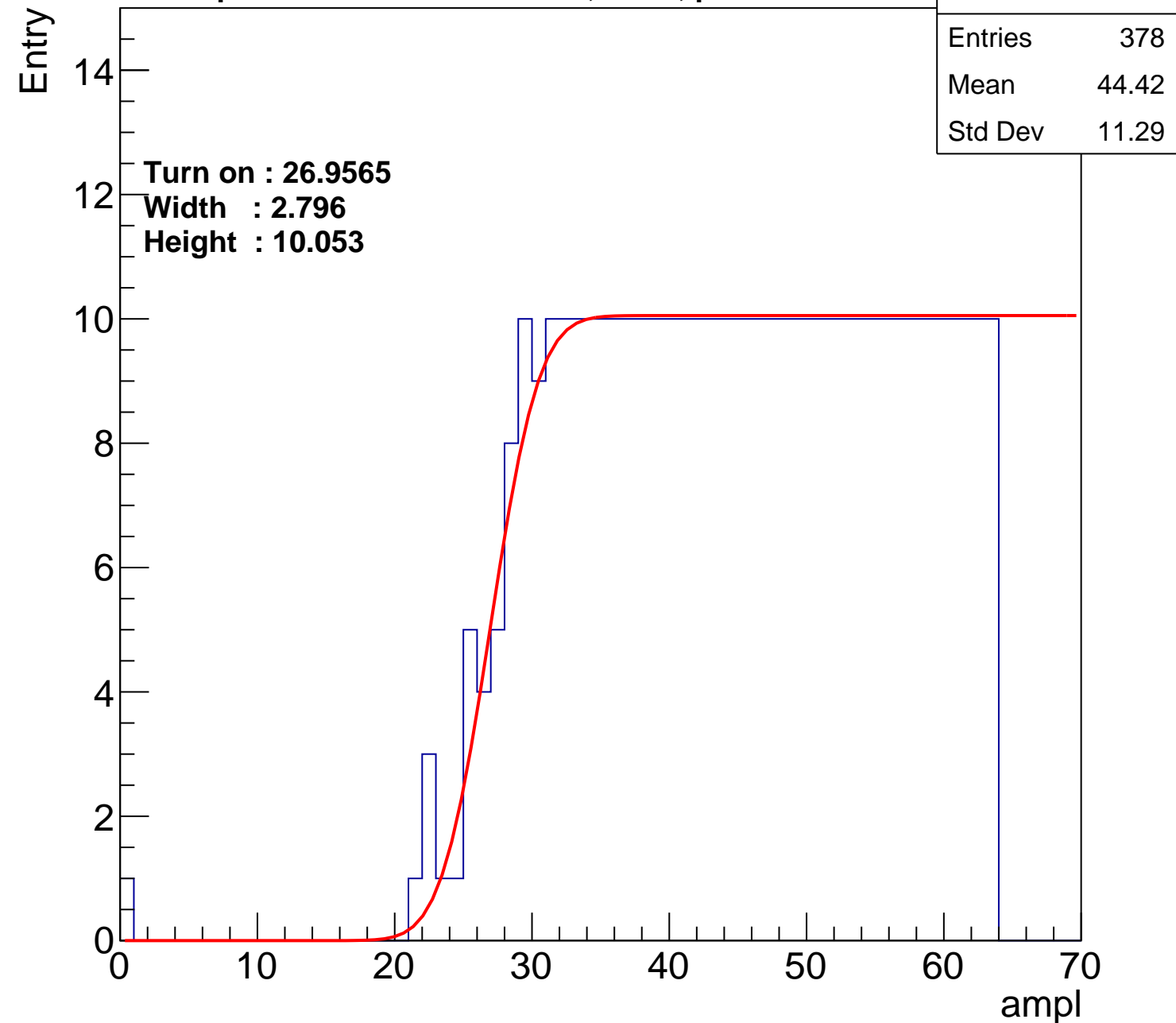
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U21-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.63
Std Dev	11.96

Turn on : 25.1483

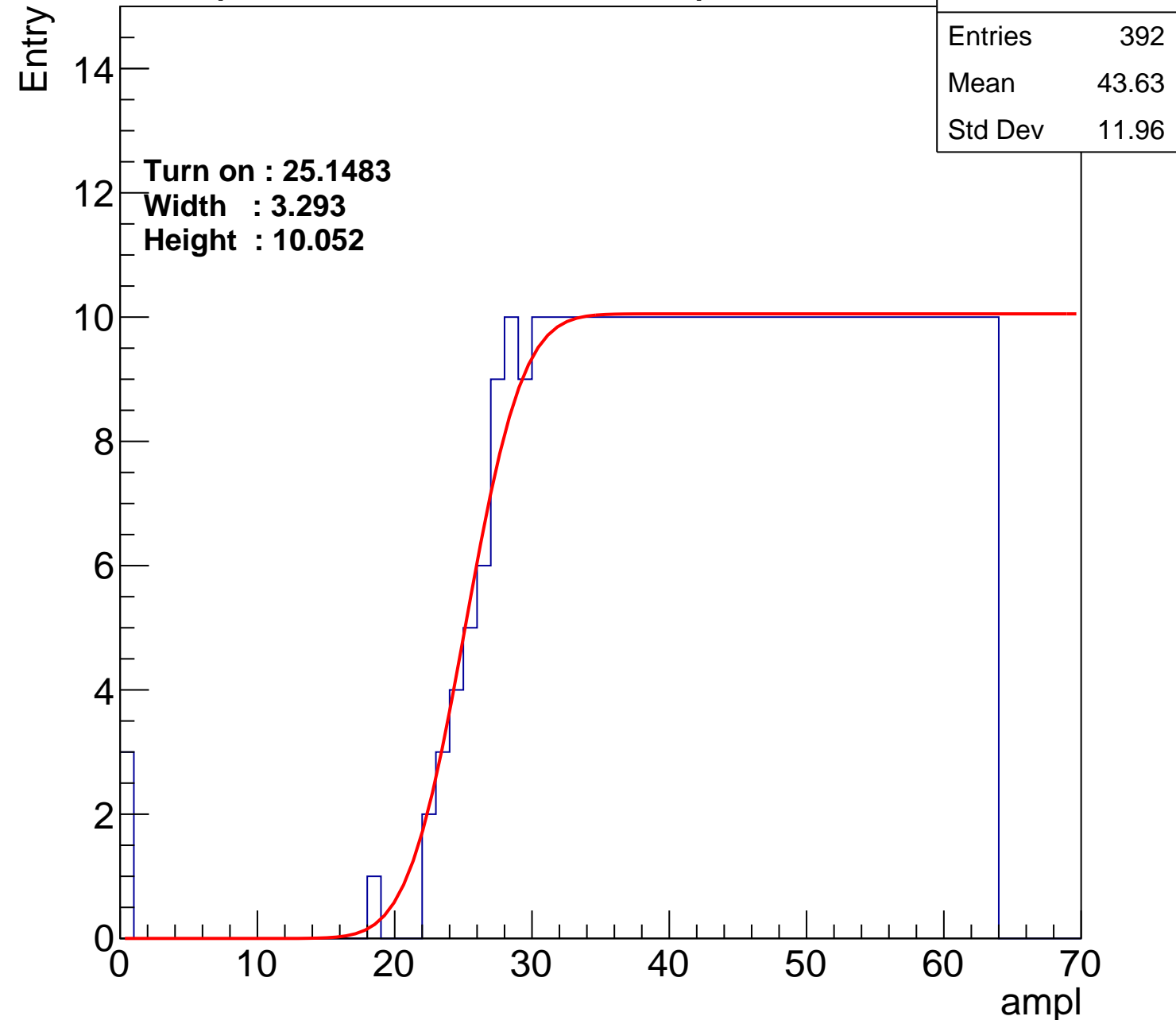
Width : 3.293

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.3430

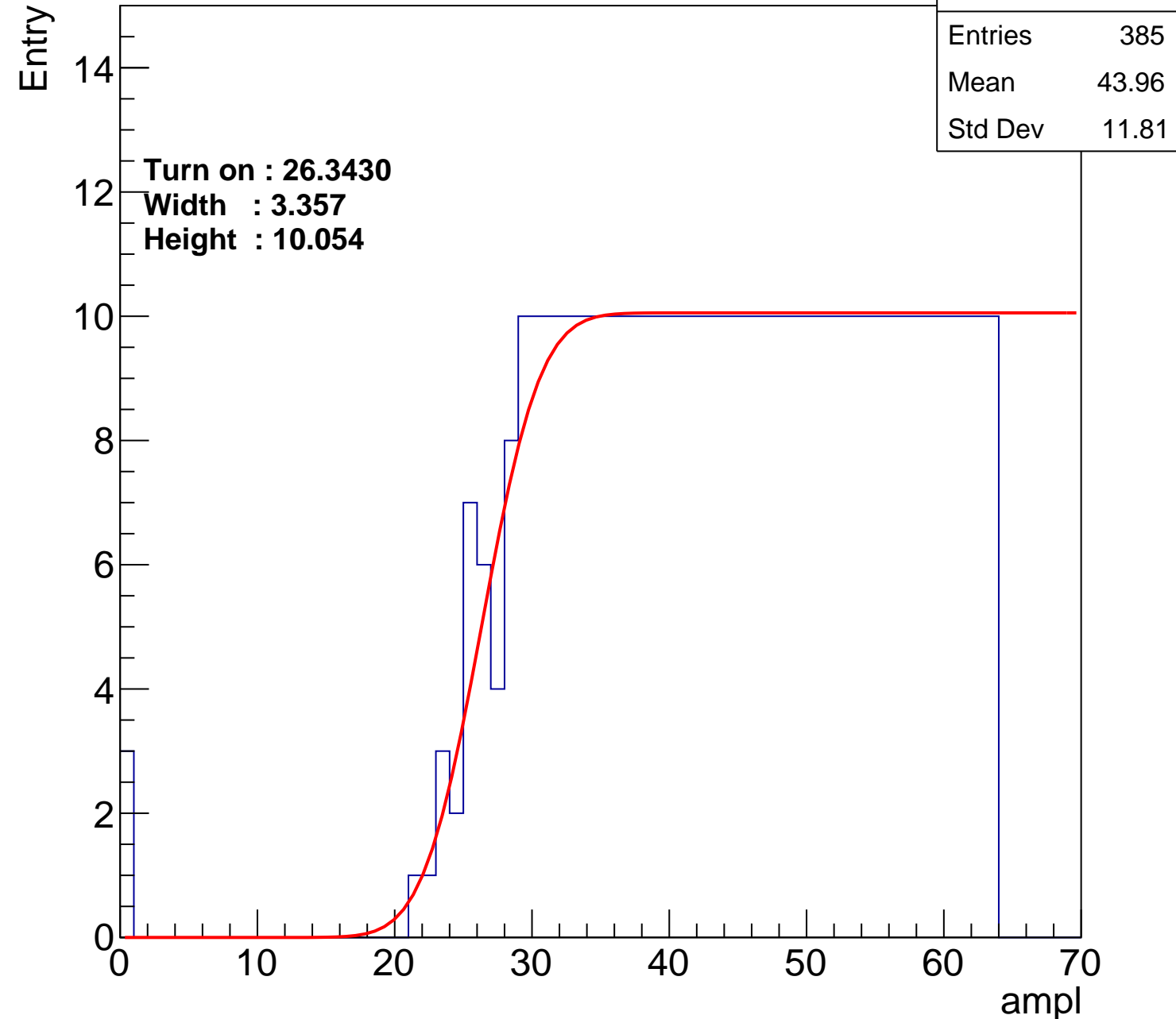
Width : 3.357

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.5463

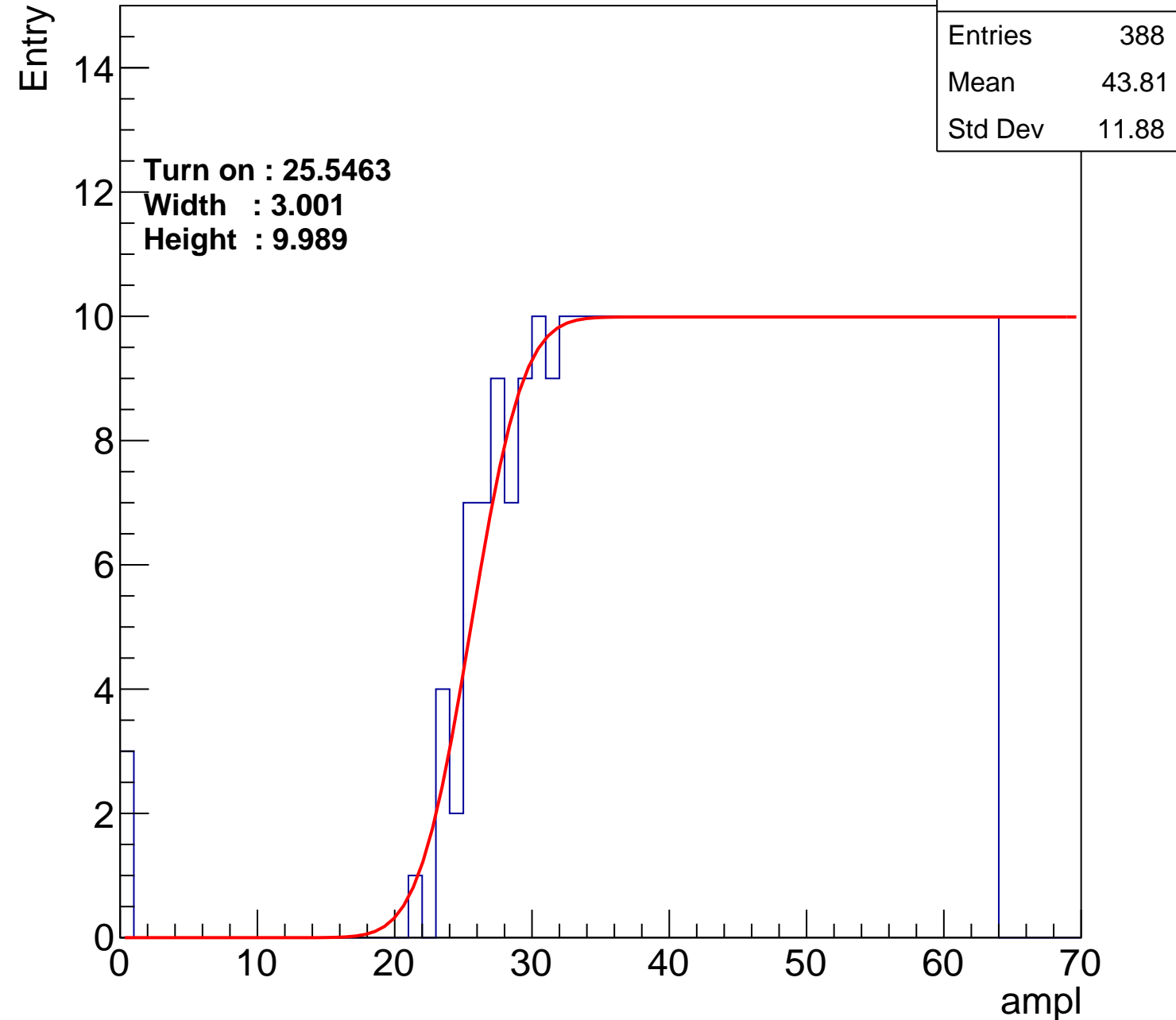
Width : 3.001

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.95
Std Dev	11.57

Turn on : 25.0515

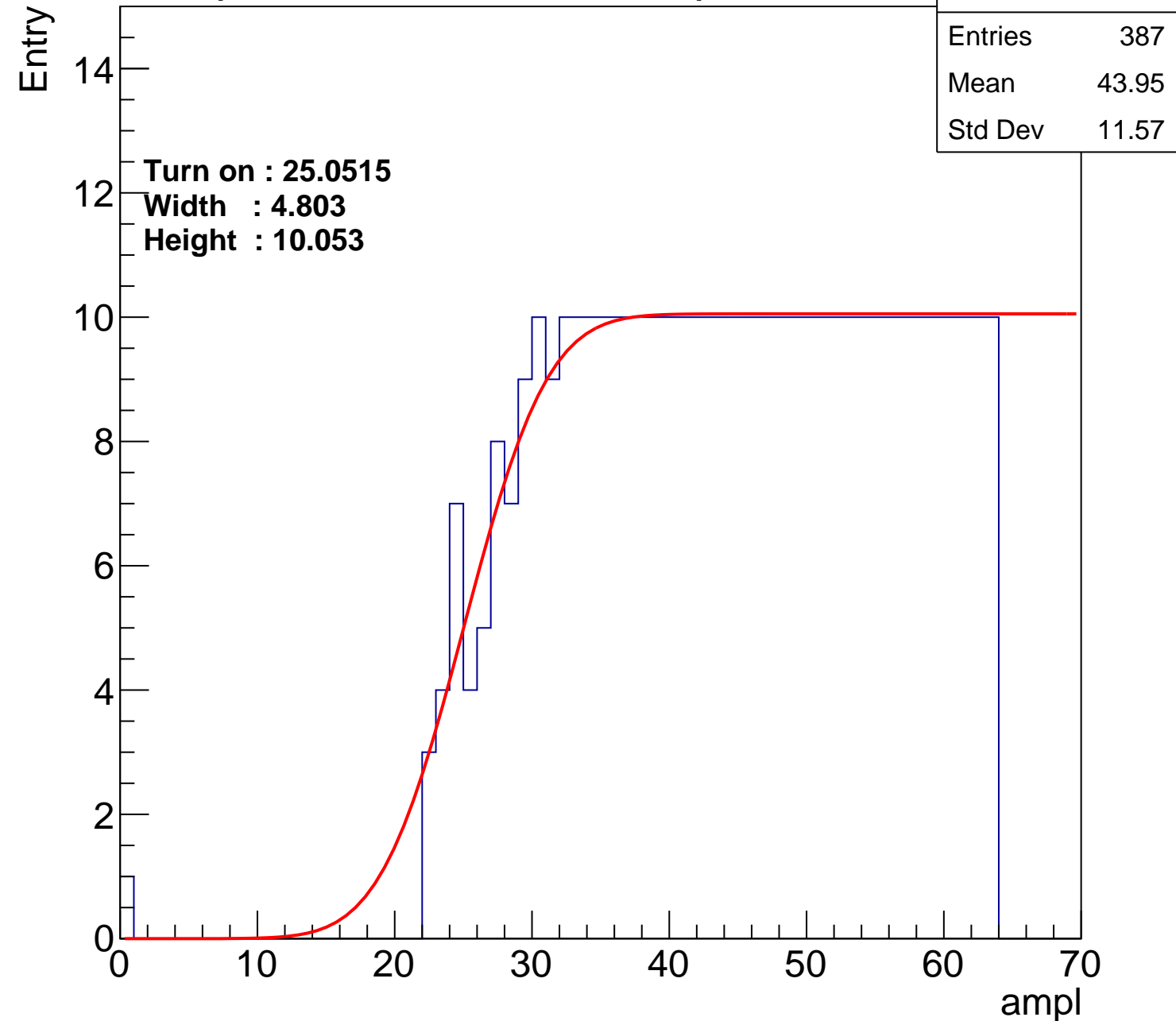
Width : 4.803

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 26.1979

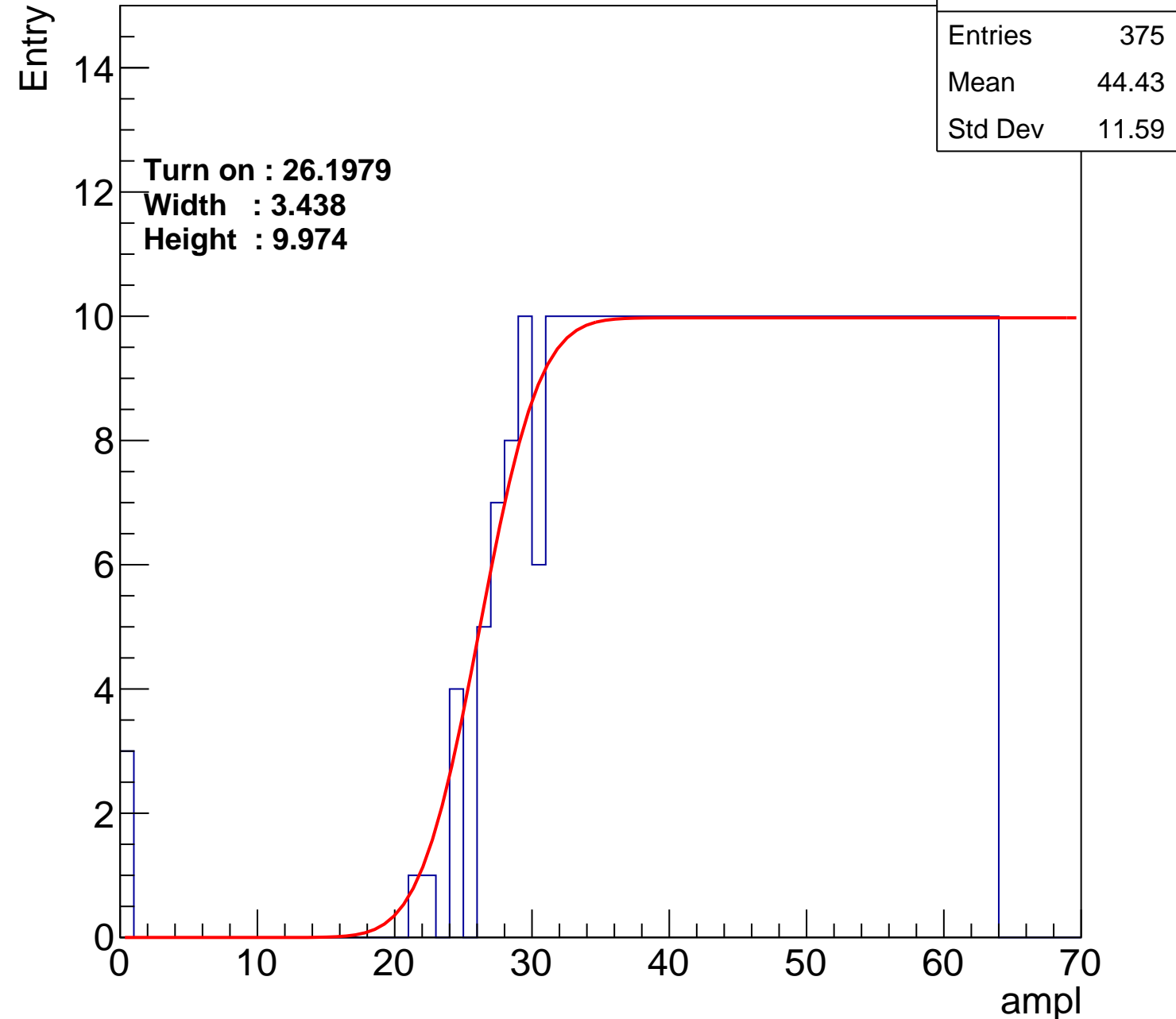
Width : 3.438

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.3356

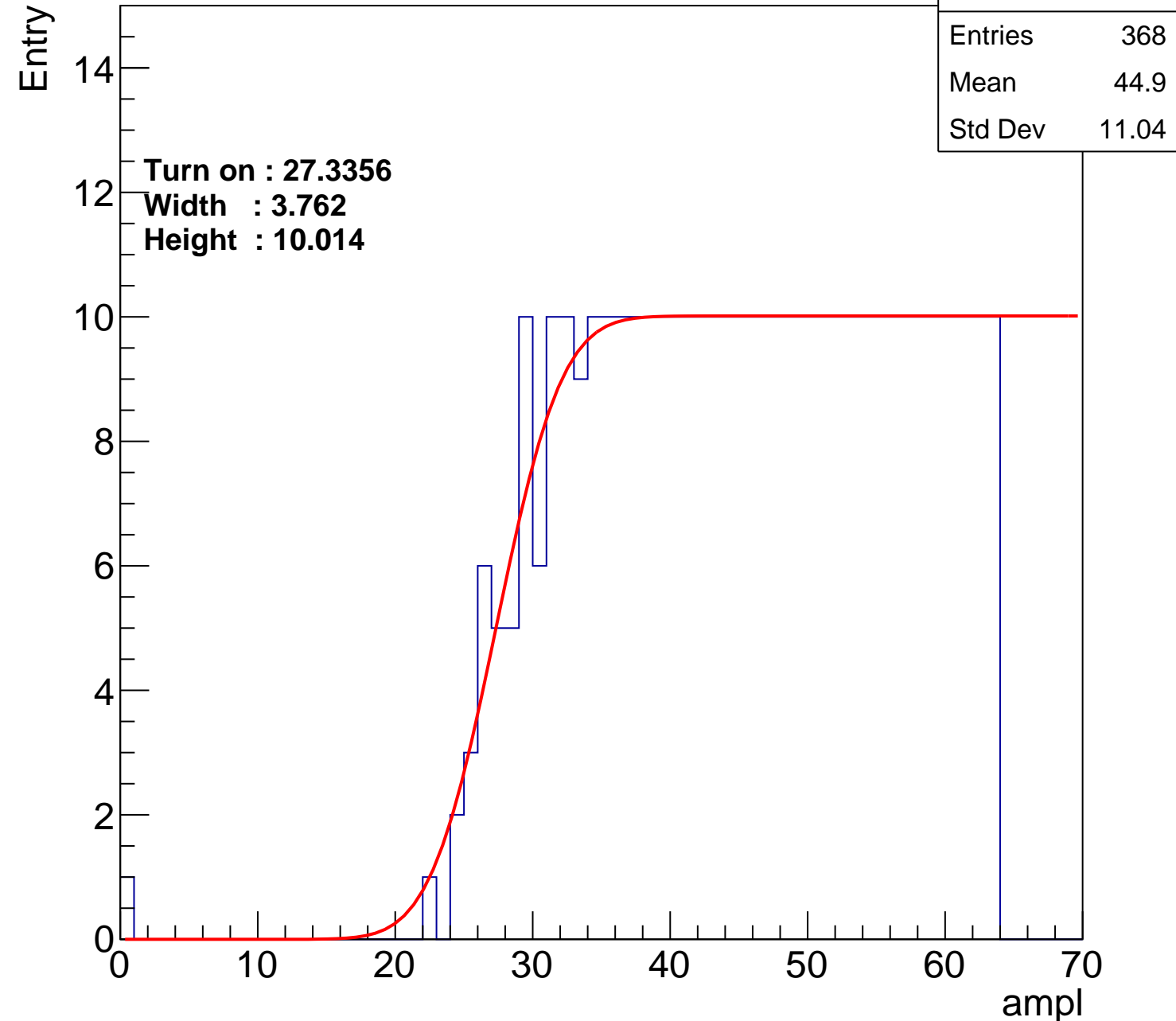
Width : 3.762

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch111

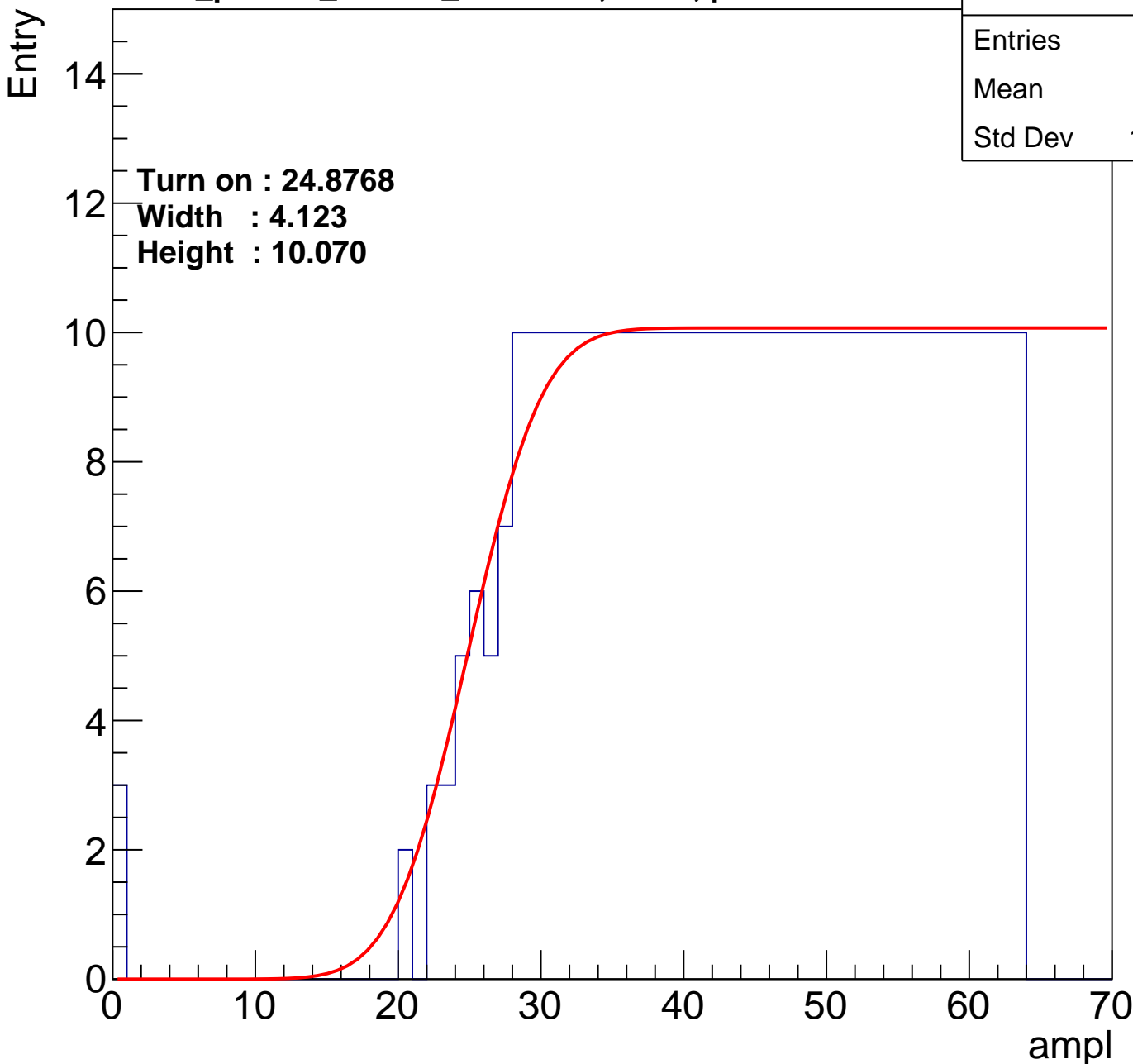
calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.51
Std Dev	12.04

Turn on : 24.8768

Width : 4.123

Height : 10.070



B0L000S, U21-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.16
Std Dev	12.13

Turn on : 27.3670

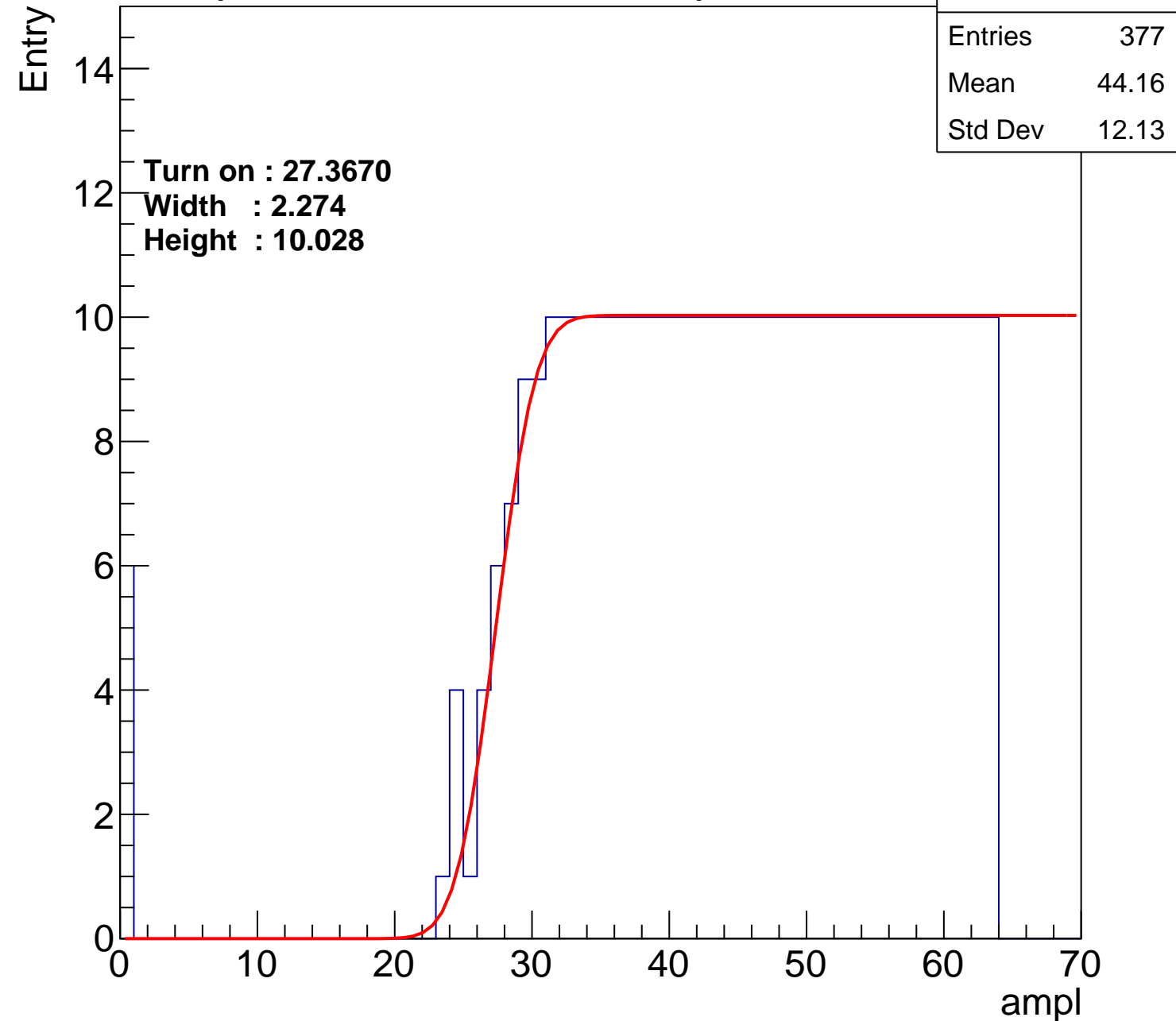
Width : 2.274

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 26.3020

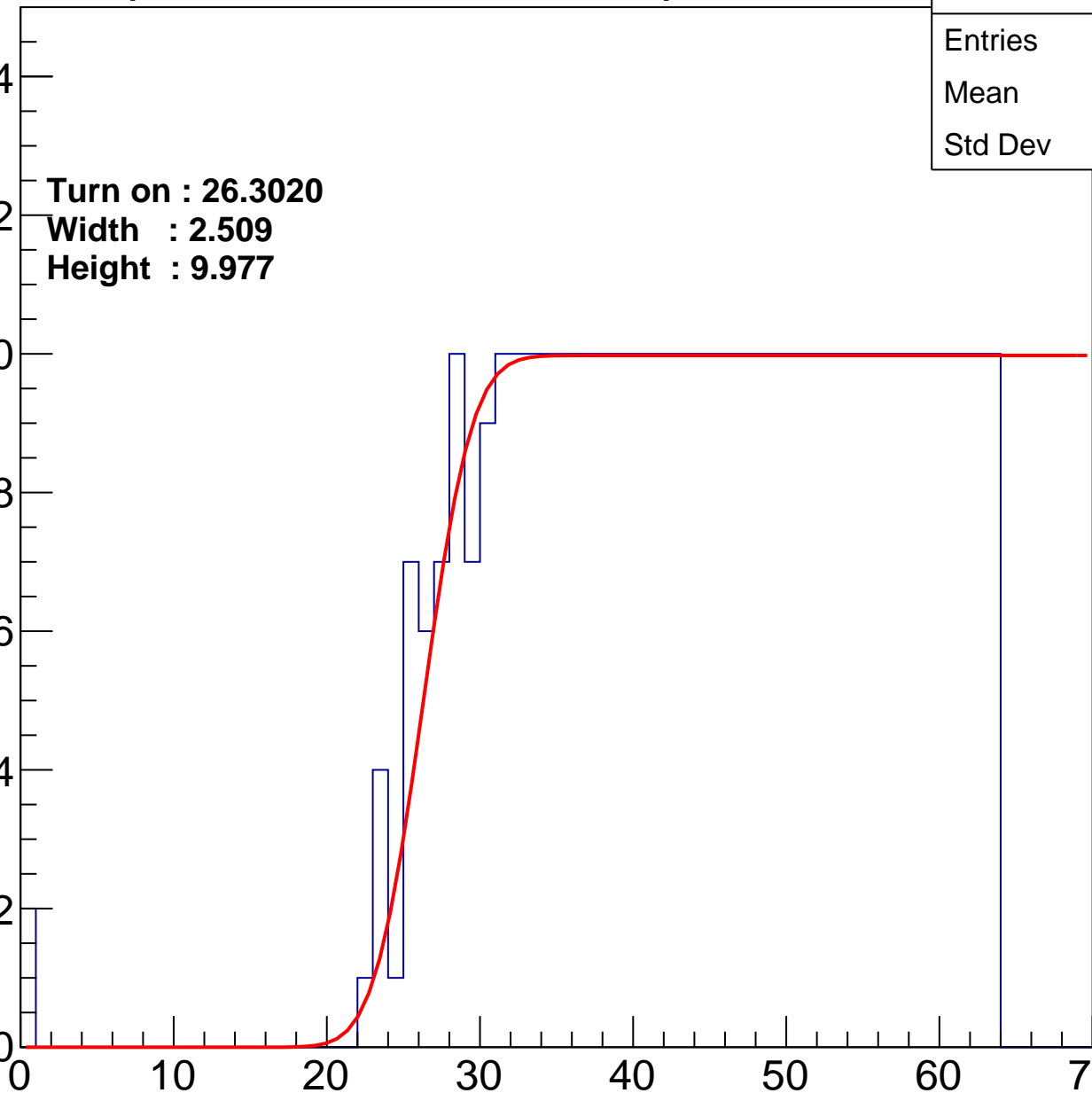
Width : 2.509

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch114

calib_packv5_042523_0143.root, FC#5, port B1

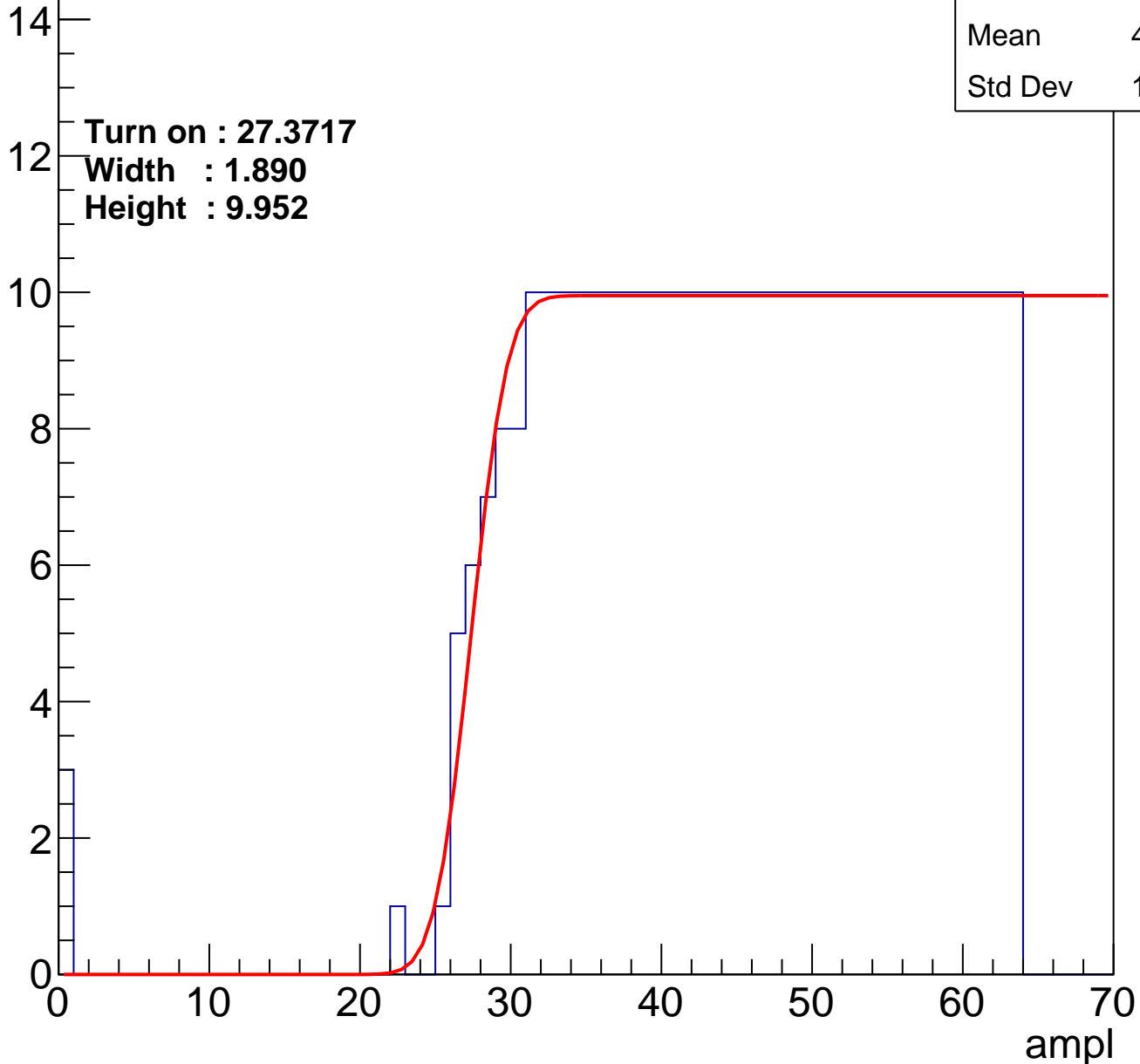
Entries	369
Mean	44.76
Std Dev	11.39

Turn on : 27.3717

Width : 1.890

Height : 9.952

Entry



B0L000S, U21-ch115

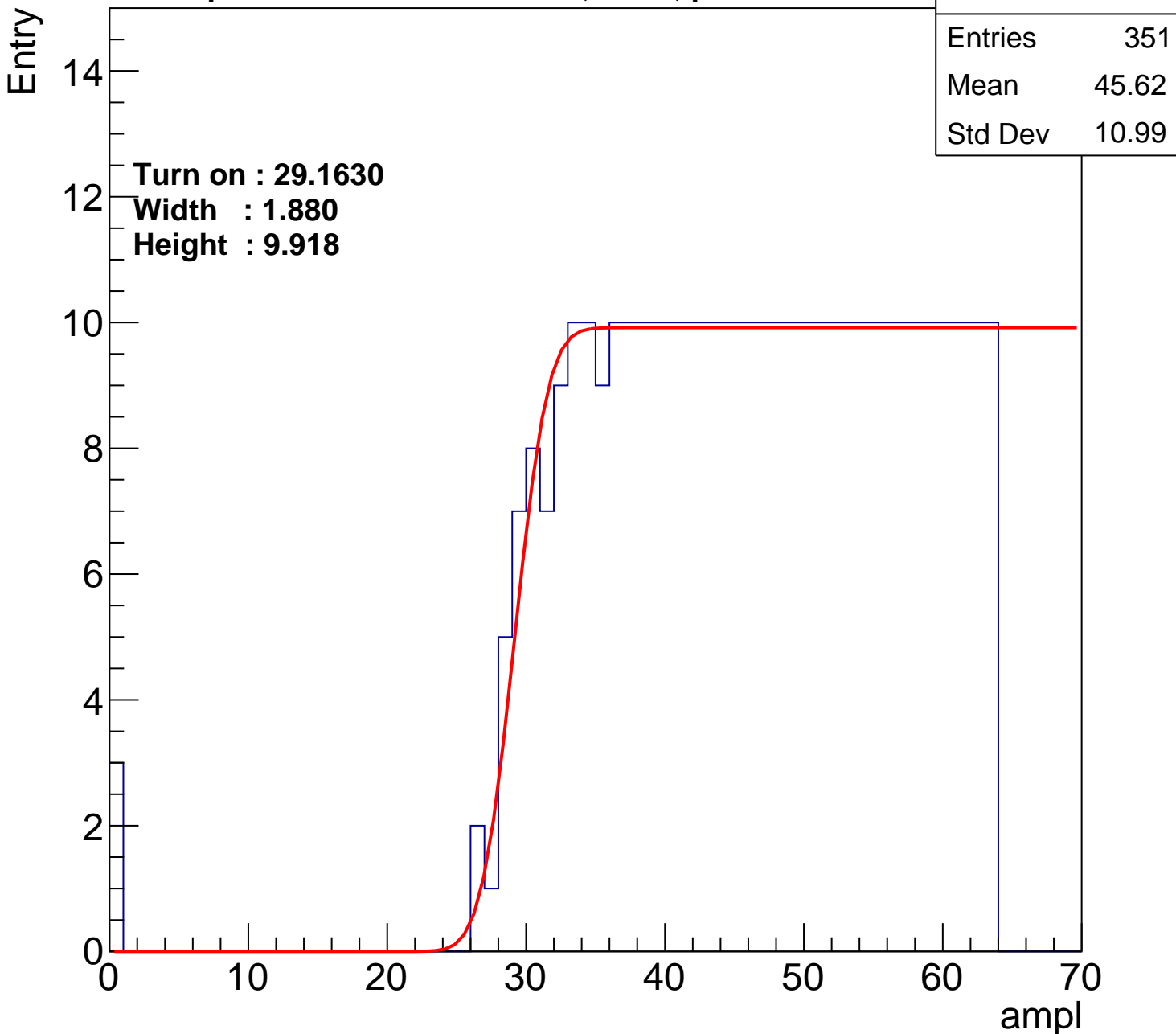
calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.62
Std Dev	10.99

Turn on : 29.1630

Width : 1.880

Height : 9.918



B0L000S, U21-ch116

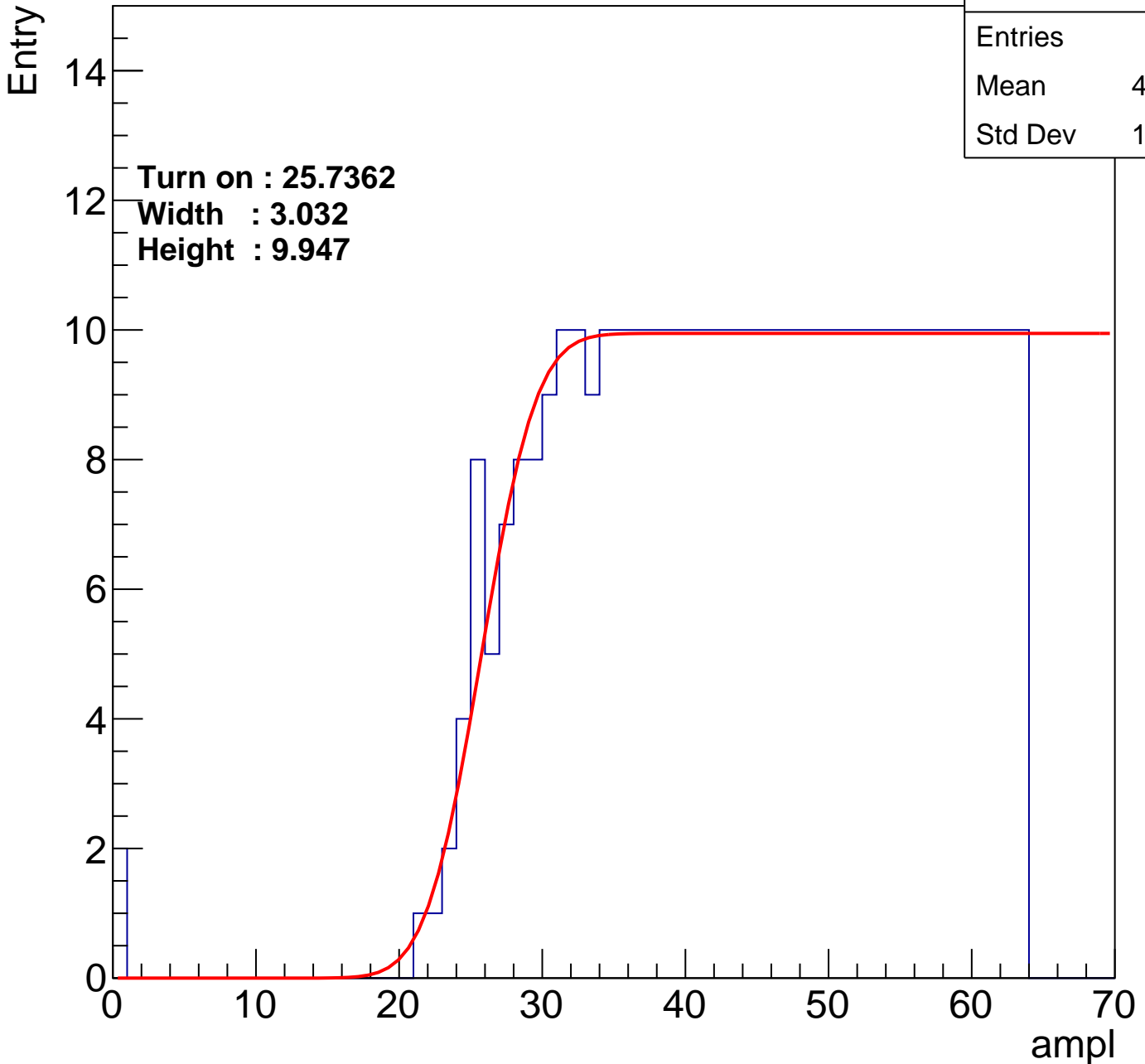
calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.03
Std Dev	11.67

Turn on : 25.7362

Width : 3.032

Height : 9.947



B0L000S, U21-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 25.6534

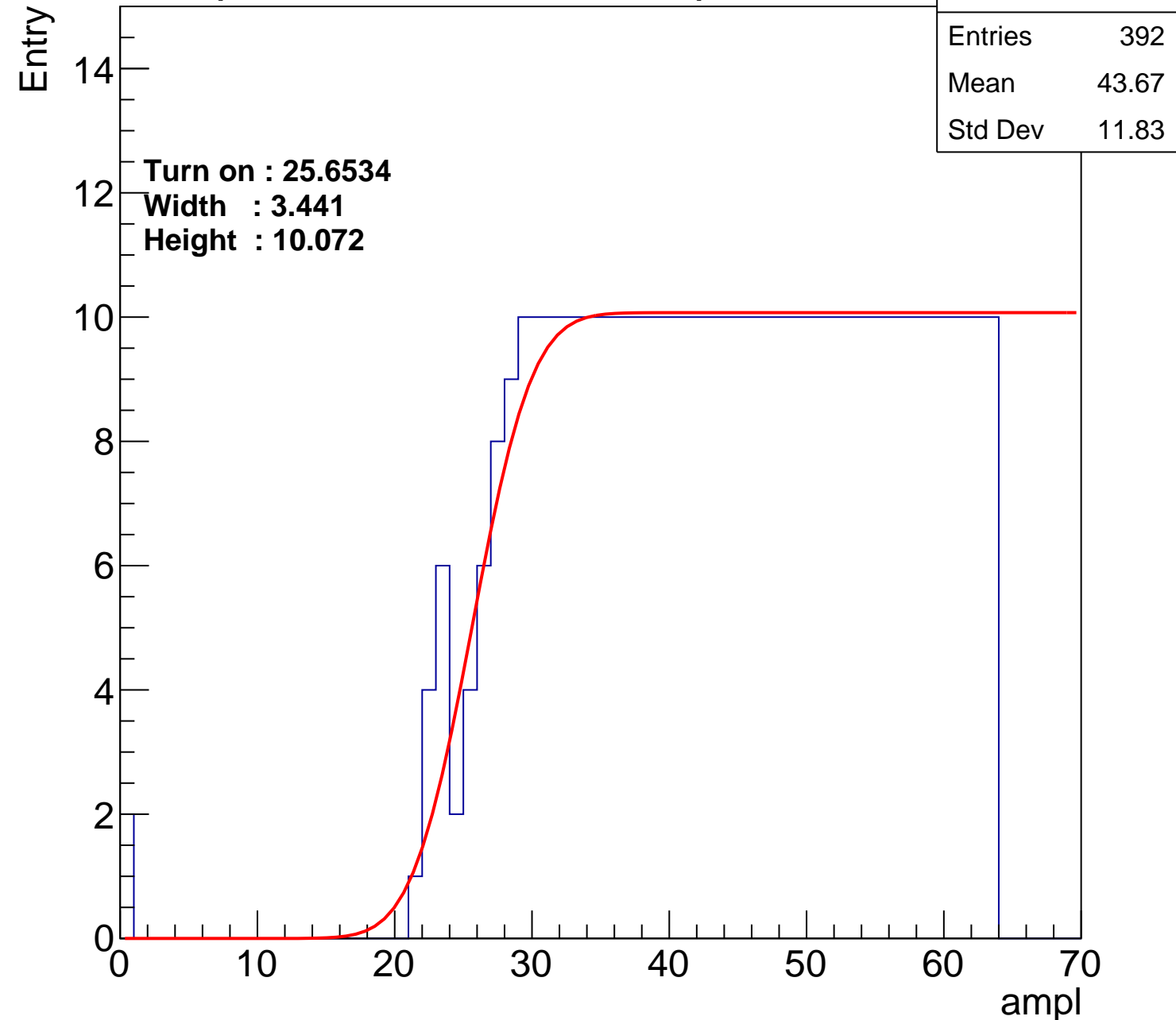
Width : 3.441

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 25.2206

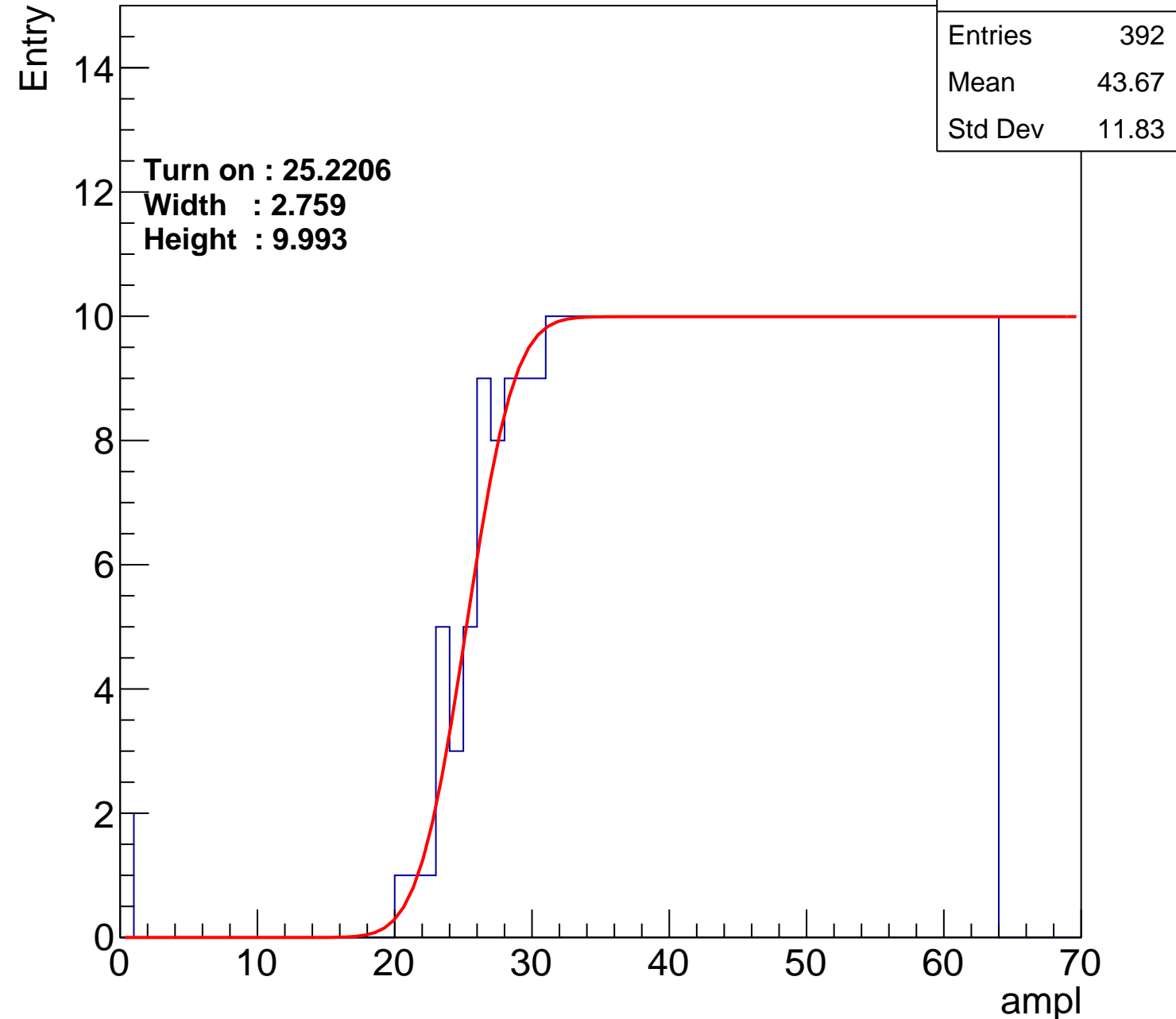
Width : 2.759

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 26.1443

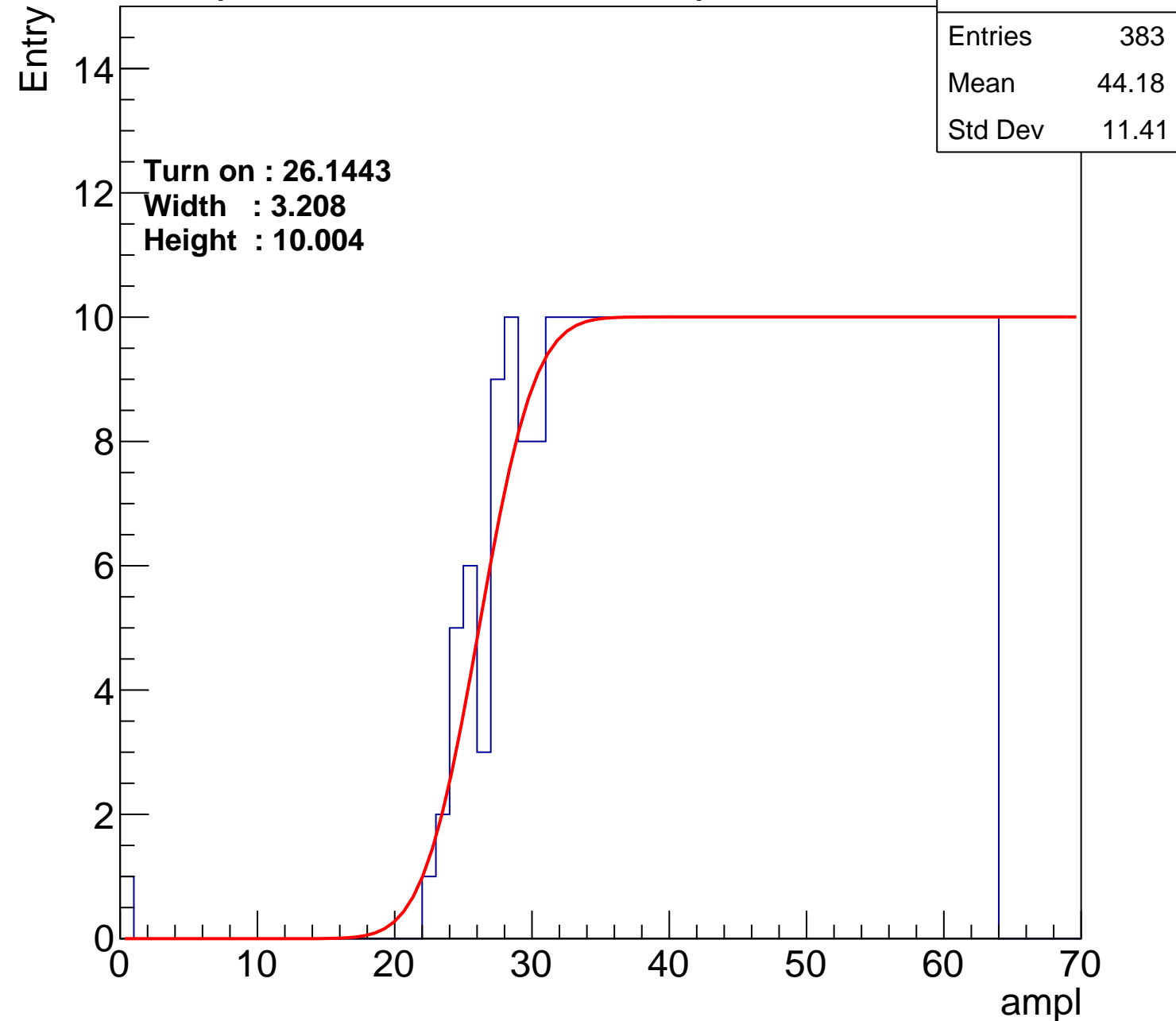
Width : 3.208

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch120

calib_packv5_042523_0143.root, FC#5, port B1

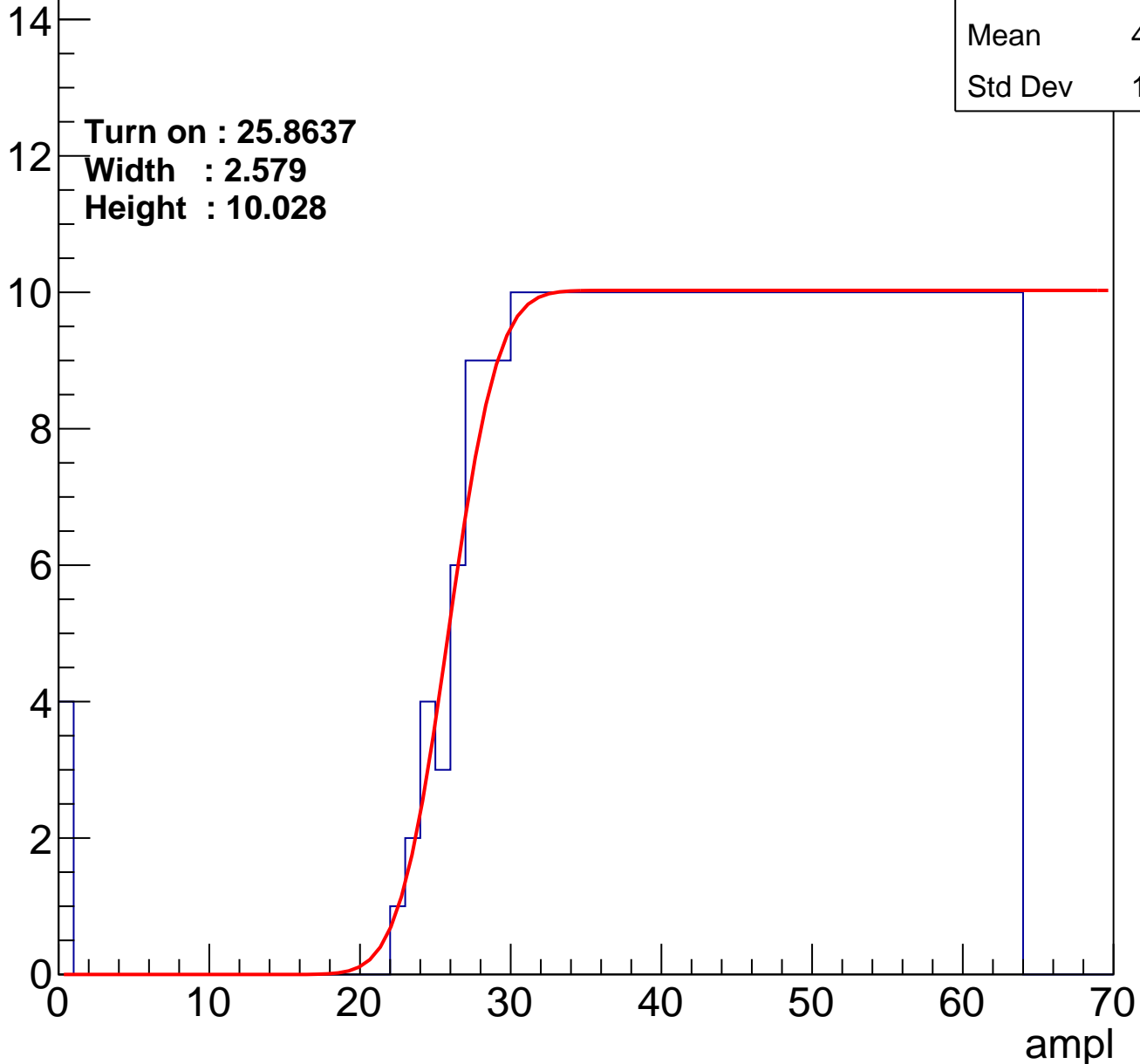
Entries	387
Mean	43.83
Std Dev	11.98

Turn on : 25.8637

Width : 2.579

Height : 10.028

Entry



B0L000S, U21-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	44.97
Std Dev	11.51

Turn on : 28.6962

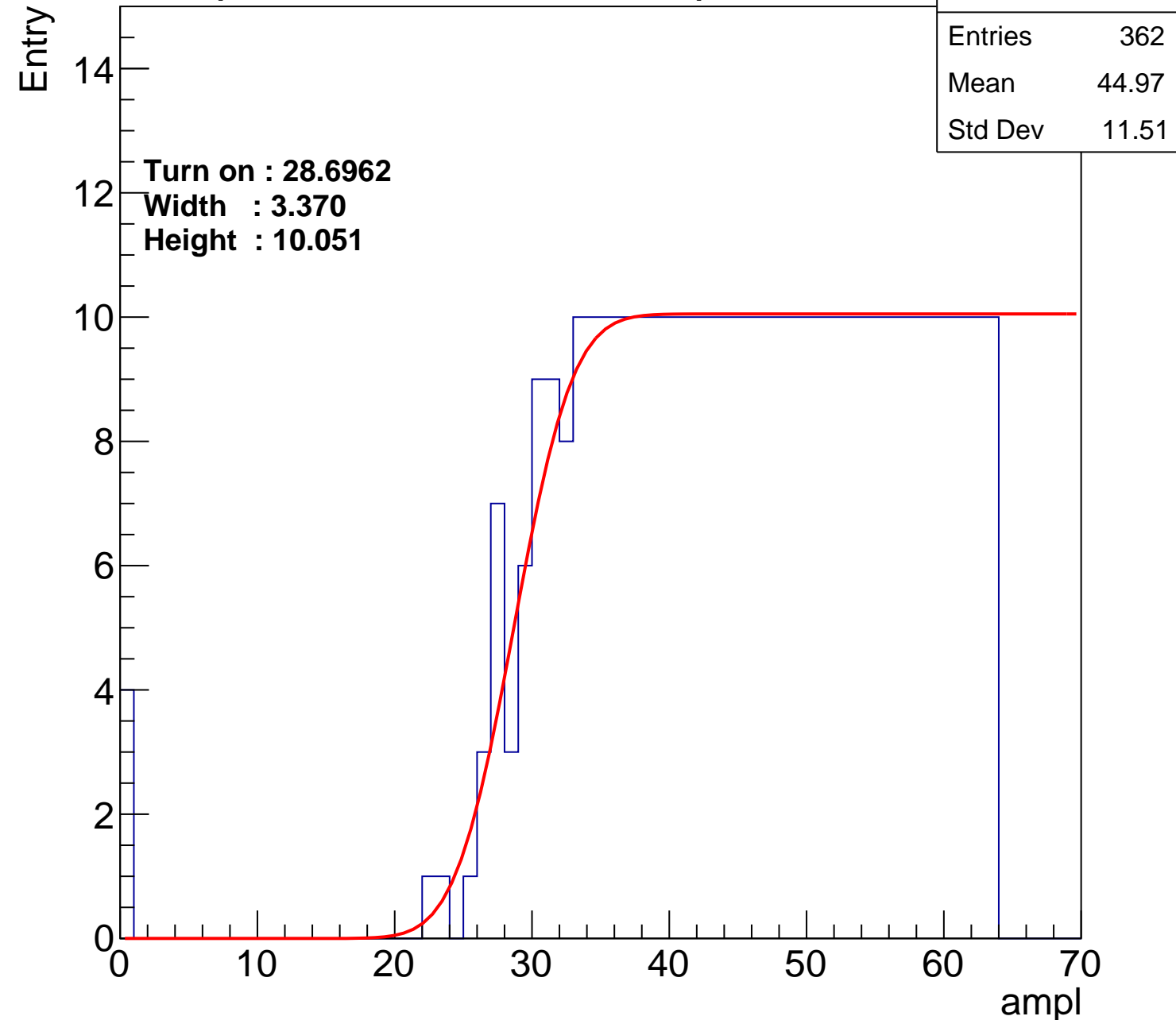
Width : 3.370

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 27.0834

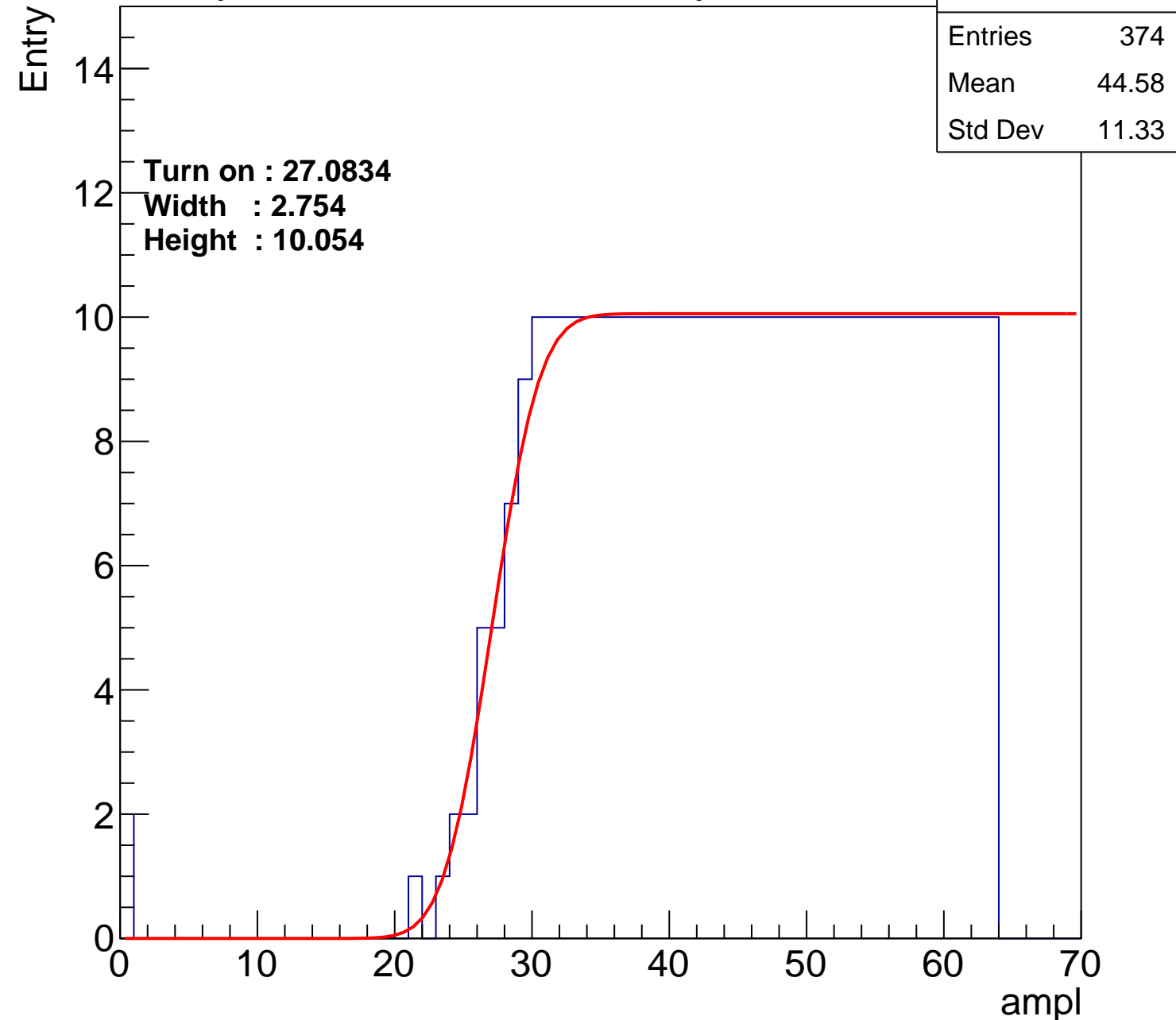
Width : 2.754

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.6
Std Dev	11.53

Turn on : 27.5323

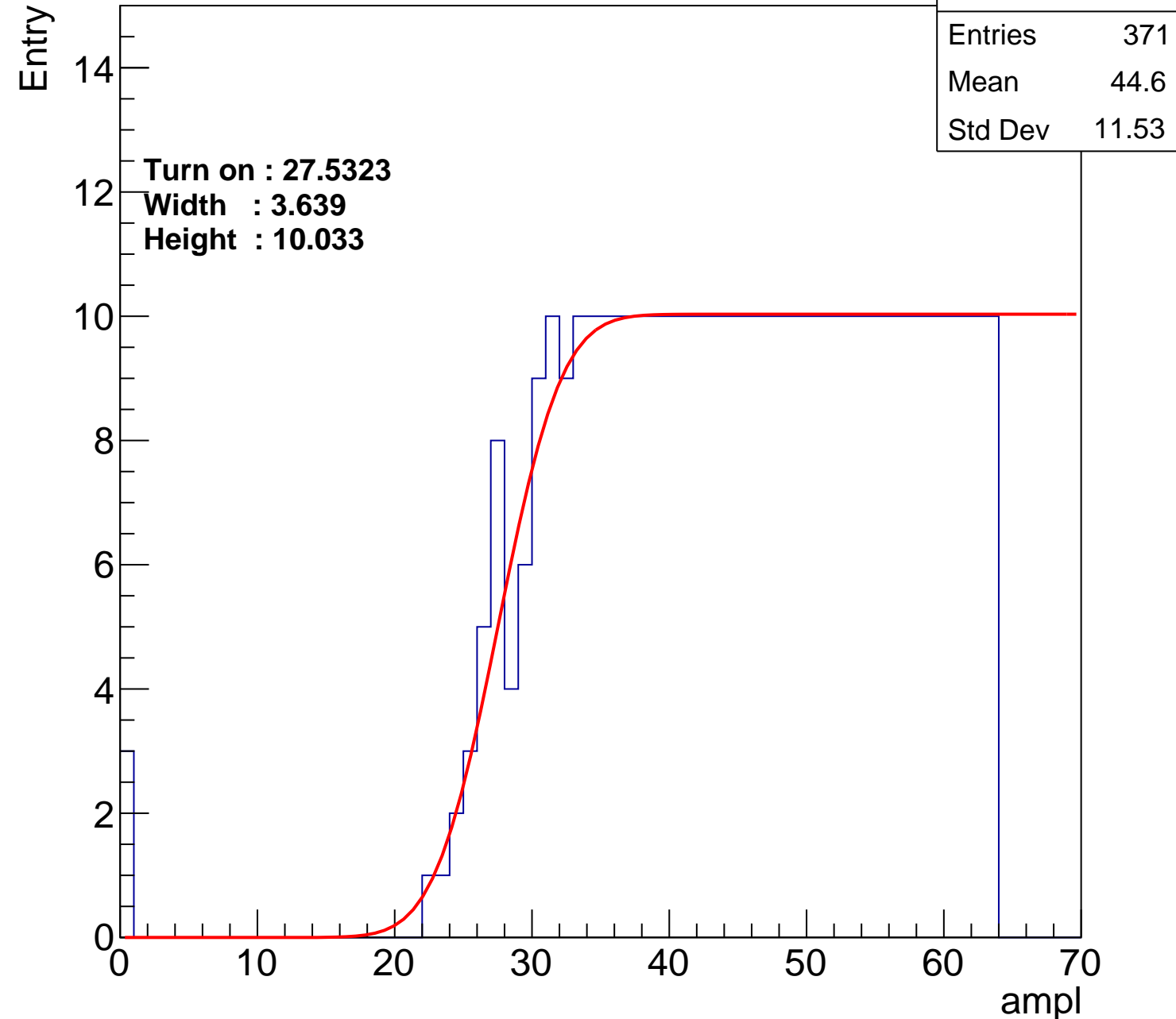
Width : 3.639

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch124

calib_packv5_042523_0143.root, FC#5, port B1

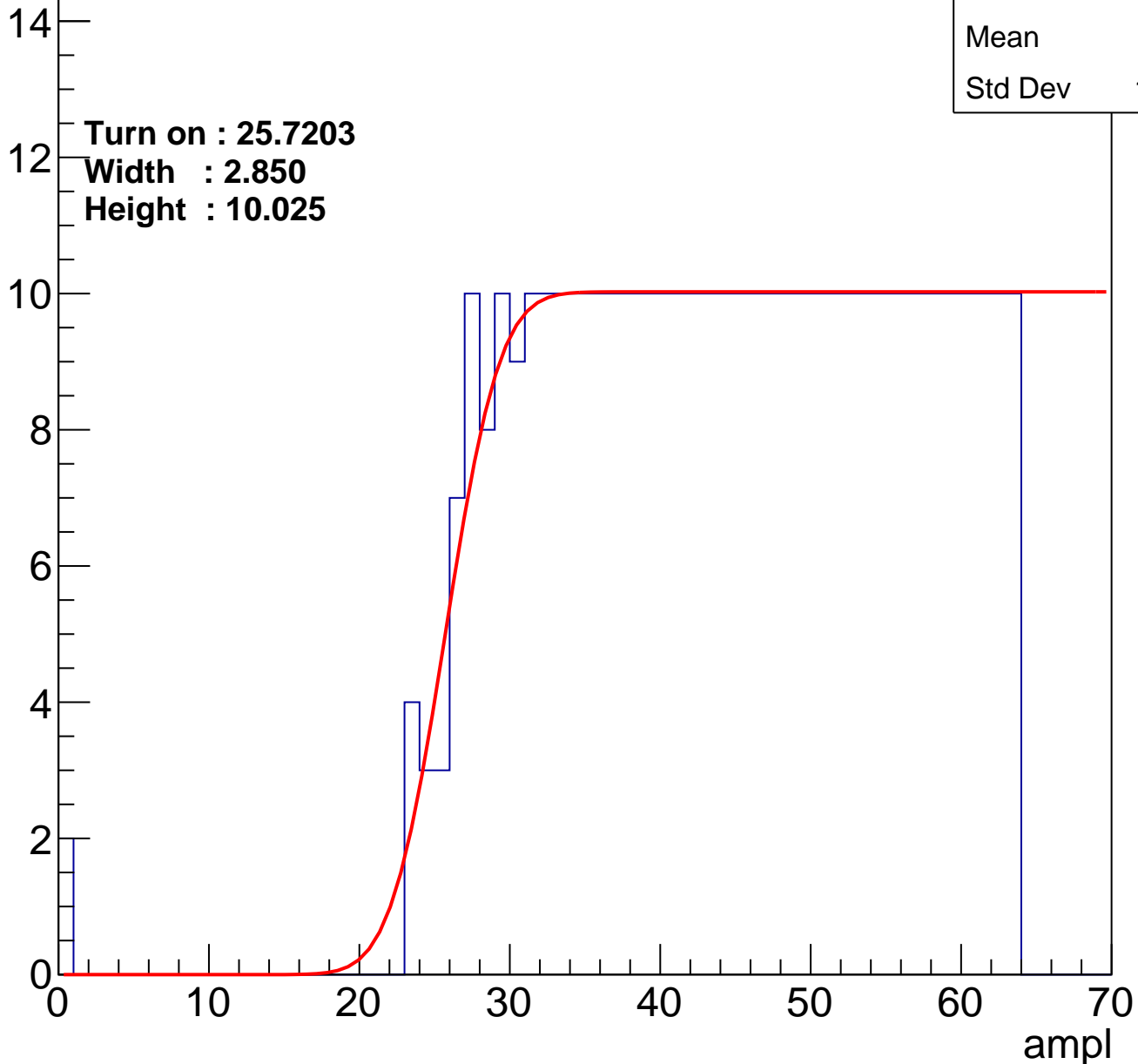
Entries	386
Mean	44
Std Dev	11.61

Turn on : 25.7203

Width : 2.850

Height : 10.025

Entry



B0L000S, U21-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.7164

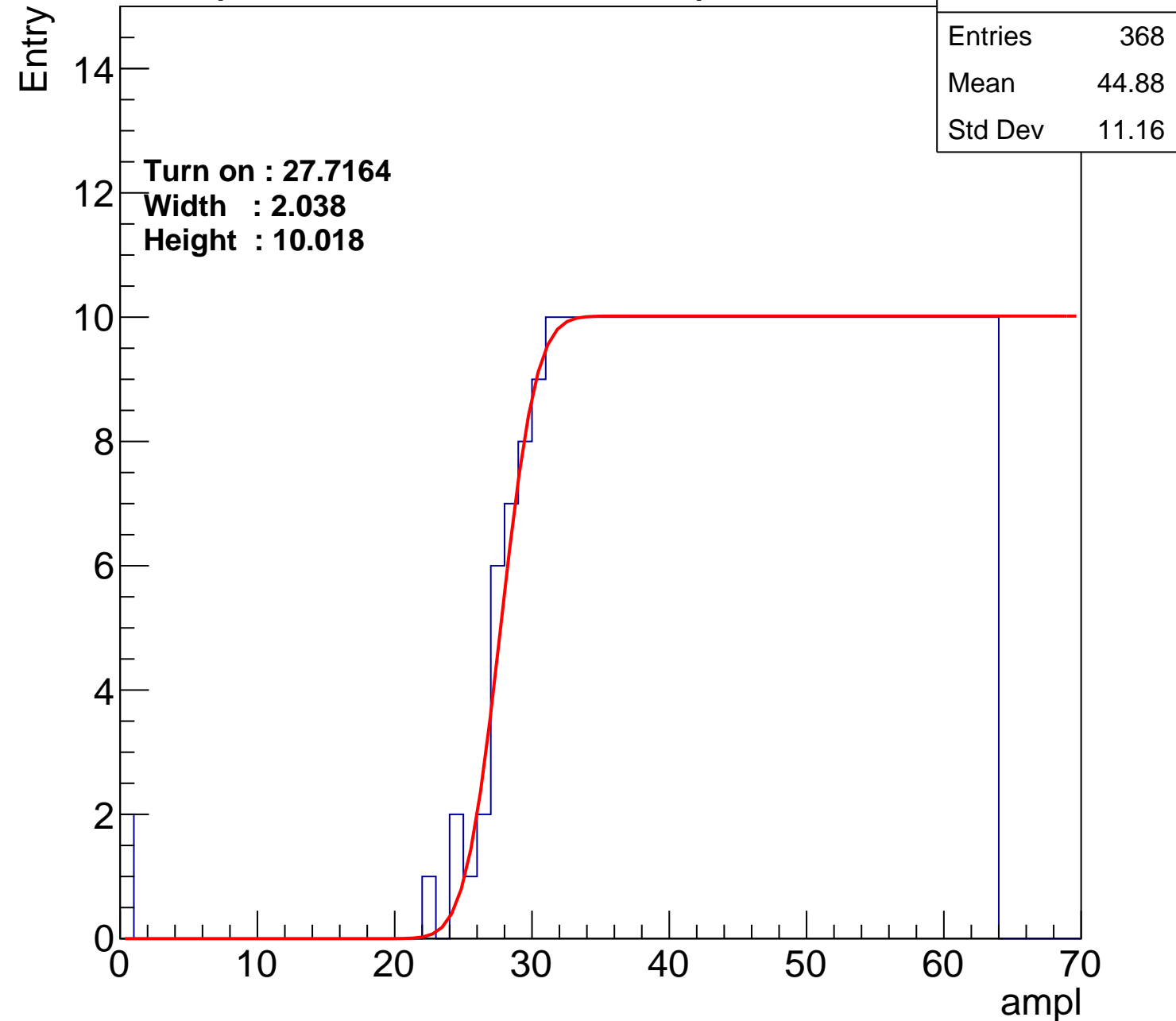
Width : 2.038

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.91
Std Dev	11.9

Turn on : 26.5494

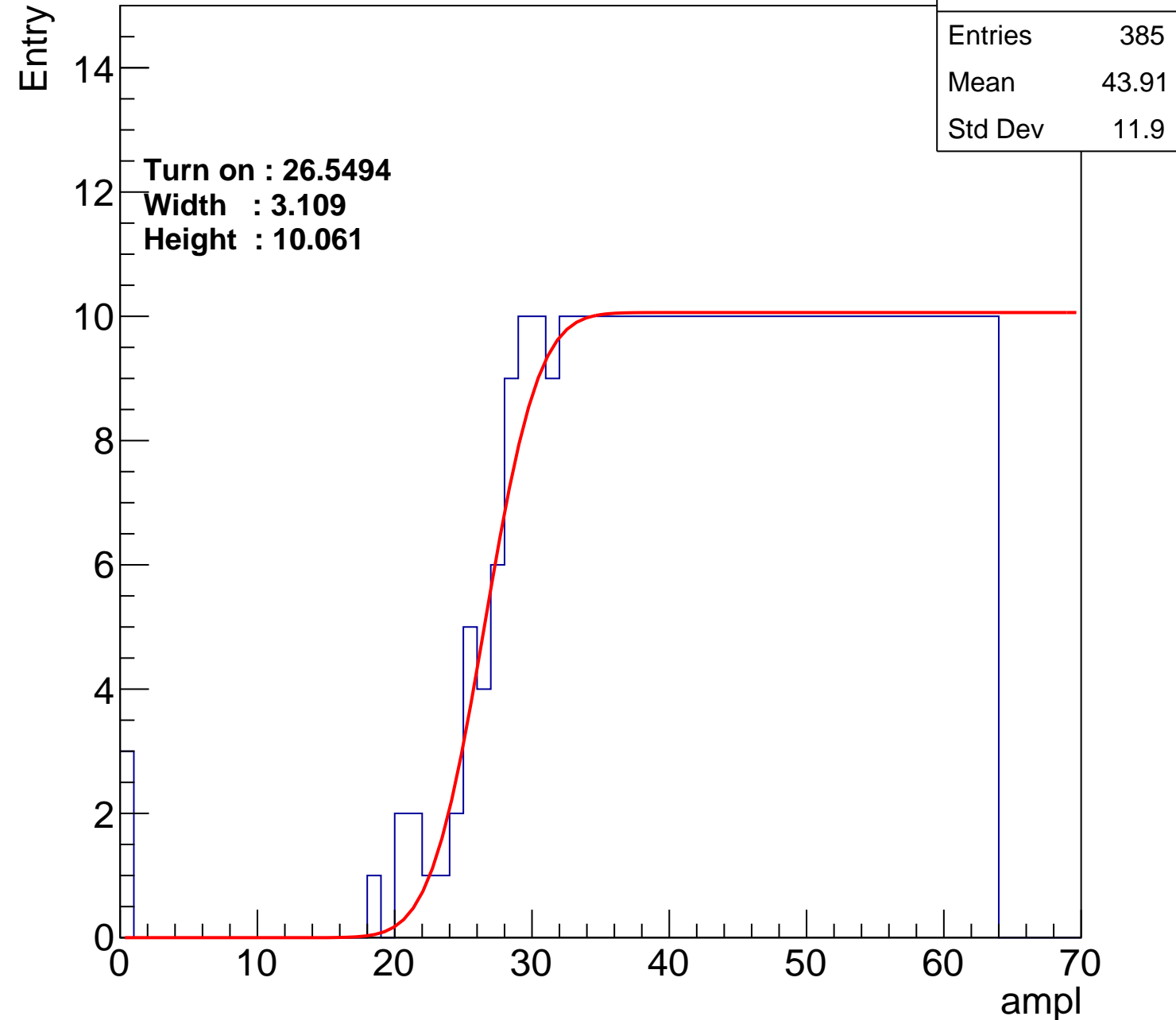
Width : 3.109

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.15
Std Dev	11.9

Turn on : 26.8717

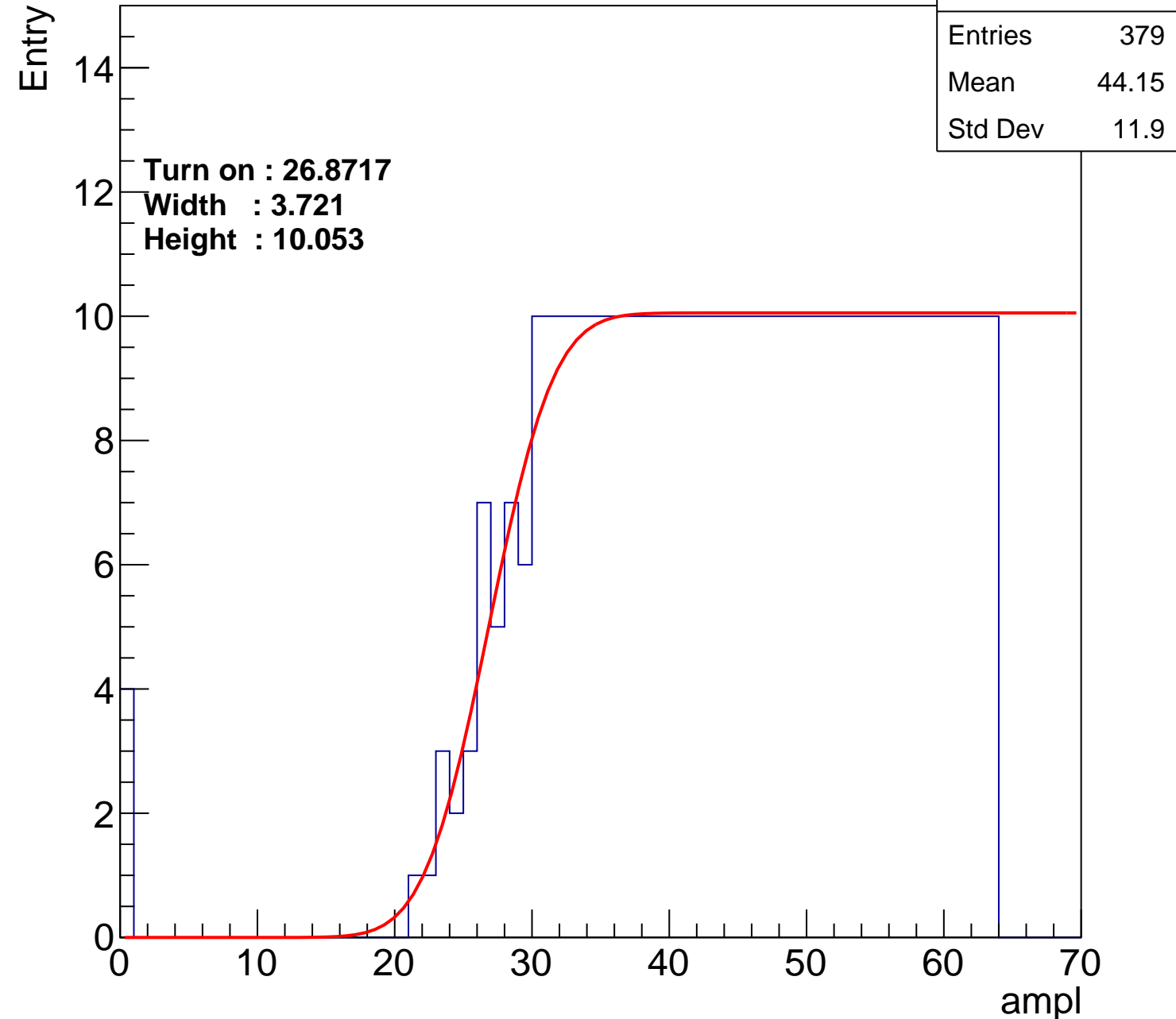
Width : 3.721

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U21-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.15
Std Dev	11.9

Turn on : 26.8717

Width : 3.721

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

