

B0L102S, U5-ch0, adc0

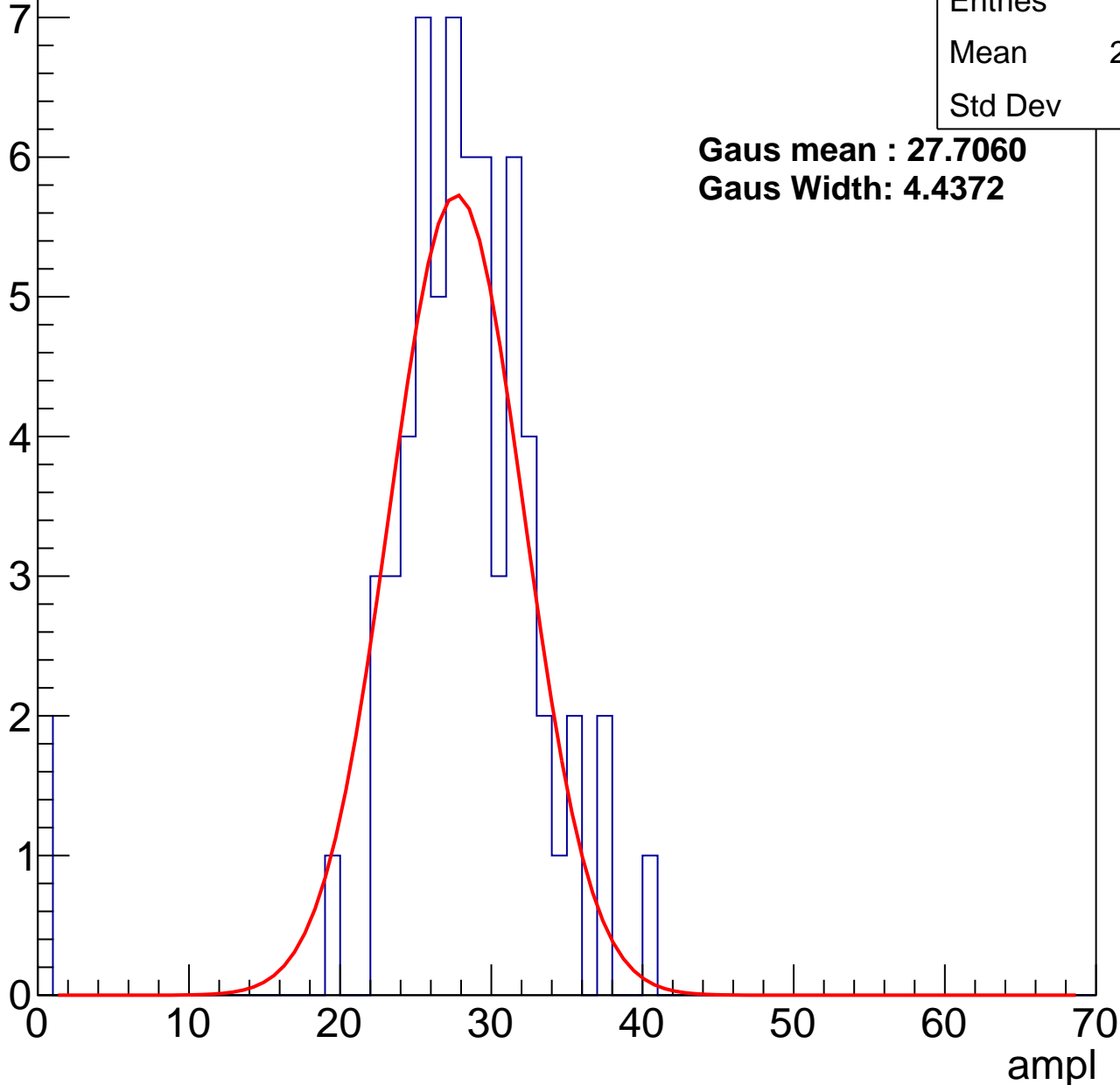
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	27.29
Std Dev	6.3

Gaus mean : 27.7060

Gaus Width: 4.4372



B0L102S, U5-ch0, adc1

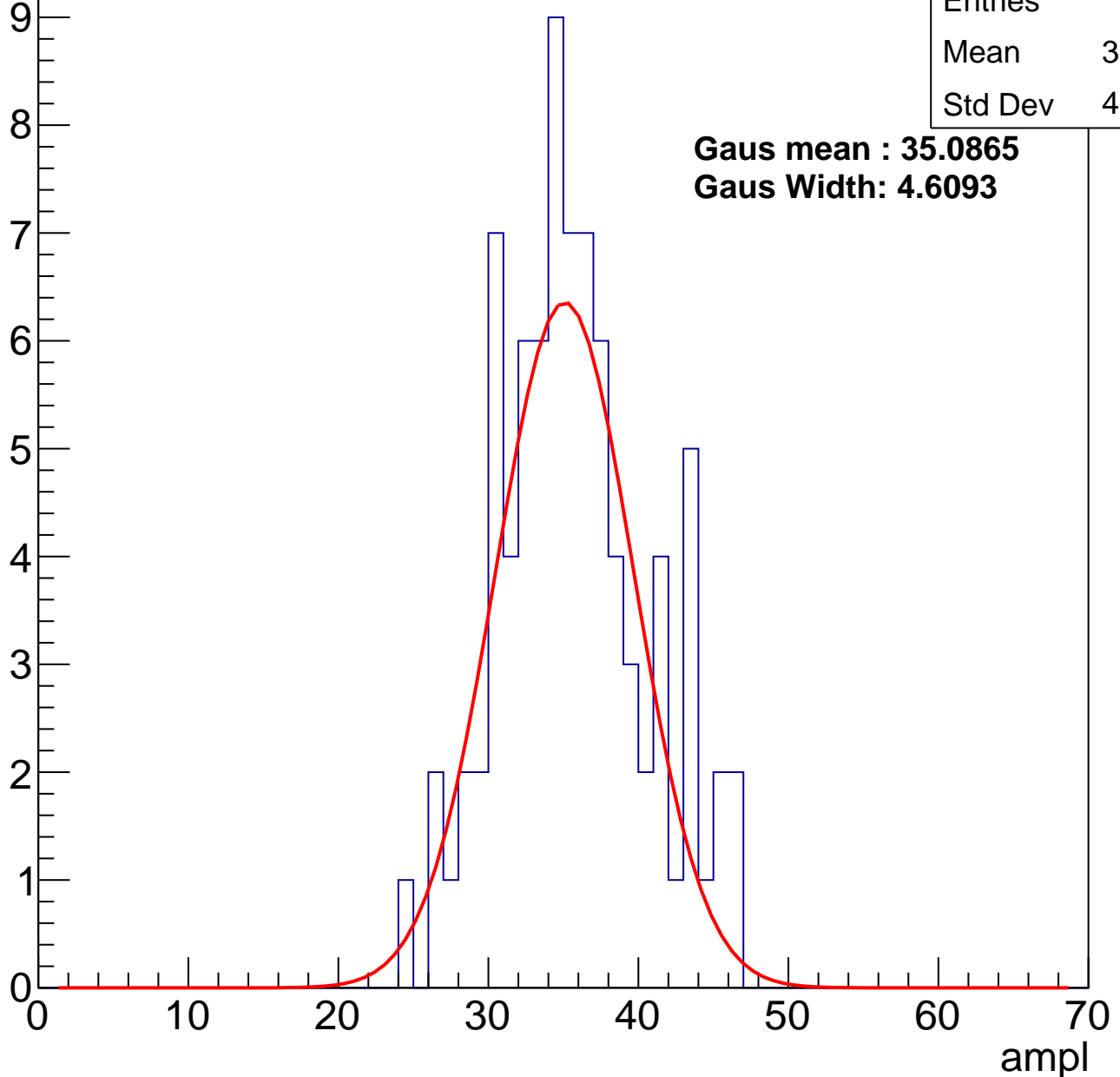
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	35.26
Std Dev	4.926

Gaus mean : 35.0865

Gaus Width: 4.6093



B0L102S, U5-ch0, adc2

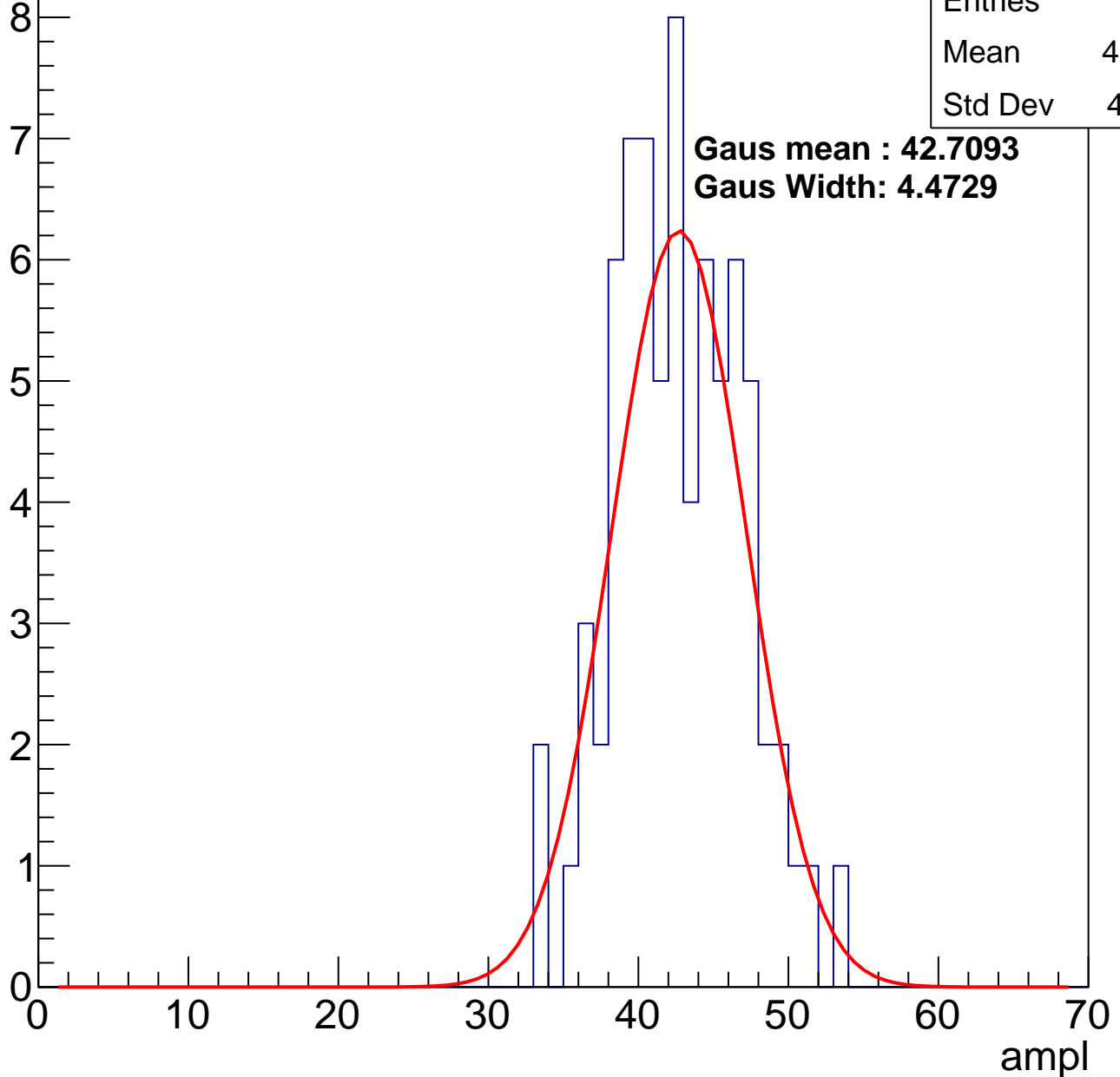
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	42.23
Std Dev	4.171

Gaus mean : 42.7093

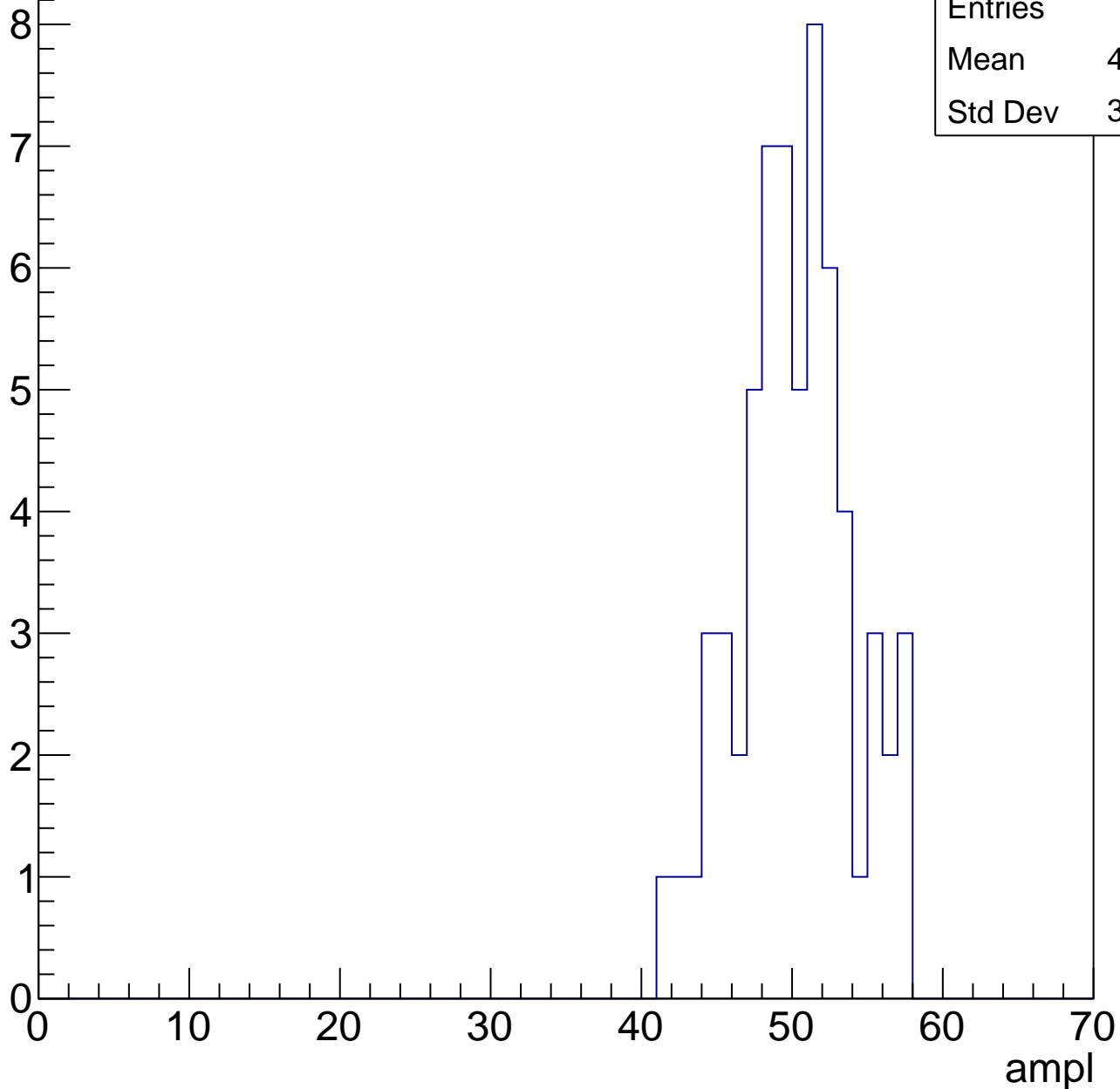
Gaus Width: 4.4729



B0L102S, U5-ch0, adc3

calib_packv5_042523_0143.root, FC#12, port B1

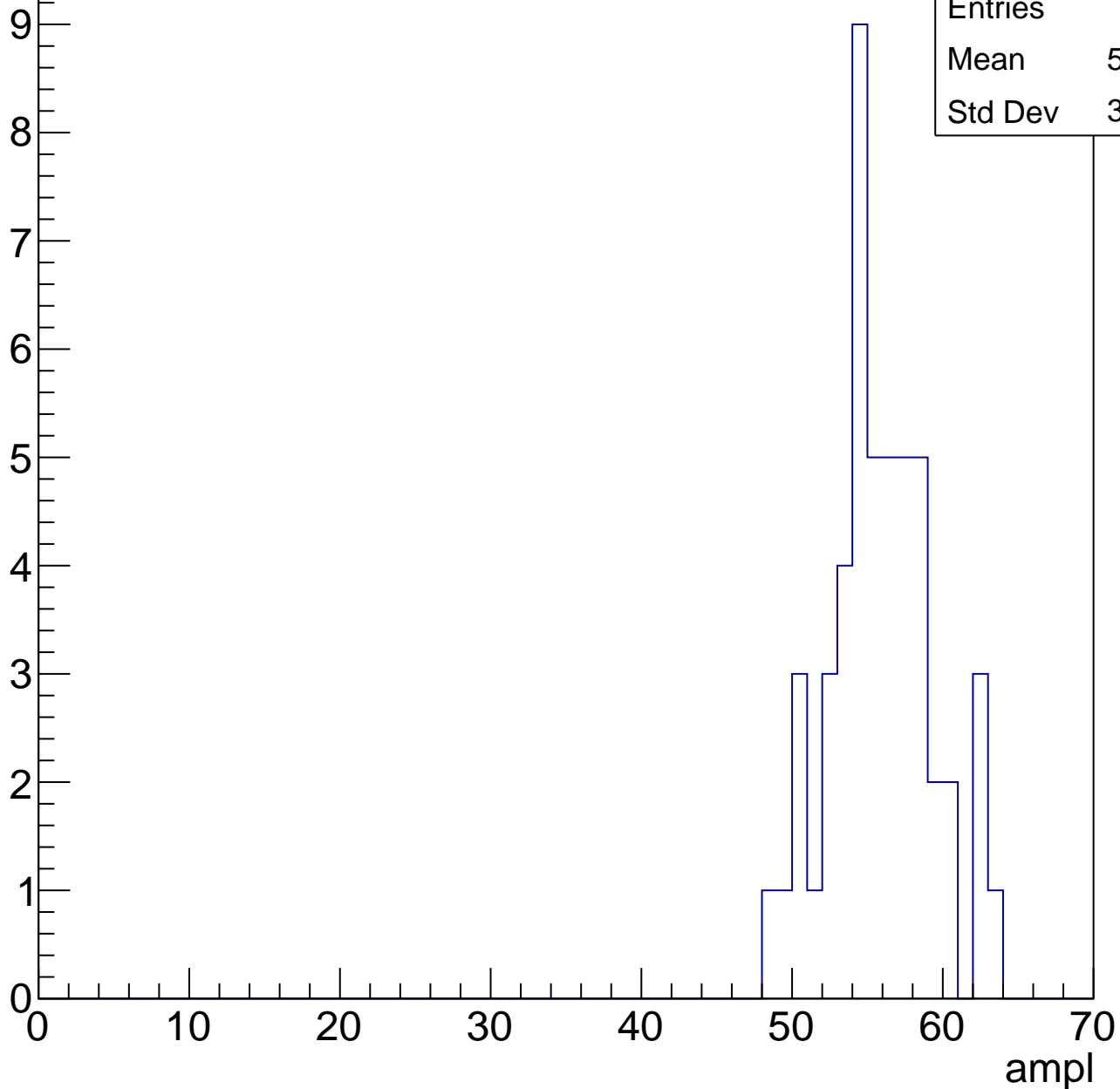
Entry



B0L102S, U5-ch0, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch0, adc5

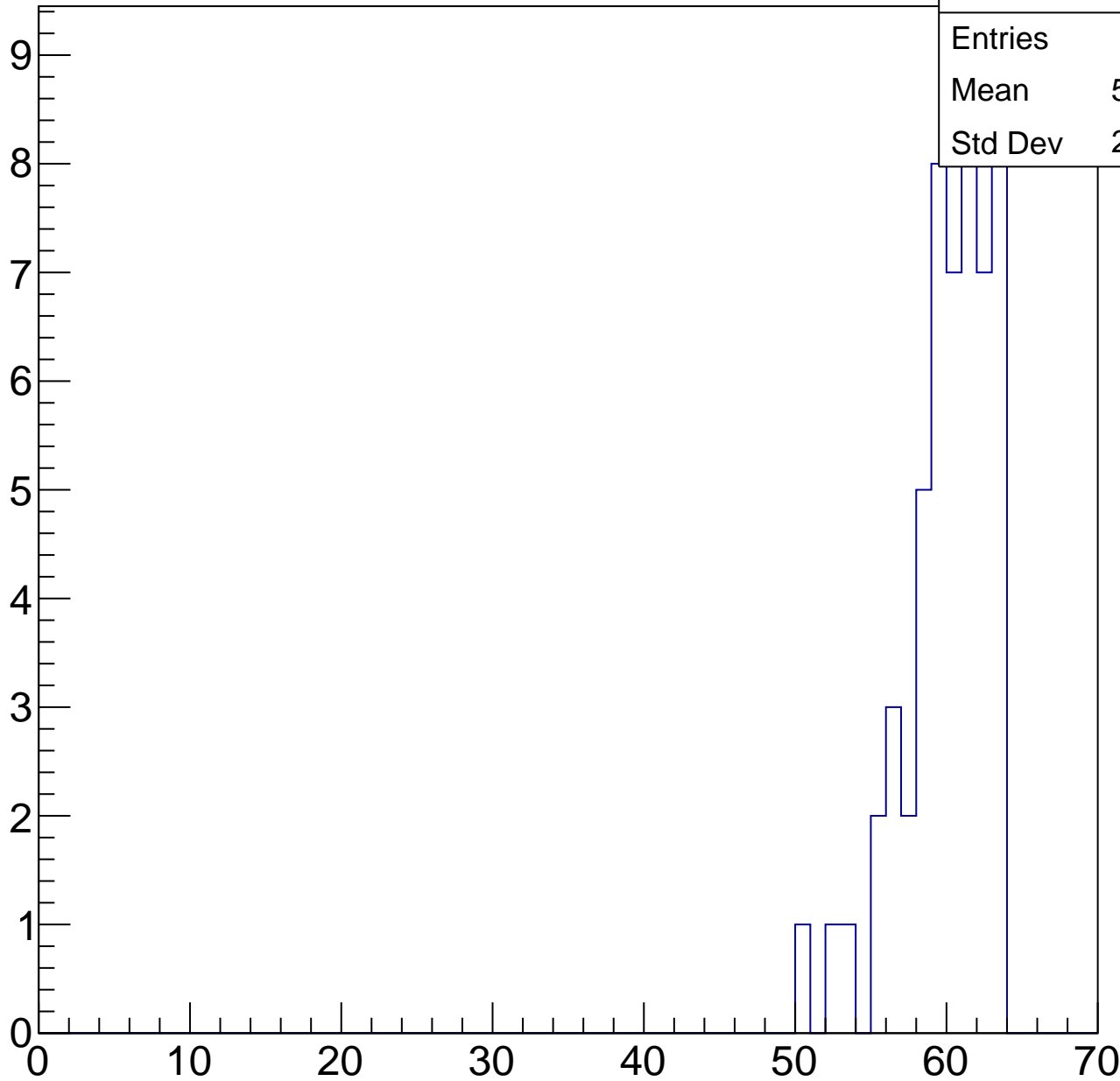
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	59.62
Std Dev	2.914

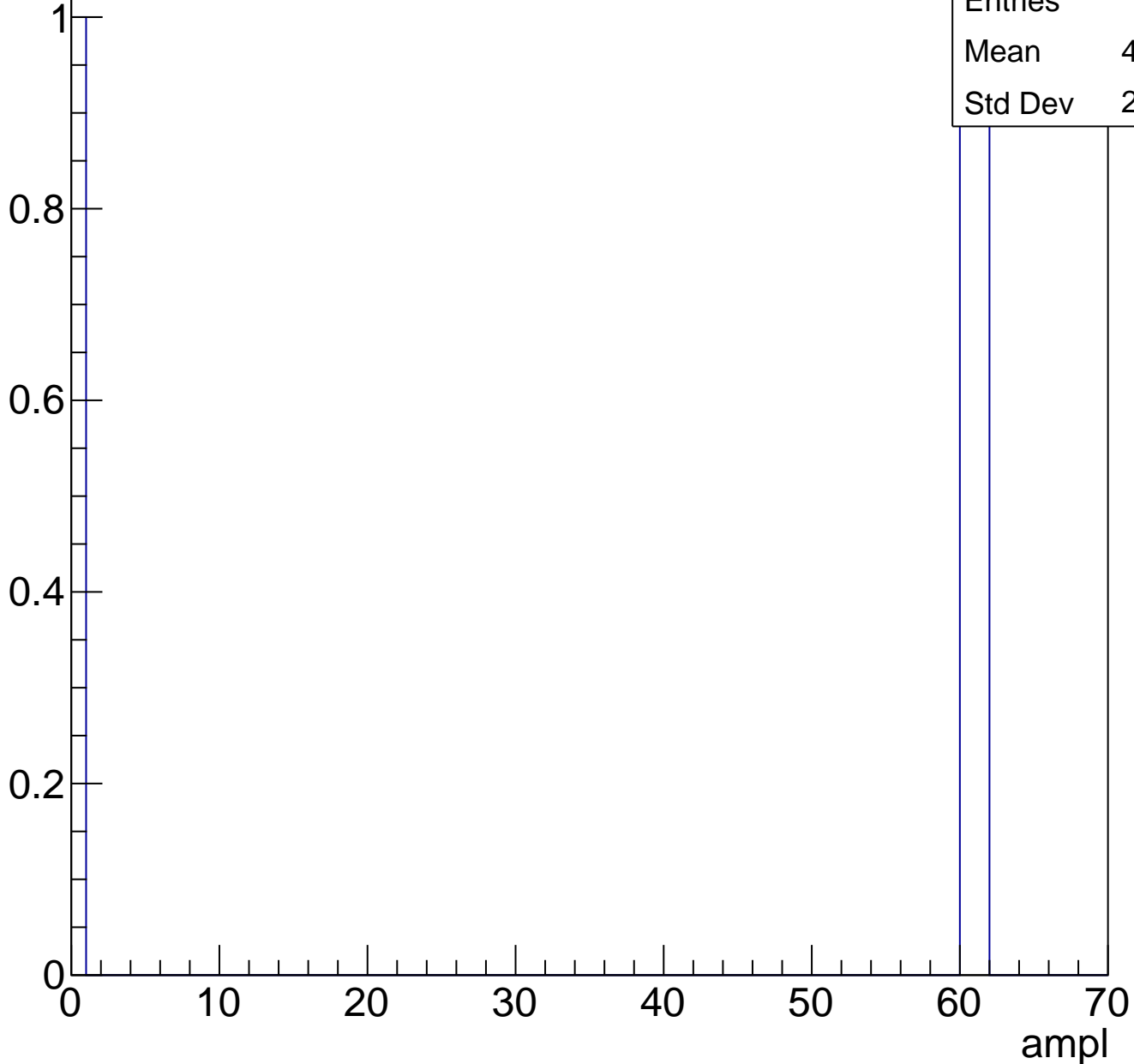
ampl



B0L102S, U5-ch0, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch0, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch1, adc0

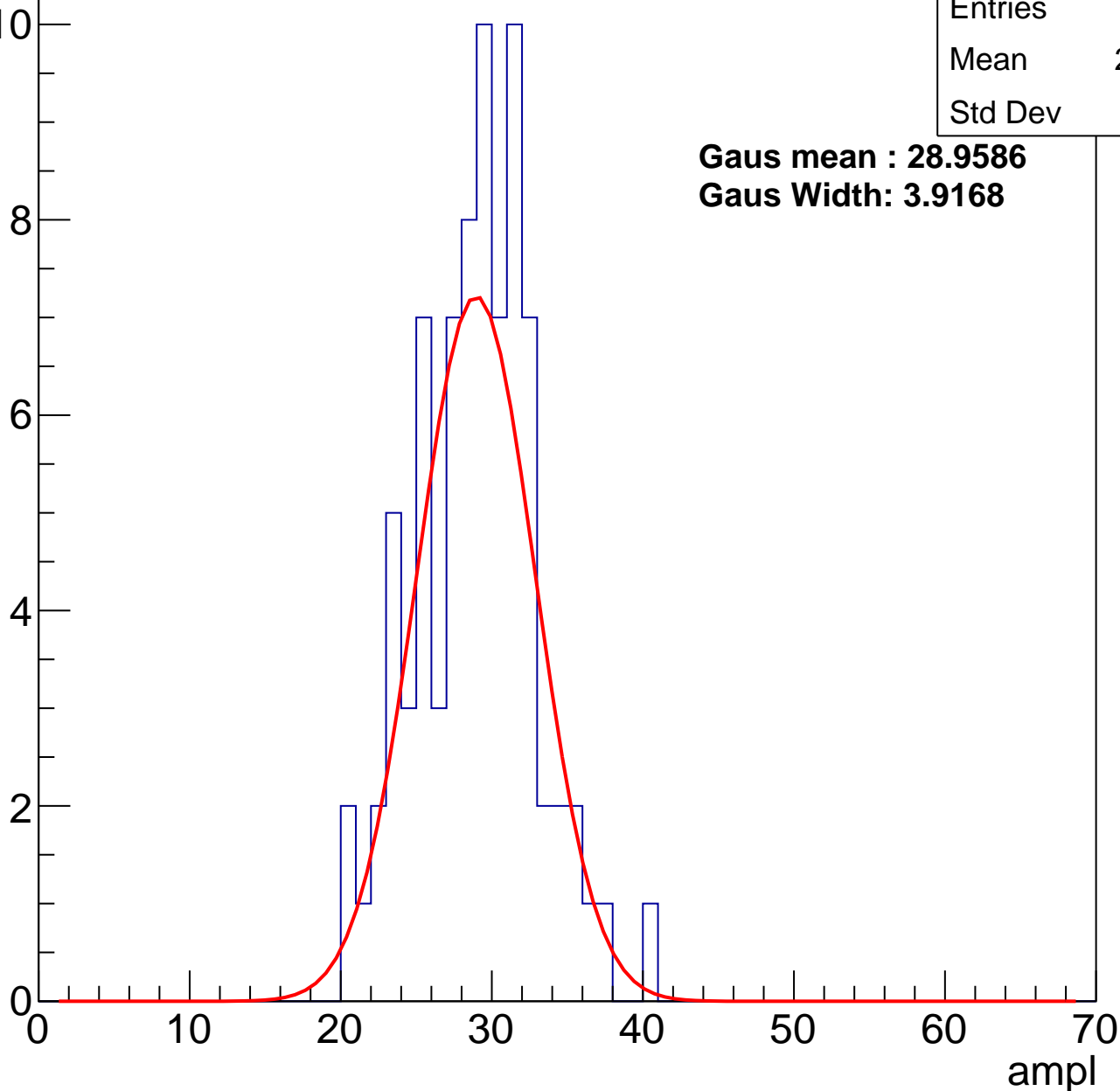
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	28.51
Std Dev	3.9

Gaus mean : 28.9586

Gaus Width: 3.9168



B0L102S, U5-ch1, adc1

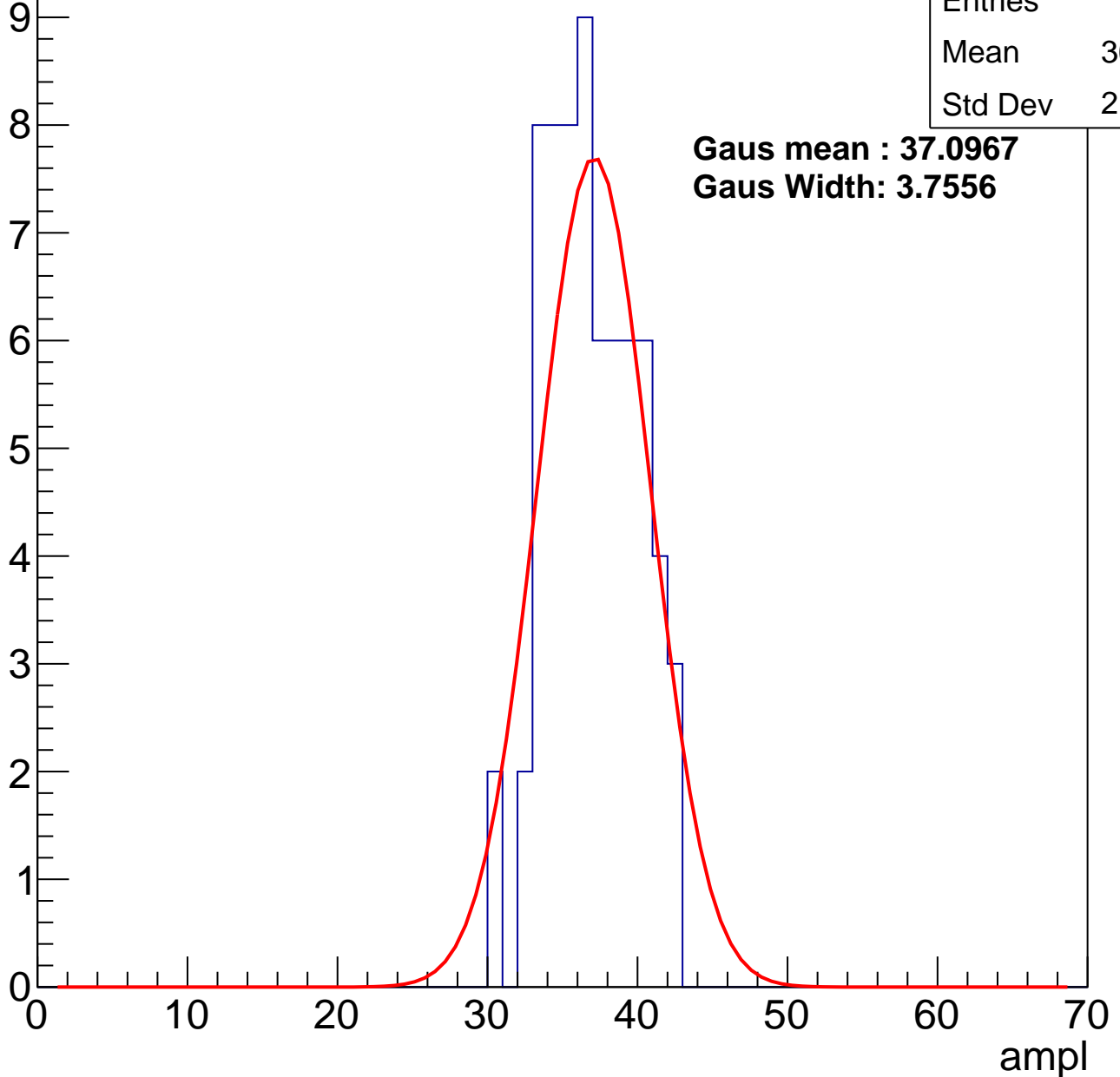
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.44
Std Dev	2.947

Gaus mean : 37.0967

Gaus Width: 3.7556



B0L102S, U5-ch1, adc2

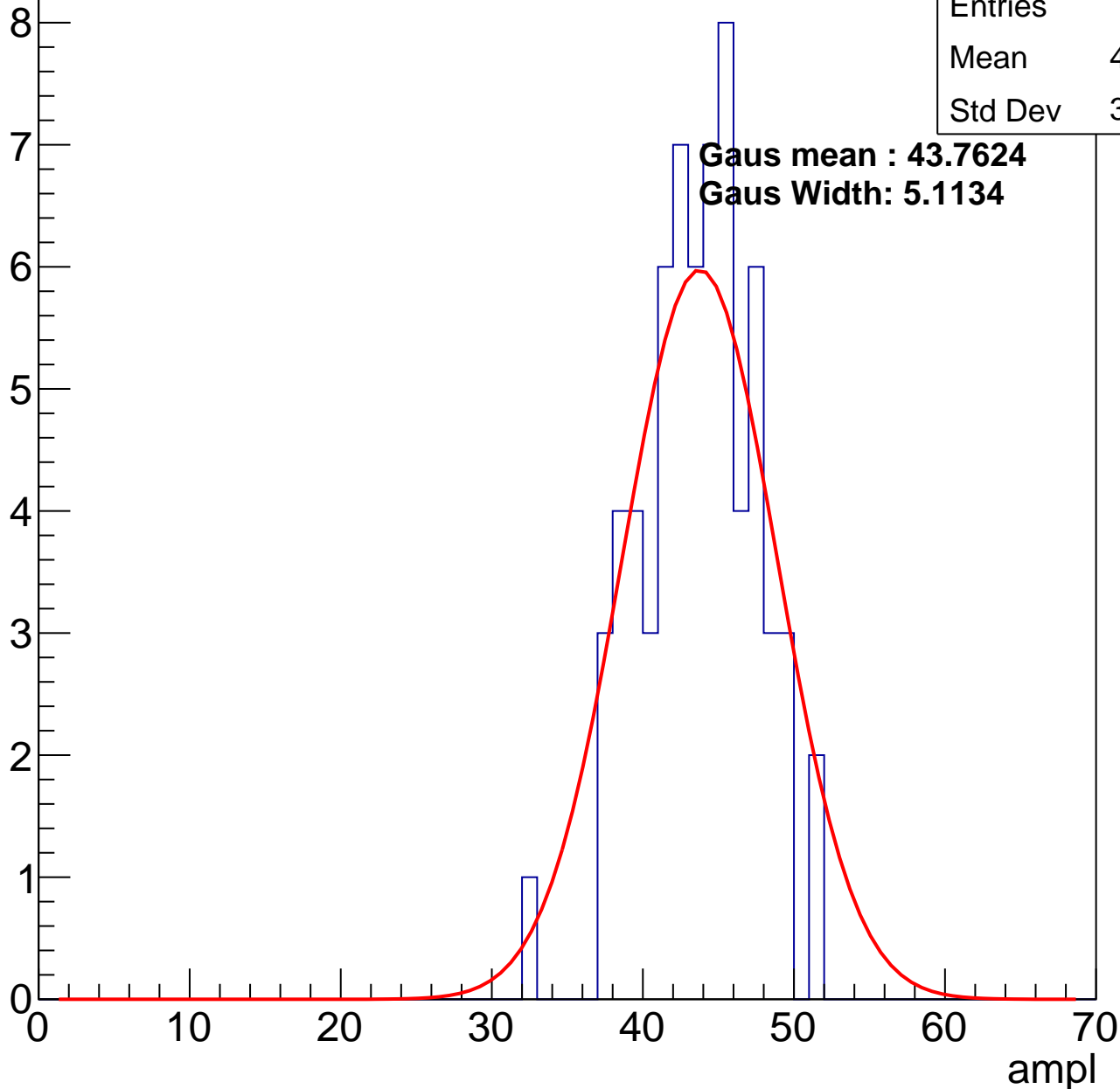
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.22
Std Dev	3.725

Gaus mean : 43.7624

Gaus Width: 5.1134

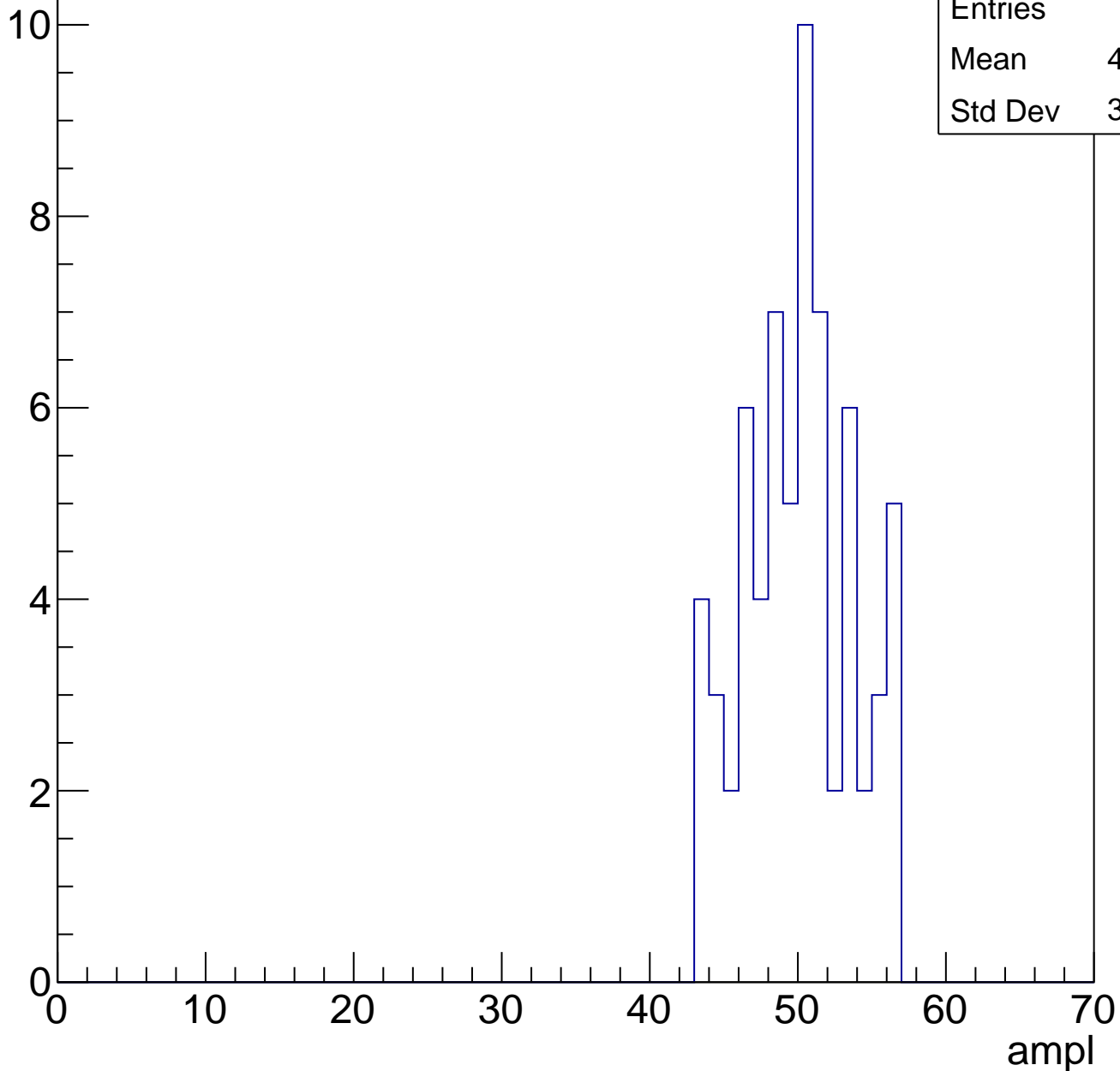


B0L102S, U5-ch1, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	66
Mean	49.56
Std Dev	3.614

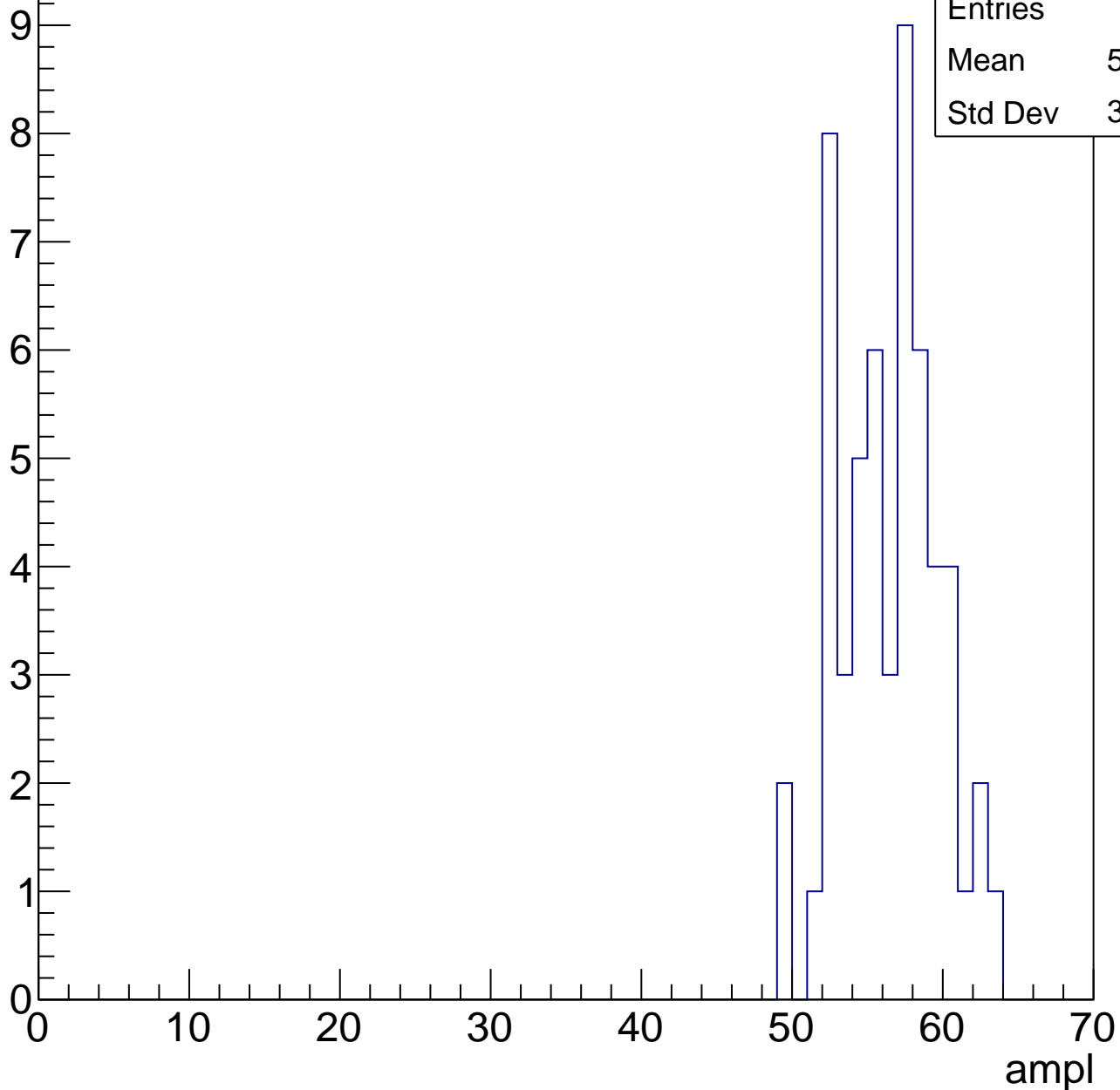
Entry



B0L102S, U5-ch1, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch1, adc5

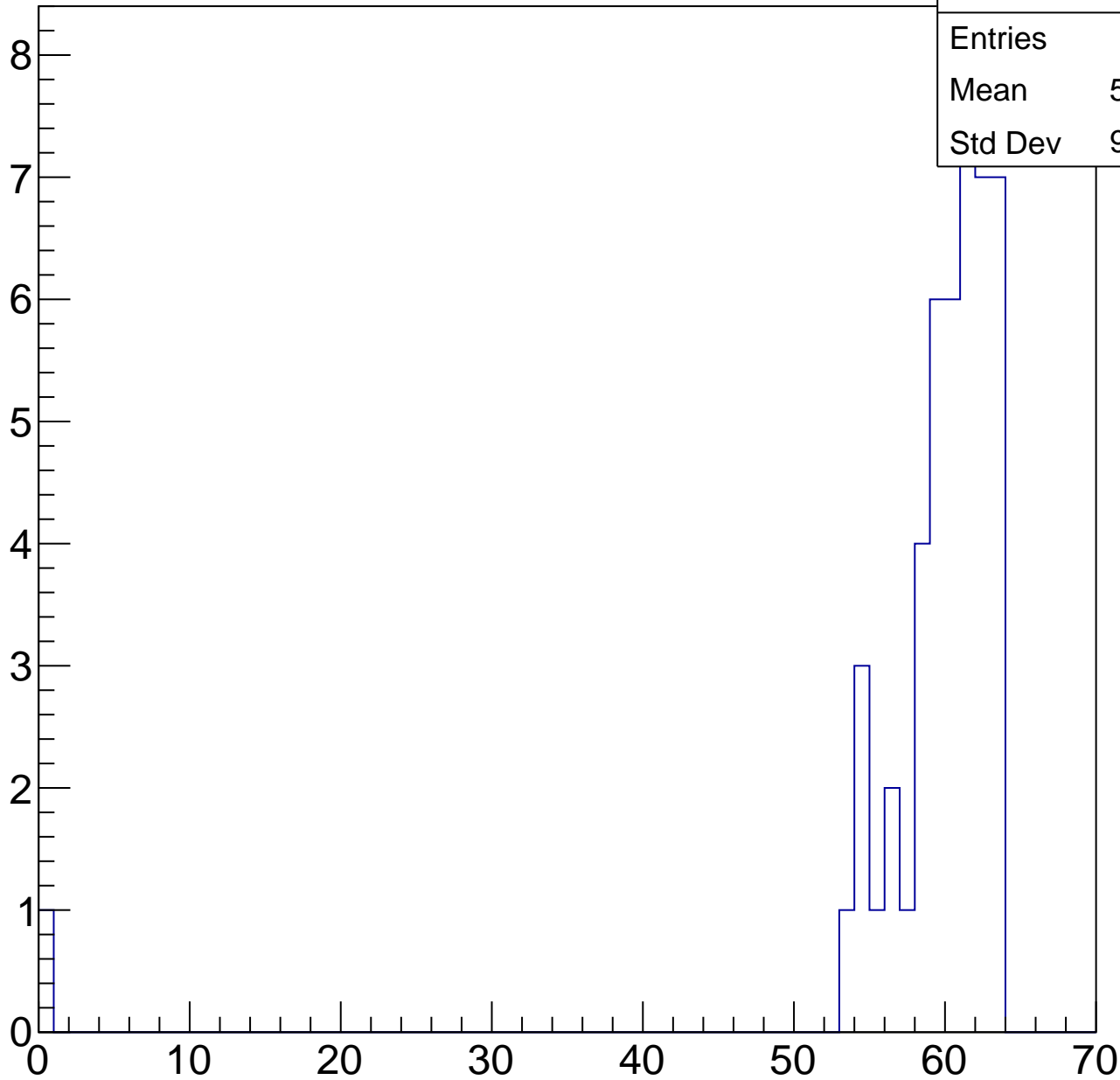
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.47
Std Dev	9.032

ampl



B0L102S, U5-ch1, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L102S, U5-ch1, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch2, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	29.45
Std Dev	4.595

Gaus mean : 30.7266

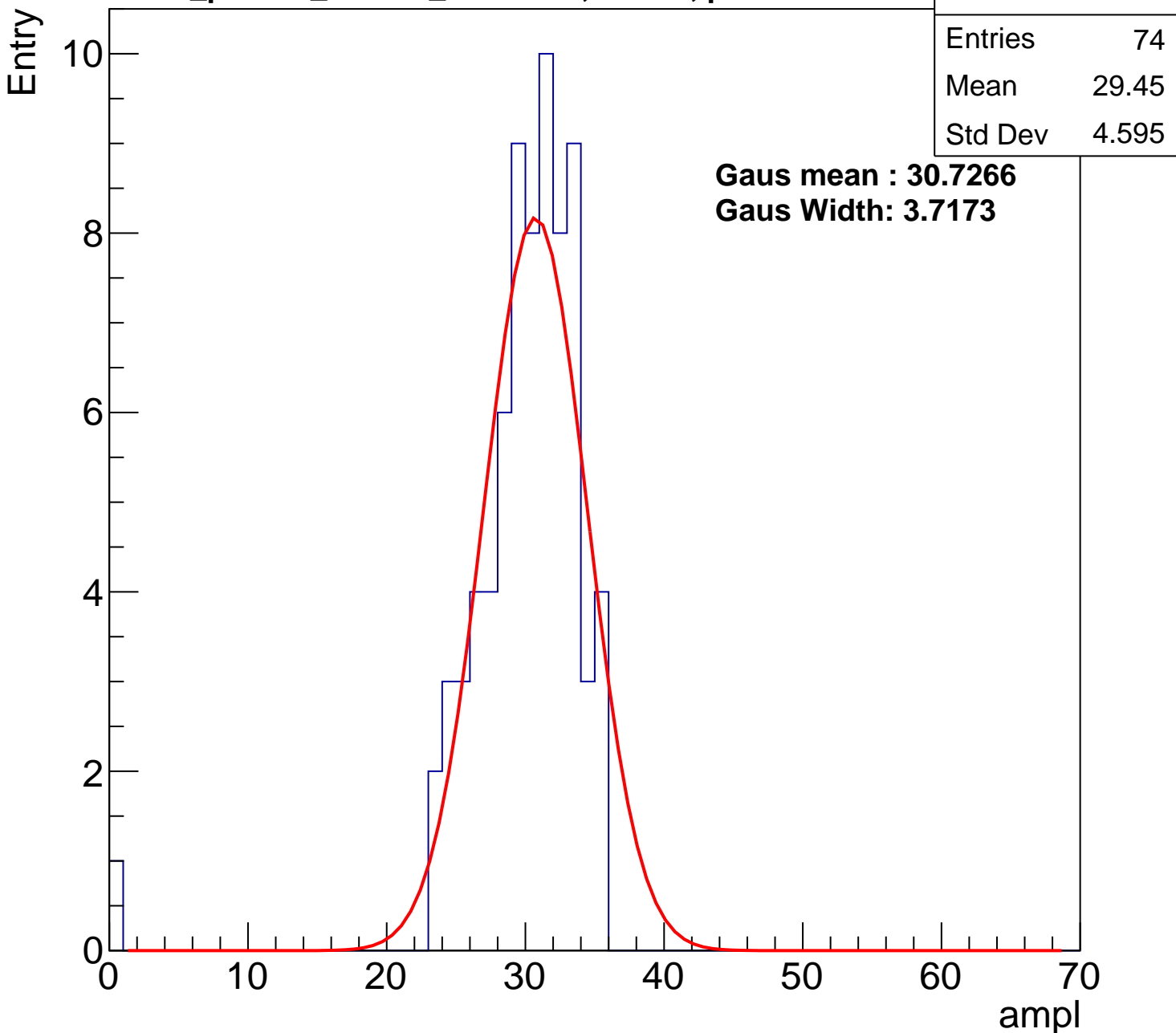
Gaus Width: 3.7173

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



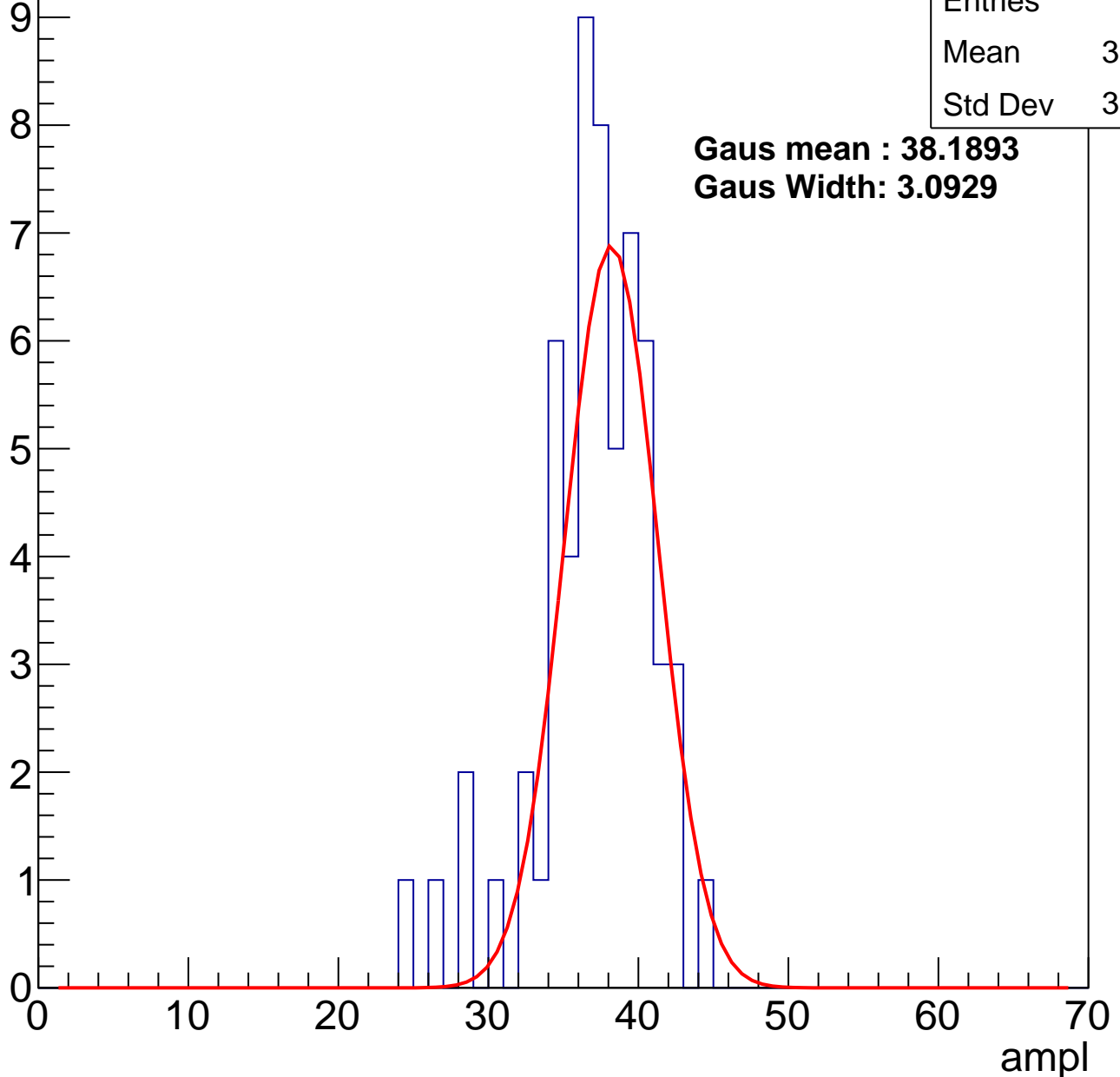
B0L102S, U5-ch2, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	36.55
Std Dev	3.858

Gaus mean : 38.1893
Gaus Width: 3.0929

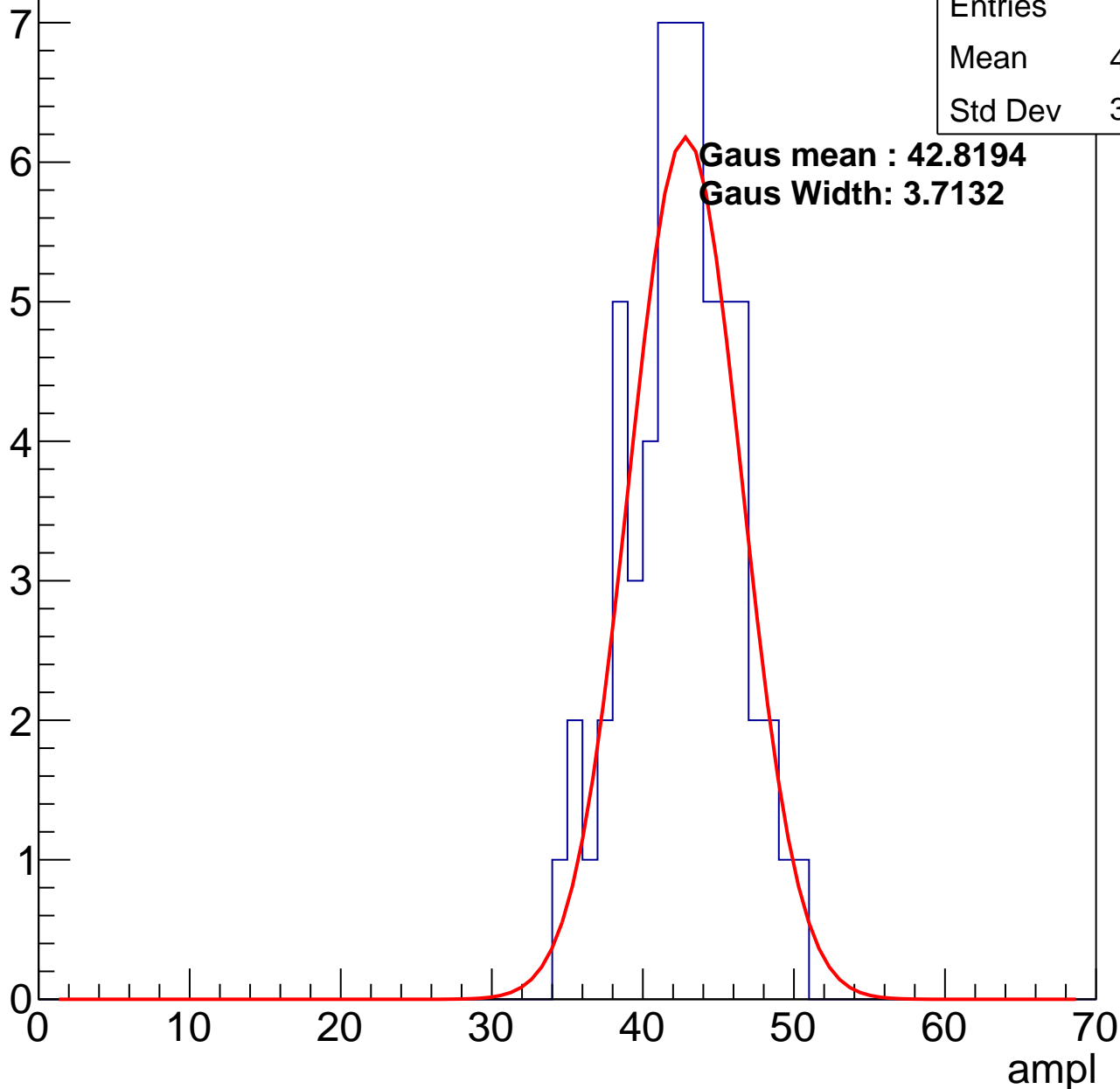


B0L102S, U5-ch2, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	42.12
Std Dev	3.569

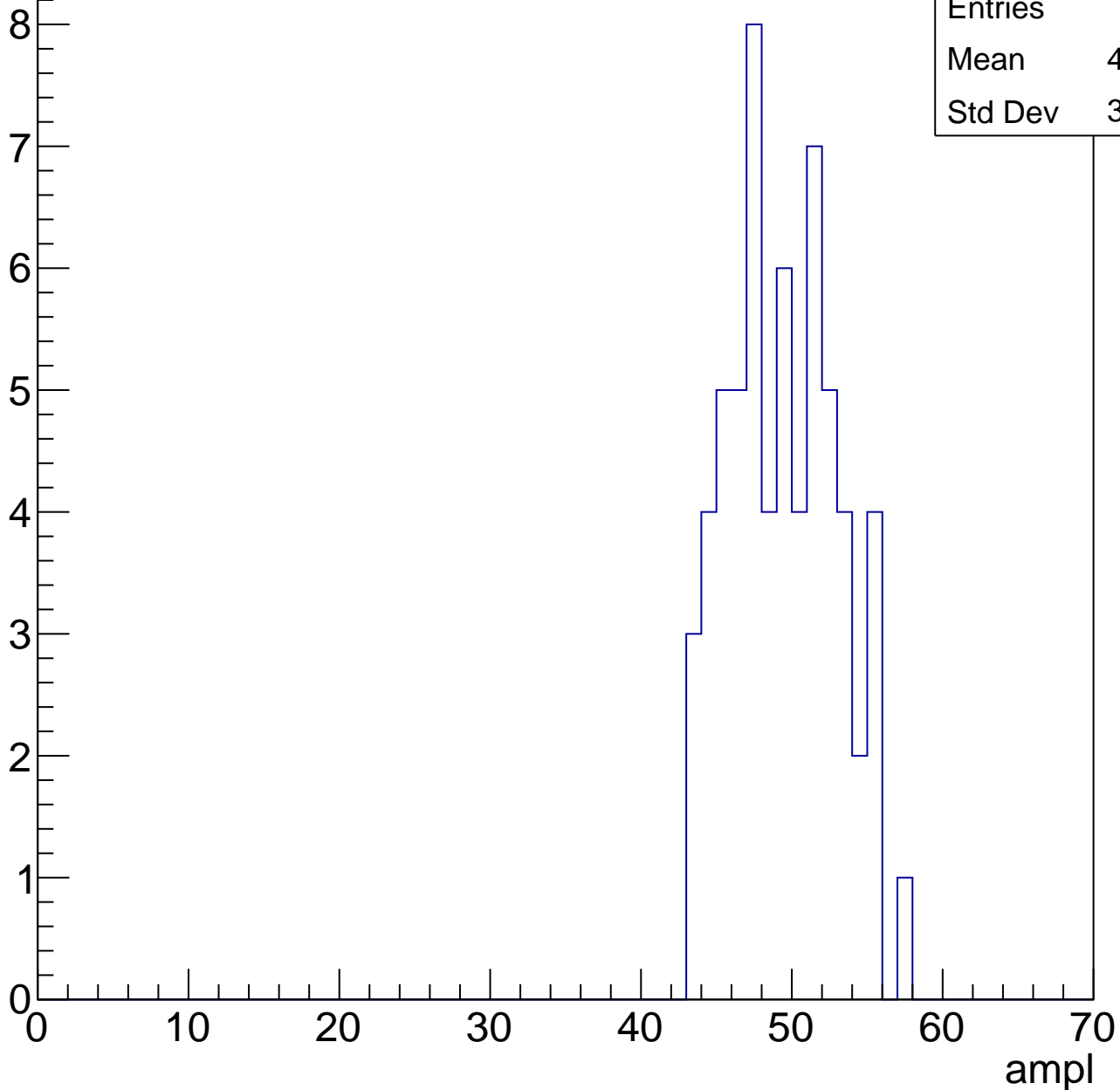


B0L102S, U5-ch2, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	48.97
Std Dev	3.519



B0L102S, U5-ch2, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 67

Mean 54.61

Std Dev 3.781

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

B0L102S, U5-ch2, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	57.97
Std Dev	9.665

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

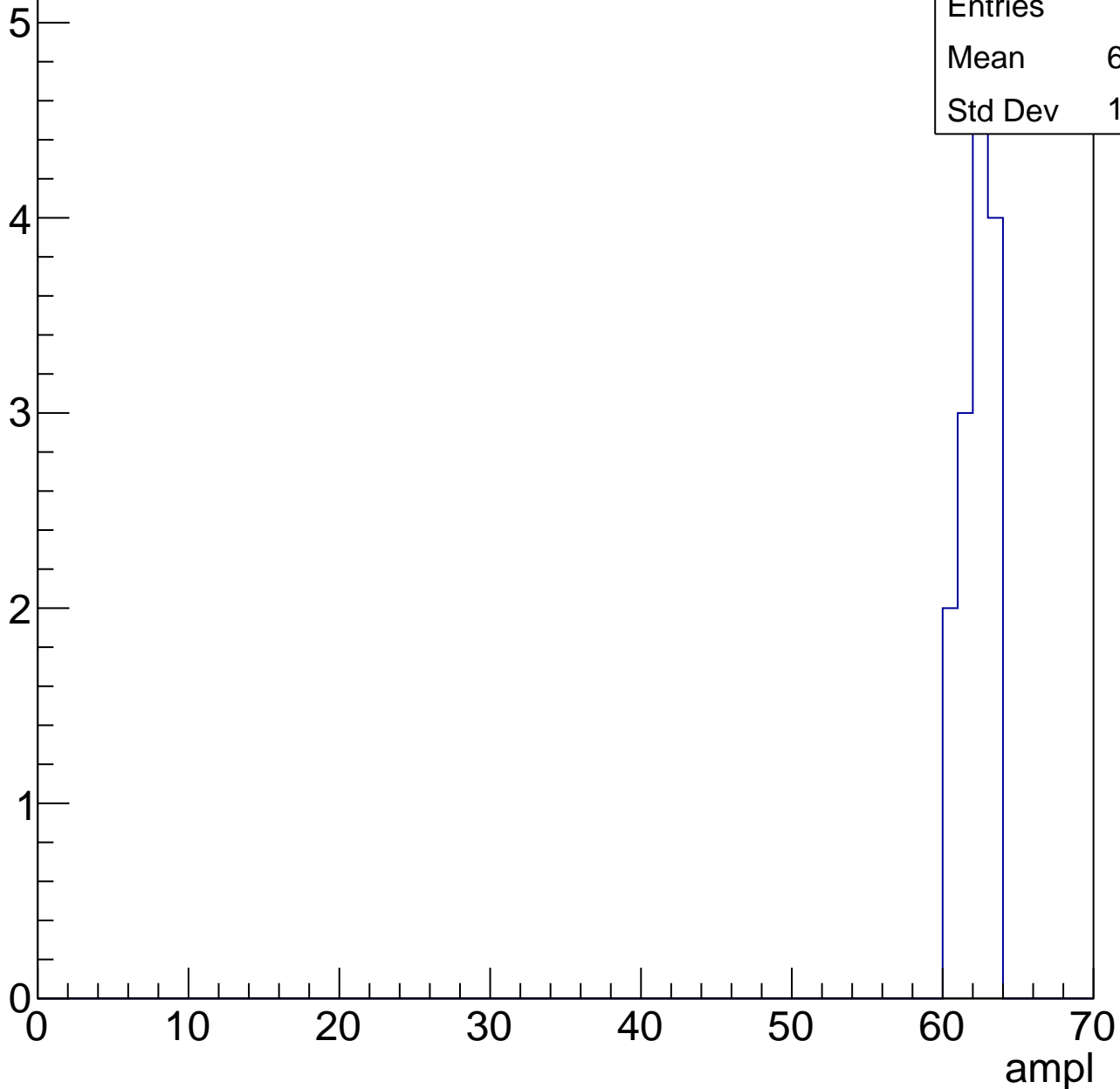
8

B0L102S, U5-ch2, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	14
Mean	61.79
Std Dev	1.013



B0L102S, U5-ch2, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch3, adc0

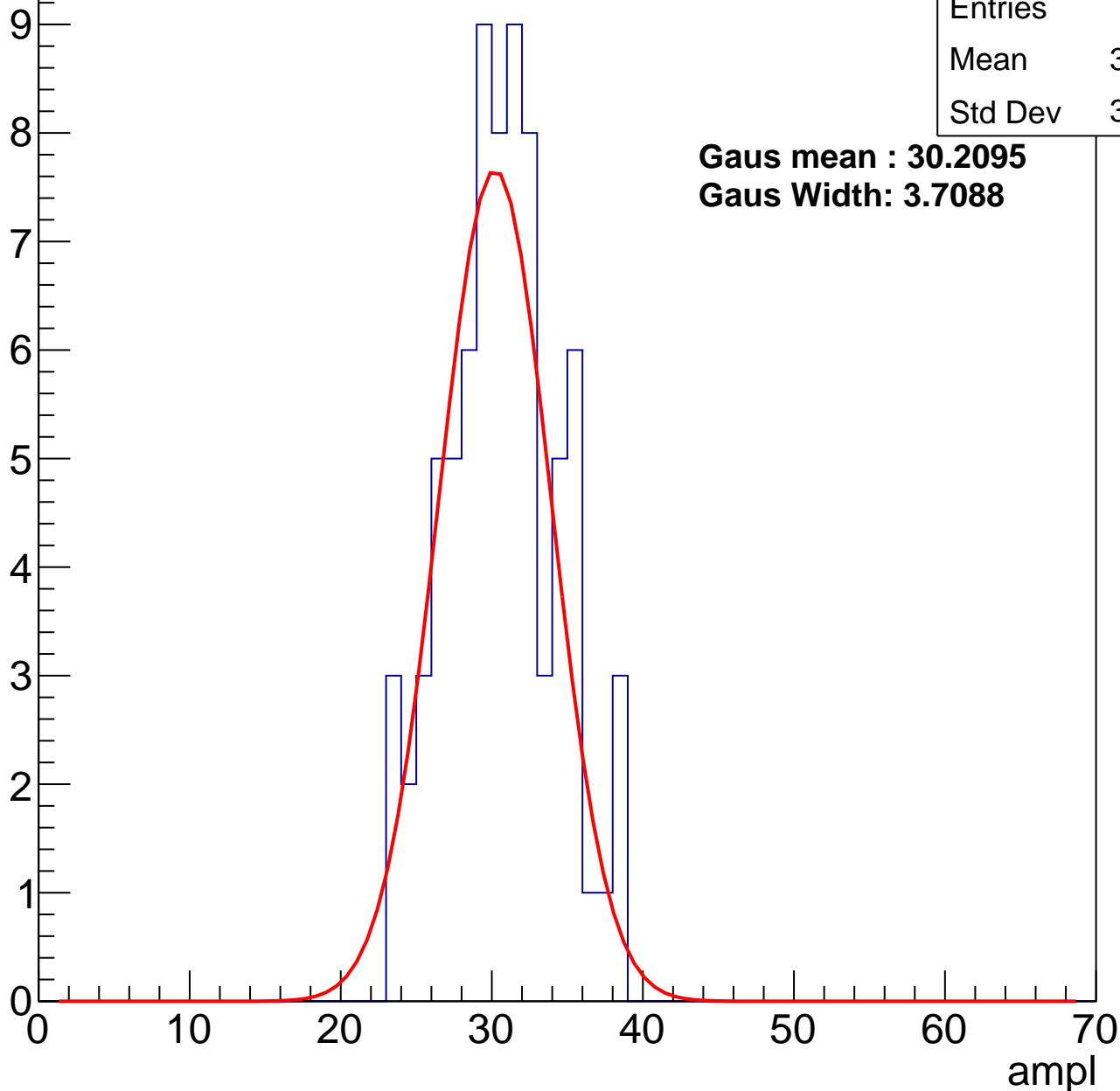
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	30.22
Std Dev	3.645

Gaus mean : 30.2095

Gaus Width: 3.7088



B0L102S, U5-ch3, adc1

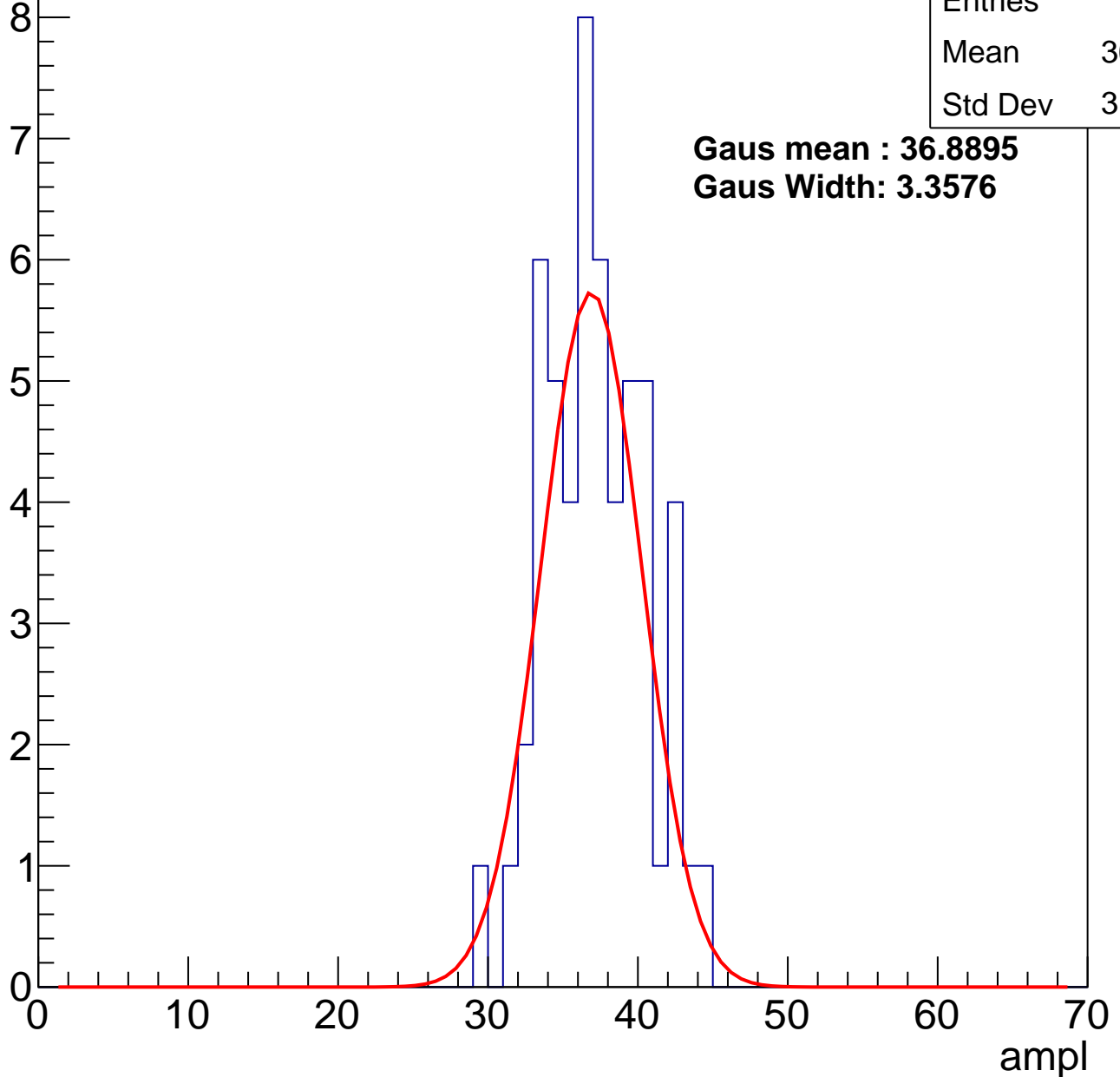
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	36.76
Std Dev	3.283

Gaus mean : 36.8895

Gaus Width: 3.3576



B0L102S, U5-ch3, adc2

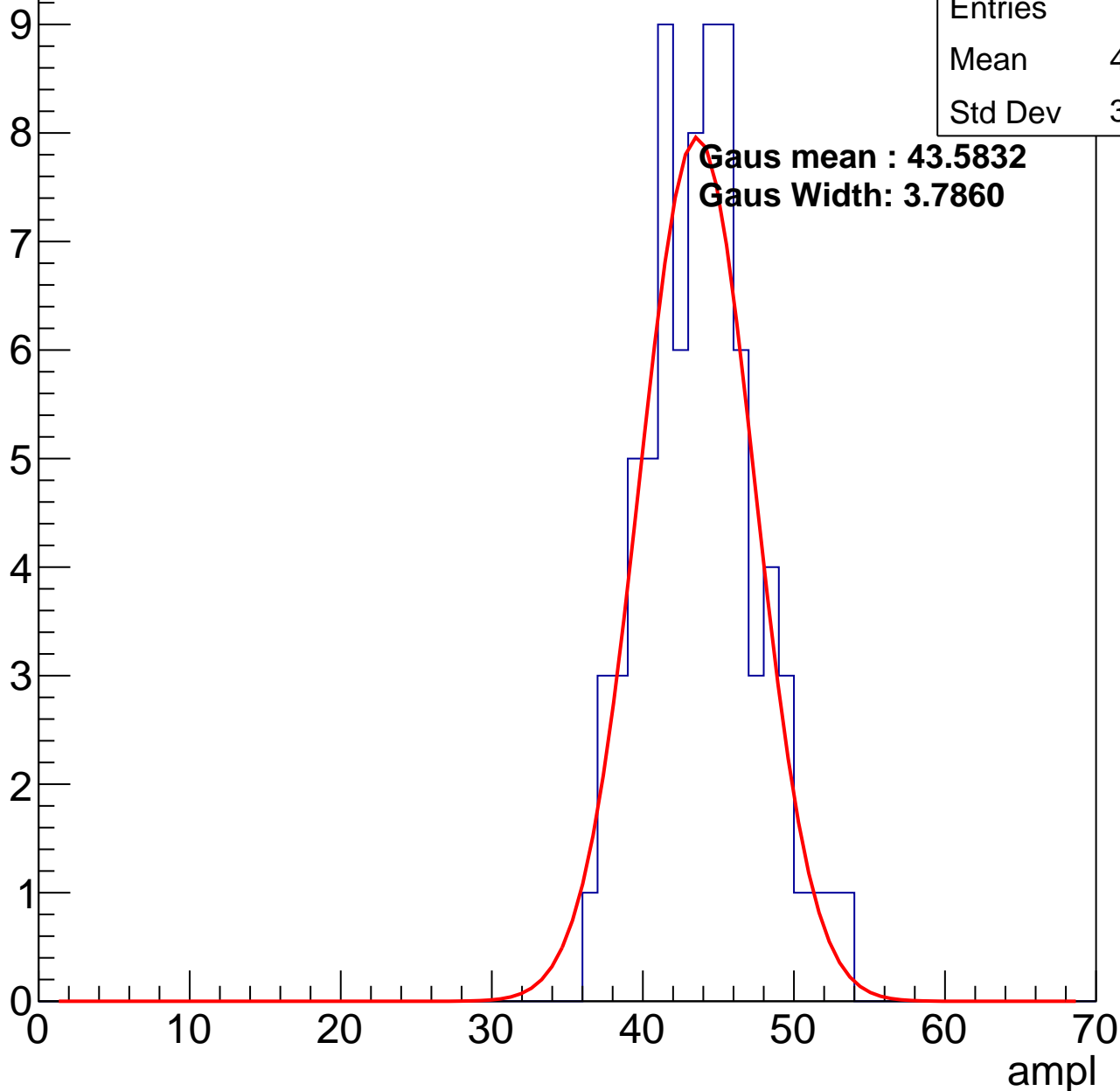
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	43.38
Std Dev	3.645

Gaus mean : 43.5832

Gaus Width: 3.7860

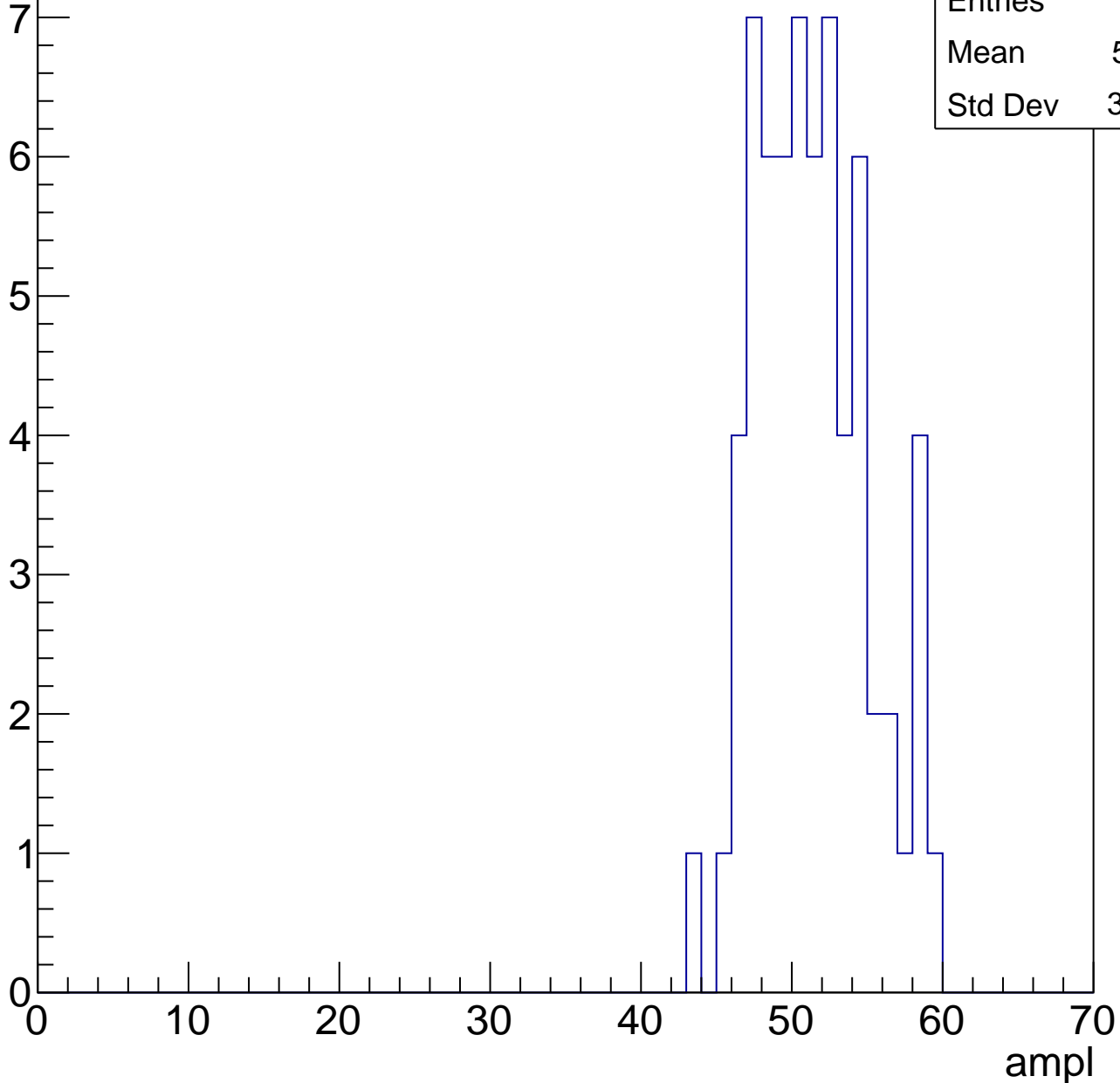


B0L102S, U5-ch3, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

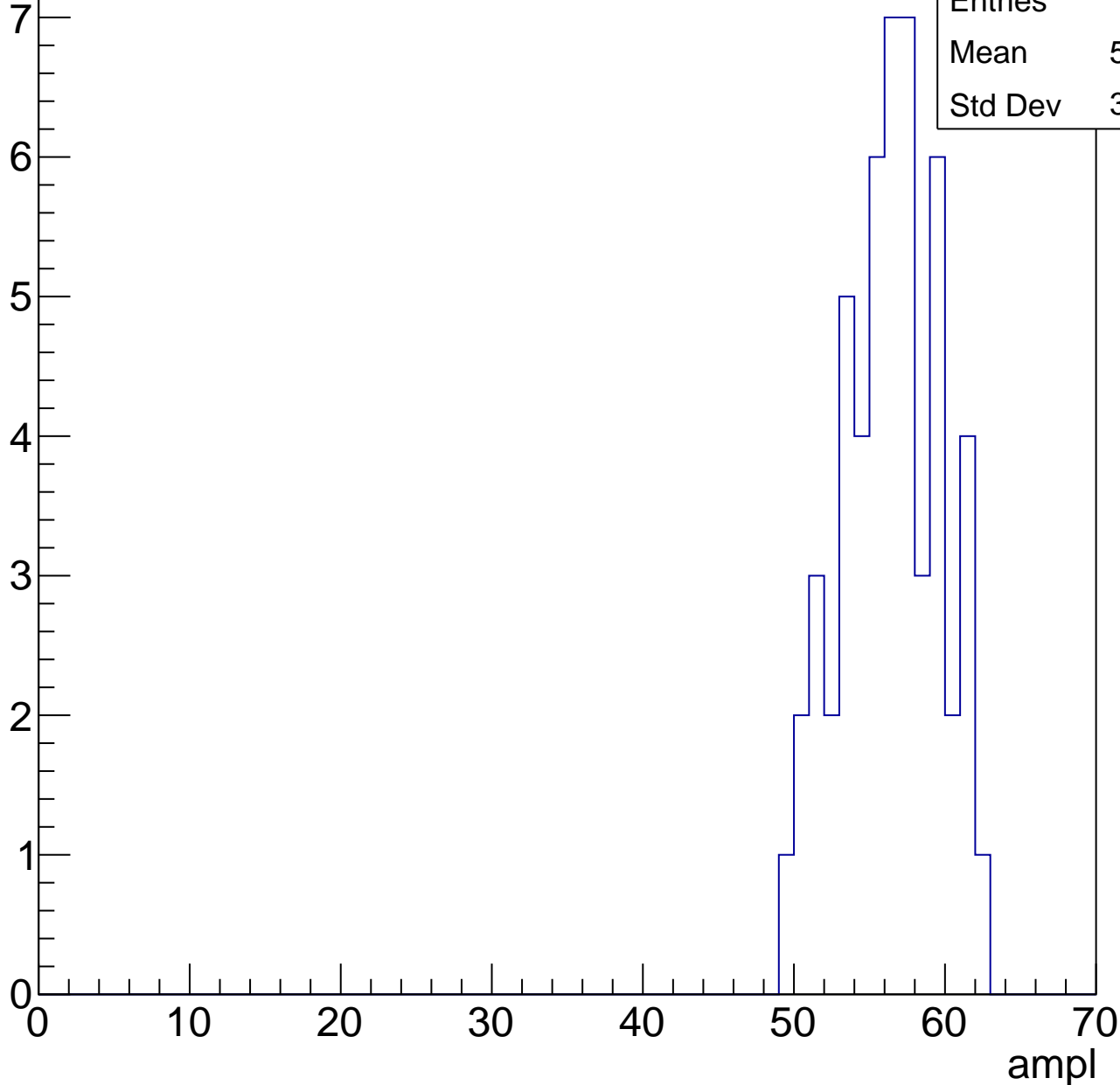
Entries	65
Mean	50.91
Std Dev	3.619



B0L102S, U5-ch3, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch3, adc5

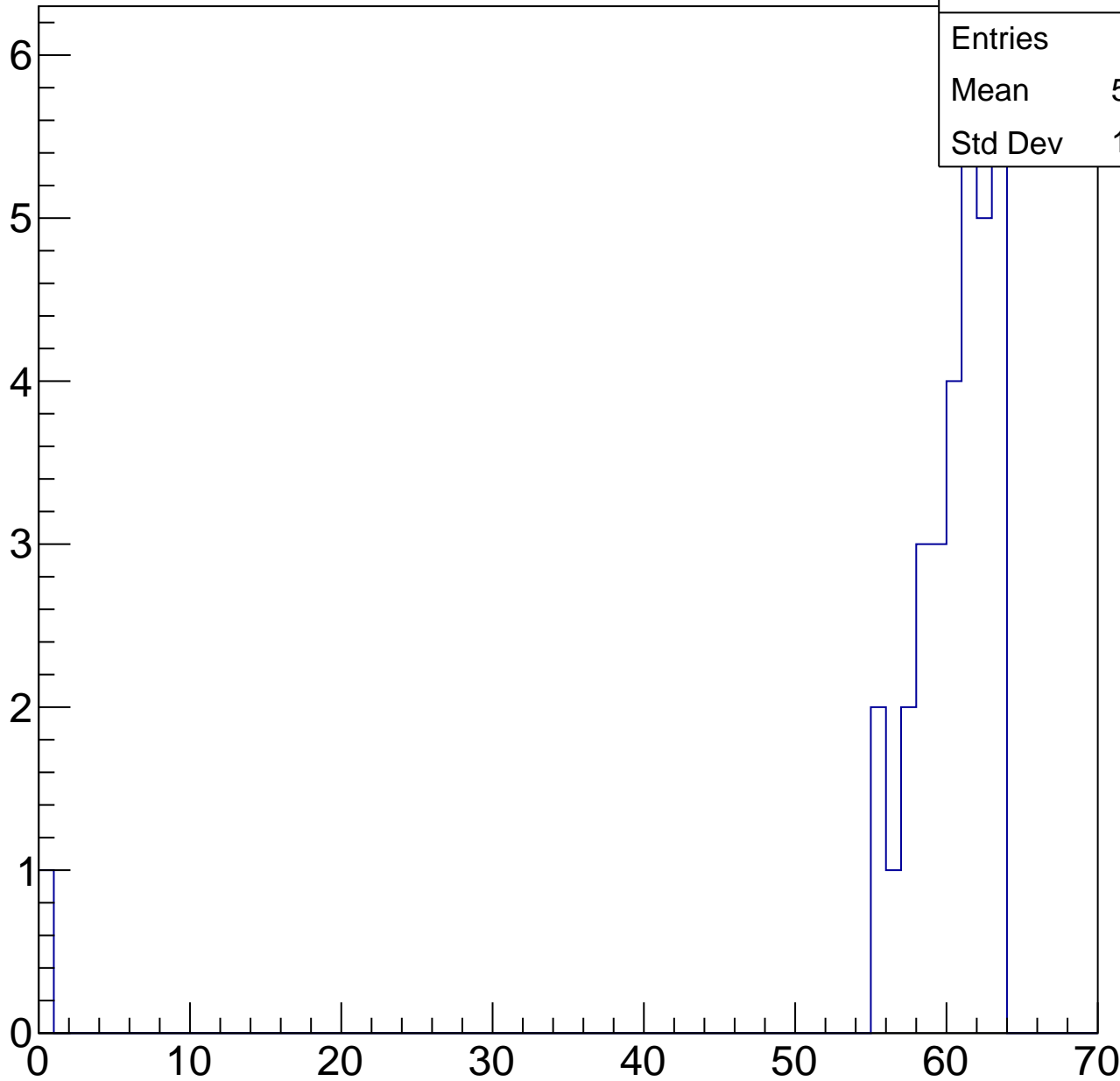
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

Entries	33
Mean	58.33
Std Dev	10.57

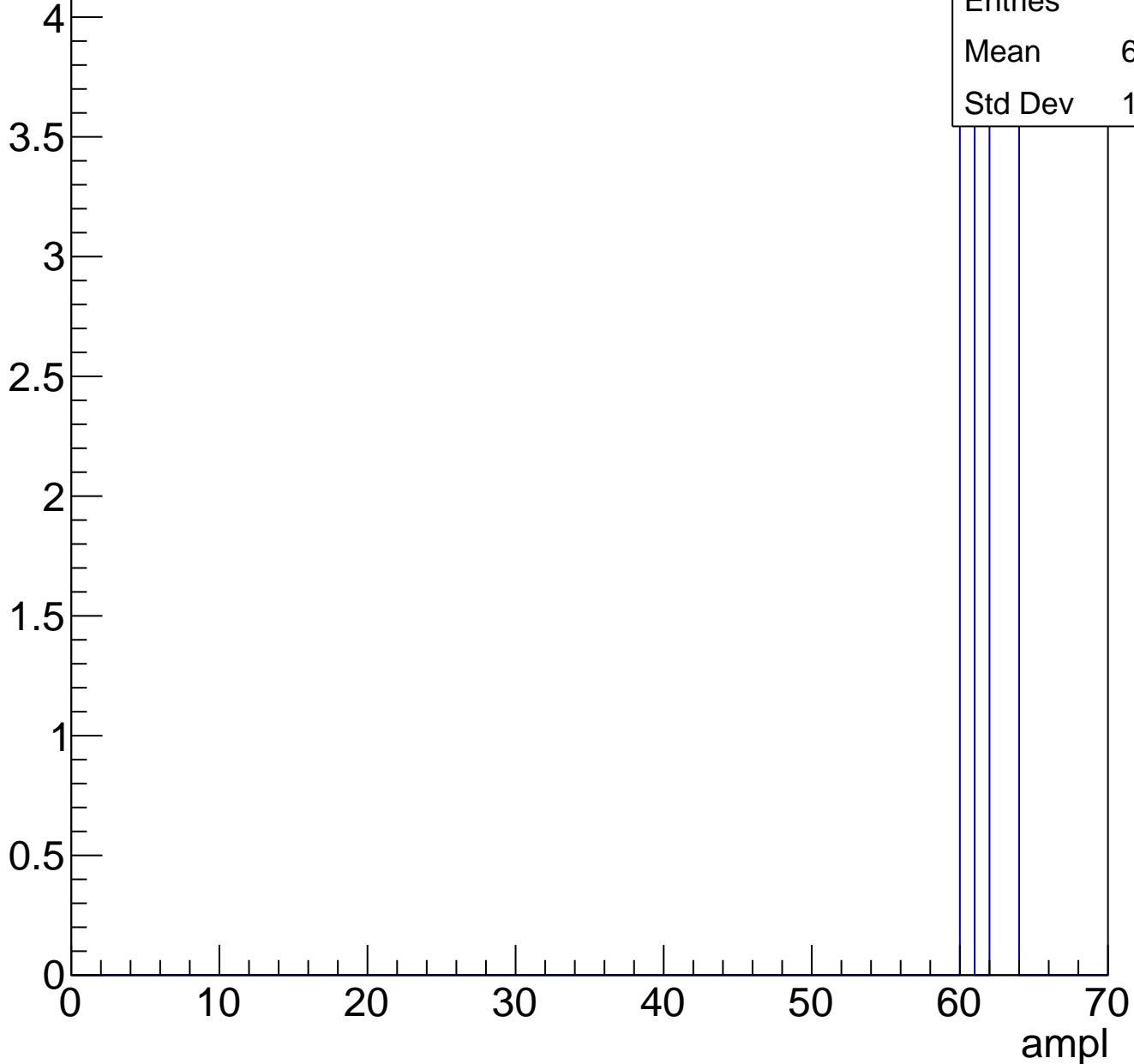
ampl



B0L102S, U5-ch3, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch3, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B0L102S, U5-ch4, adc0

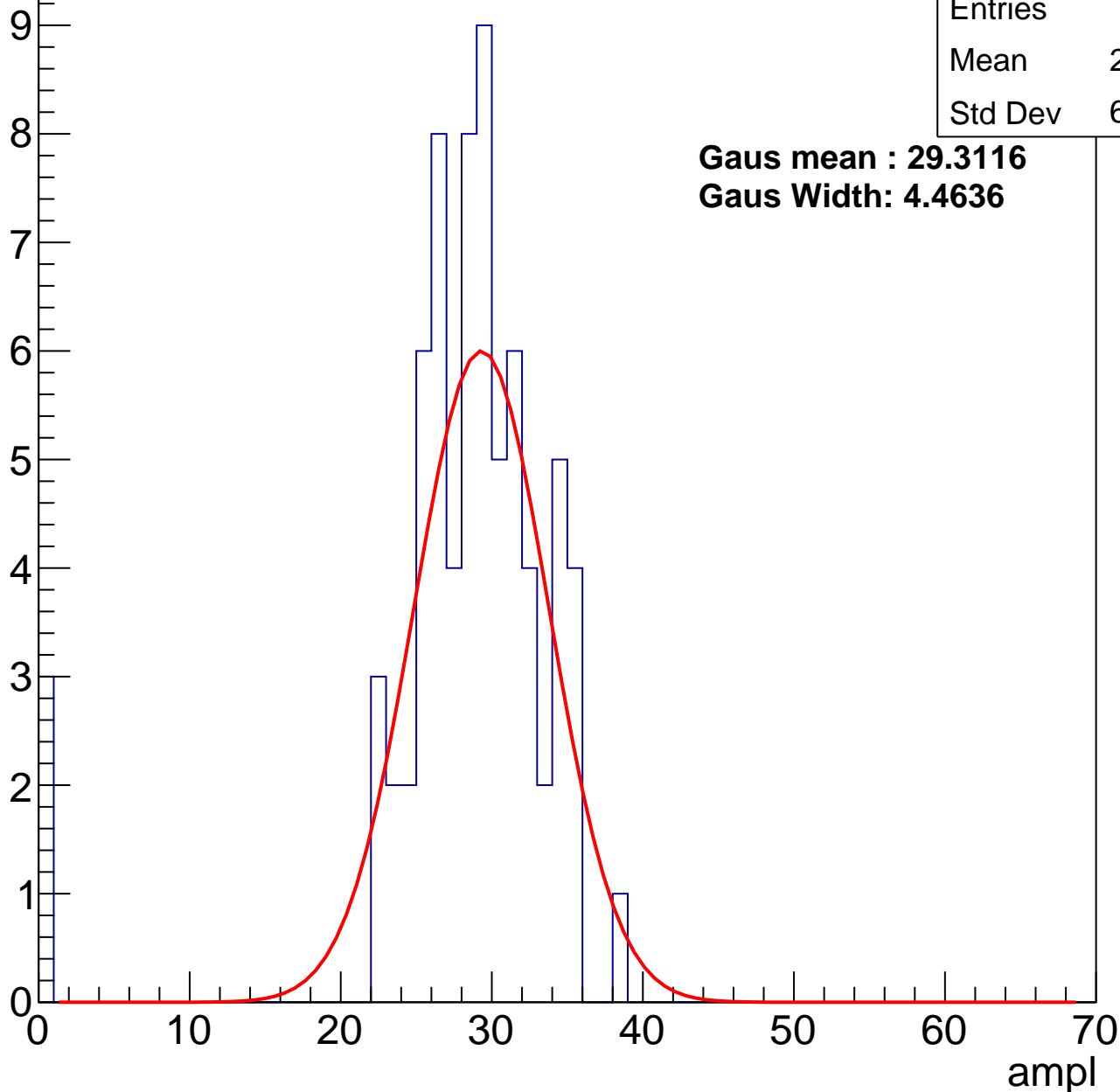
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	27.62
Std Dev	6.765

Gaus mean : 29.3116

Gaus Width: 4.4636

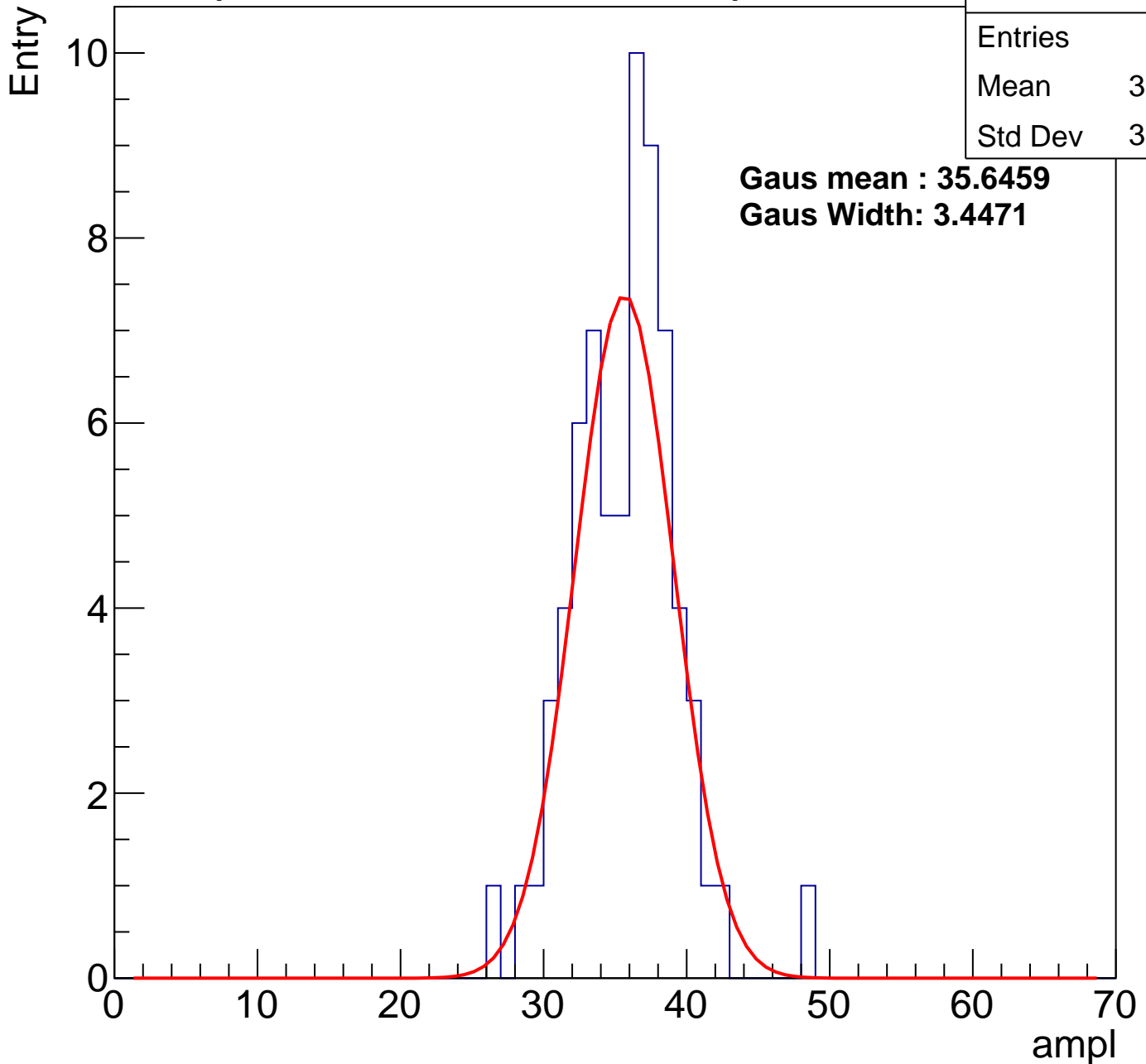


B0L102S, U5-ch4, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	35.23
Std Dev	3.592

Gaus mean : 35.6459
Gaus Width: 3.4471



B0L102S, U5-ch4, adc2

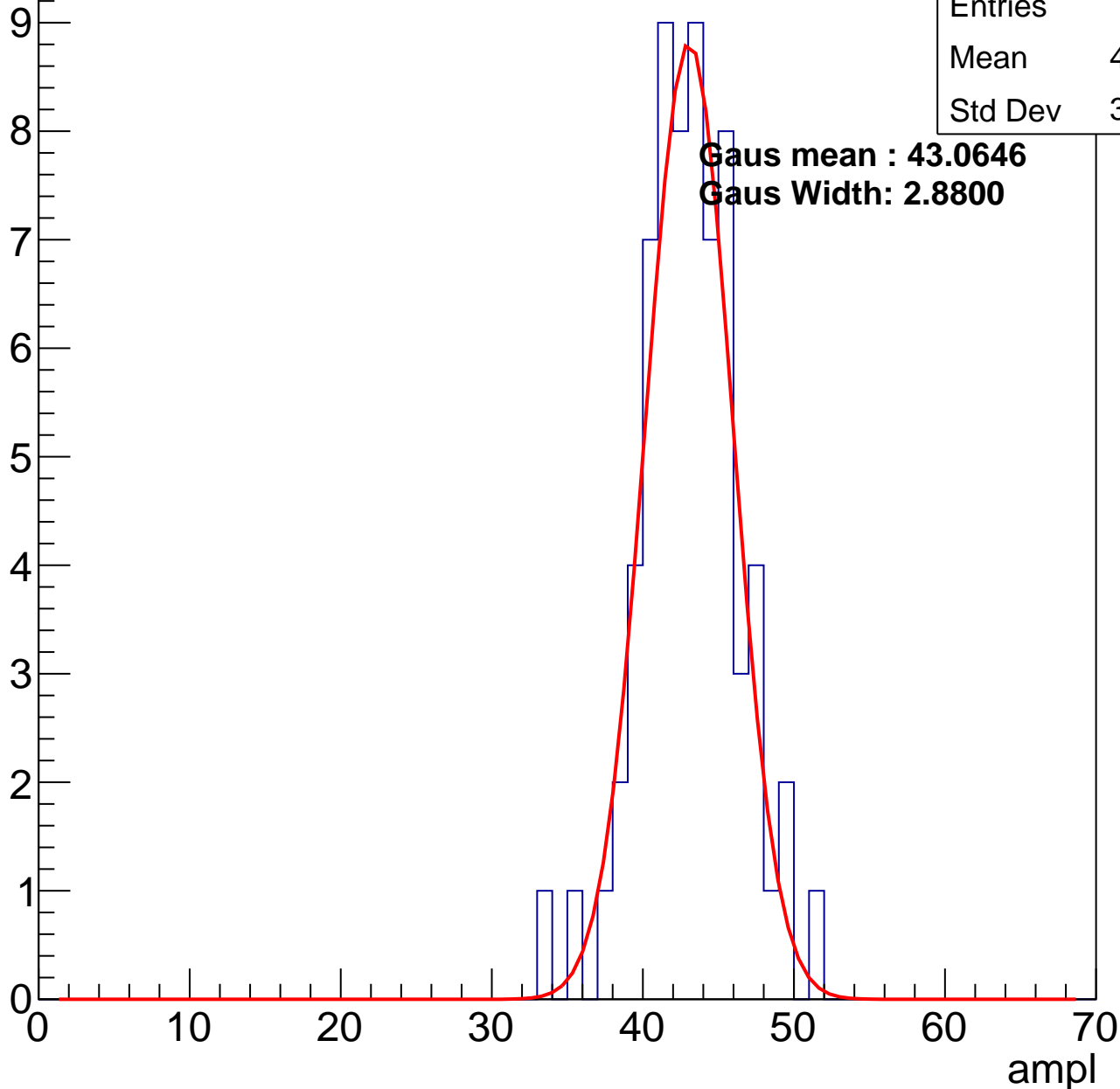
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.65
Std Dev	3.217

Gaus mean : 43.0646

Gaus Width: 2.8800

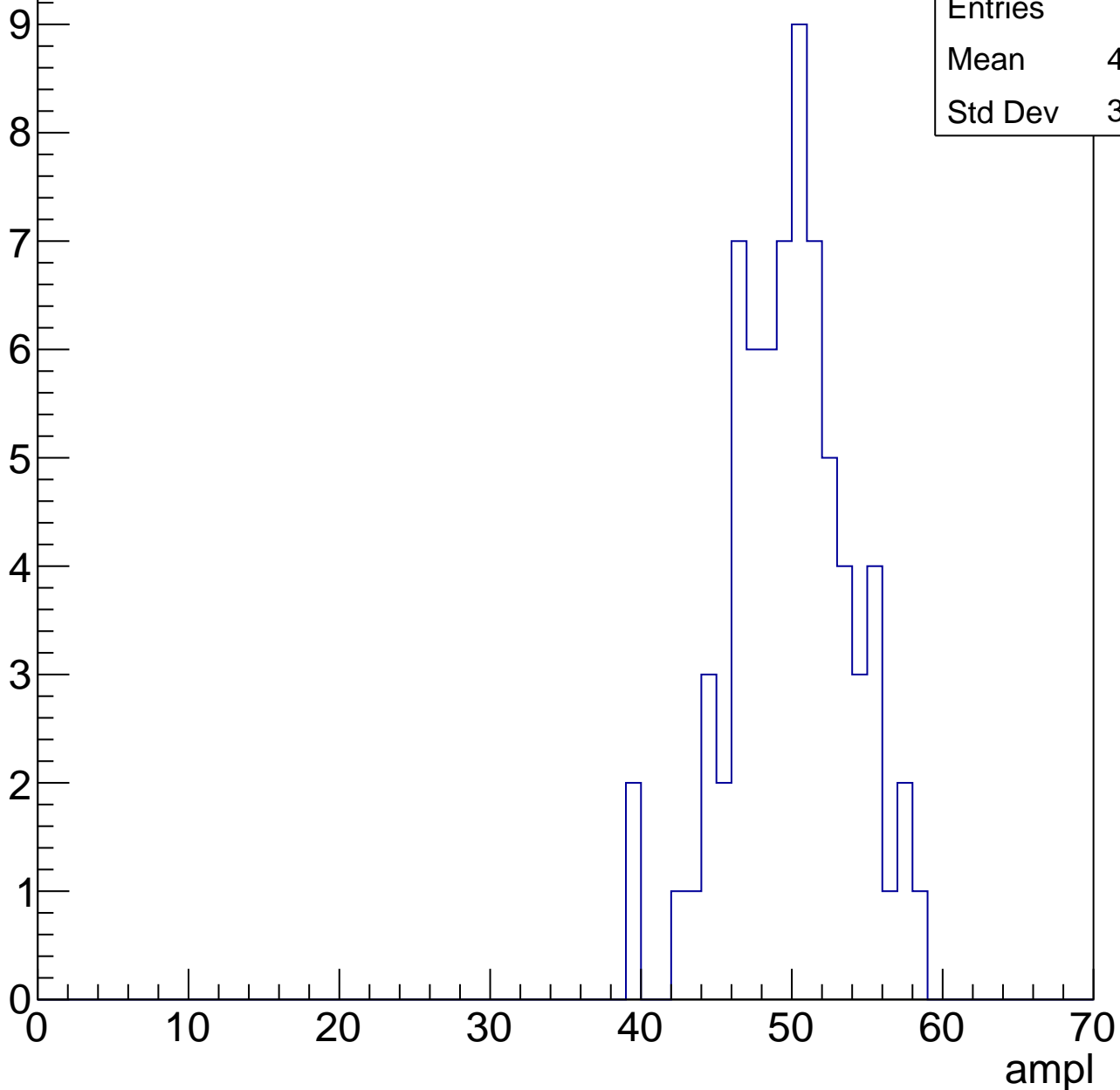


B0L102S, U5-ch4, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

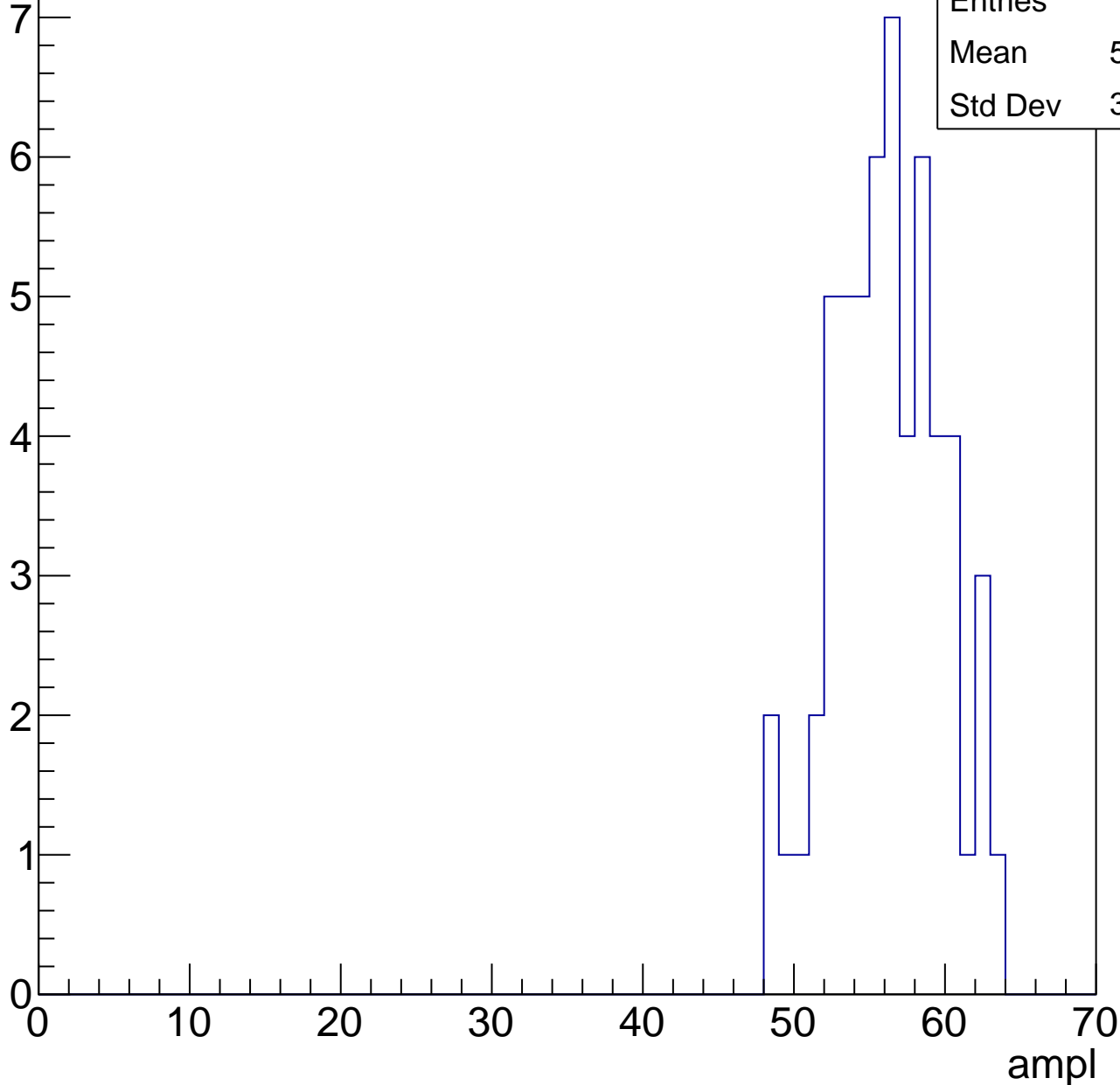
Entries	71
Mean	49.42
Std Dev	3.928



B0L102S, U5-ch4, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch4, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	59.54
Std Dev	2.558

0

10

20

30

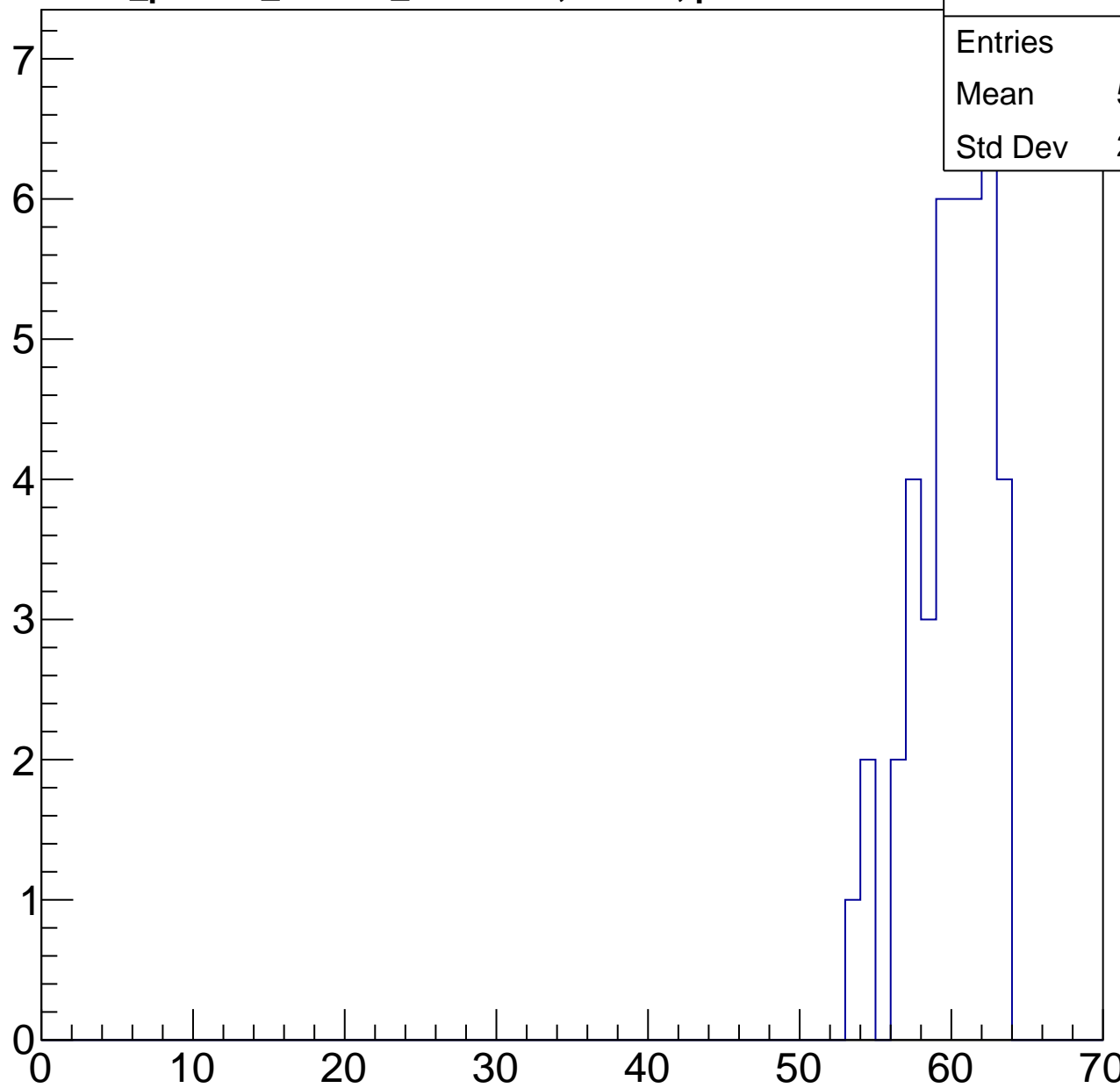
40

50

60

70

ampl

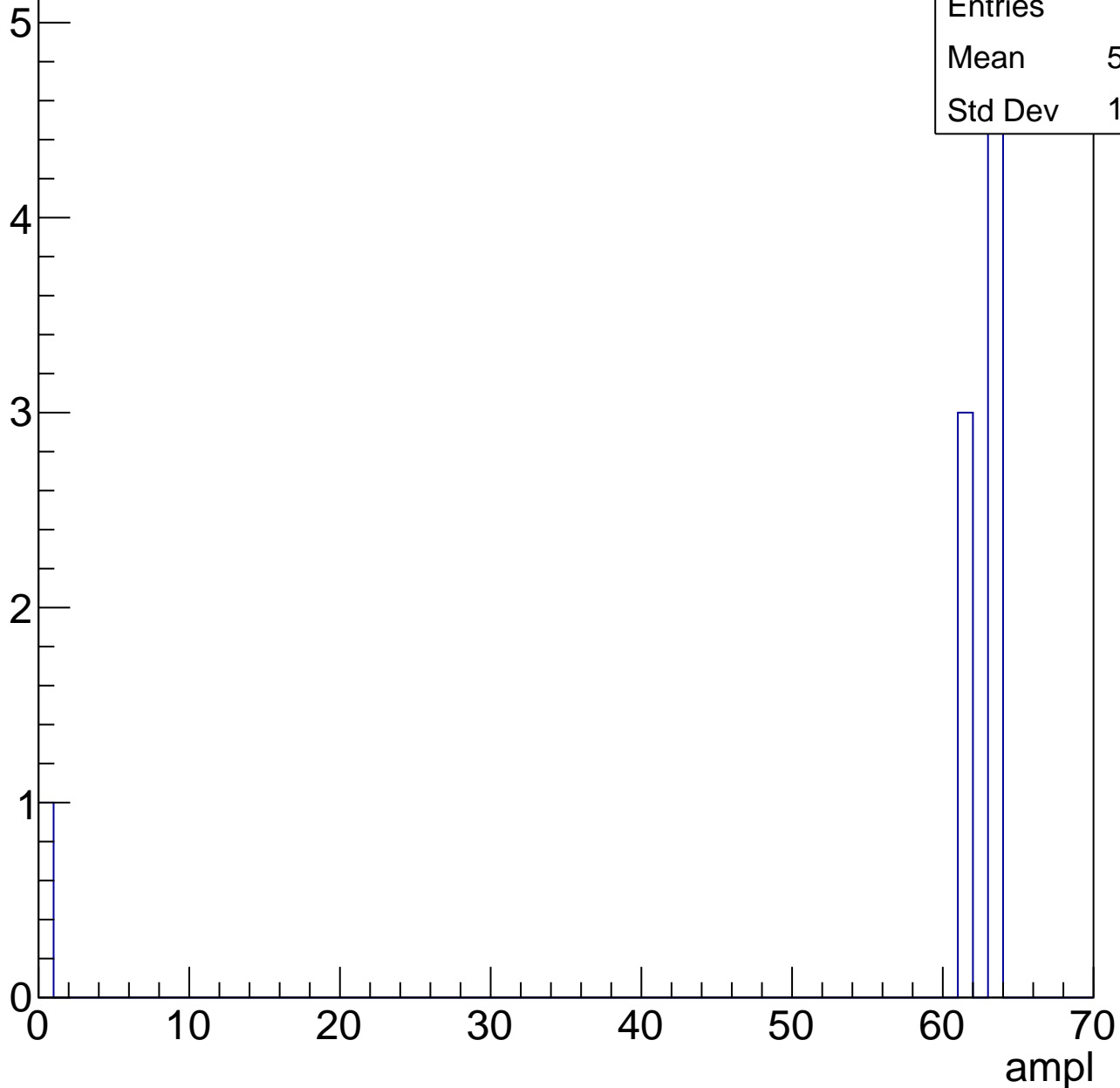


B0L102S, U5-ch4, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	9
Mean	55.33
Std Dev	19.58



B0L102S, U5-ch4, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch5, adc0

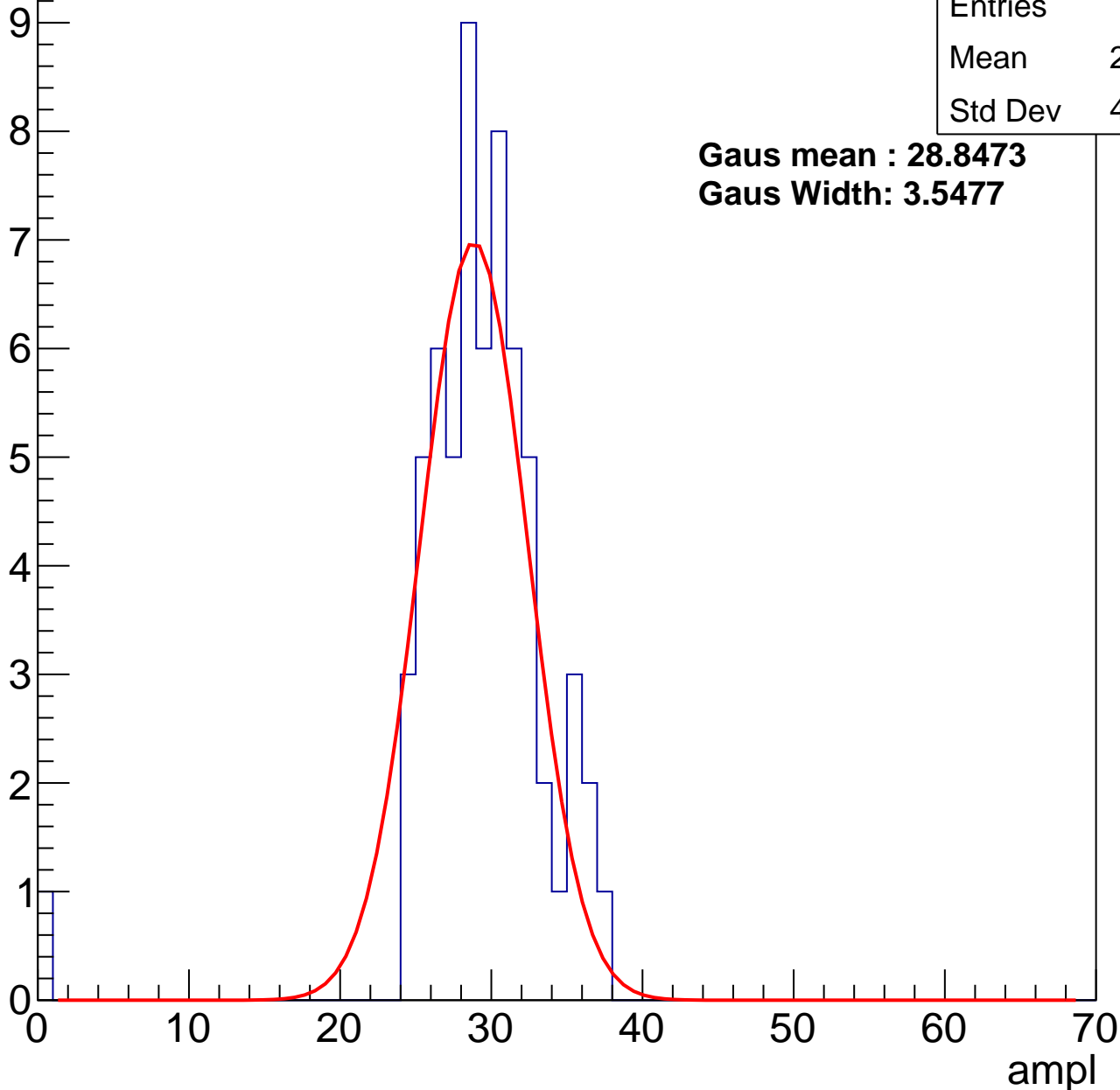
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.79
Std Dev	4.847

Gaus mean : 28.8473

Gaus Width: 3.5477



B0L102S, U5-ch5, adc1

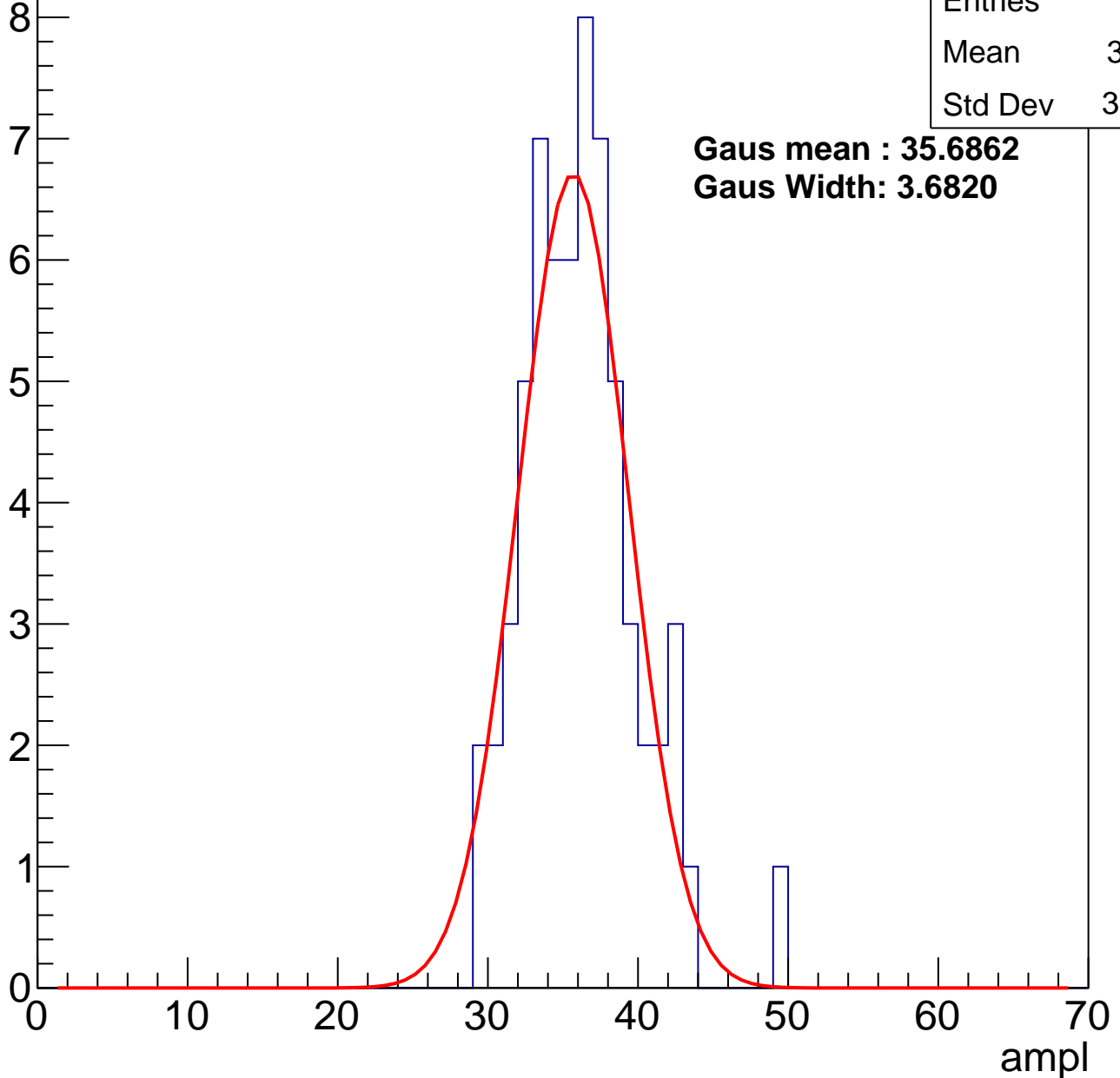
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	35.71
Std Dev	3.726

Gaus mean : 35.6862

Gaus Width: 3.6820



B0L102S, U5-ch5, adc2

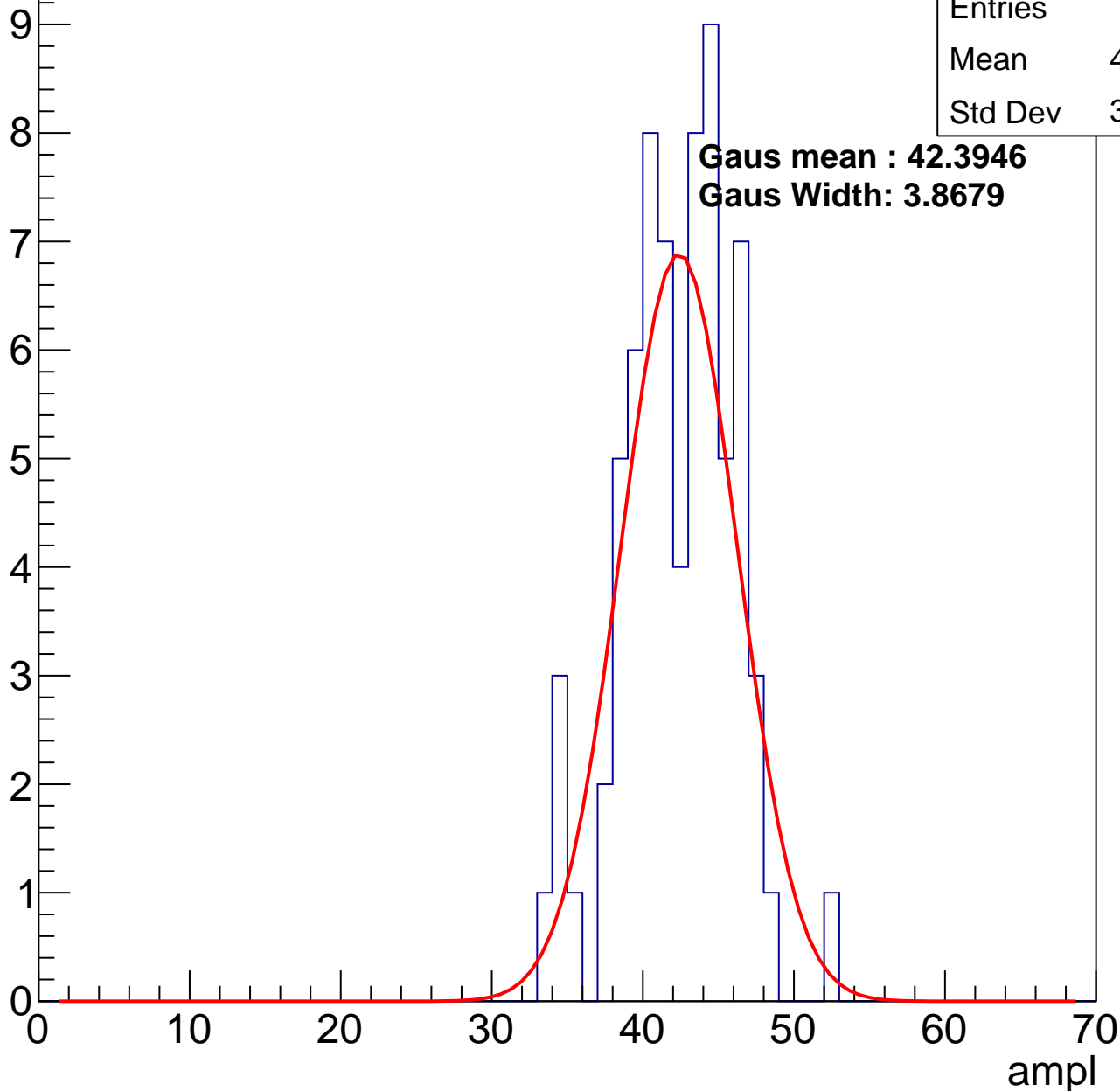
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.85
Std Dev	3.668

Gaus mean : 42.3946

Gaus Width: 3.8679



B0L102S, U5-ch5, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	49.36
Std Dev	3.82

Entry

10

8

6

4

2

0

0

10

20

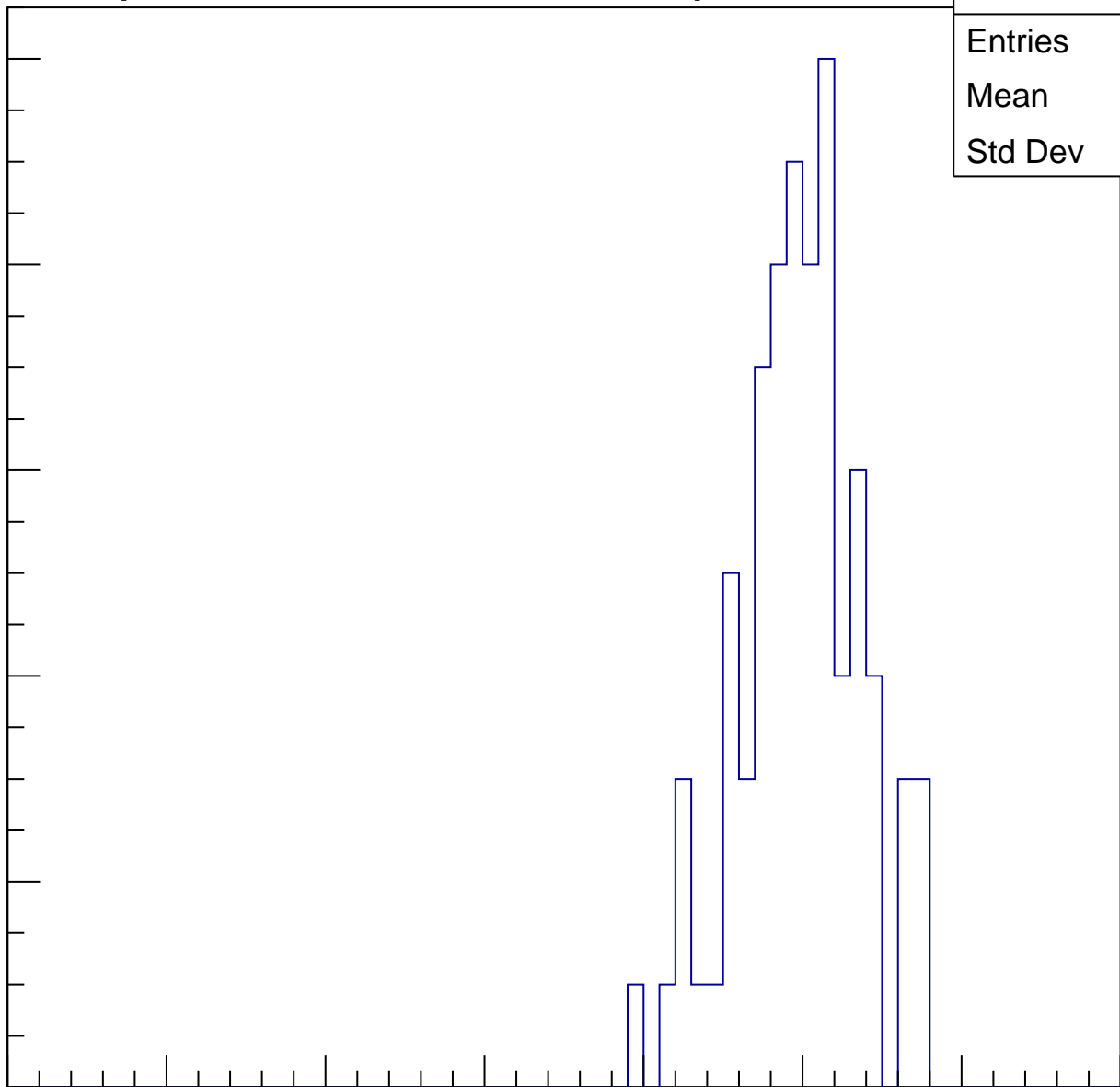
30

40

50

60

ampl

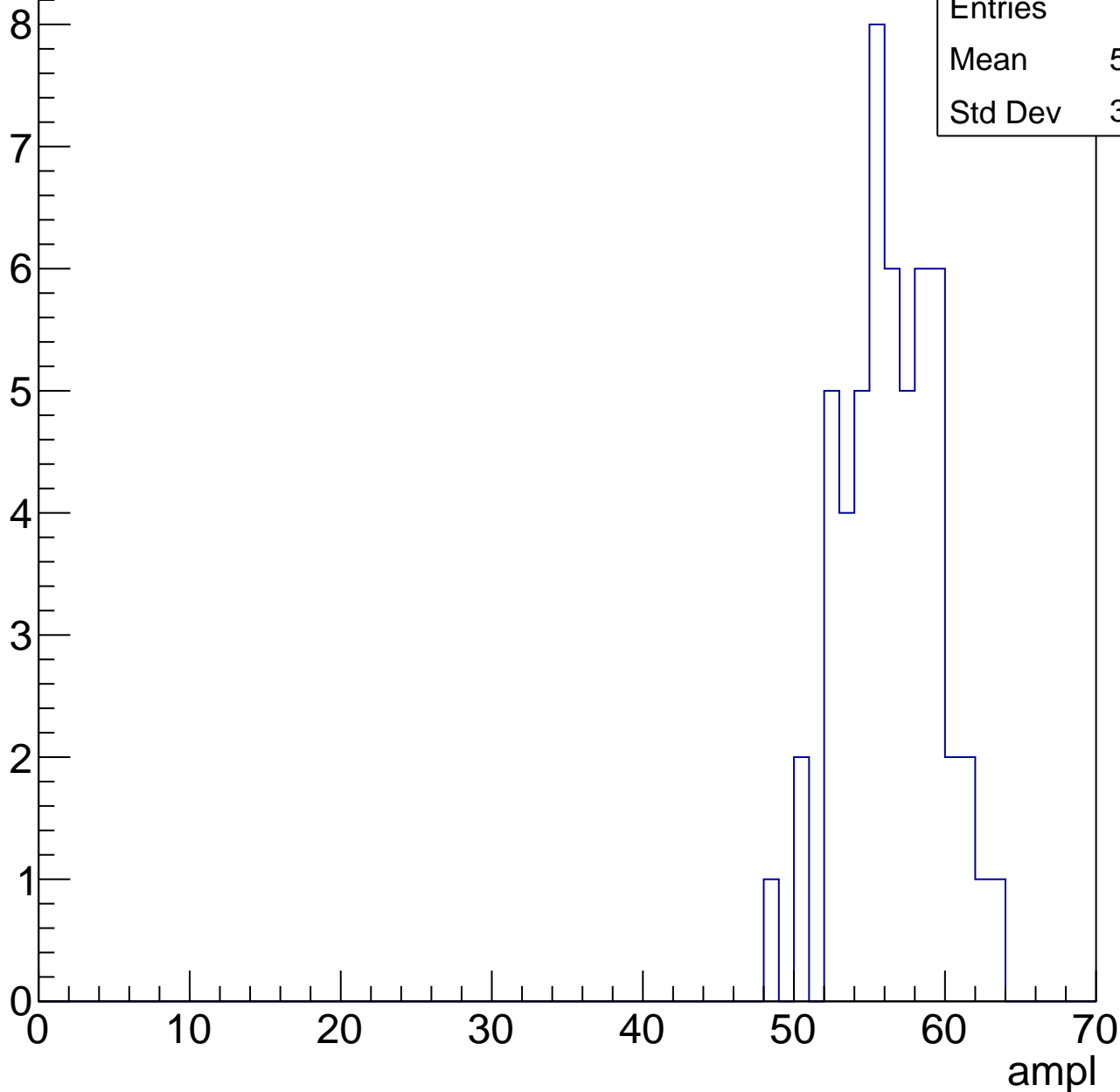


B0L102S, U5-ch5, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

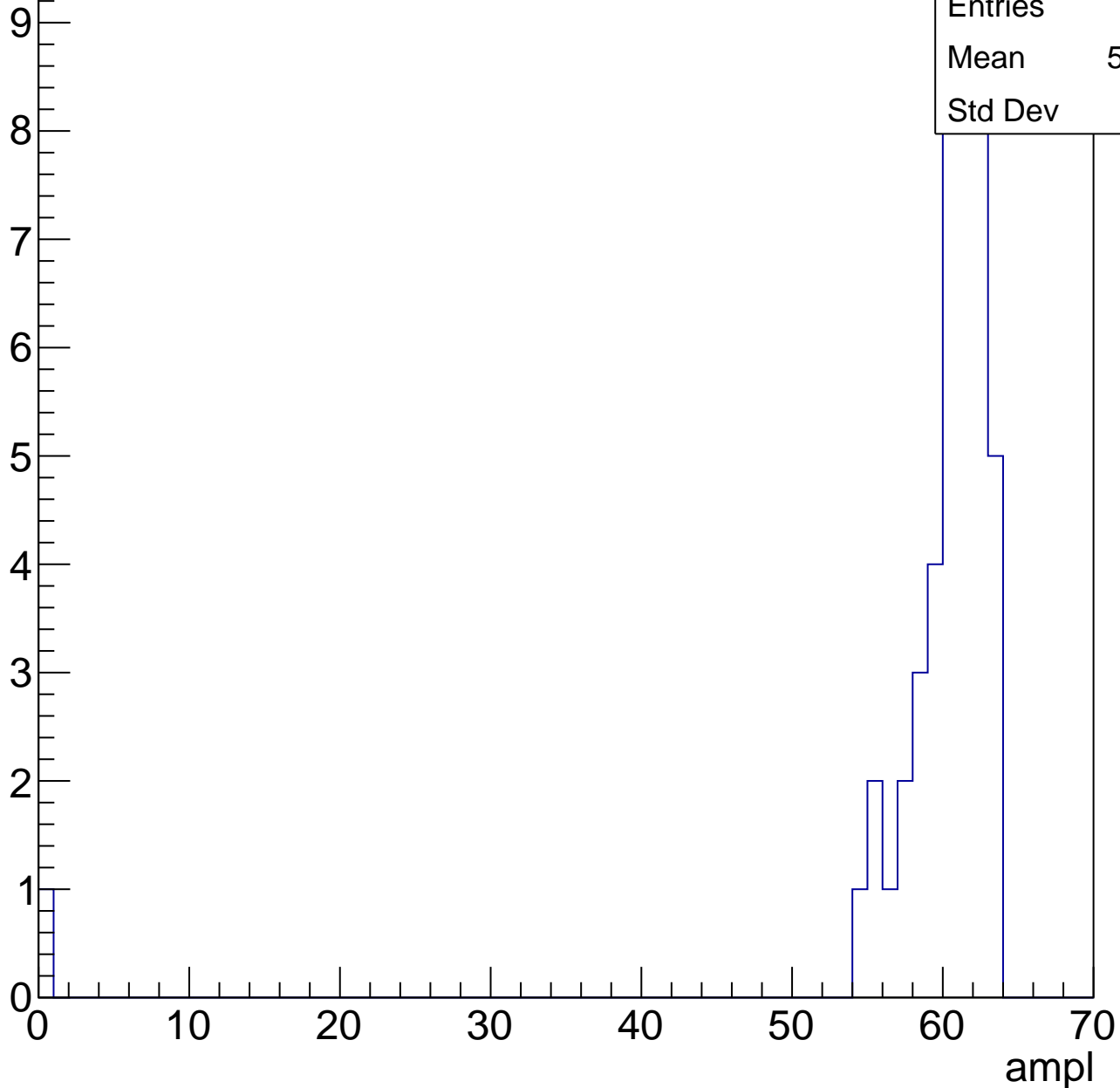
Entries	54
Mean	55.93
Std Dev	3.132



B0L102S, U5-ch5, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

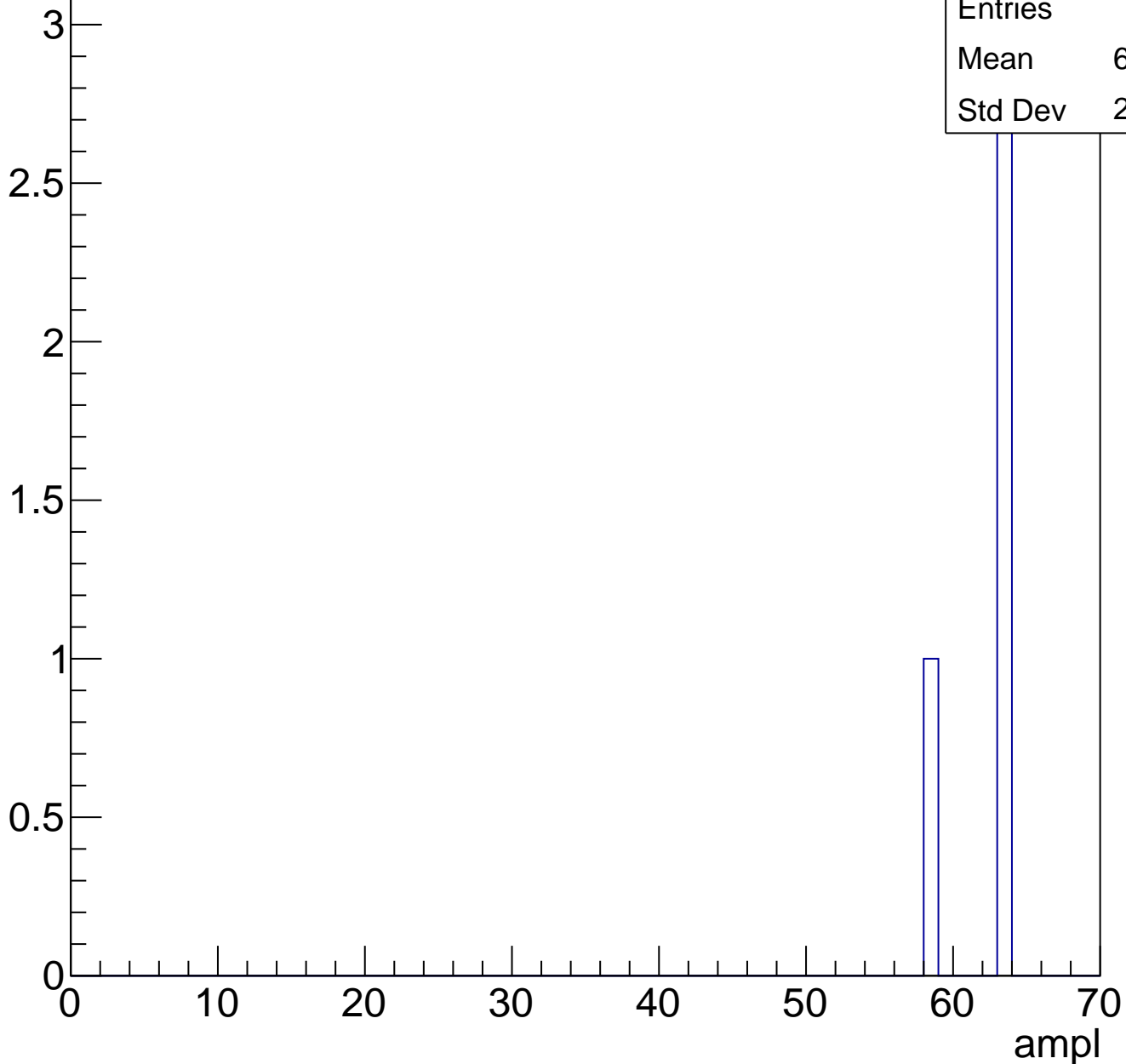


Entries	44
Mean	58.75
Std Dev	9.24

B0L102S, U5-ch5, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch5, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L102S, U5-ch6, adc0

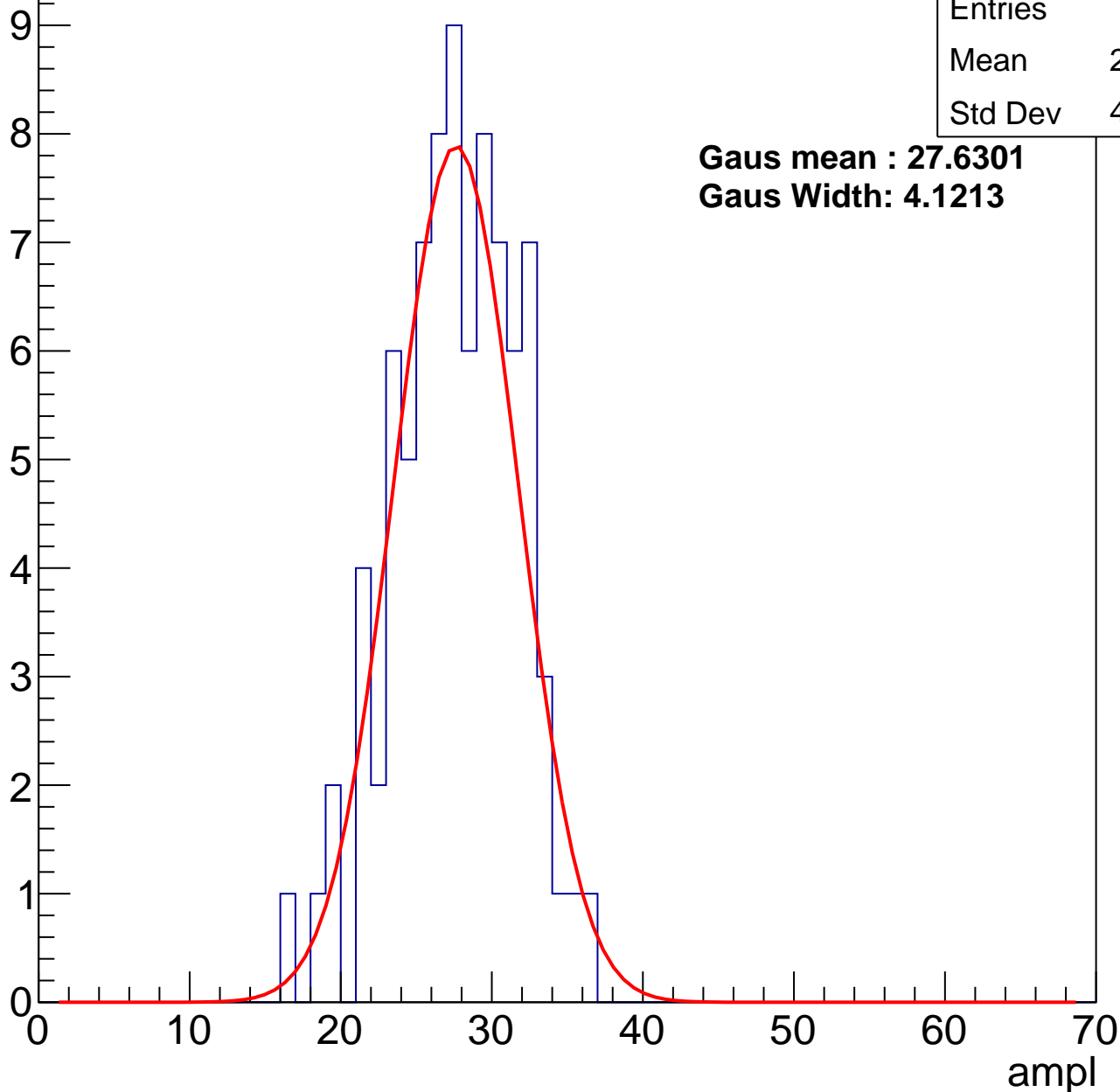
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	27.15
Std Dev	4.019

Gaus mean : 27.6301

Gaus Width: 4.1213



B0L102S, U5-ch6, adc1

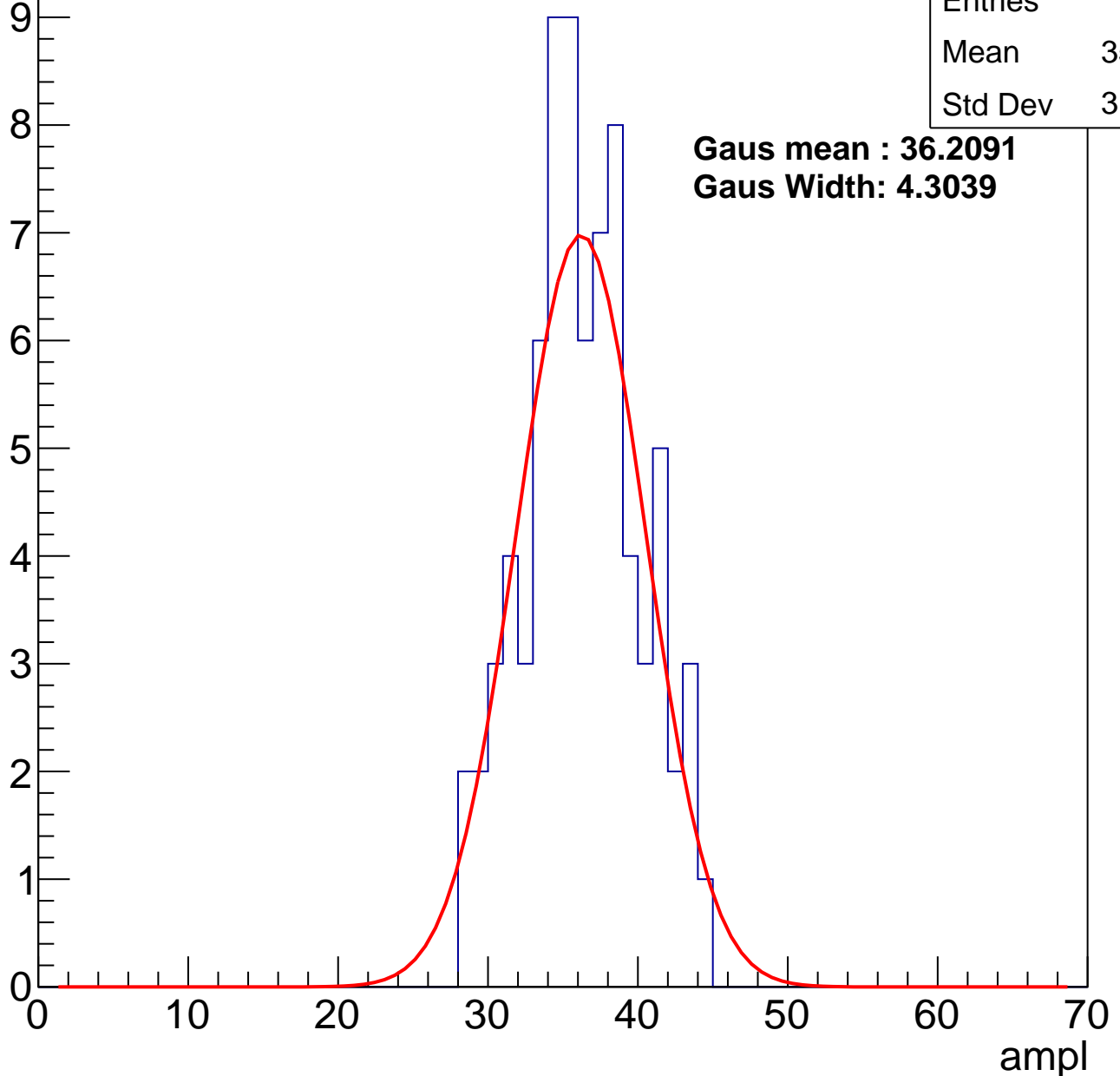
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.84
Std Dev	3.797

Gaus mean : 36.2091

Gaus Width: 4.3039



B0L102S, U5-ch6, adc2

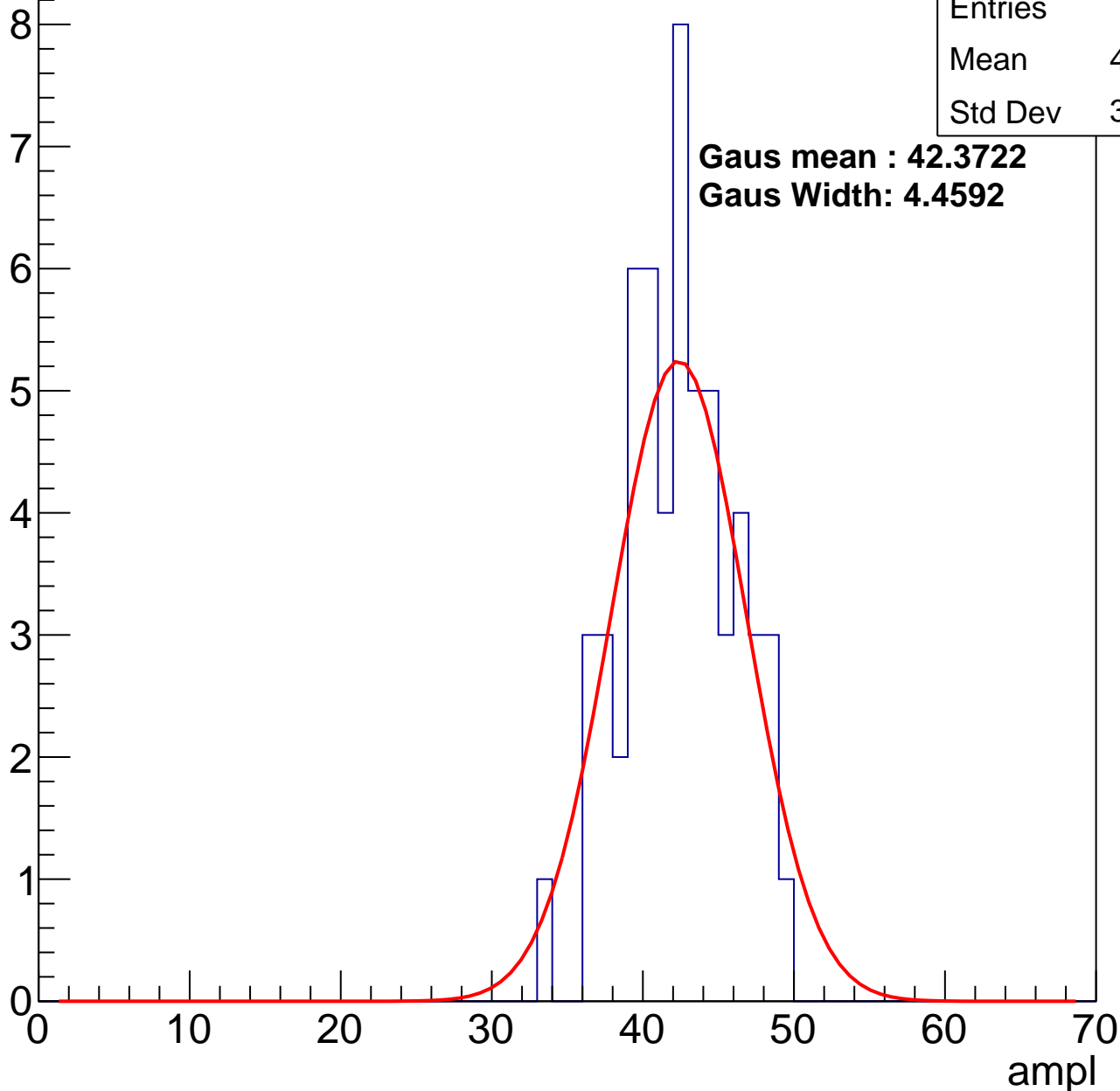
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	41.93
Std Dev	3.568

Gaus mean : 42.3722

Gaus Width: 4.4592

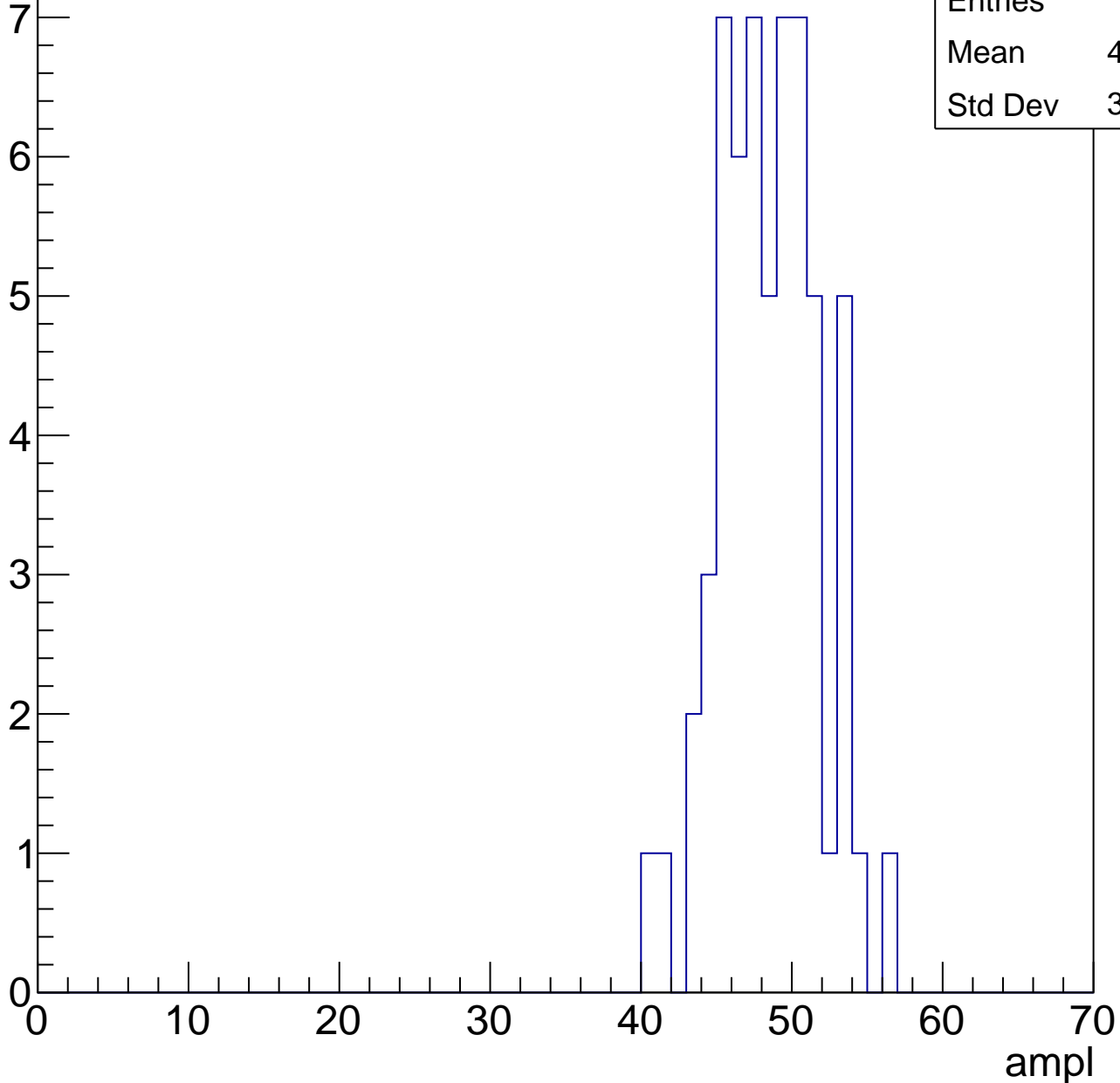


B0L102S, U5-ch6, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	48.03
Std Dev	3.257

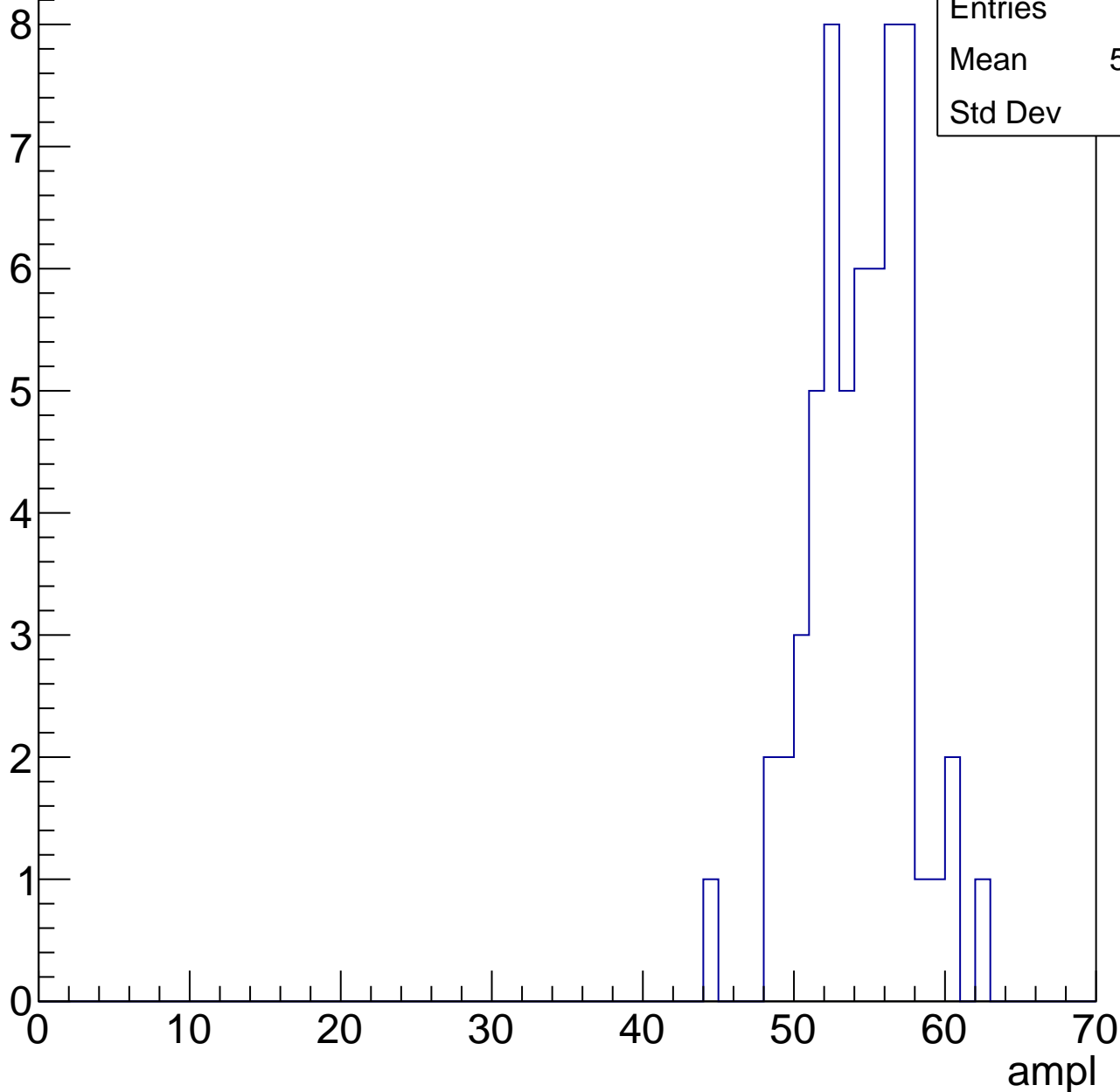


B0L102S, U5-ch6, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	53.92
Std Dev	3.29



B0L102S, U5-ch6, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	62
Mean	59.58
Std Dev	2.762

Entry

10

8

6

4

2

0

0

10

20

30

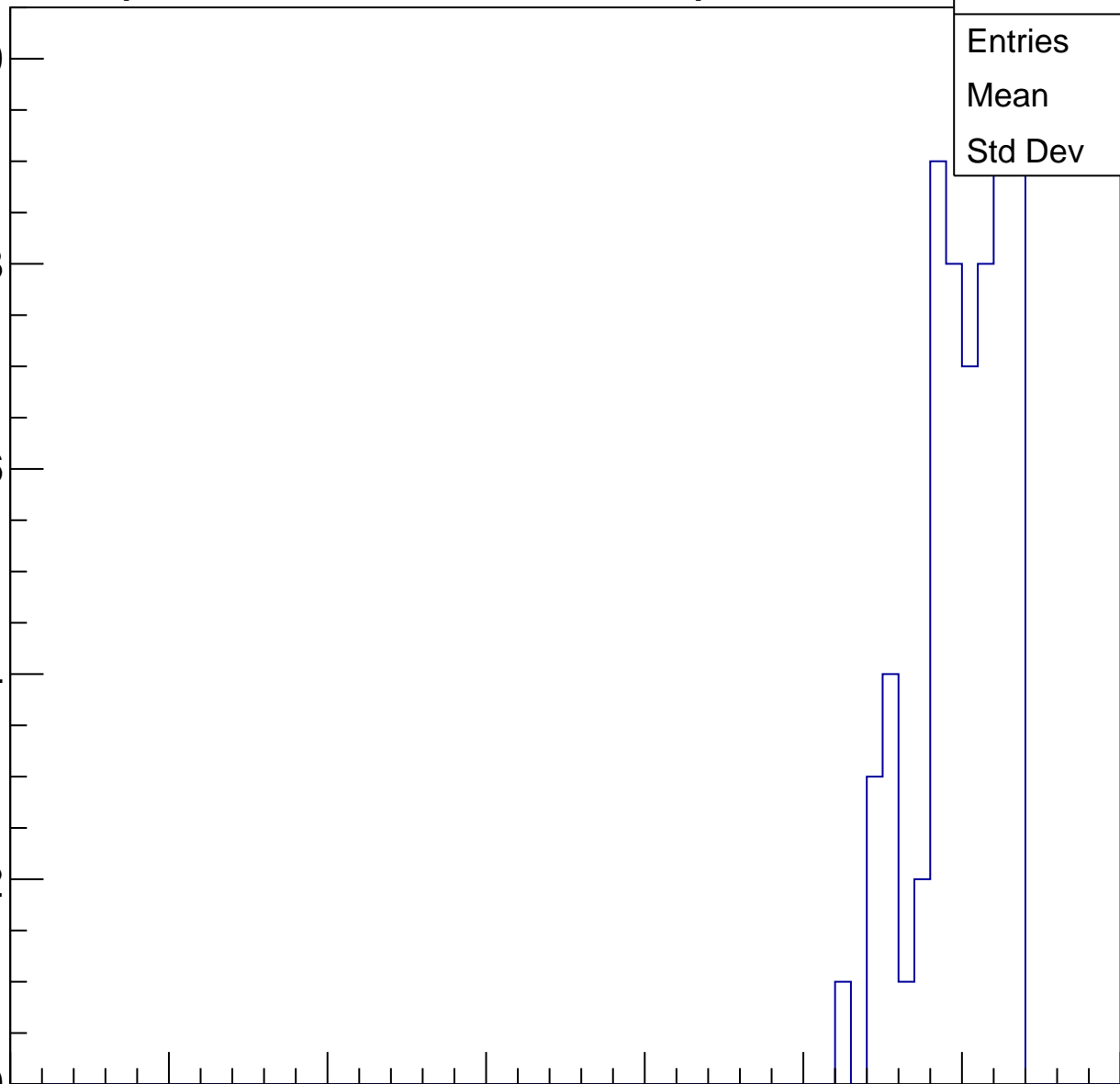
40

50

60

70

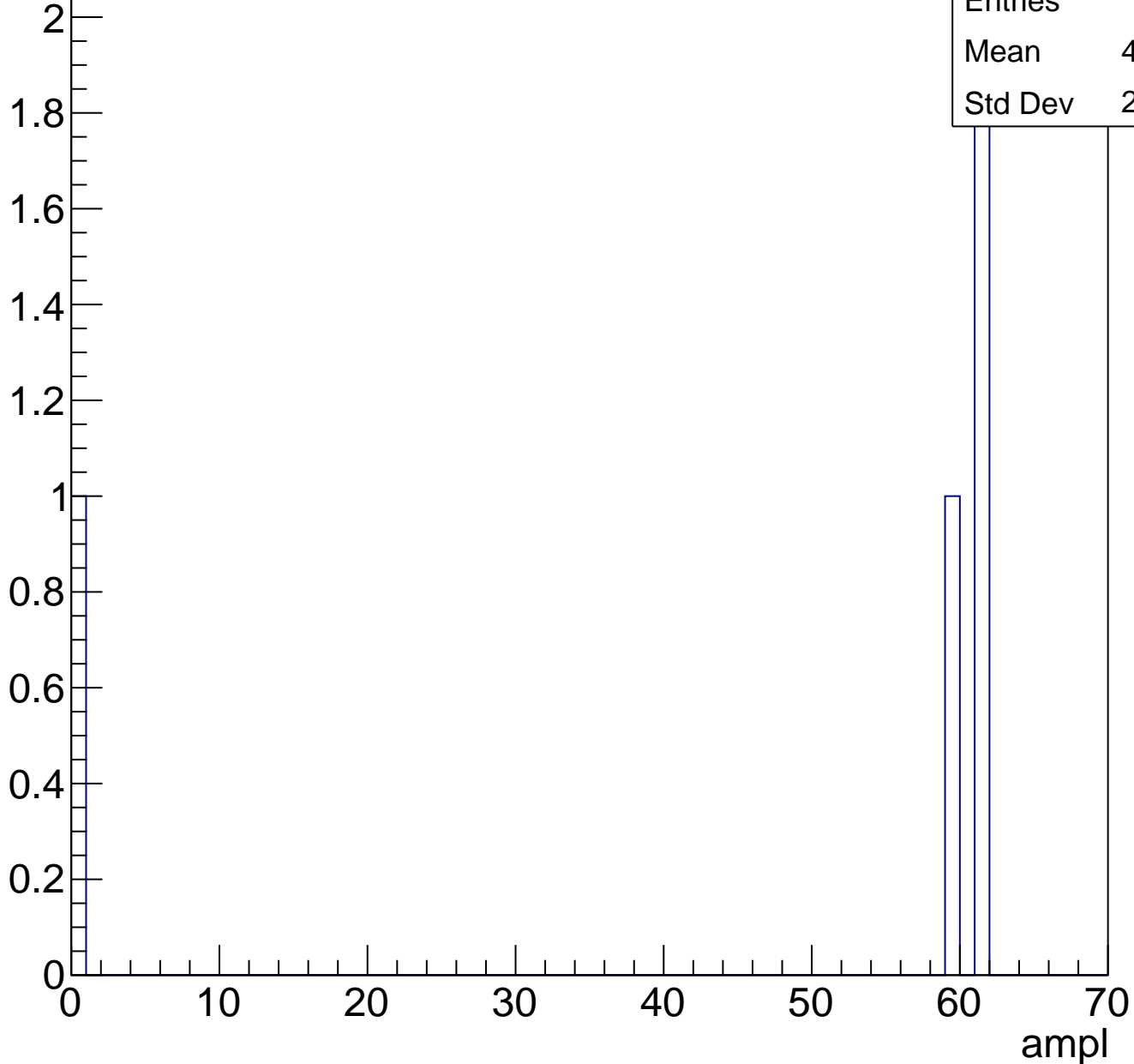
ampl



B0L102S, U5-ch6, adc6

calib_packv5_042523_0143.root, FC#12, port B1

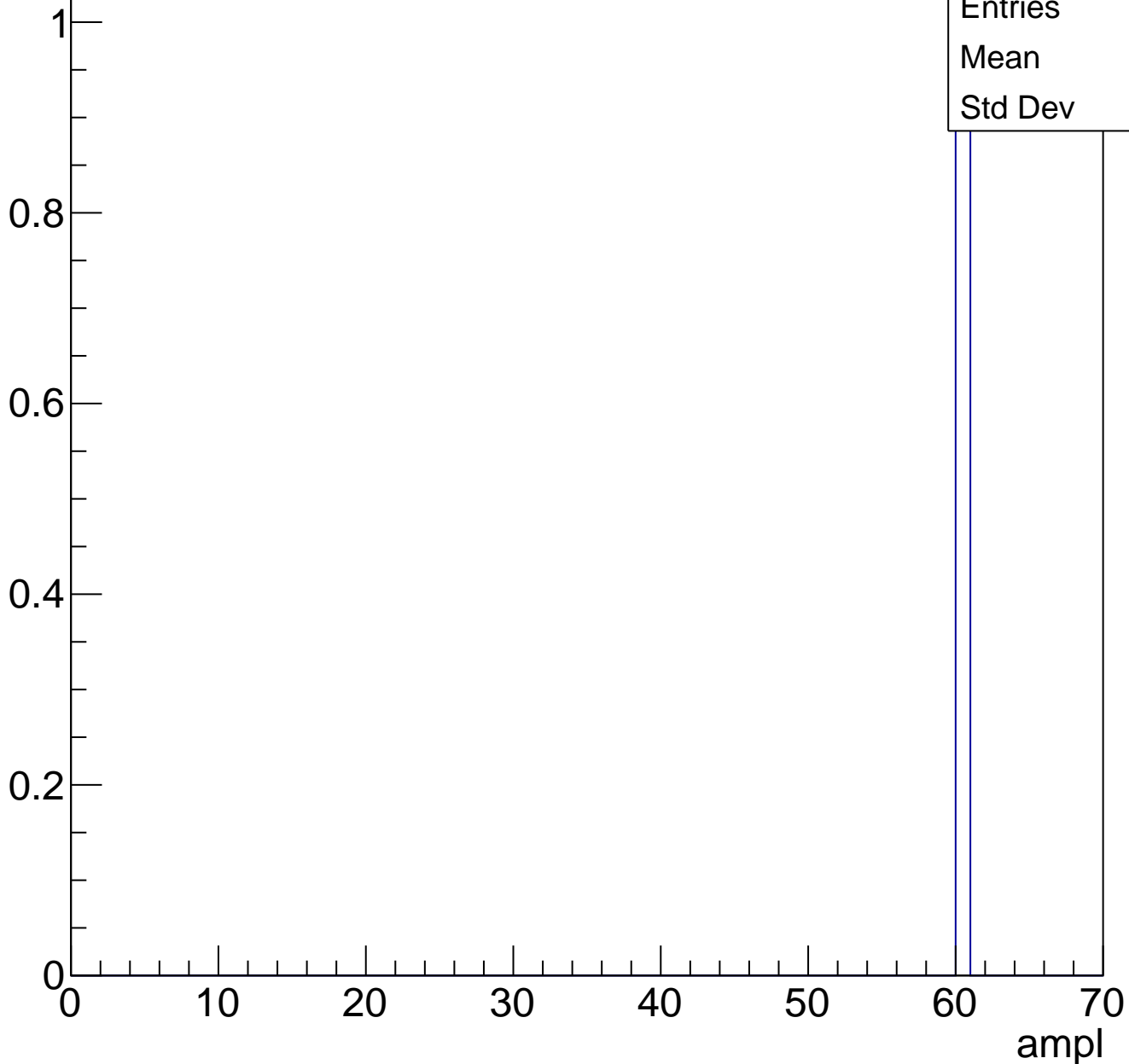
Entry



B0L102S, U5-ch6, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch7, adc0

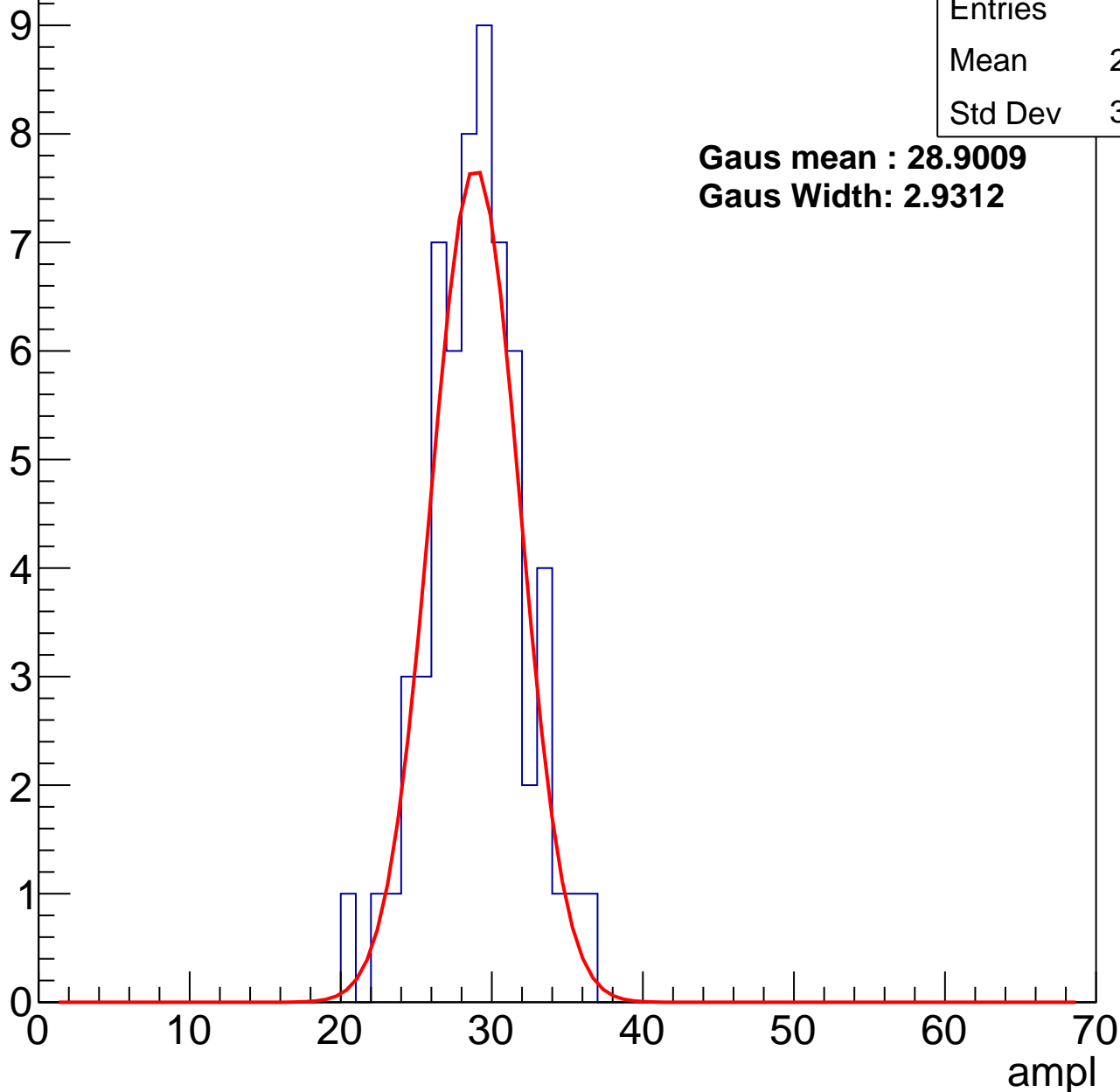
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	28.49
Std Dev	3.113

Gaus mean : 28.9009

Gaus Width: 2.9312



B0L102S, U5-ch7, adc1

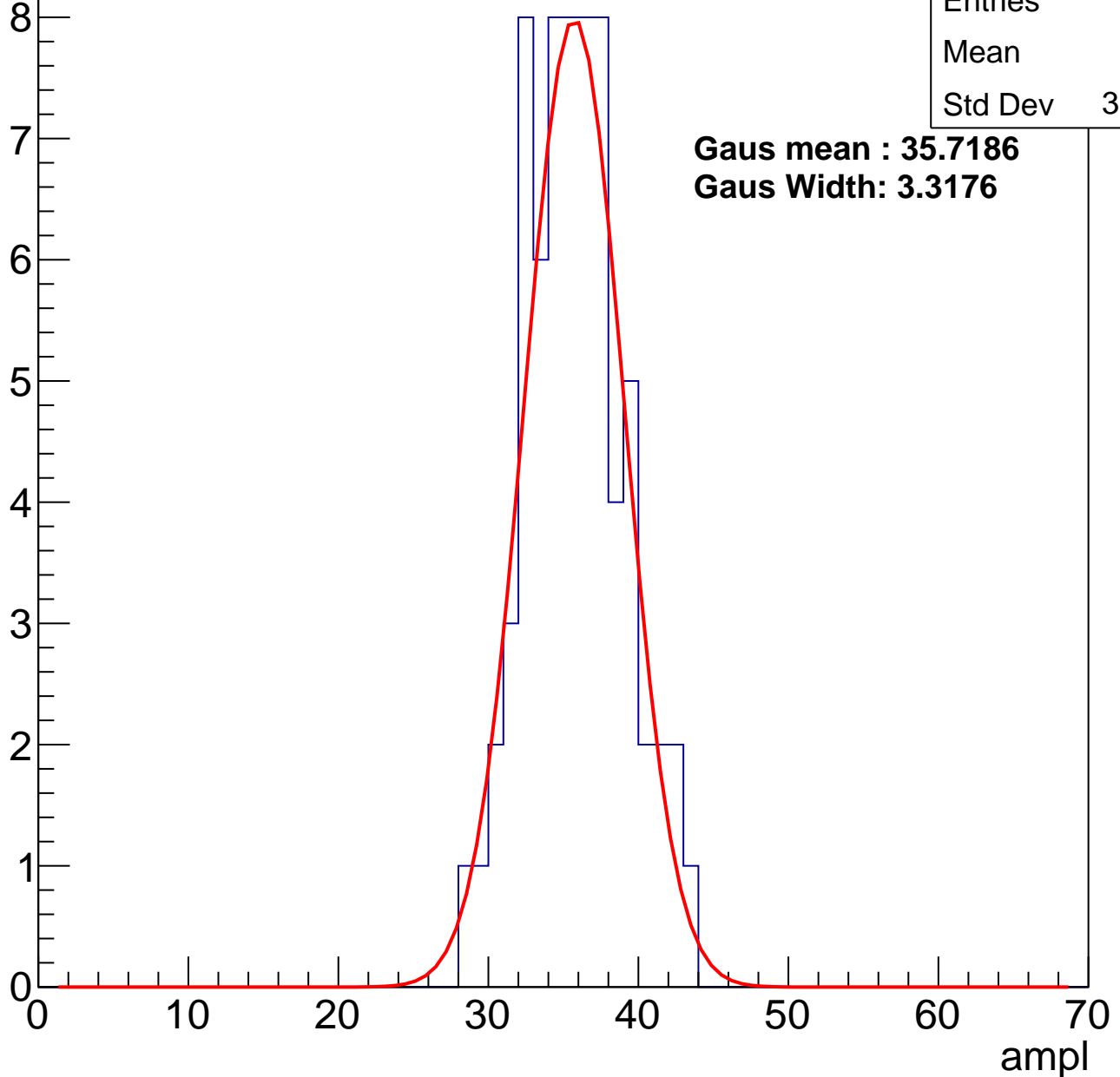
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	35.3
Std Dev	3.232

Gaus mean : 35.7186

Gaus Width: 3.3176



B0L102S, U5-ch7, adc2

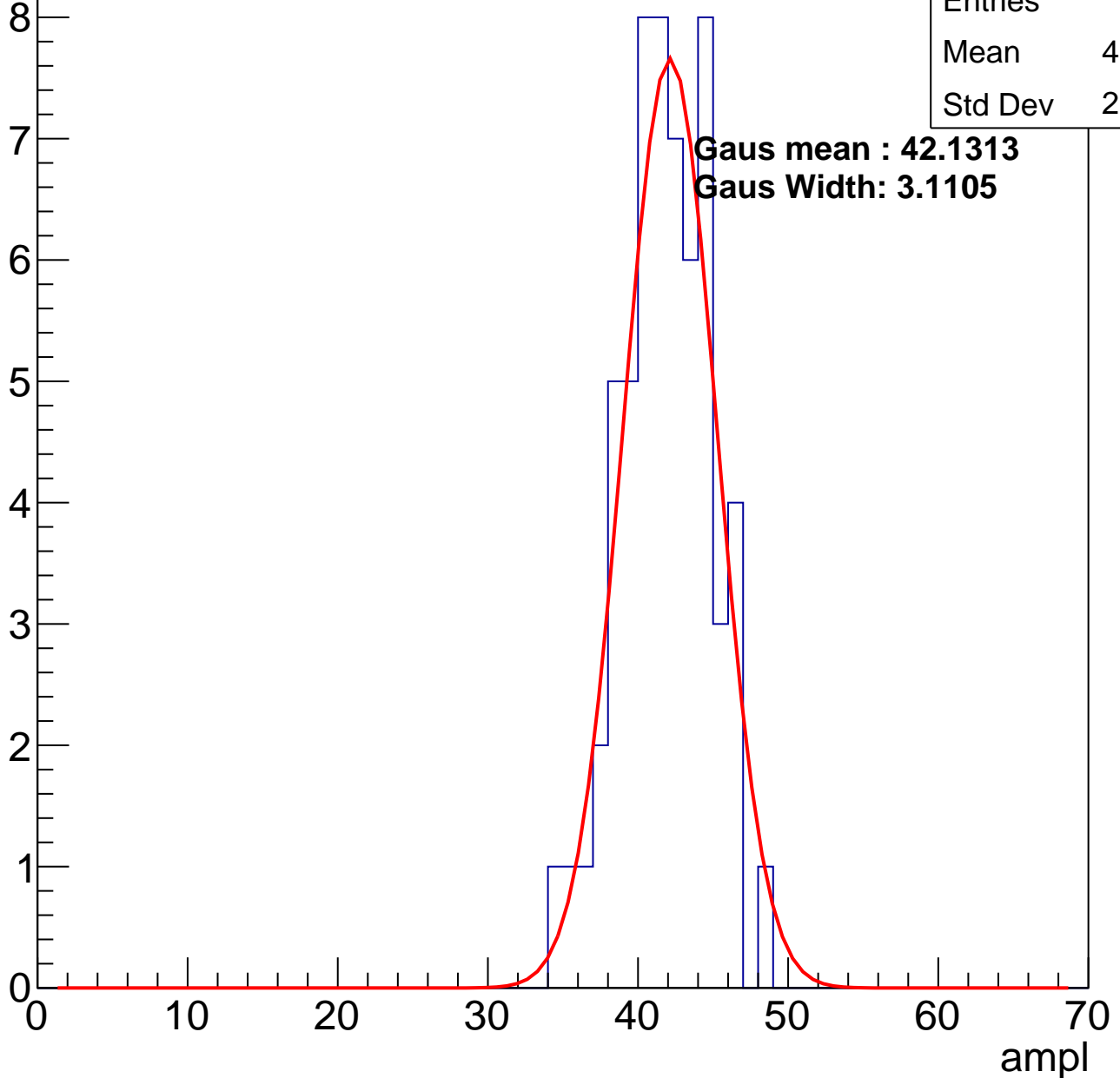
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.38
Std Dev	2.905

Gaus mean : 42.1313

Gaus Width: 3.1105



B0L102S, U5-ch7, adc3

calib_packv5_042523_0143.root, FC#12, port B1

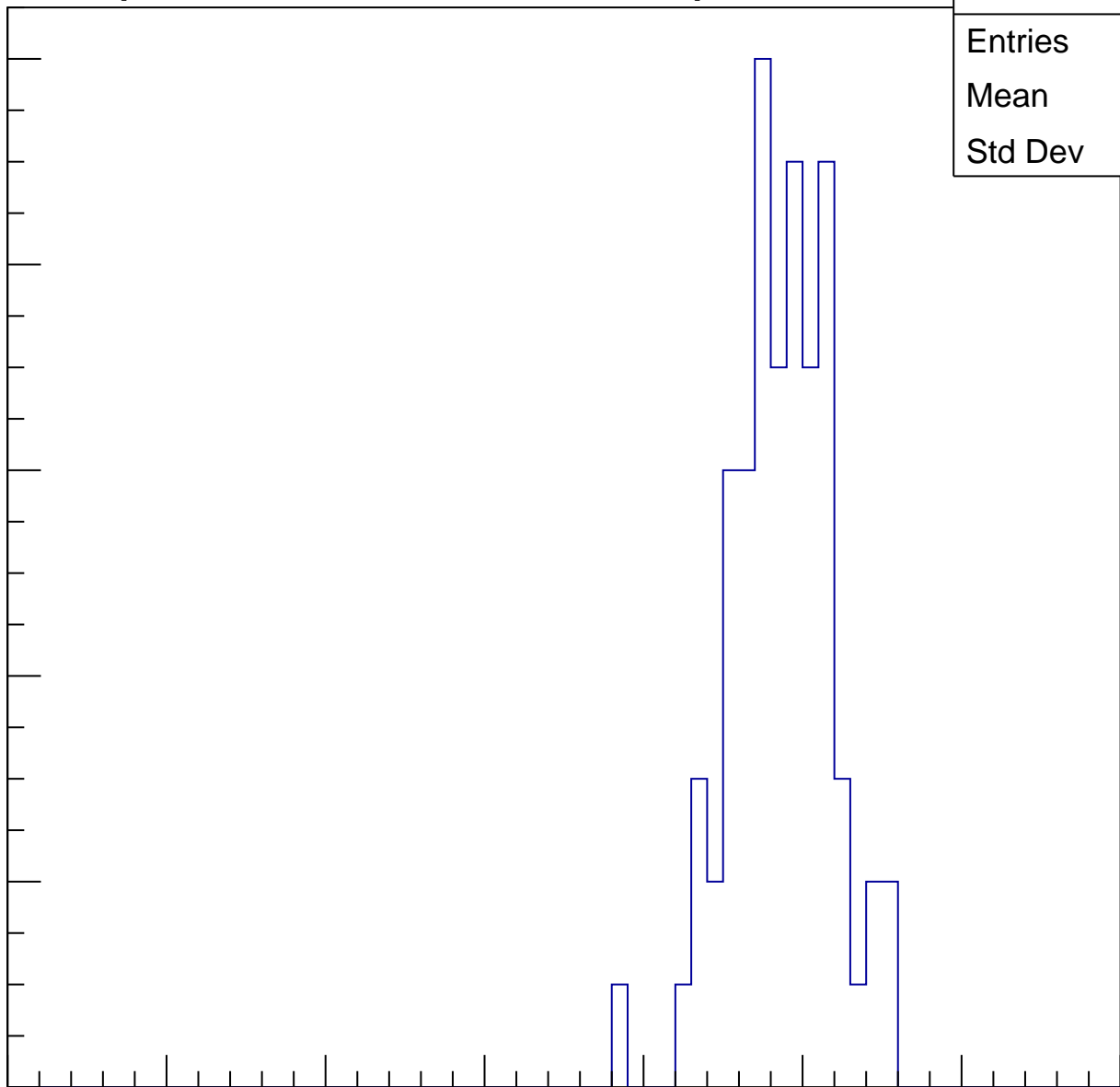
Entries	69
Mean	48.2
Std Dev	3.16

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch7, adc4

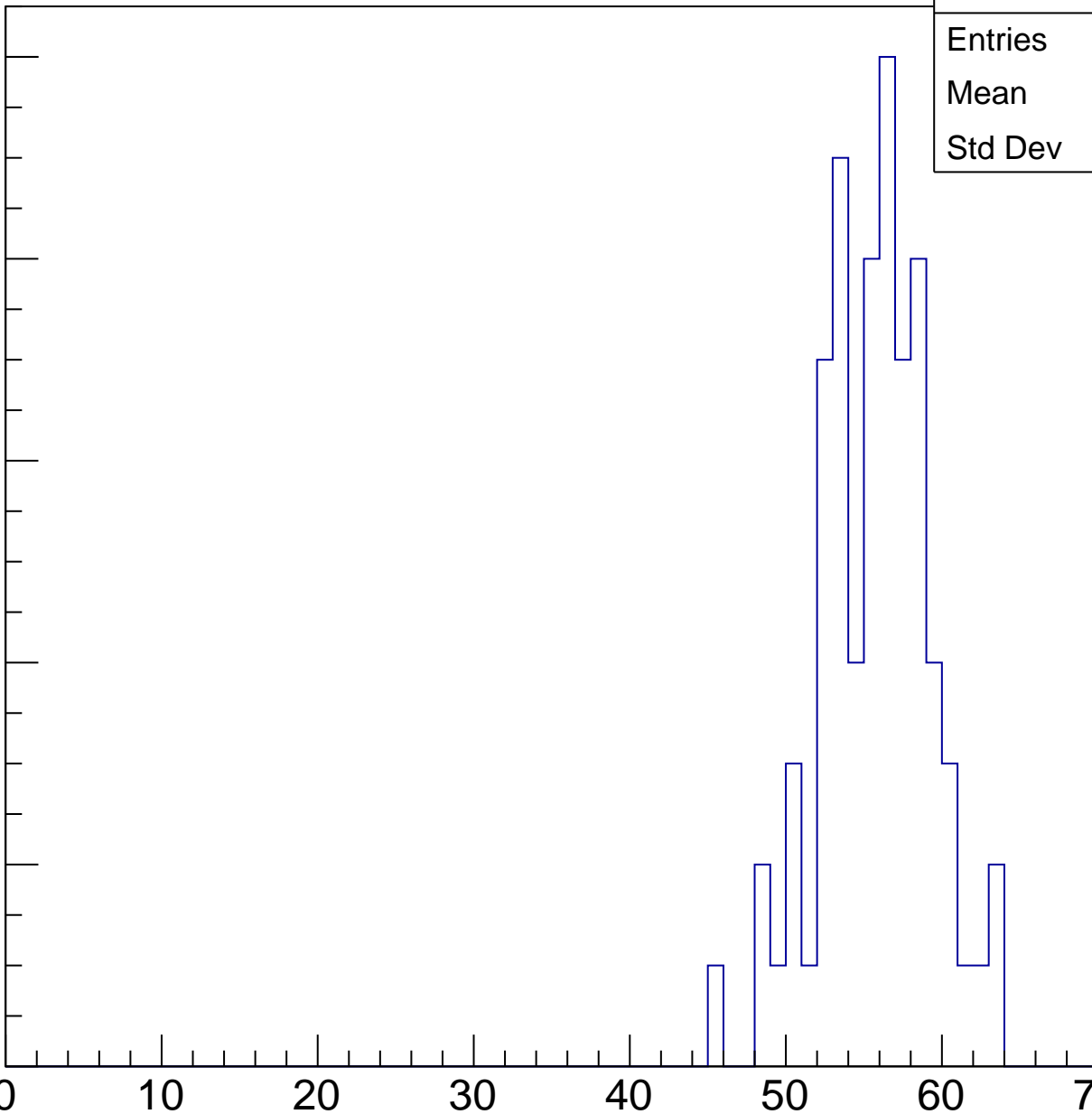
calib_packv5_042523_0143.root, FC#12, port B1

Entries	72
Mean	55.22
Std Dev	3.509

Entry

10
8
6
4
2
0

ampl



B0L102S, U5-ch7, adc5

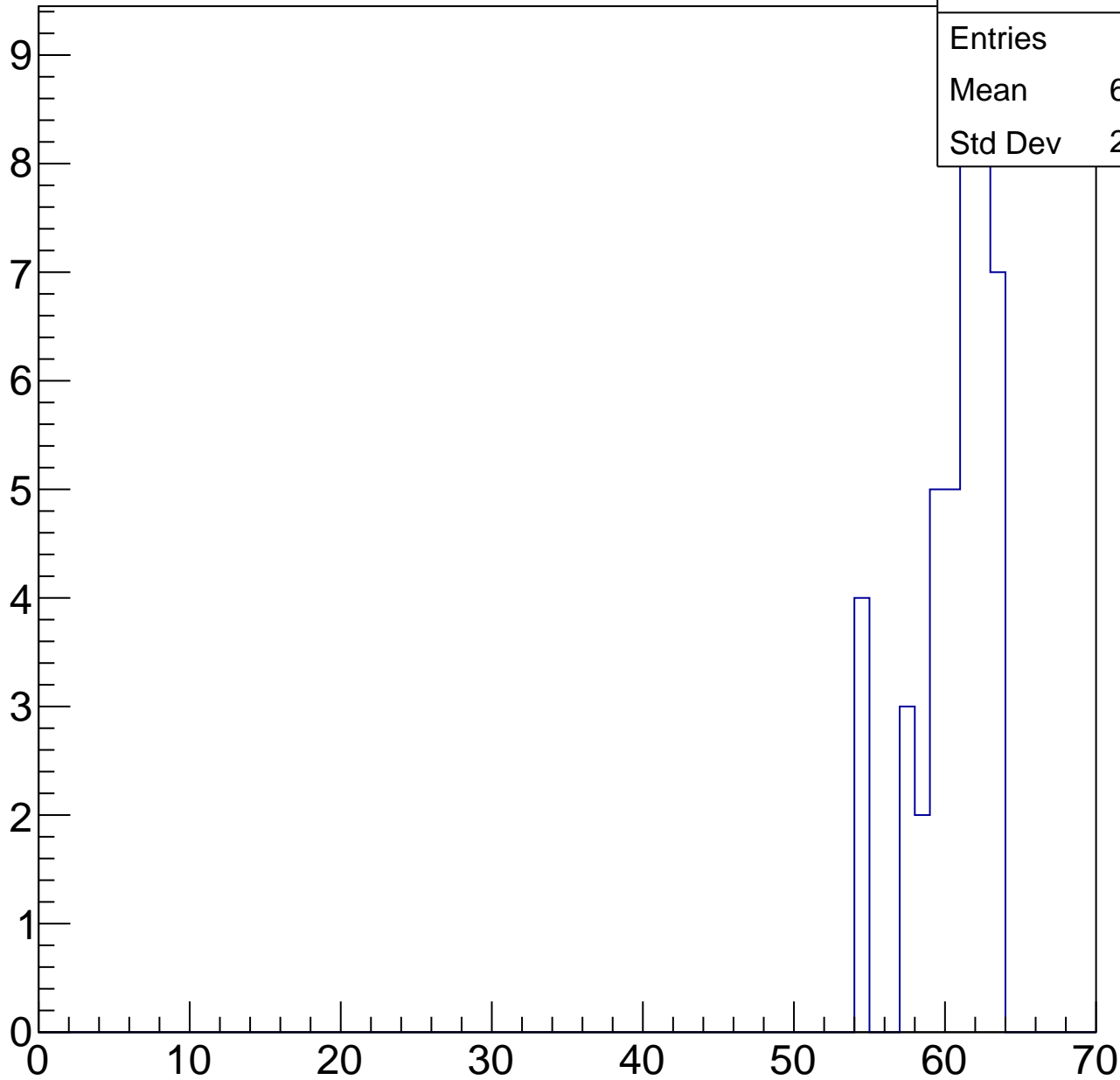
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.12
Std Dev	2.599

ampl



B0L102S, U5-ch7, adc6

calib_packv5_042523_0143.root, FC#12, port B1

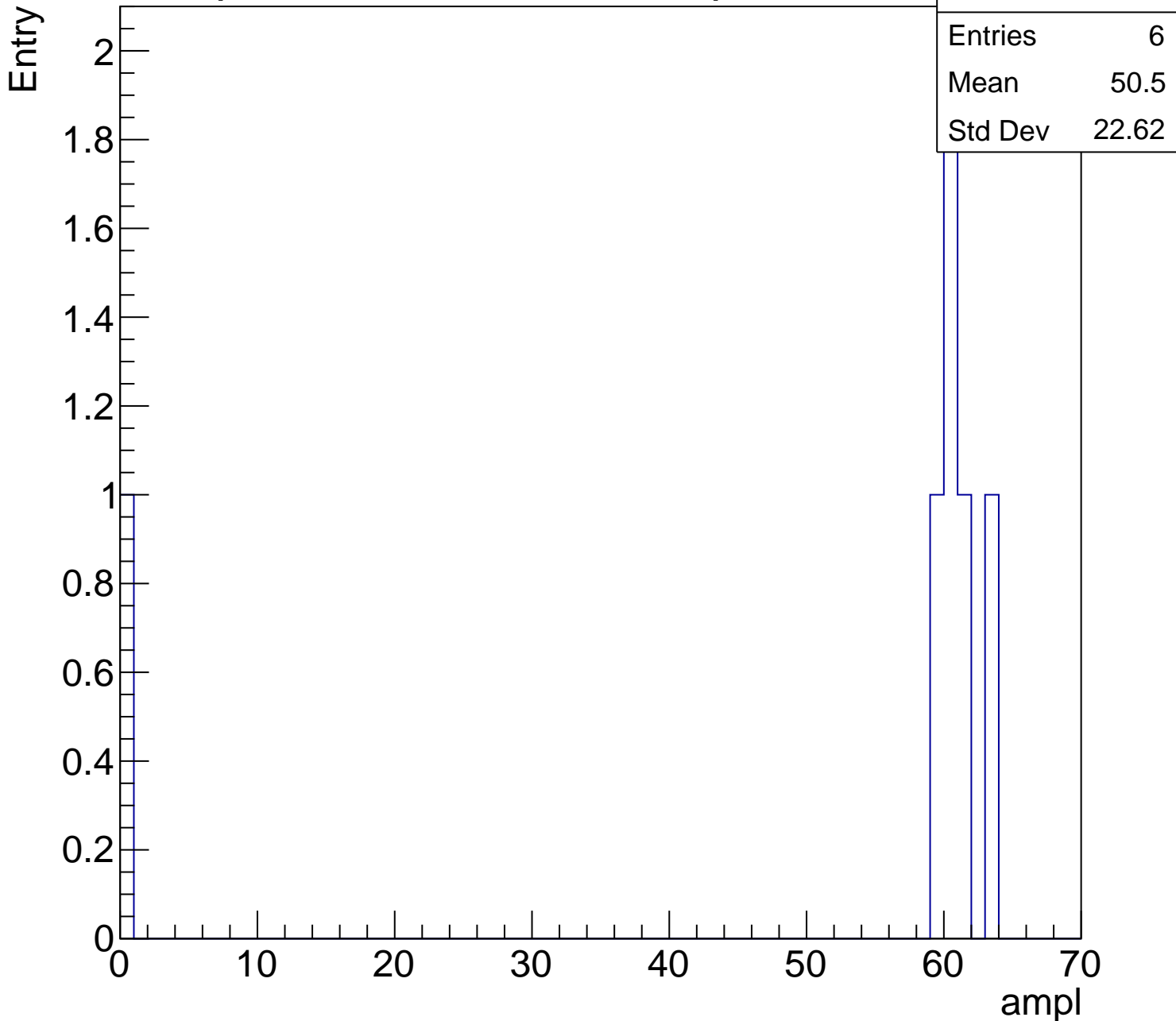
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.5
Std Dev	22.62

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch7, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch8, adc0

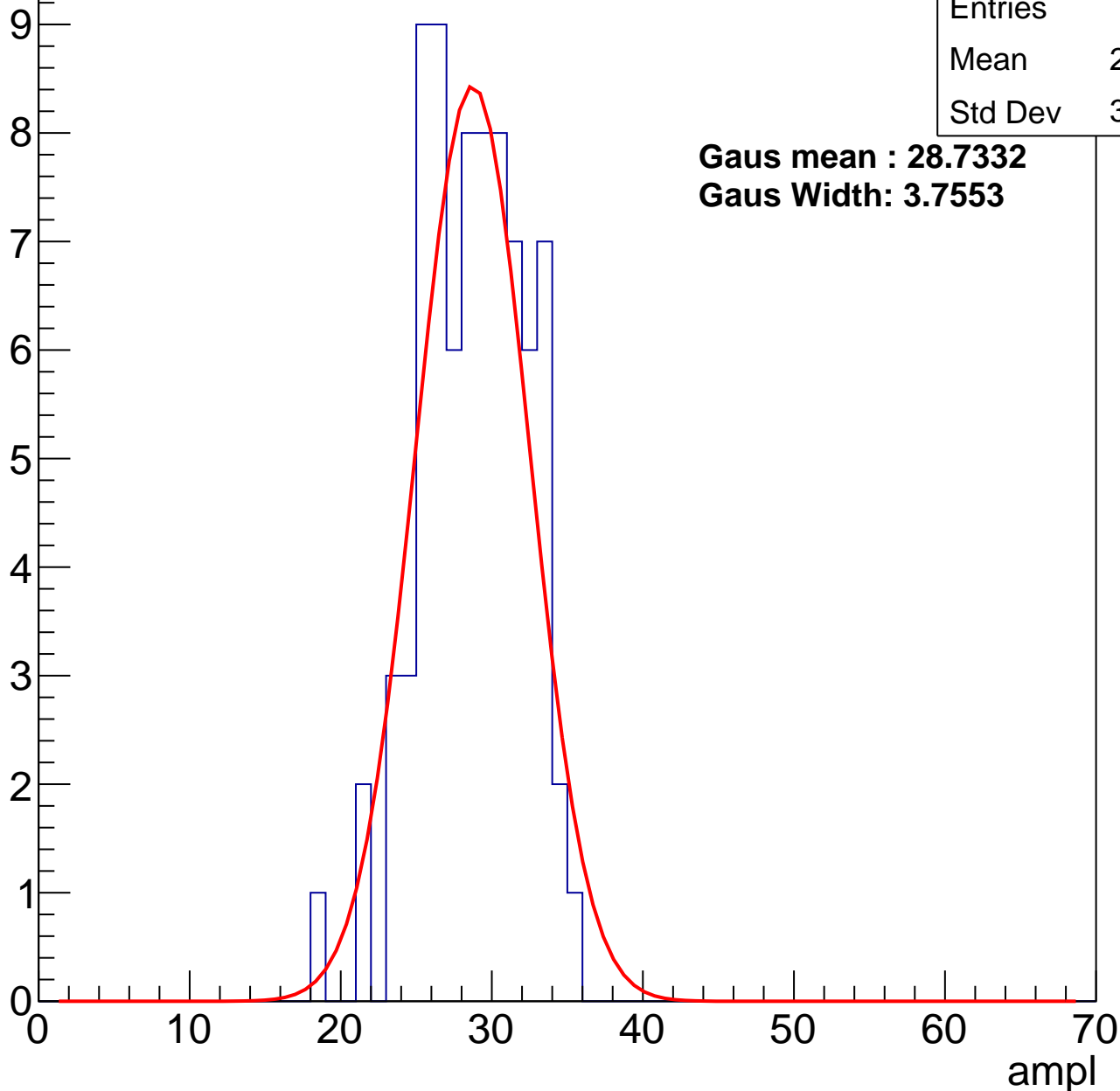
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	28.26
Std Dev	3.416

Gaus mean : 28.7332

Gaus Width: 3.7553



B0L102S, U5-ch8, adc1

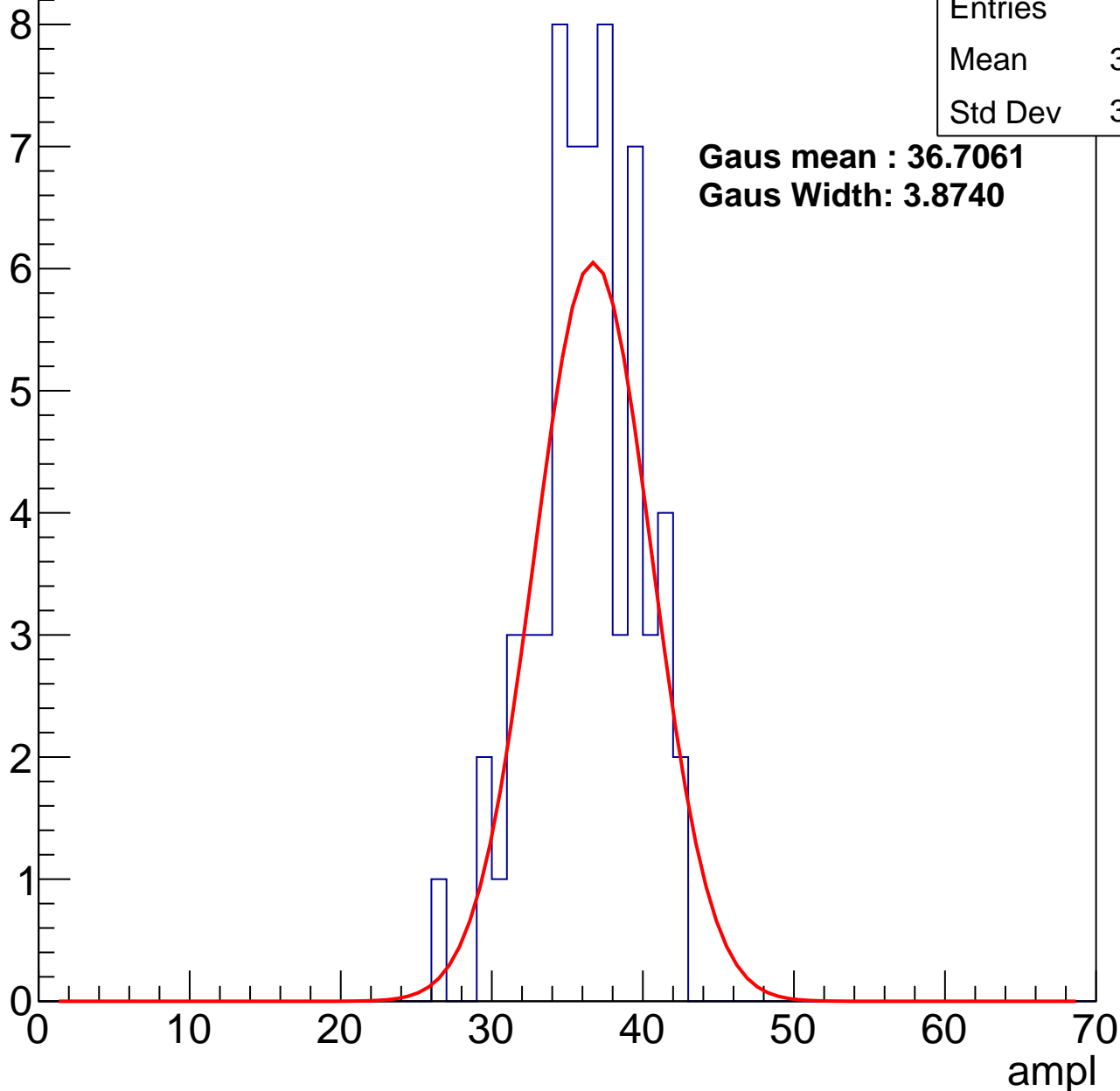
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	35.84
Std Dev	3.418

Gaus mean : 36.7061

Gaus Width: 3.8740



B0L102S, U5-ch8, adc2

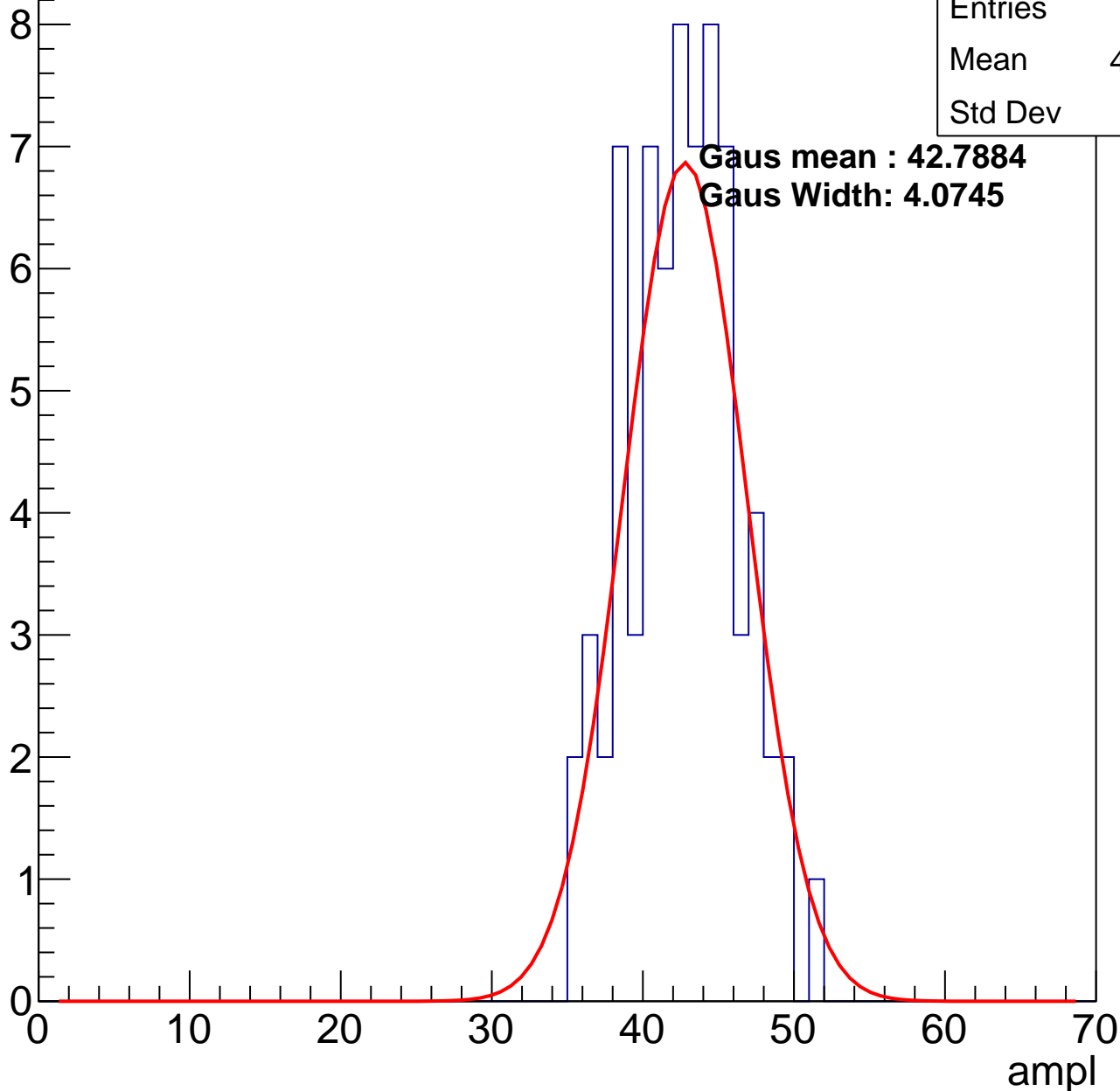
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	42.17
Std Dev	3.59

Gaus mean : 42.7884

Gaus Width: 4.0745

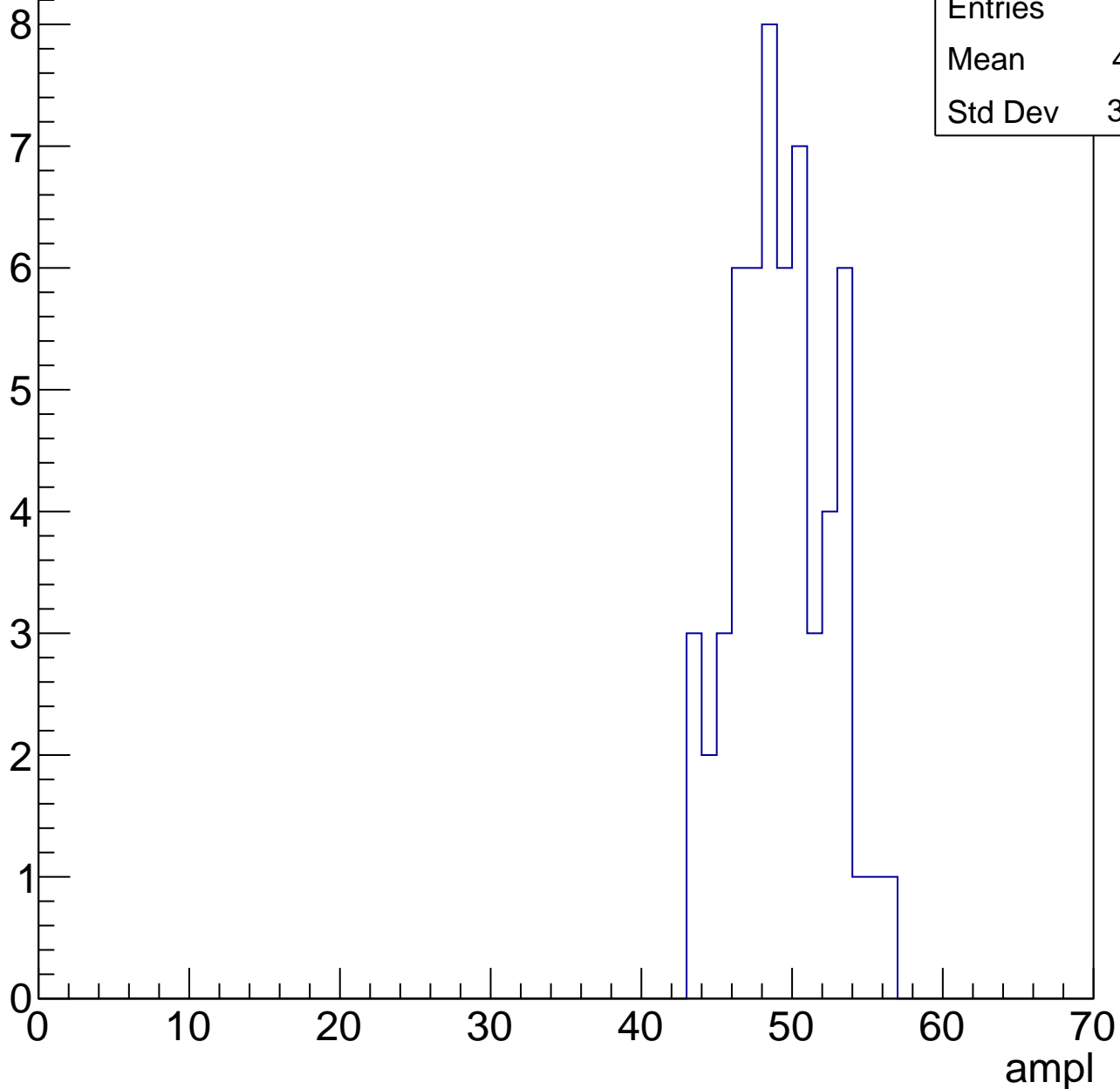


B0L102S, U5-ch8, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.81
Std Dev	3.109

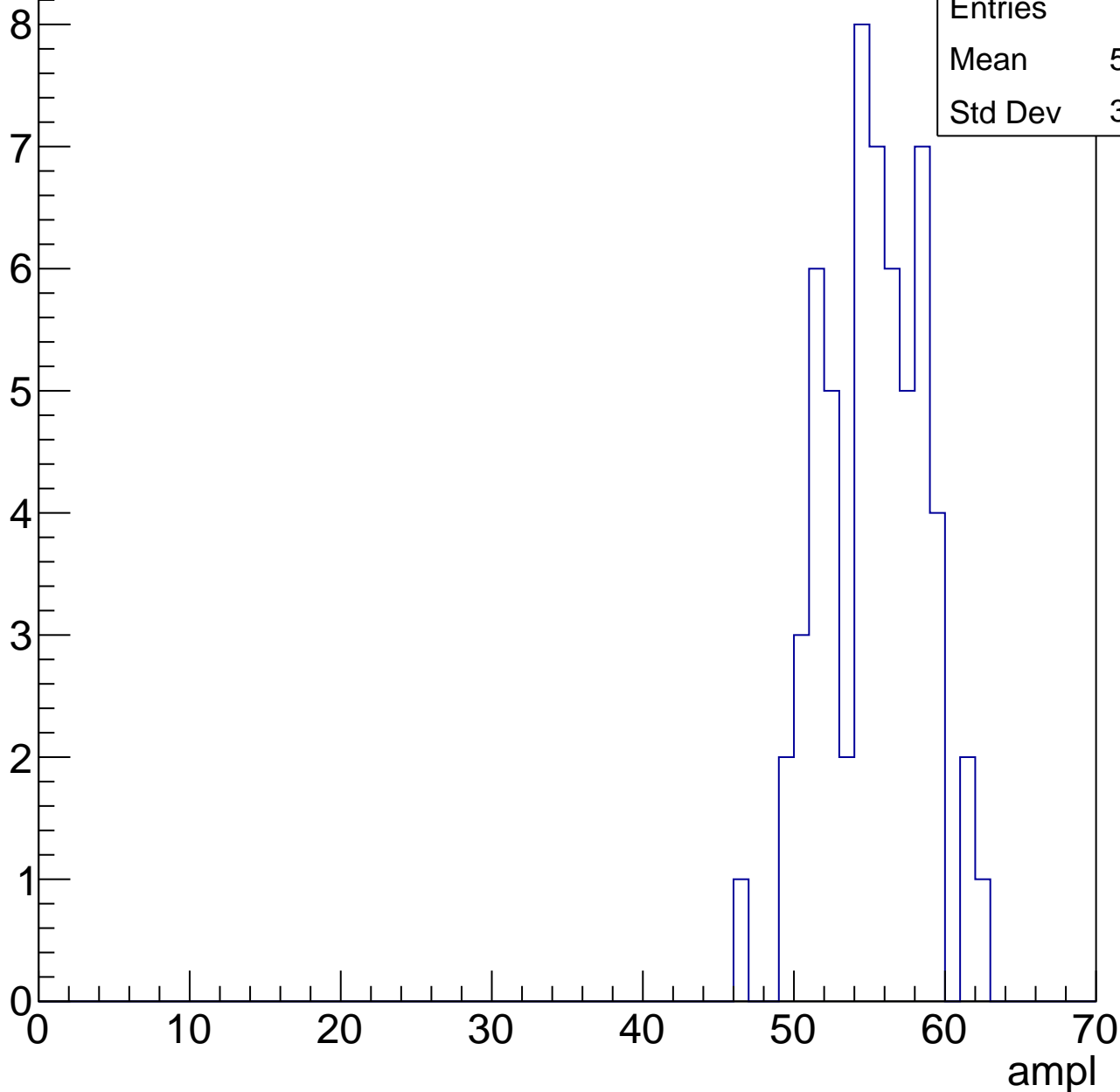


B0L102S, U5-ch8, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	54.75
Std Dev	3.332



B0L102S, U5-ch8, adc5

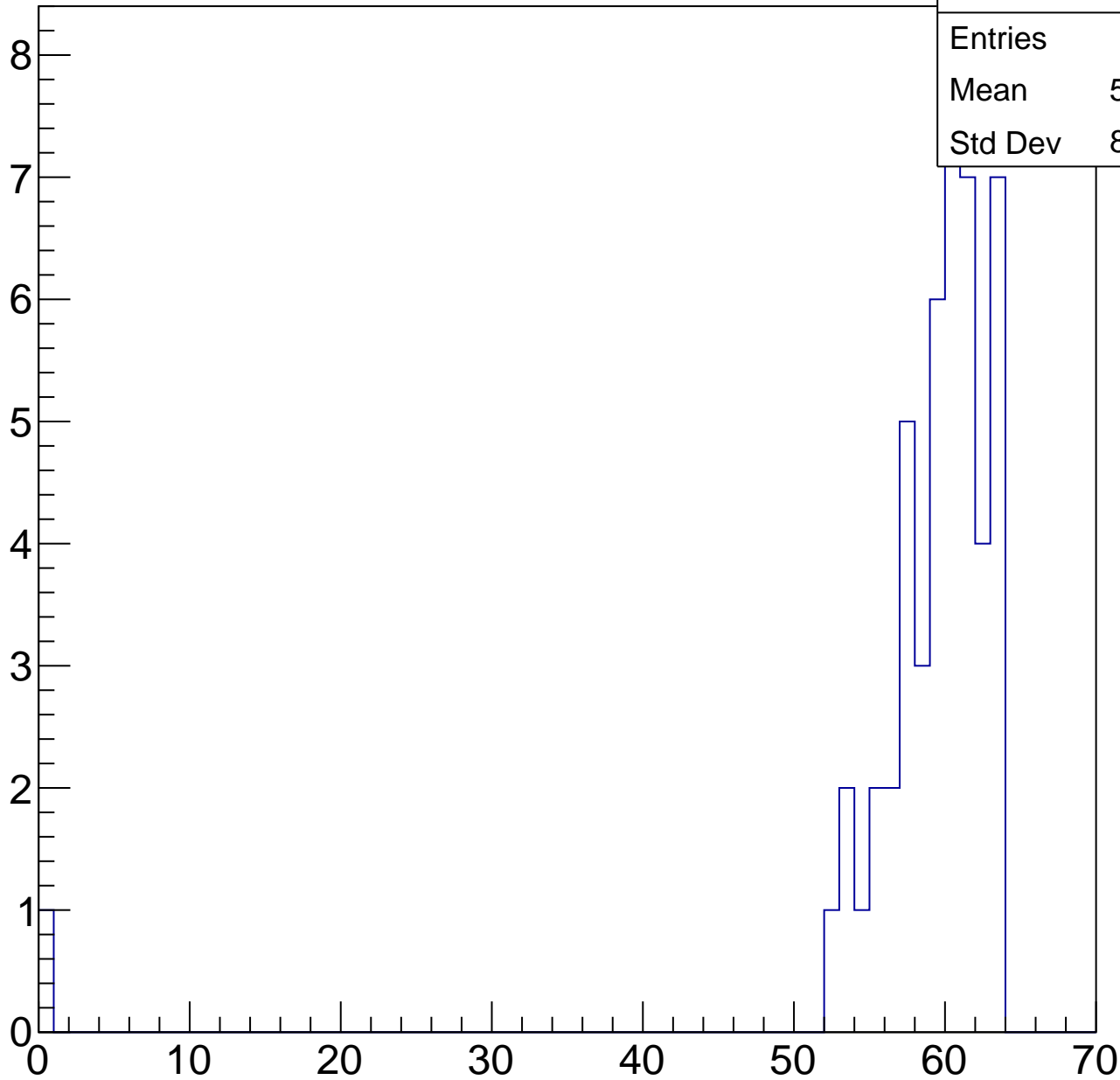
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.02
Std Dev	8.849

ampl

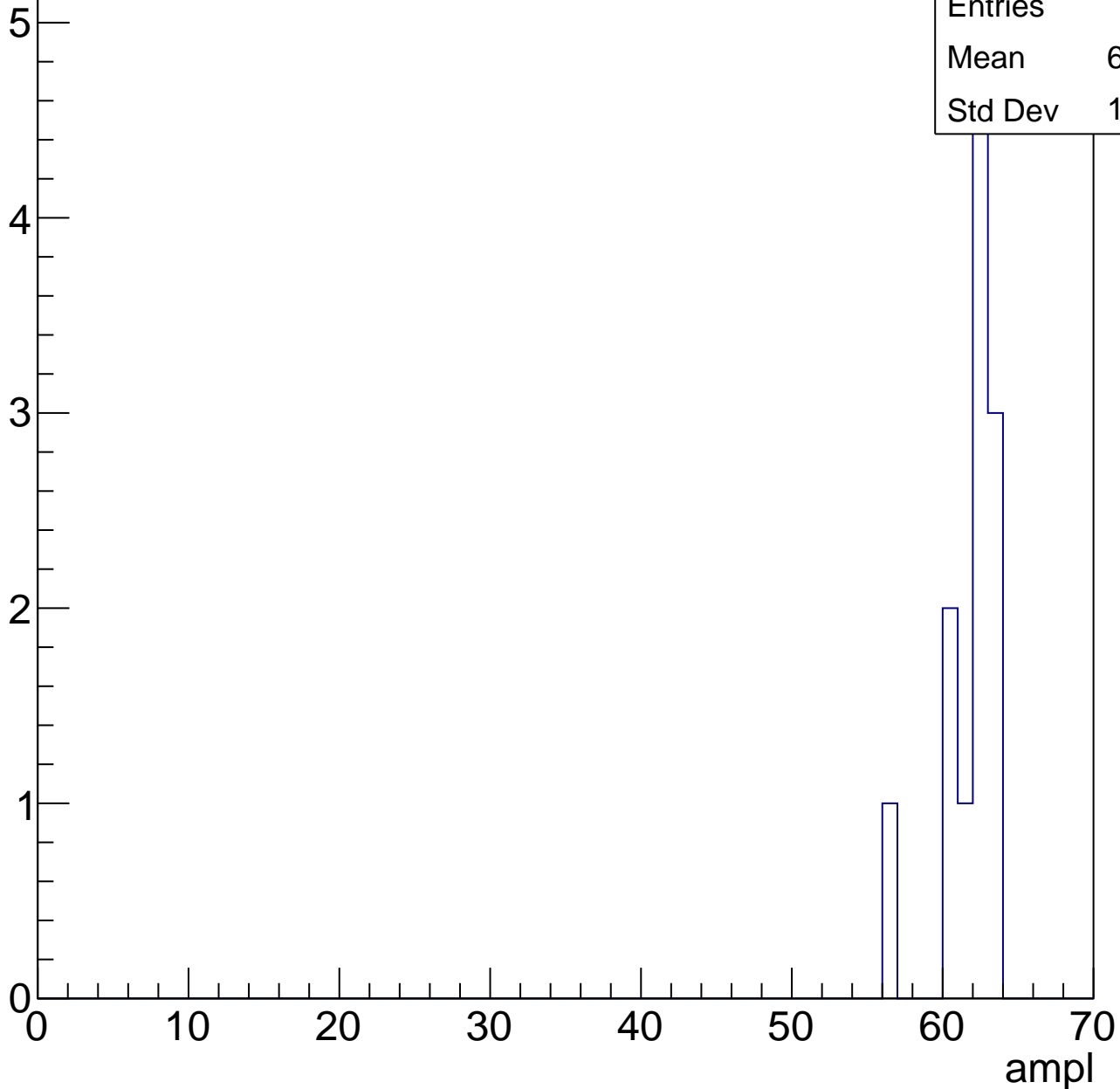


B0L102S, U5-ch8, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.33
Std Dev	1.886



B0L102S, U5-ch8, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch9, adc0

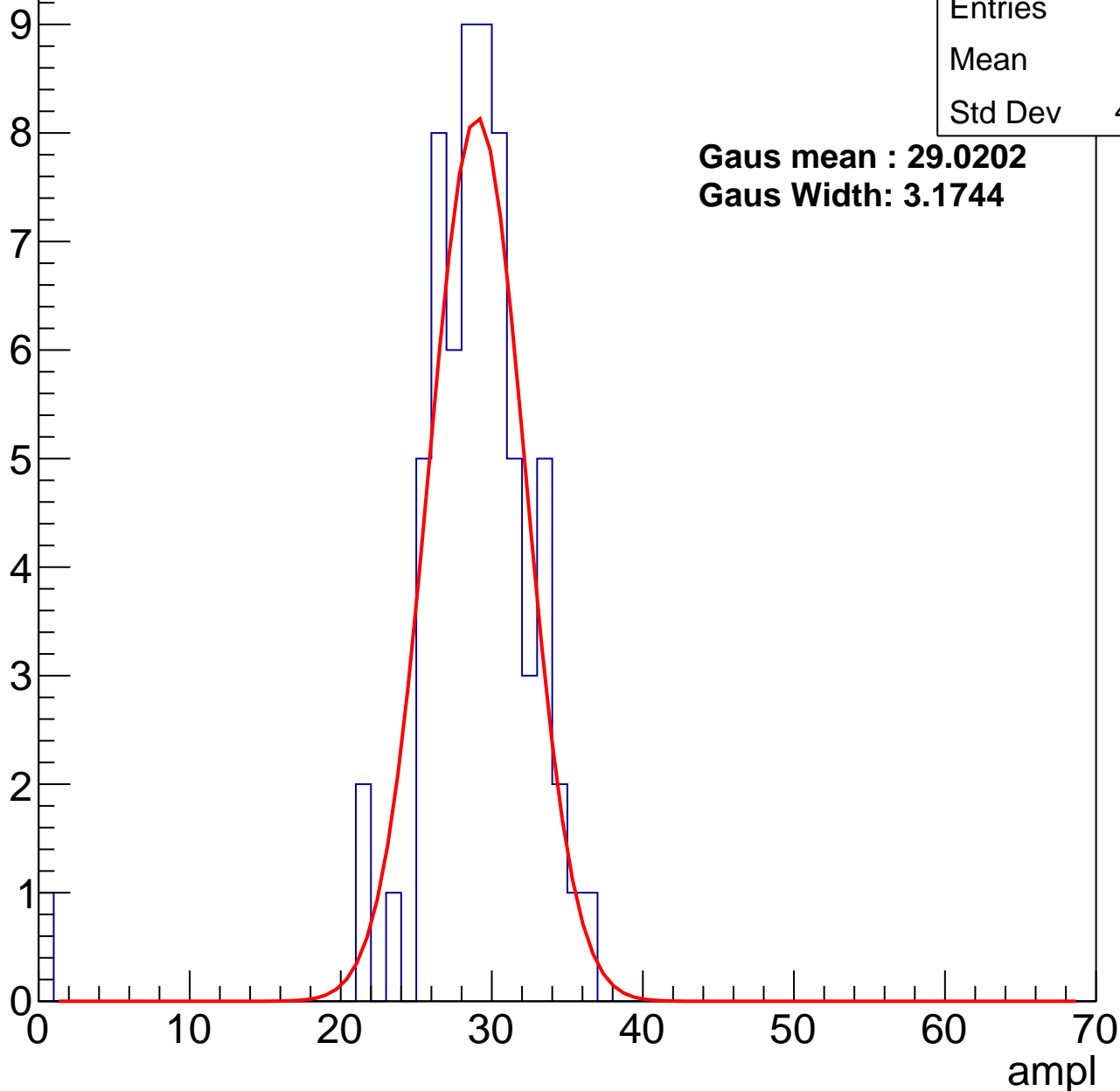
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	28.3
Std Dev	4.651

Gaus mean : 29.0202

Gaus Width: 3.1744



B0L102S, U5-ch9, adc1

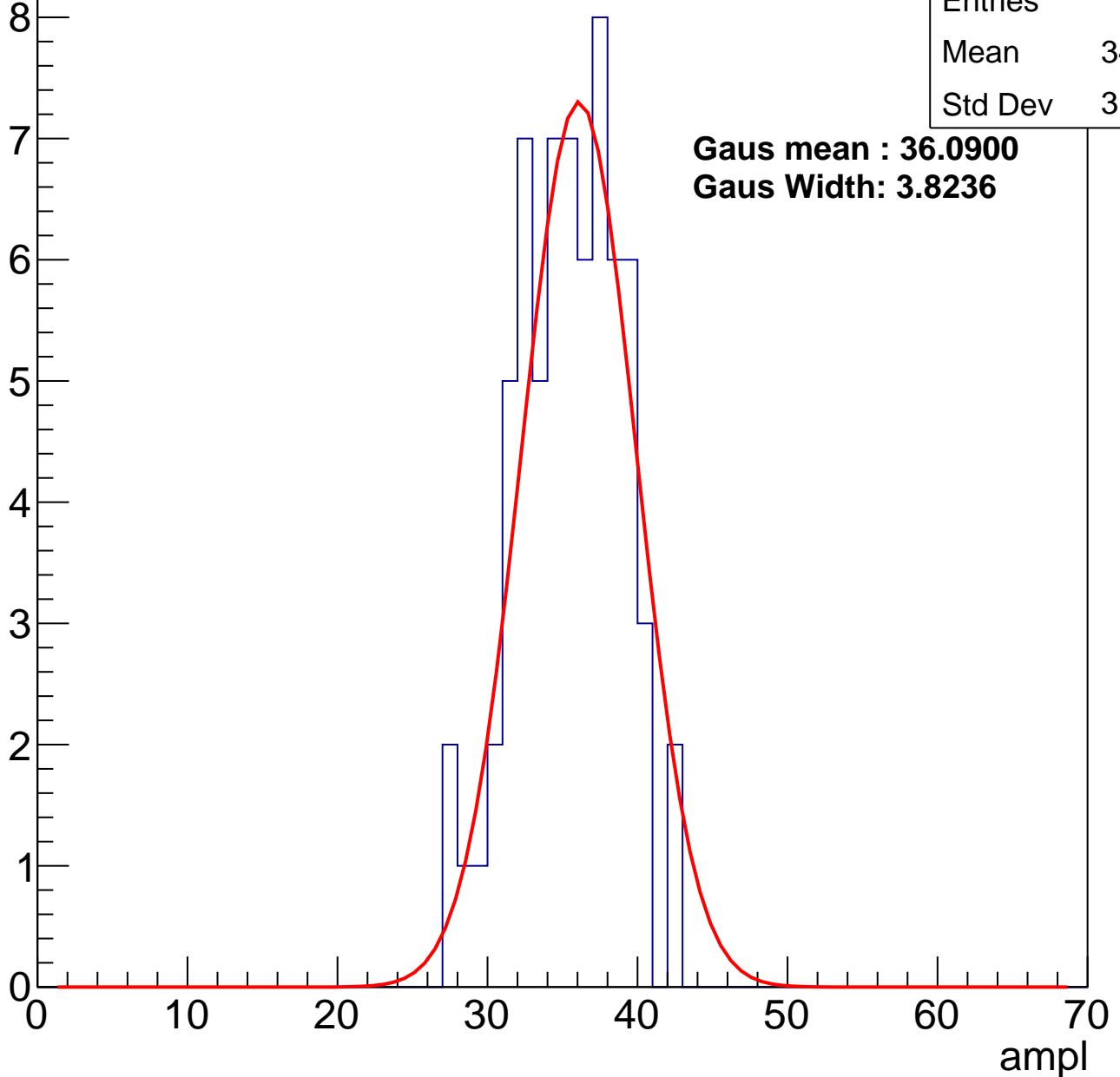
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	34.94
Std Dev	3.417

Gaus mean : 36.0900

Gaus Width: 3.8236



B0L102S, U5-ch9, adc2

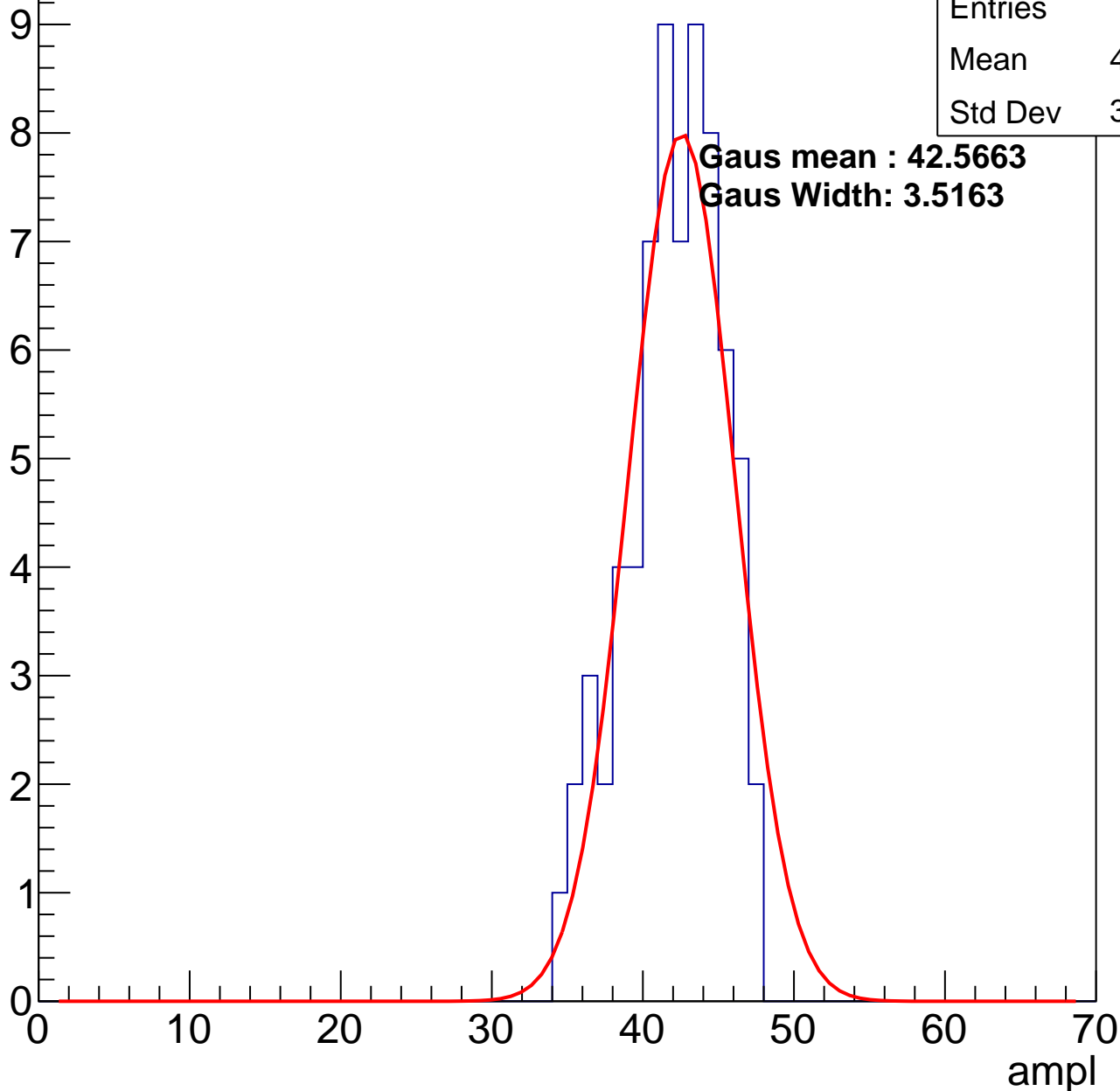
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	41.59
Std Dev	3.118

Gaus mean : 42.5663

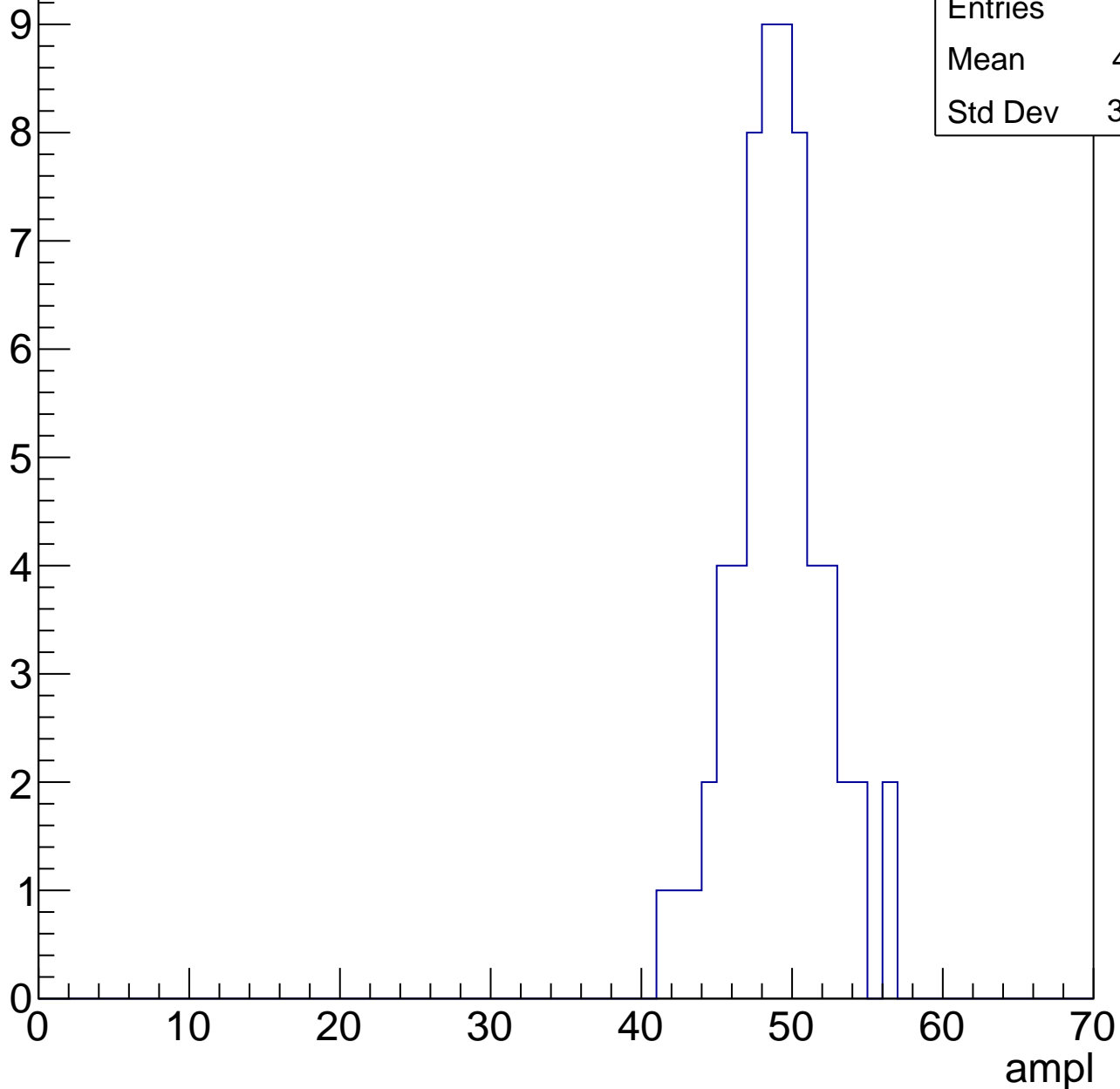
Gaus Width: 3.5163



B0L102S, U5-ch9, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



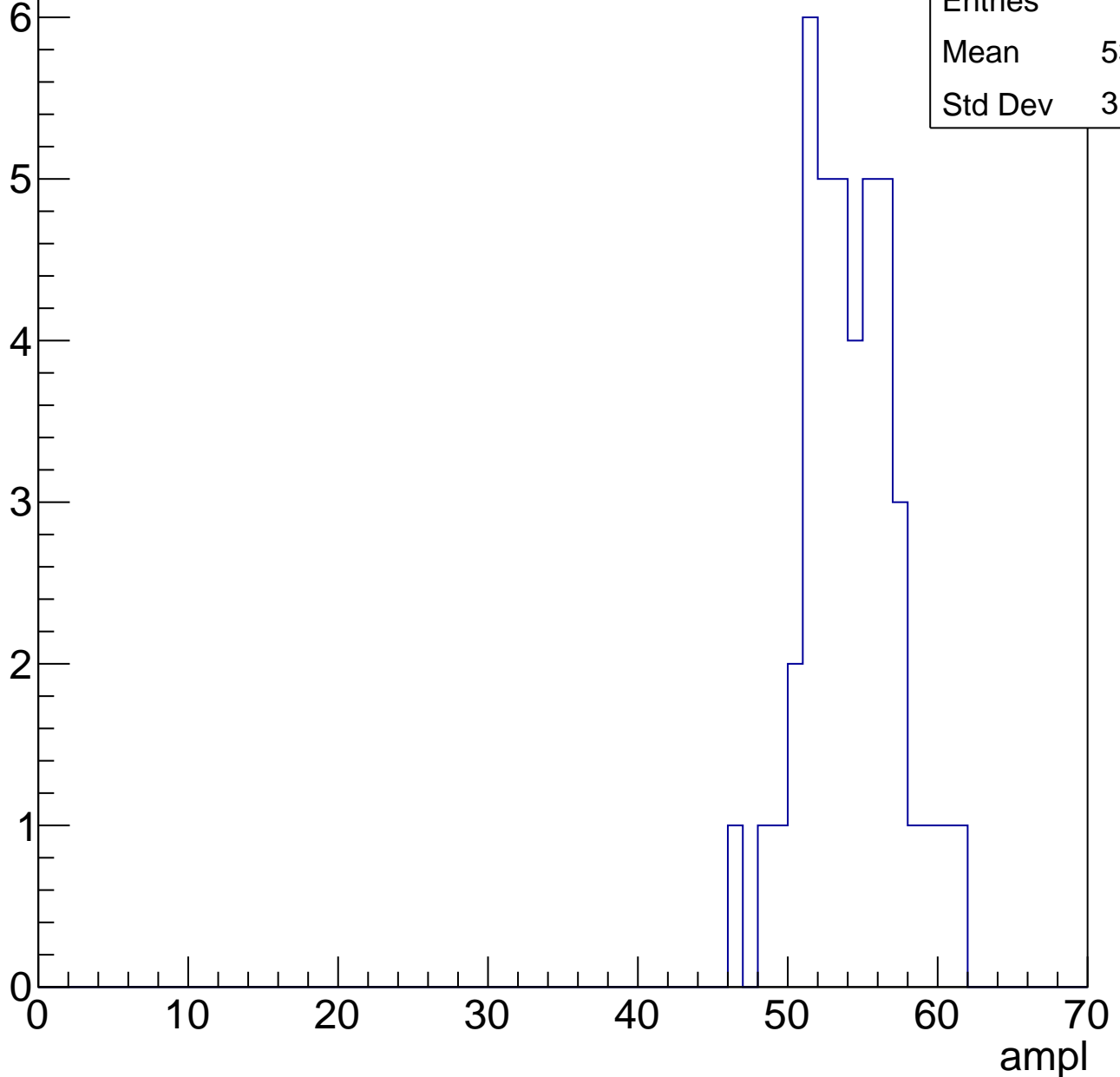
Entries	61
Mean	48.61
Std Dev	3.058

B0L102S, U5-ch9, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	53.67
Std Dev	3.122



B0L102S, U5-ch9, adc5

calib_packv5_042523_0143.root, FC#12, port B1

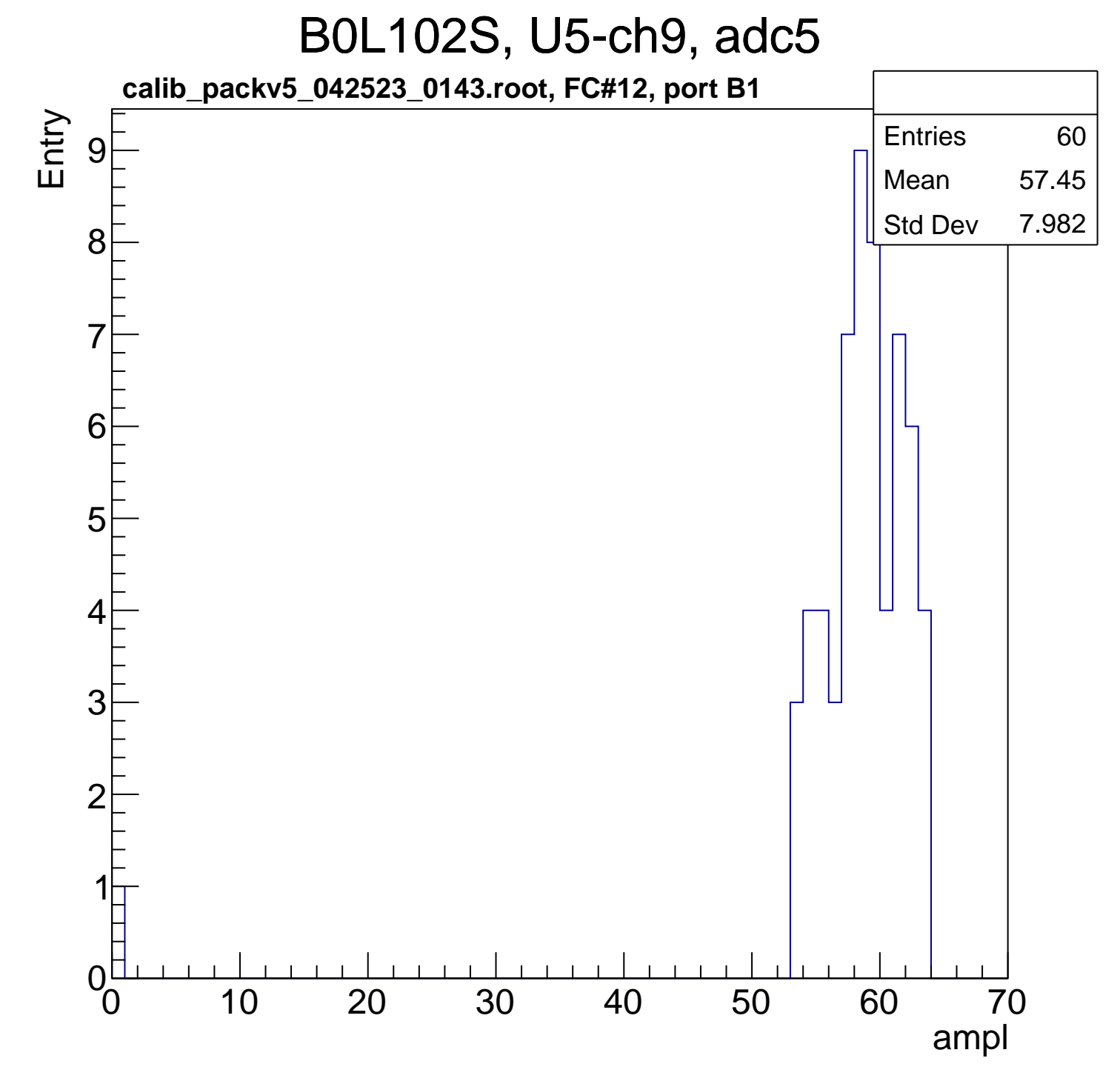
Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	57.45
Std Dev	7.982

ampl

0 10 20 30 40 50 60 70

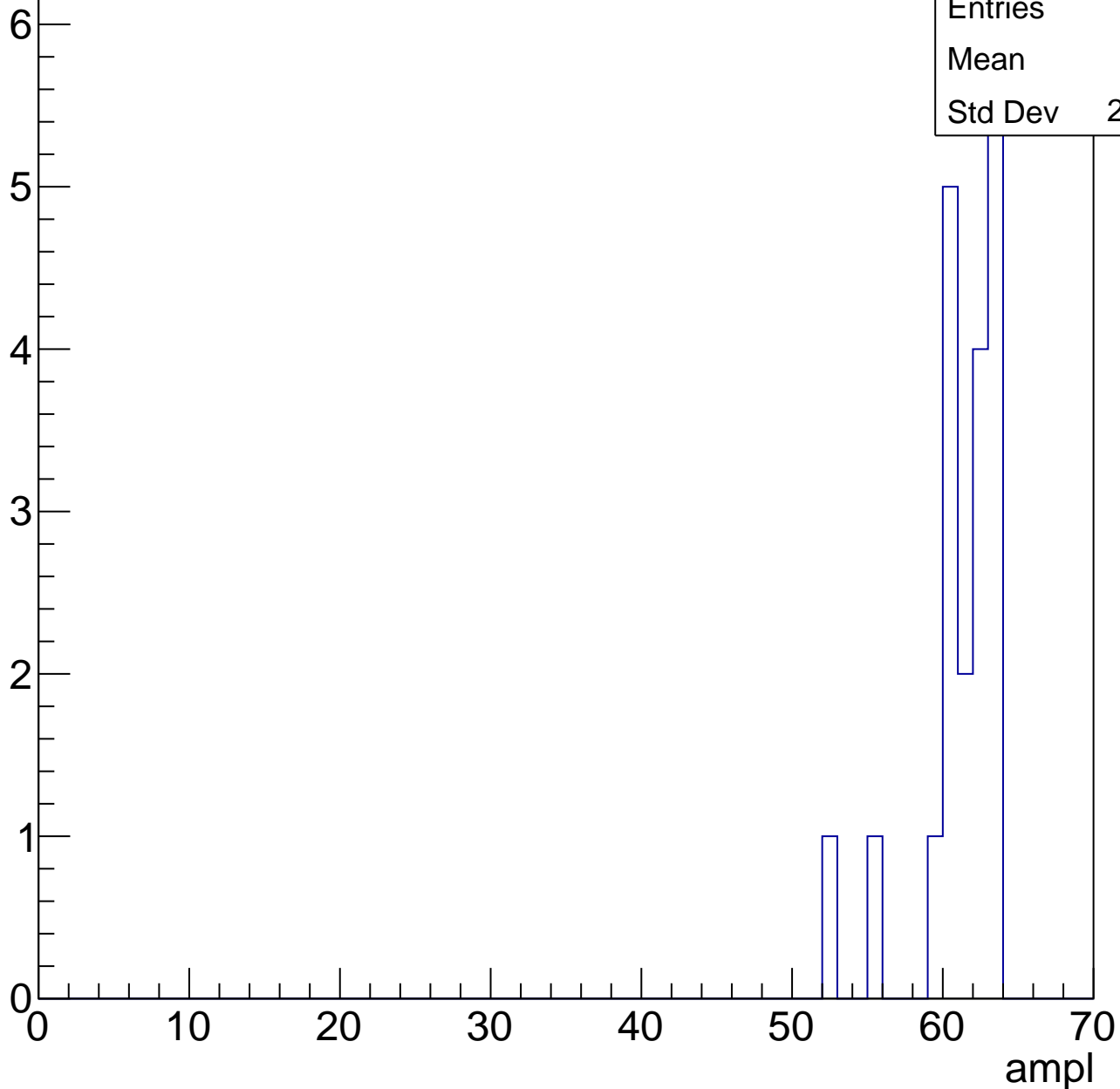


B0L102S, U5-ch9, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	20
Mean	60.7
Std Dev	2.759



B0L102S, U5-ch9, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch10, adc0

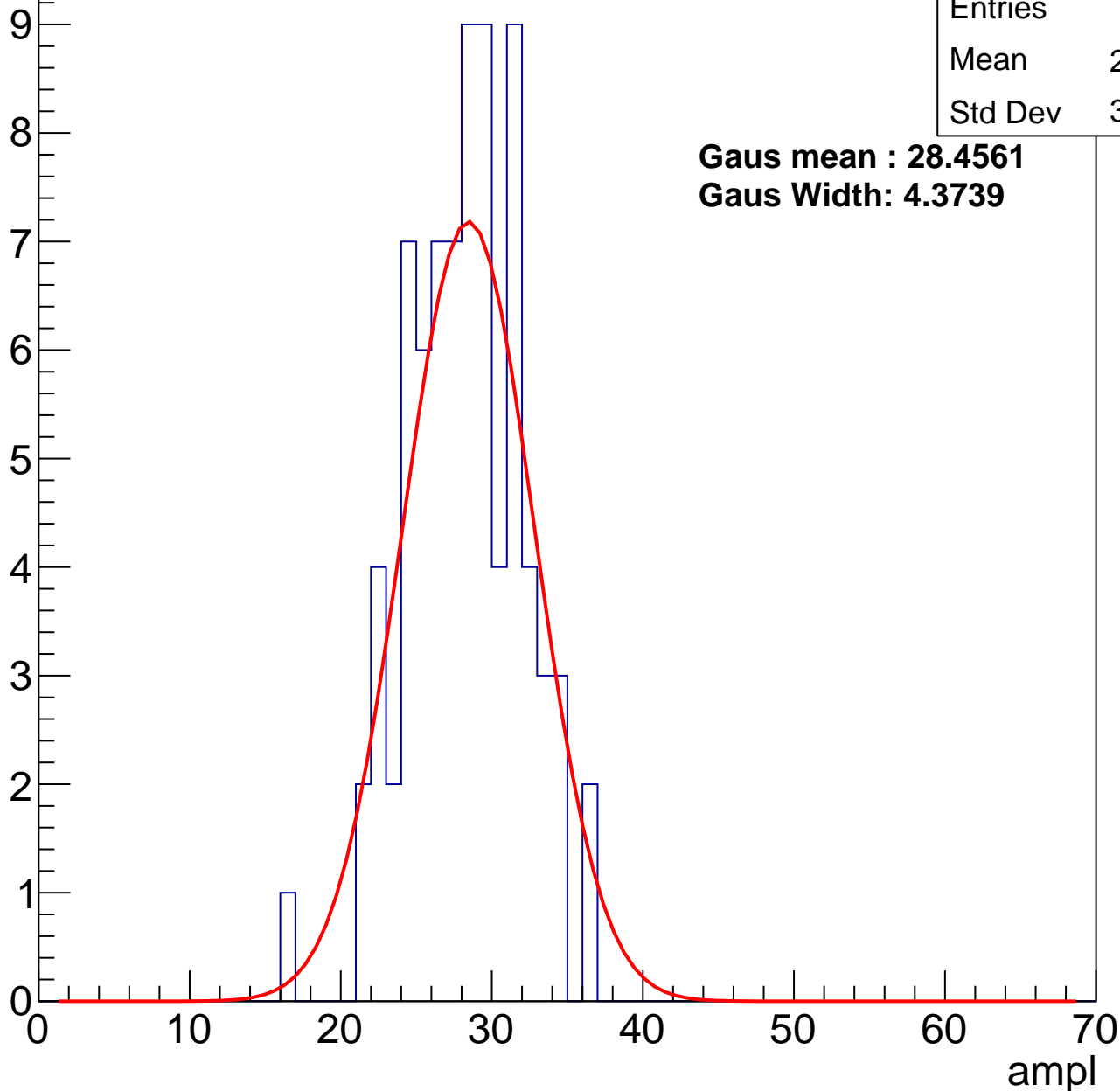
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	27.77
Std Dev	3.755

Gaus mean : 28.4561

Gaus Width: 4.3739



B0L102S, U5-ch10, adc1

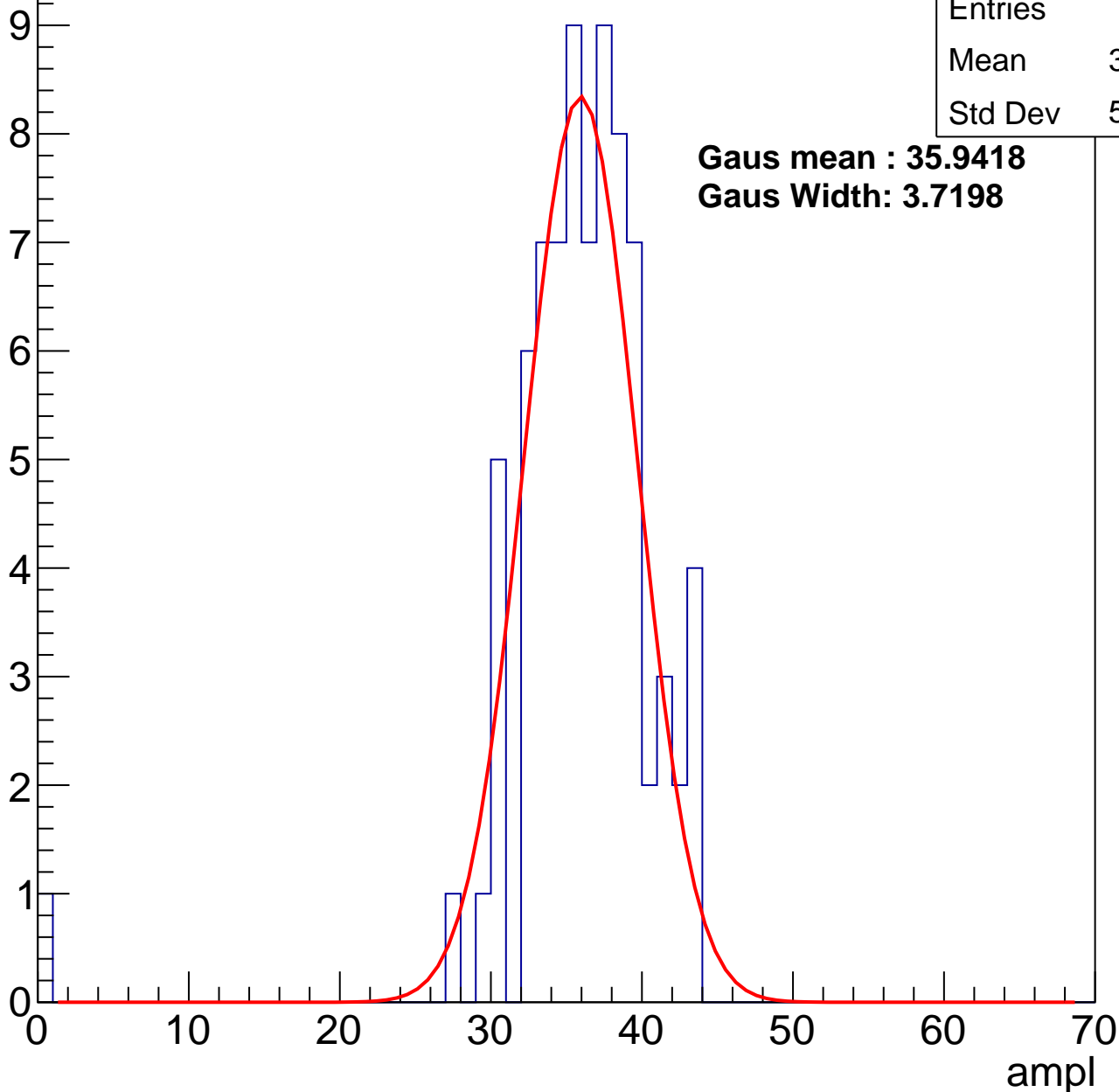
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.48
Std Dev	5.348

Gaus mean : 35.9418

Gaus Width: 3.7198



B0L102S, U5-ch10, adc2

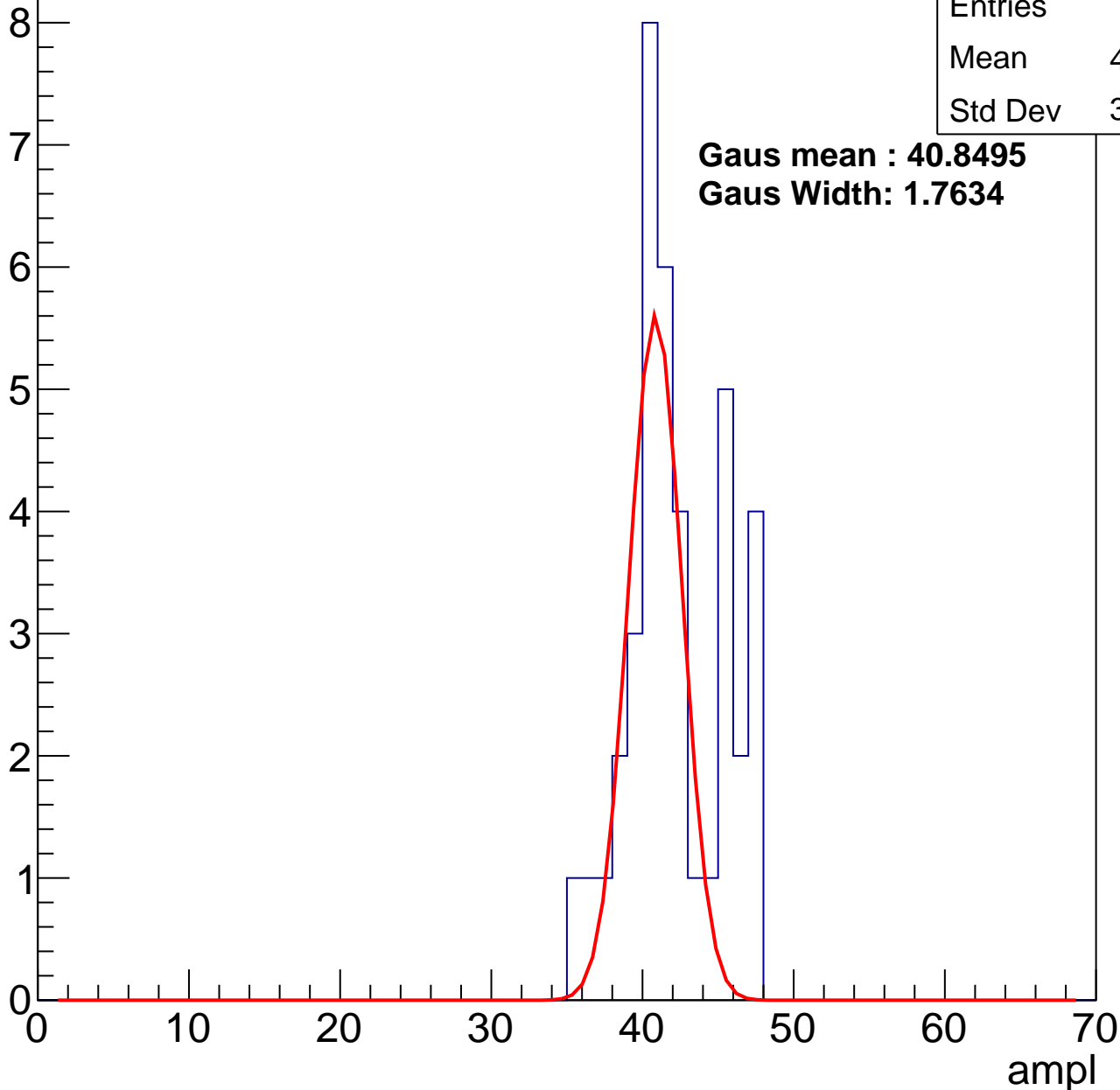
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	41.72
Std Dev	3.146

Gaus mean : 40.8495

Gaus Width: 1.7634



B0L102S, U5-ch10, adc3

calib_packv5_042523_0143.root, FC#12, port B1

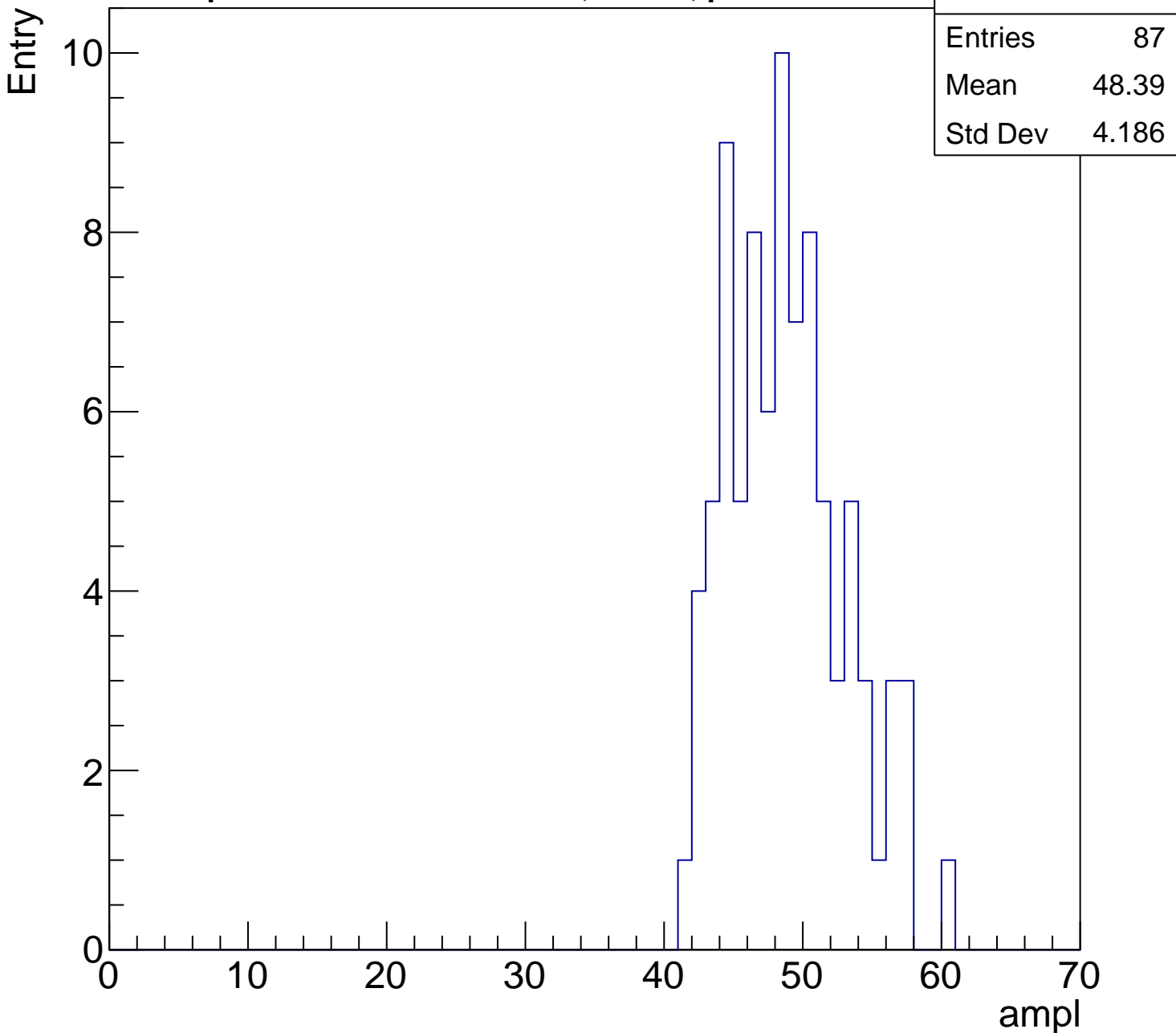
Entries	87
Mean	48.39
Std Dev	4.186

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

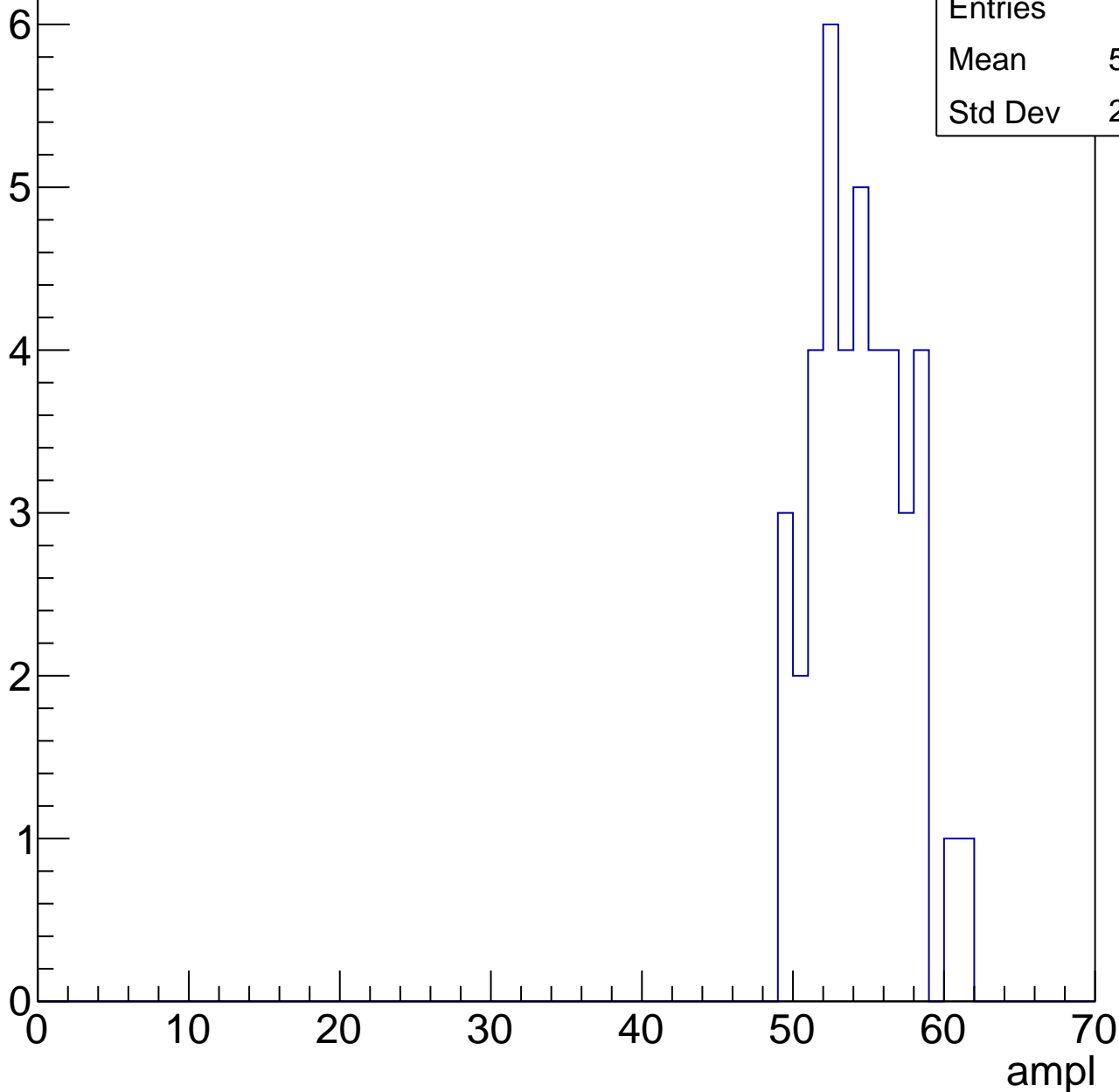


B0L102S, U5-ch10, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	53.98
Std Dev	2.992

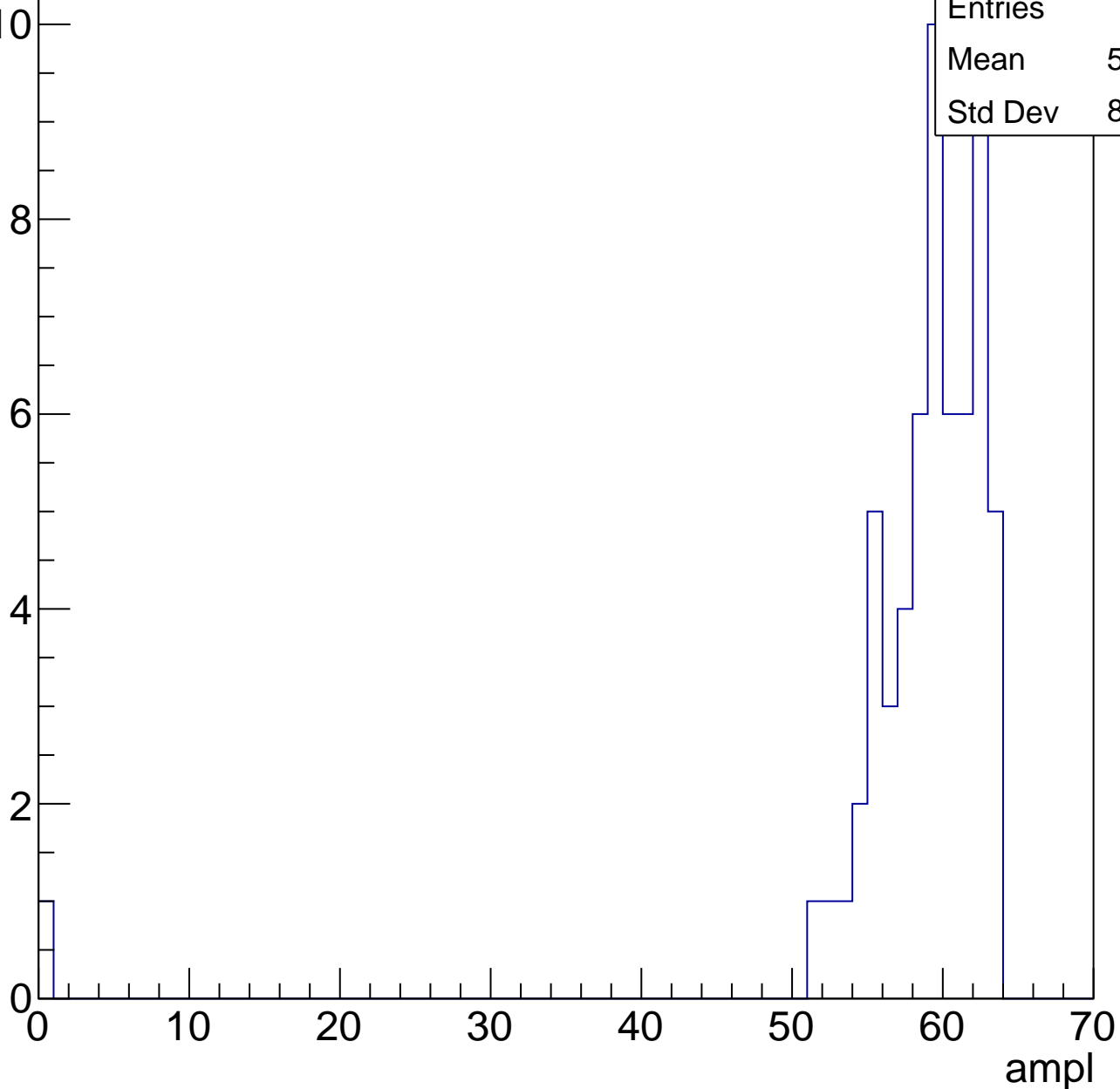


B0L102S, U5-ch10, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	57.87
Std Dev	8.086

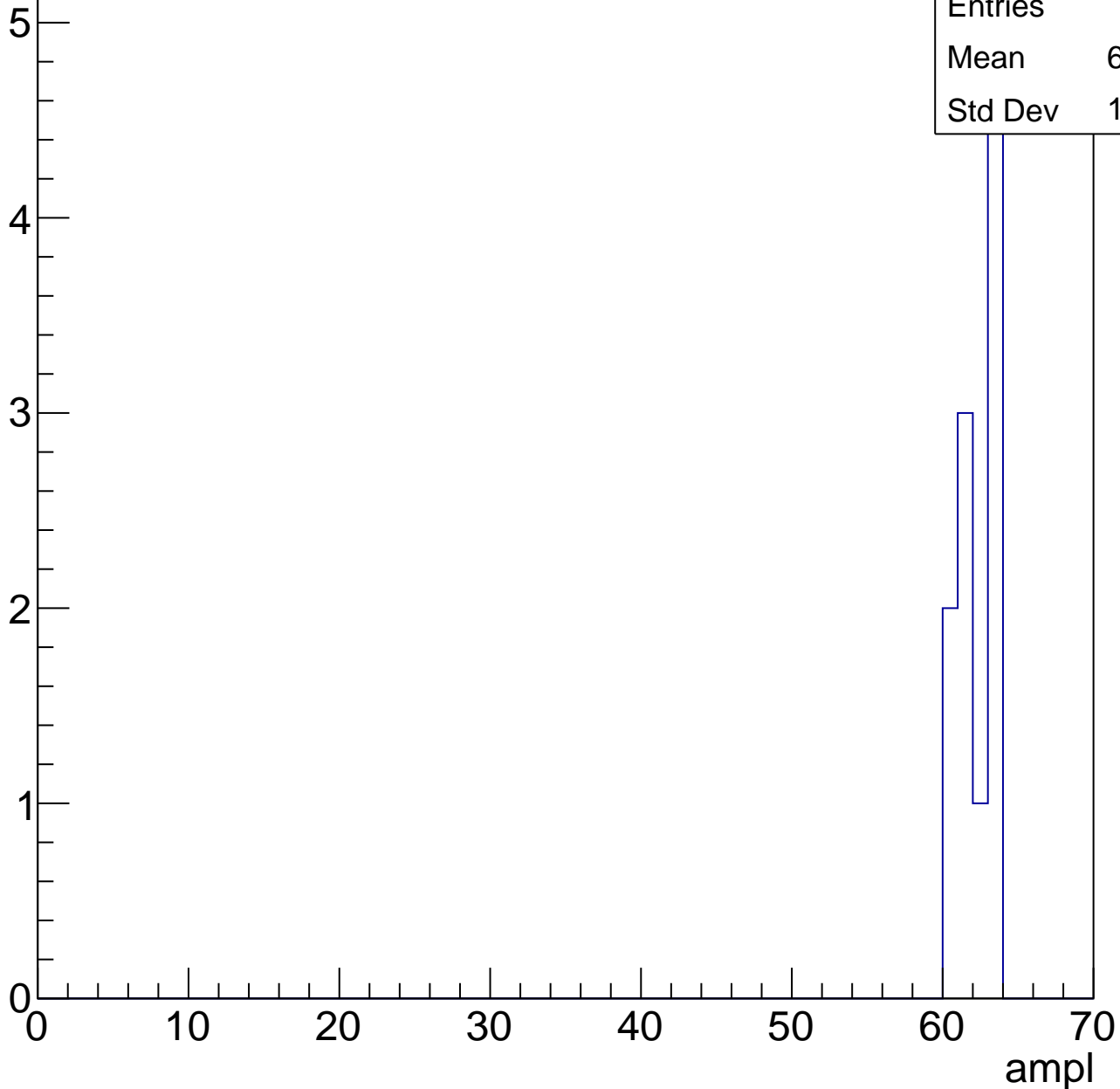


B0L102S, U5-ch10, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.82
Std Dev	1.192



B0L102S, U5-ch10, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch11, adc0

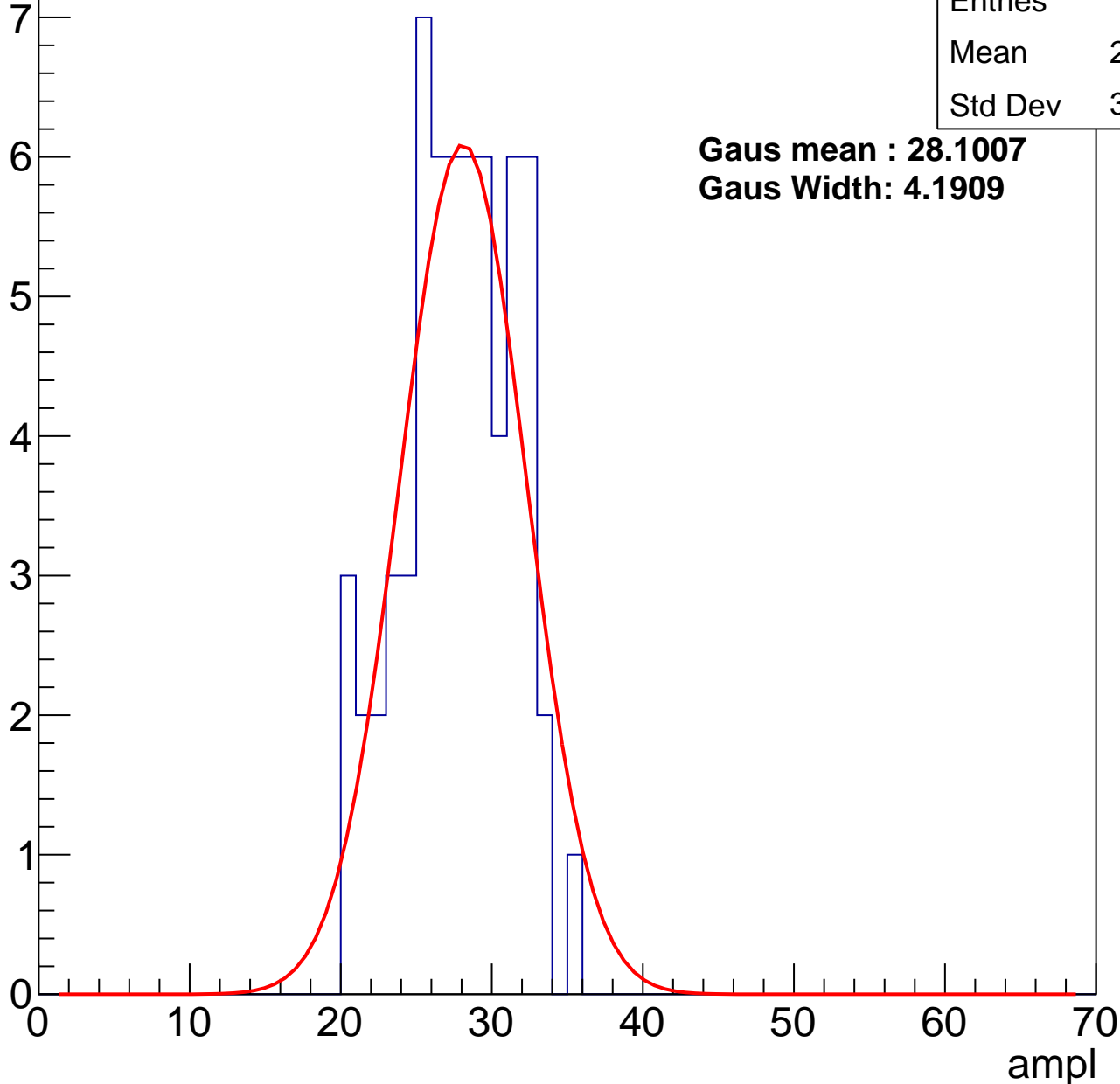
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	27.32
Std Dev	3.607

Gaus mean : 28.1007

Gaus Width: 4.1909



B0L102S, U5-ch11, adc1

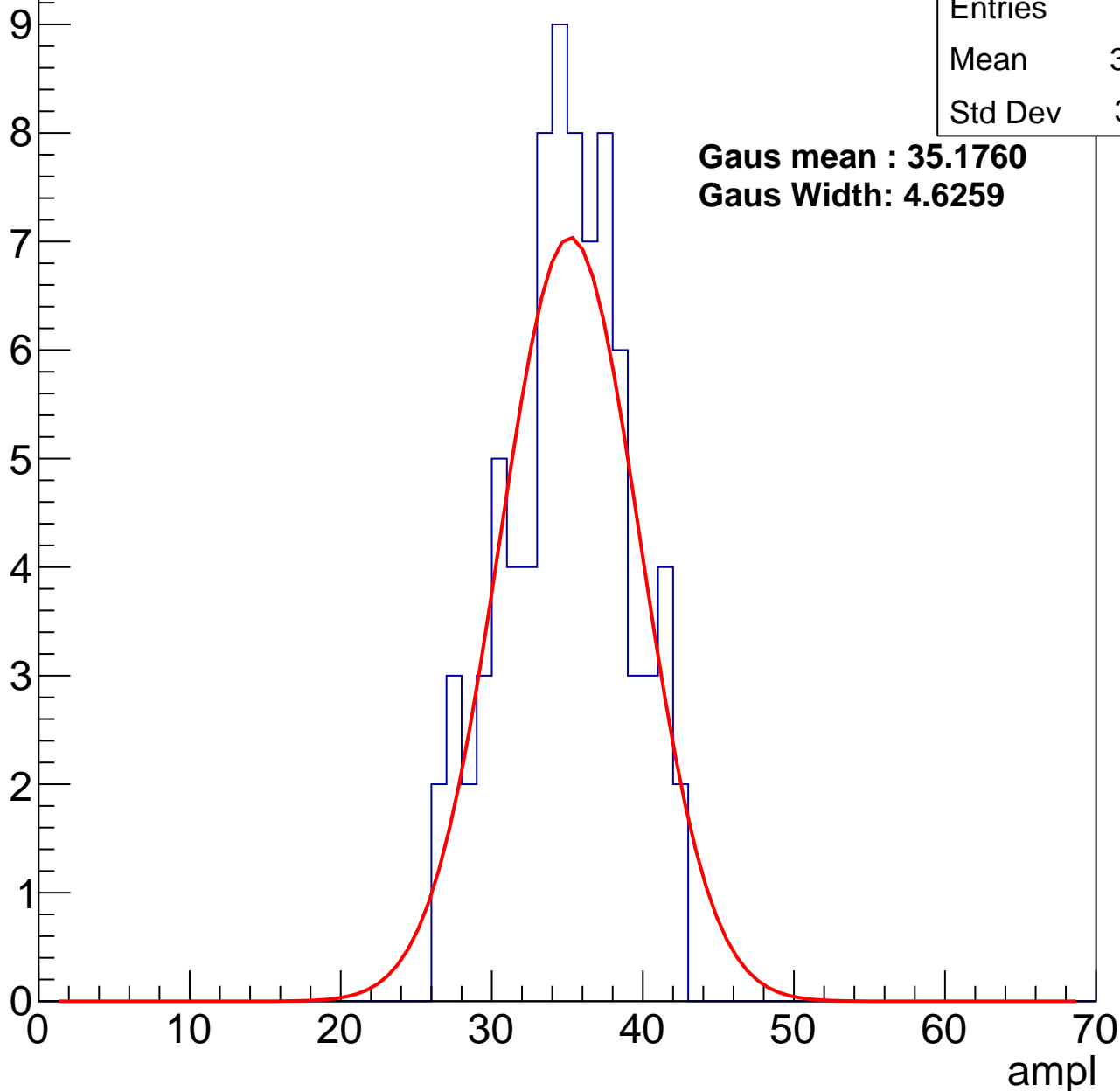
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	34.43
Std Dev	3.941

Gaus mean : 35.1760

Gaus Width: 4.6259



B0L102S, U5-ch11, adc2

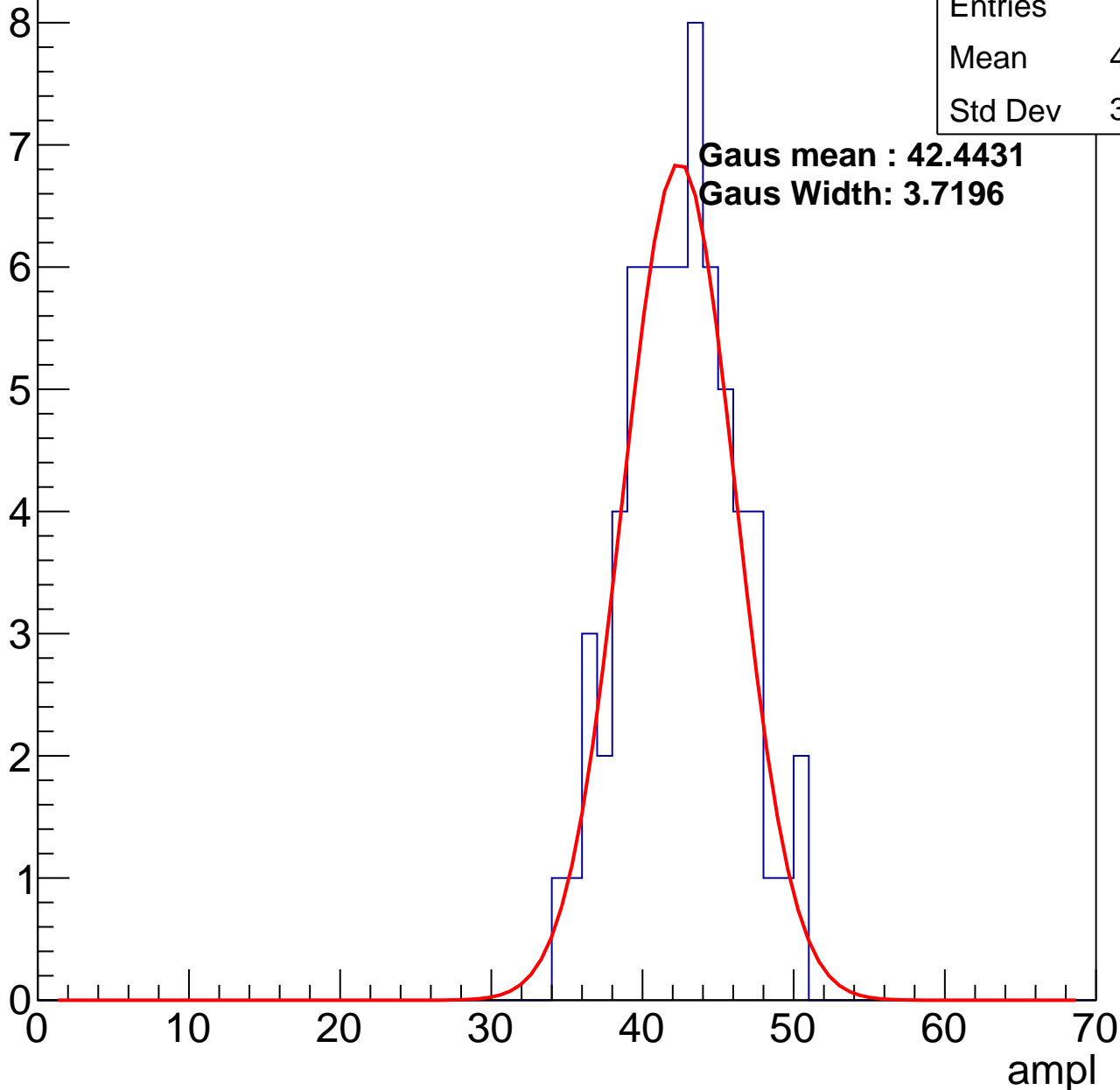
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	42.08
Std Dev	3.636

Gaus mean : 42.4431

Gaus Width: 3.7196

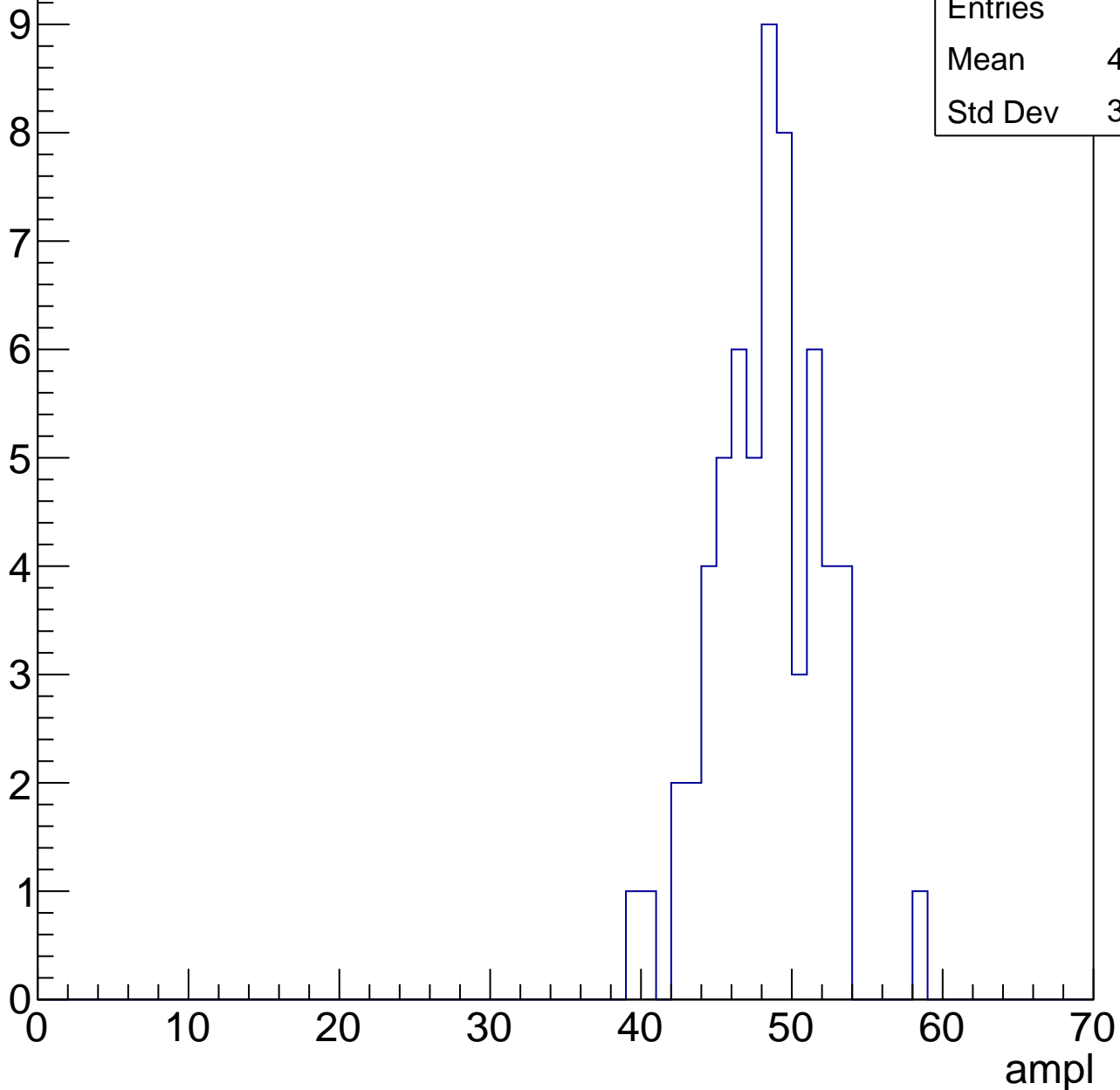


B0L102S, U5-ch11, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

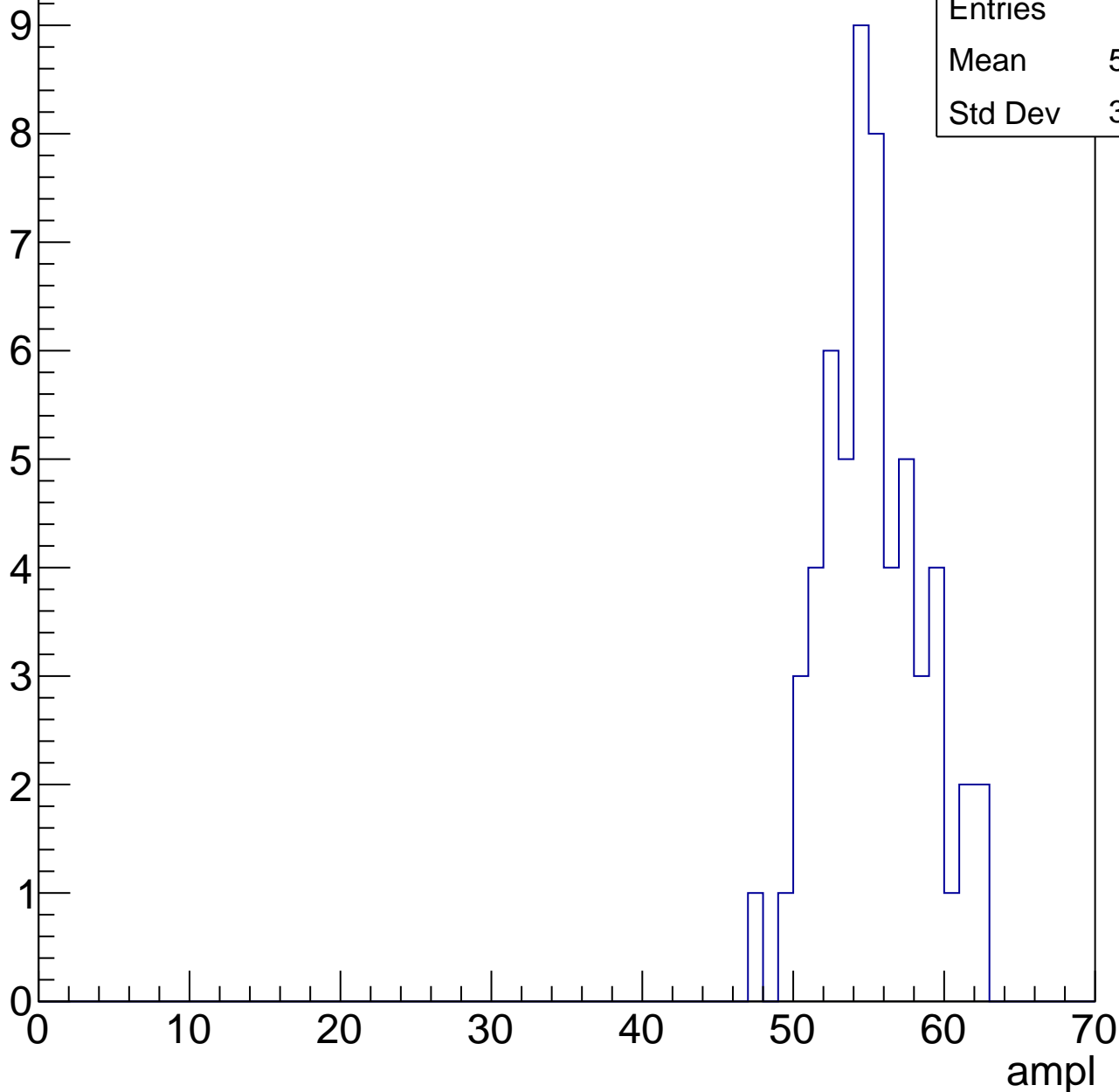
Entries	61
Mean	47.85
Std Dev	3.487



B0L102S, U5-ch11, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

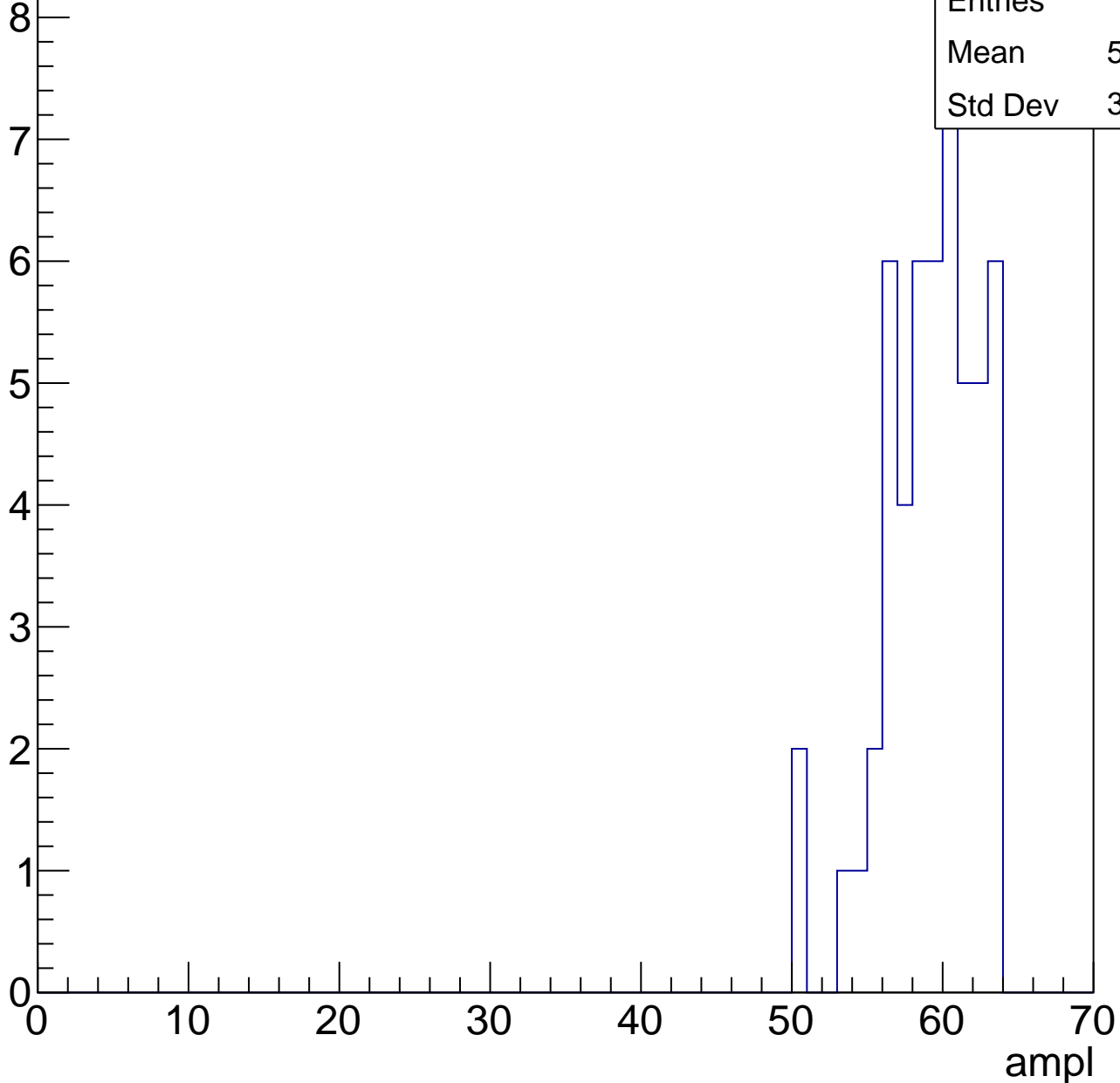


B0L102S, U5-ch11, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

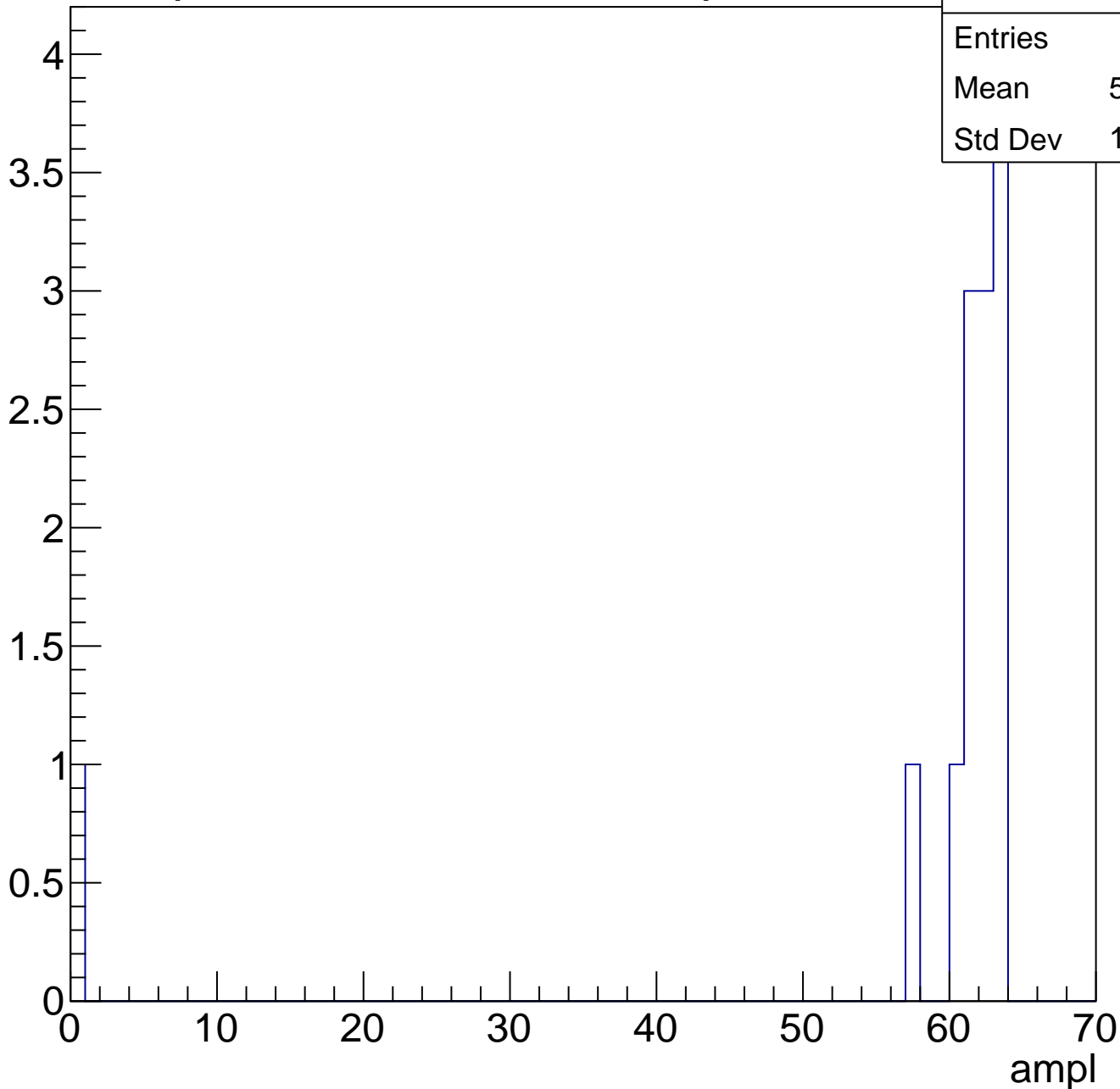
Entries	52
Mean	58.77
Std Dev	3.092



B0L102S, U5-ch11, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch11, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch12, adc0

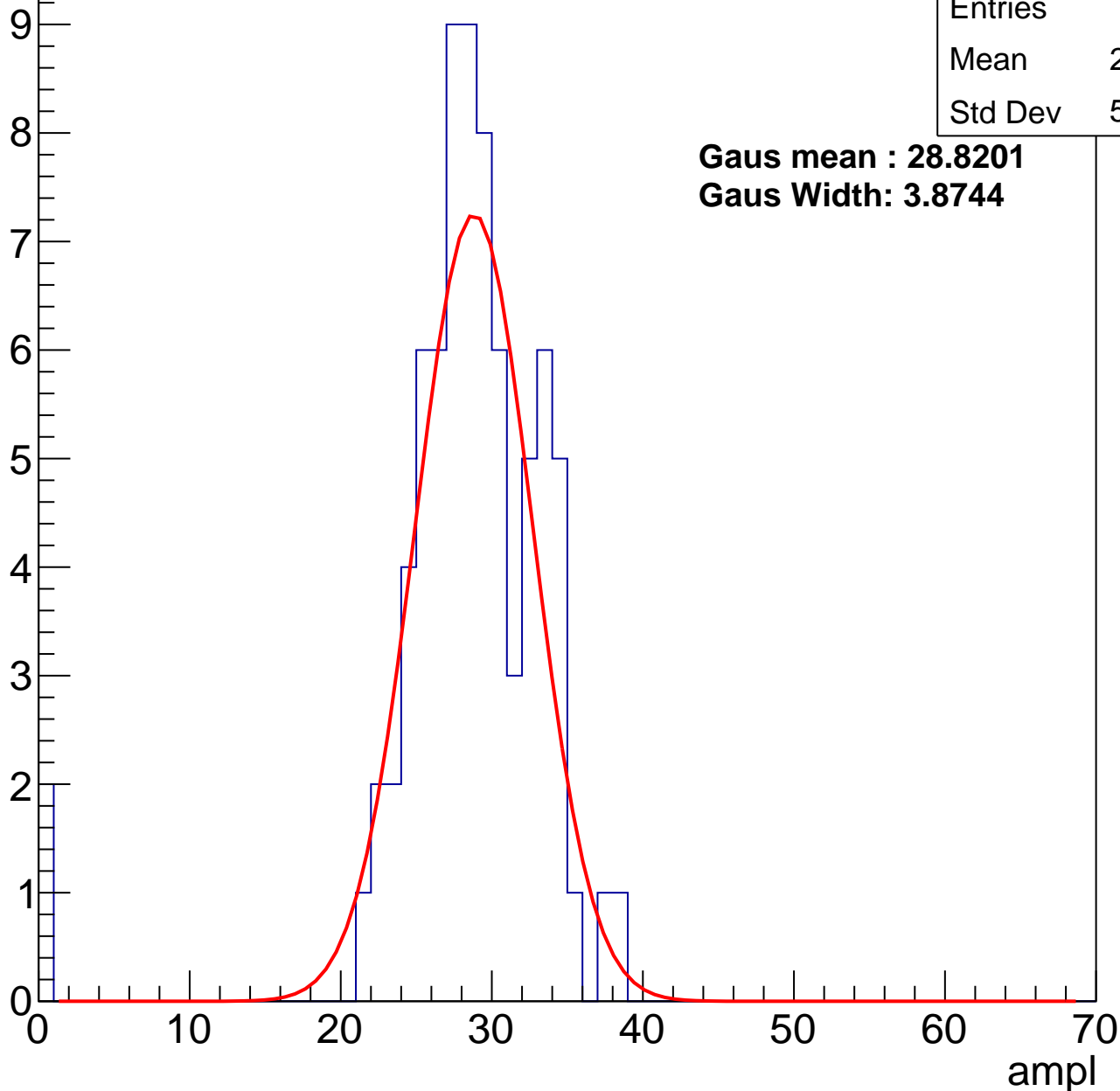
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	27.94
Std Dev	5.798

Gaus mean : 28.8201

Gaus Width: 3.8744



B0L102S, U5-ch12, adc1

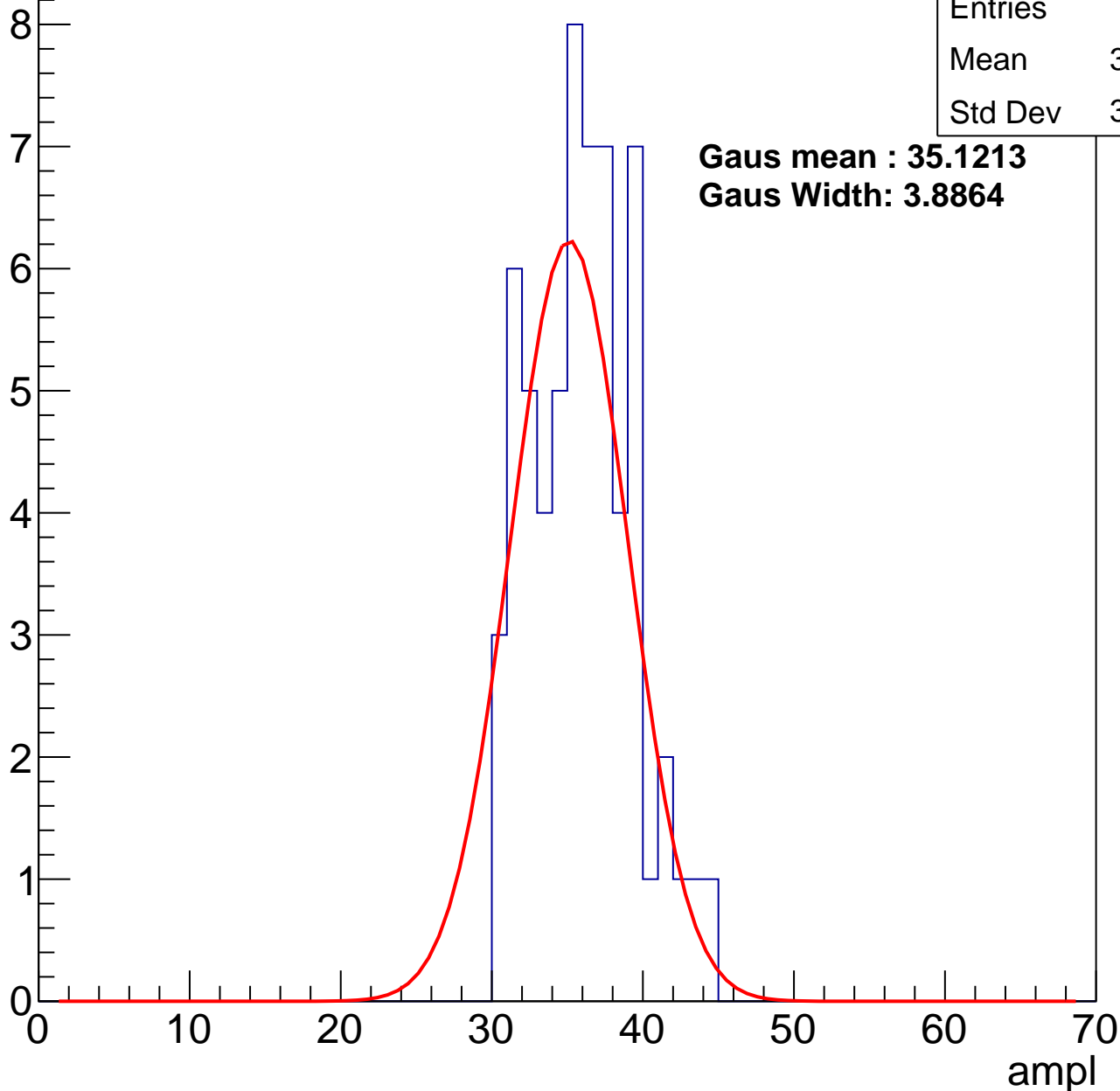
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	35.56
Std Dev	3.339

Gaus mean : 35.1213

Gaus Width: 3.8864



B0L102S, U5-ch12, adc2

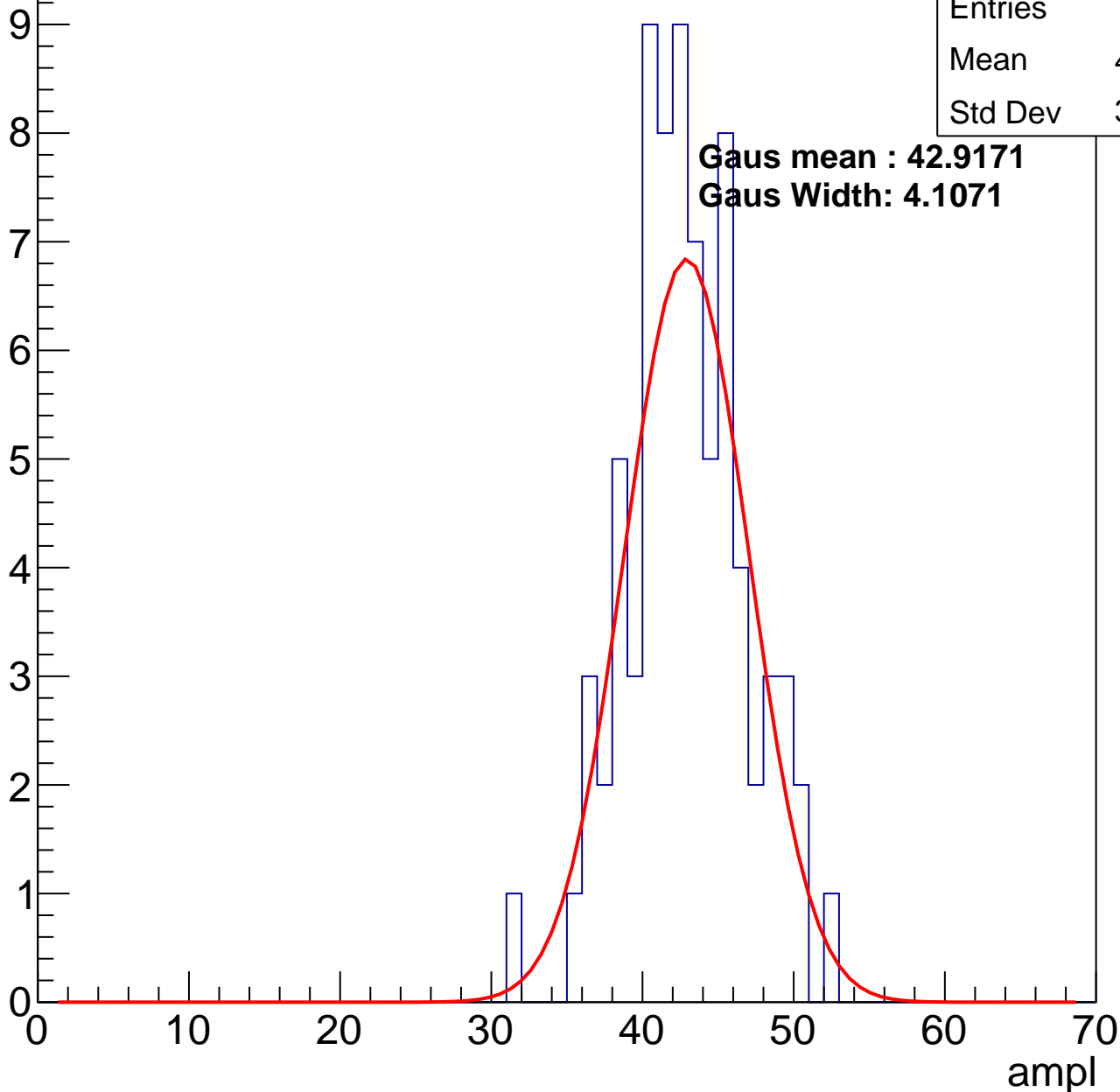
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	42.41
Std Dev	3.911

Gaus mean : 42.9171

Gaus Width: 4.1071

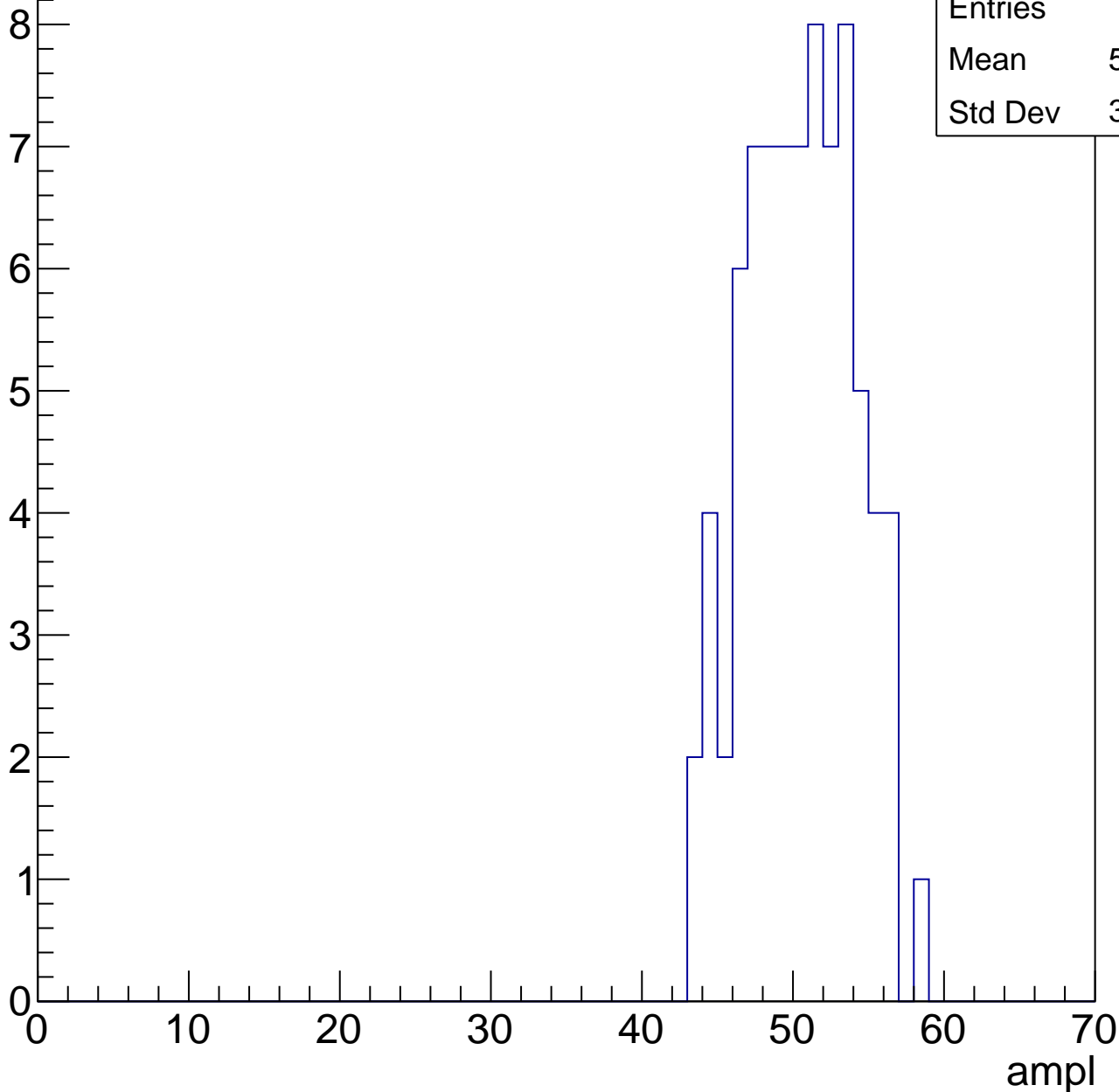


B0L102S, U5-ch12, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	50.05
Std Dev	3.525

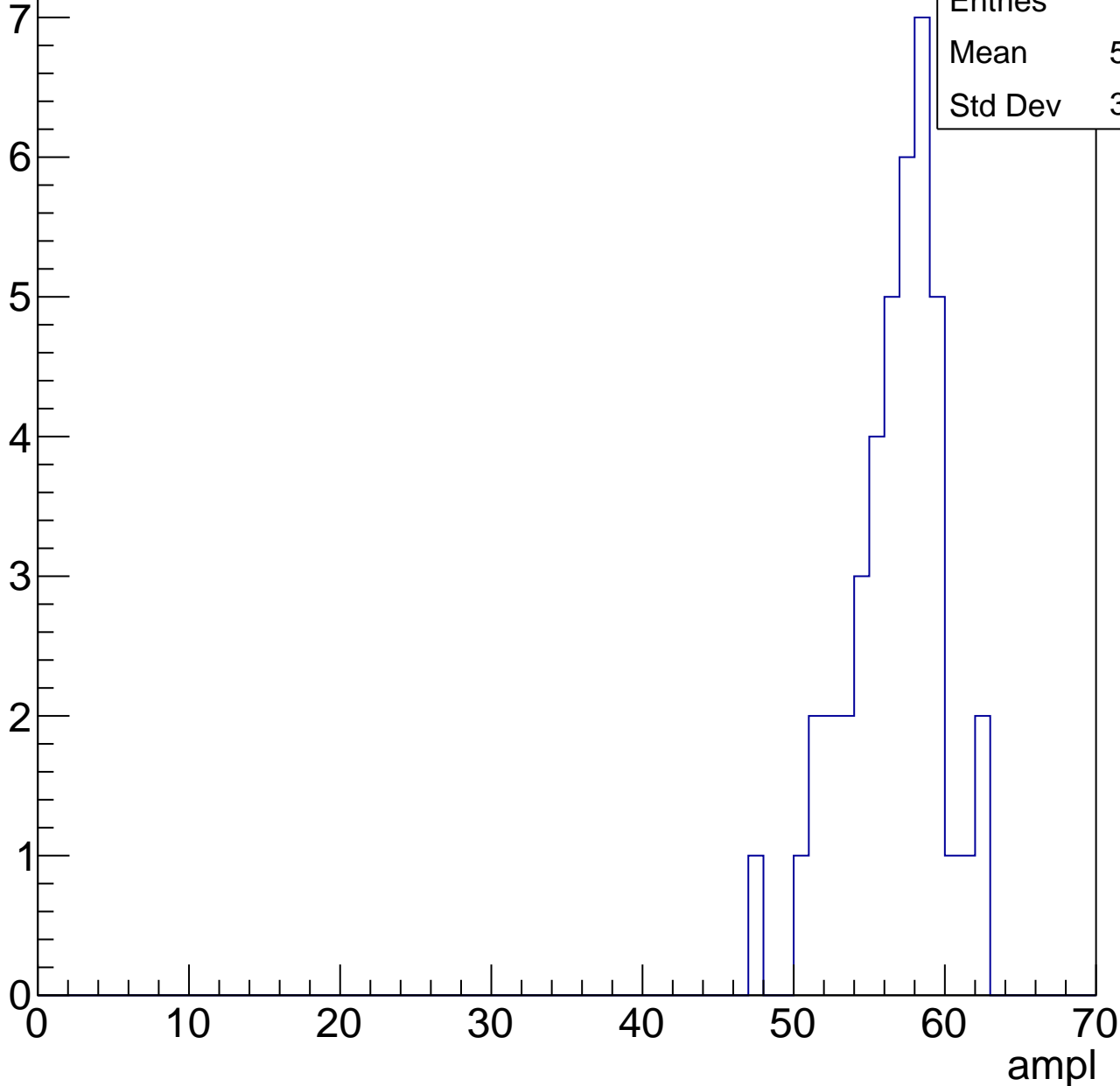


B0L102S, U5-ch12, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

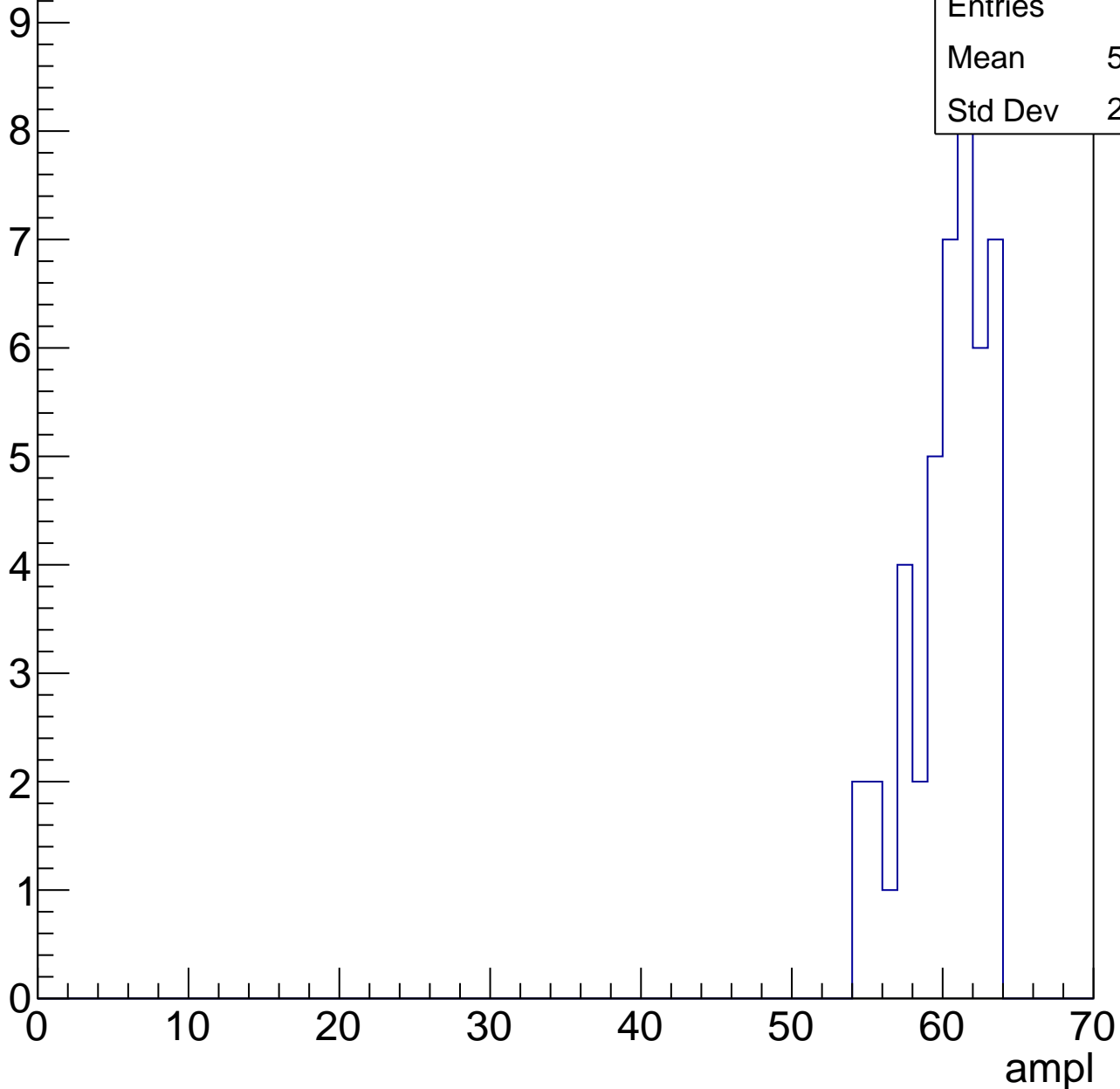
Entries	42
Mean	56.17
Std Dev	3.177



B0L102S, U5-ch12, adc5

calib_packv5_042523_0143.root, FC#12, port B1

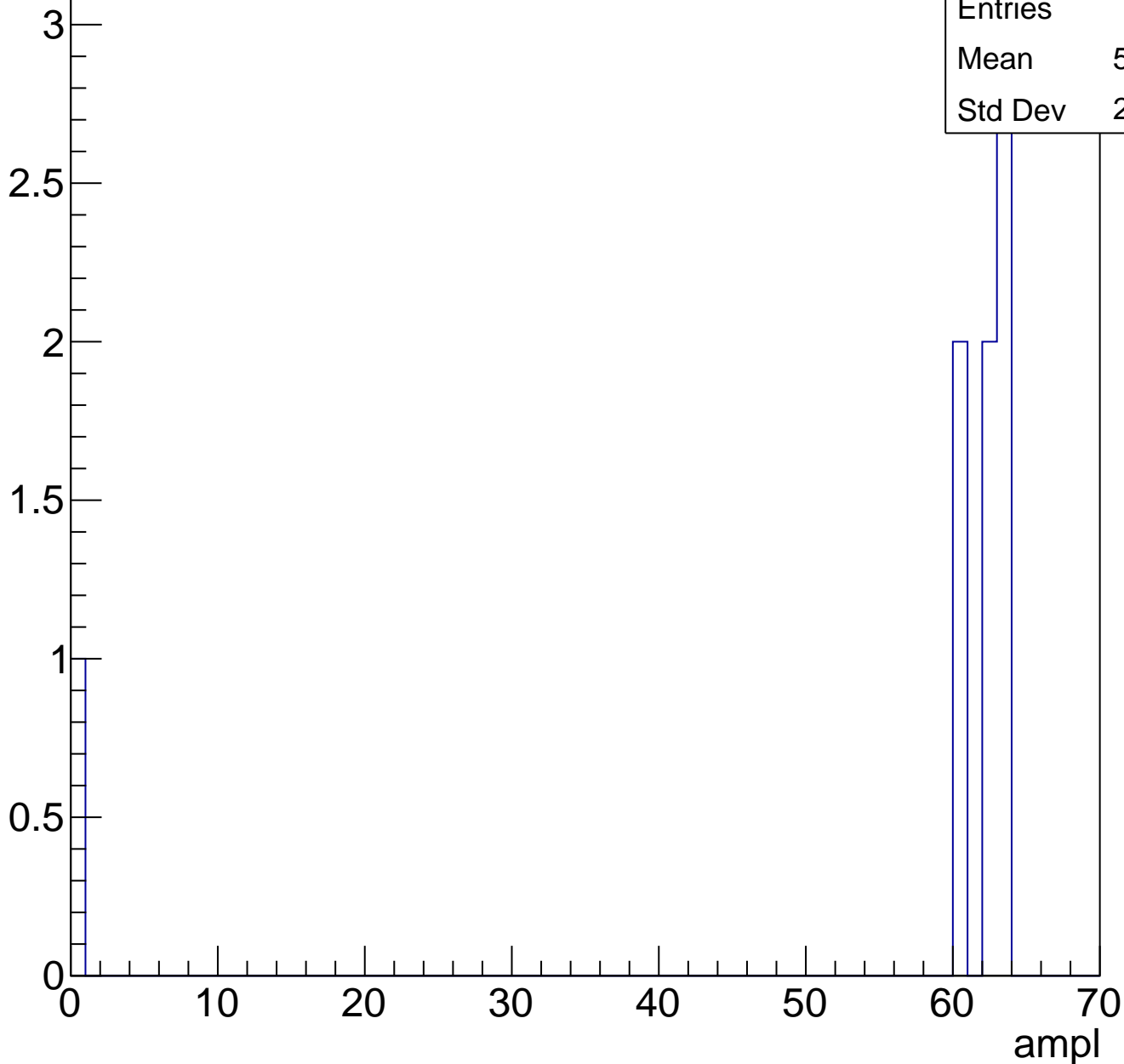
Entry



B0L102S, U5-ch12, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch12, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	20
Std Dev	0

B0L102S, U5-ch13, adc0

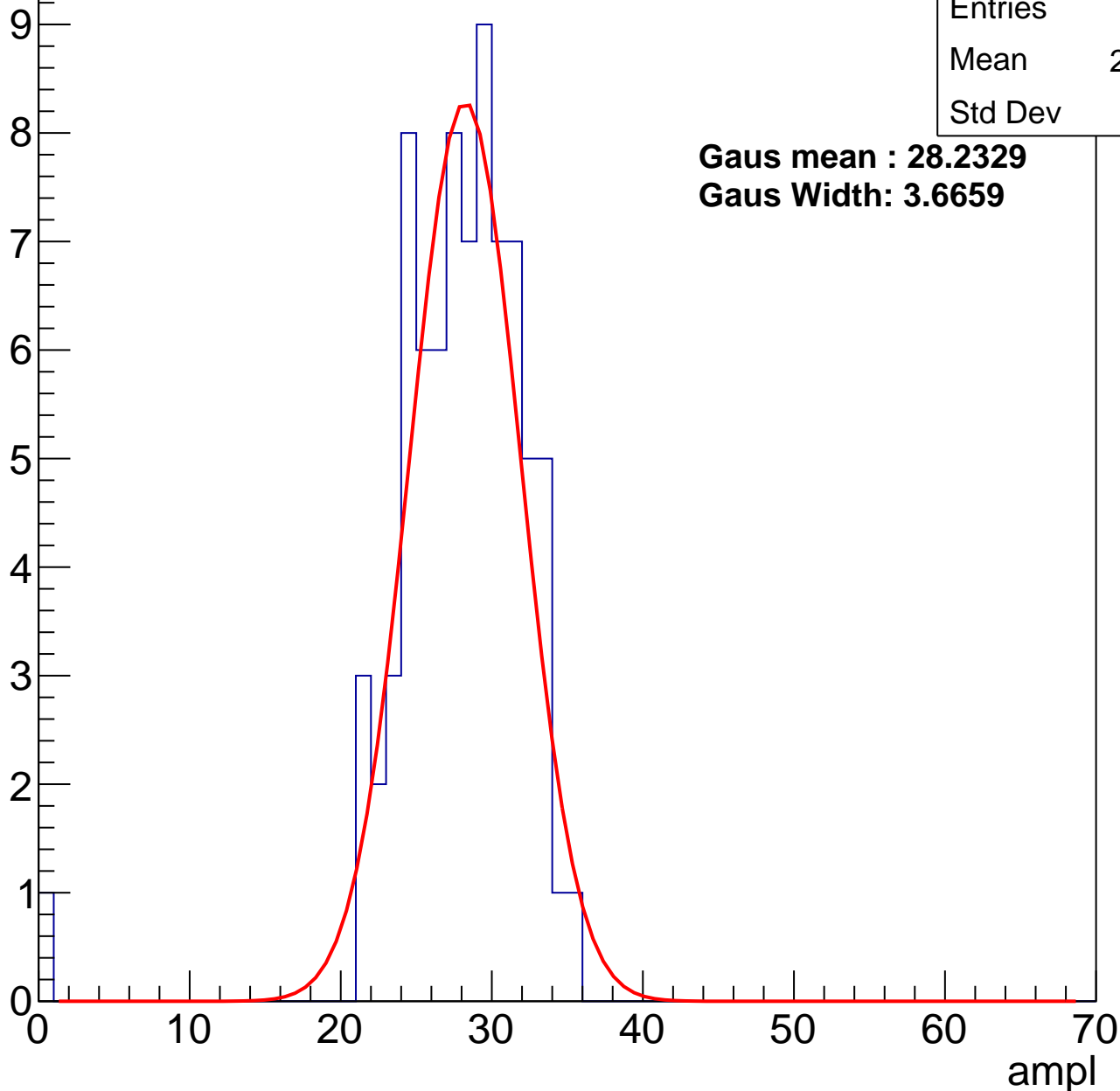
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	27.44
Std Dev	4.58

Gaus mean : 28.2329

Gaus Width: 3.6659



B0L102S, U5-ch13, adc1

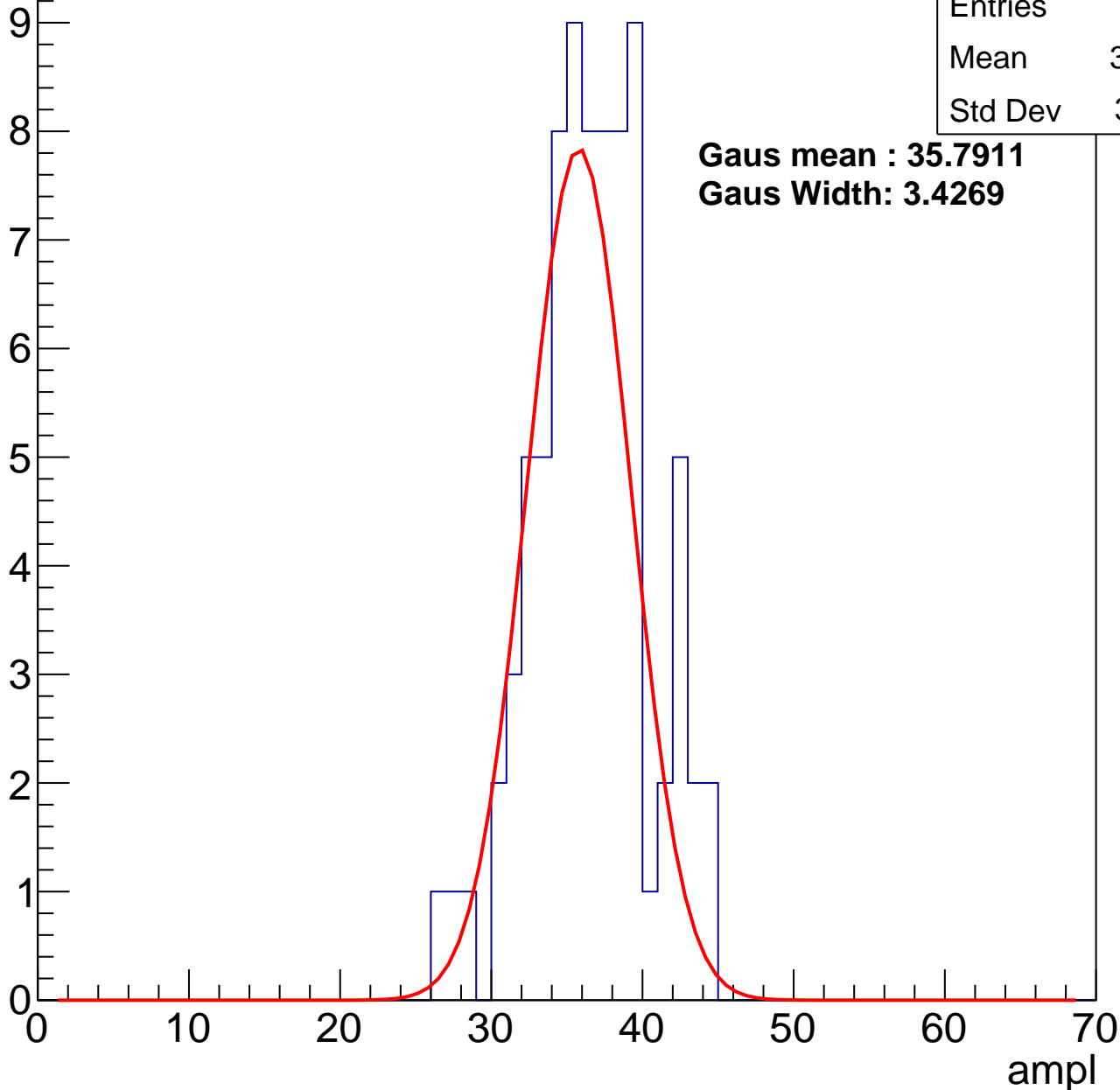
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	36.14
Std Dev	3.791

Gaus mean : 35.7911

Gaus Width: 3.4269



B0L102S, U5-ch13, adc2

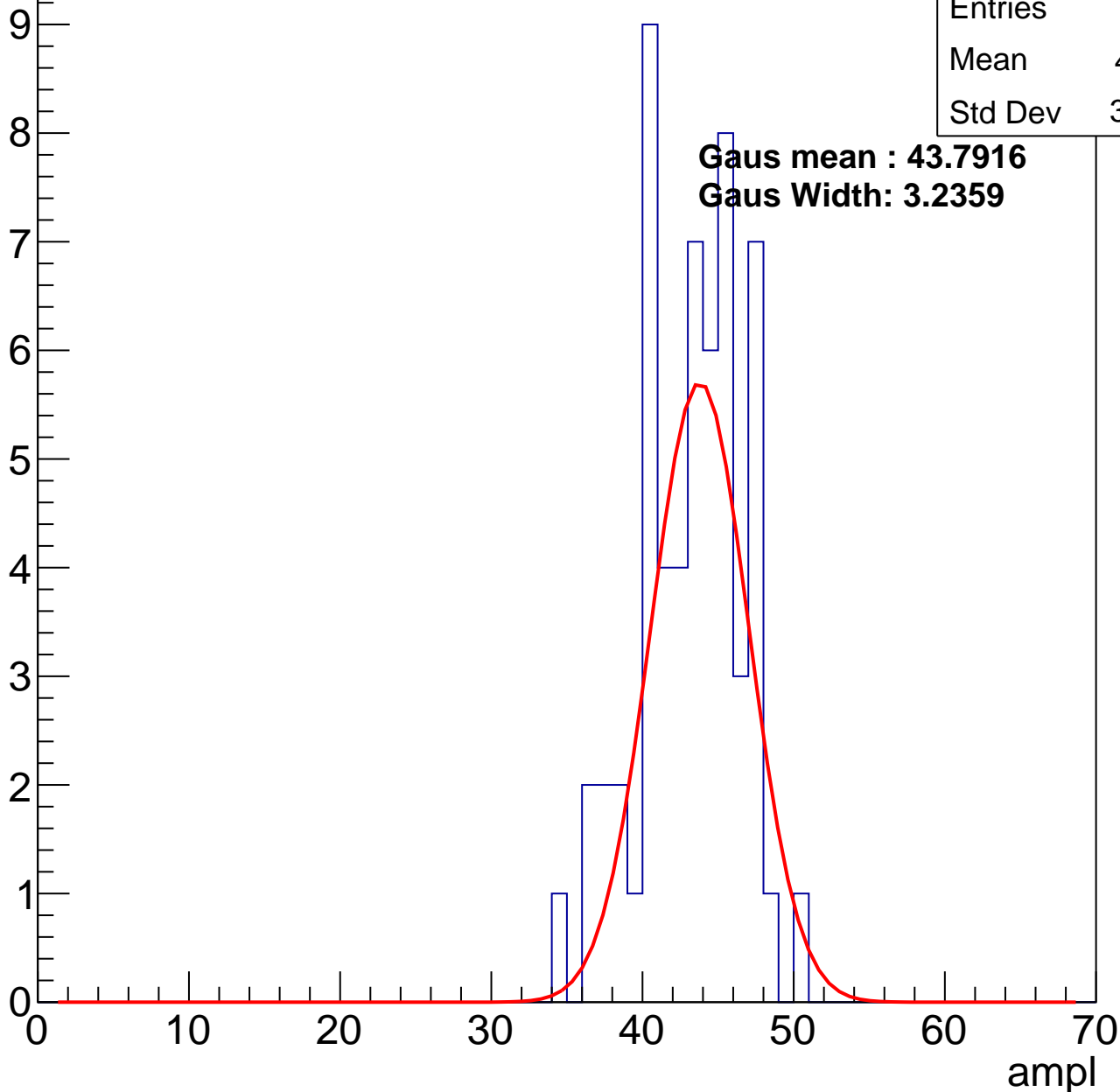
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	42.71
Std Dev	3.384

Gaus mean : 43.7916

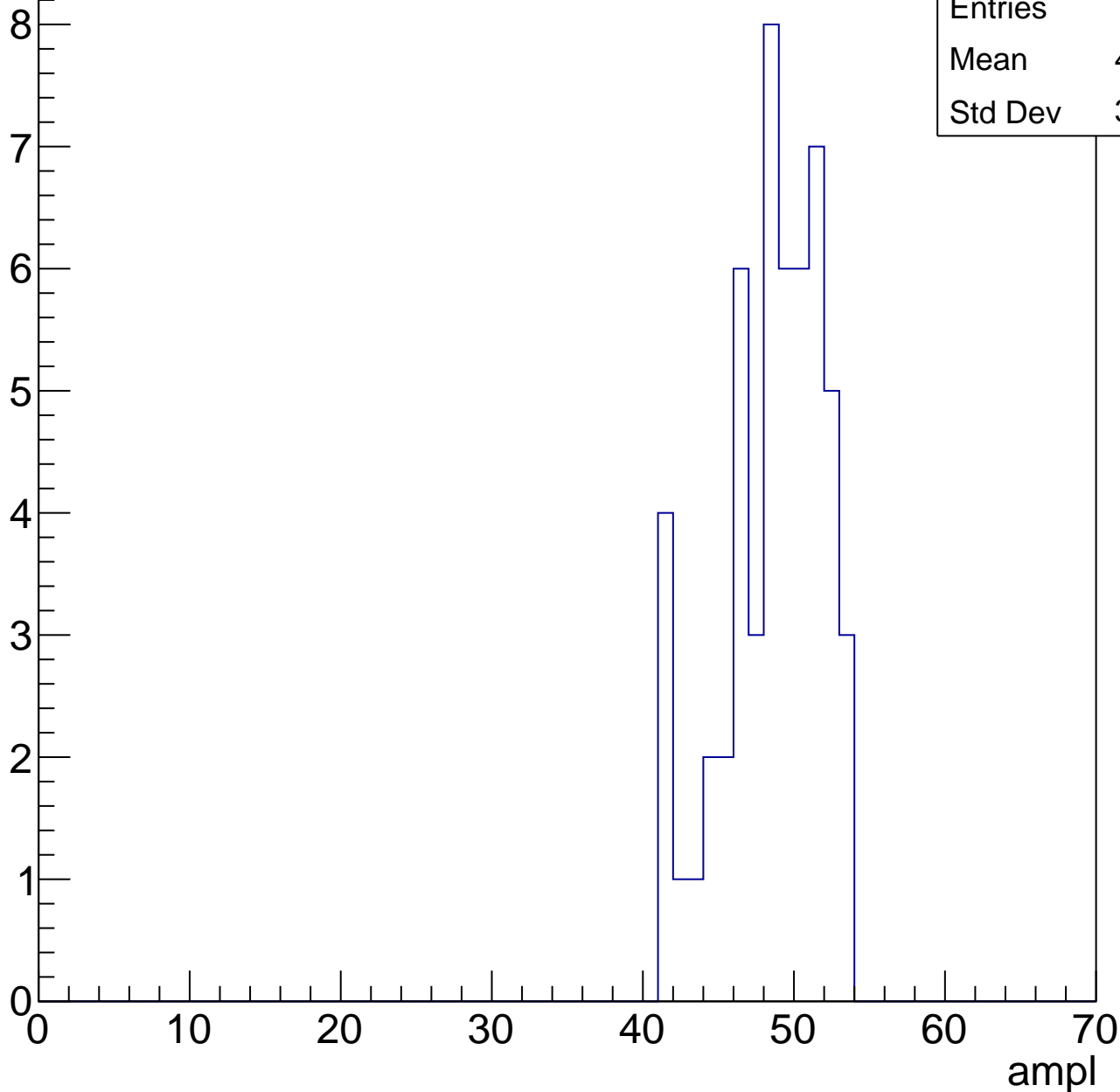
Gaus Width: 3.2359



B0L102S, U5-ch13, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



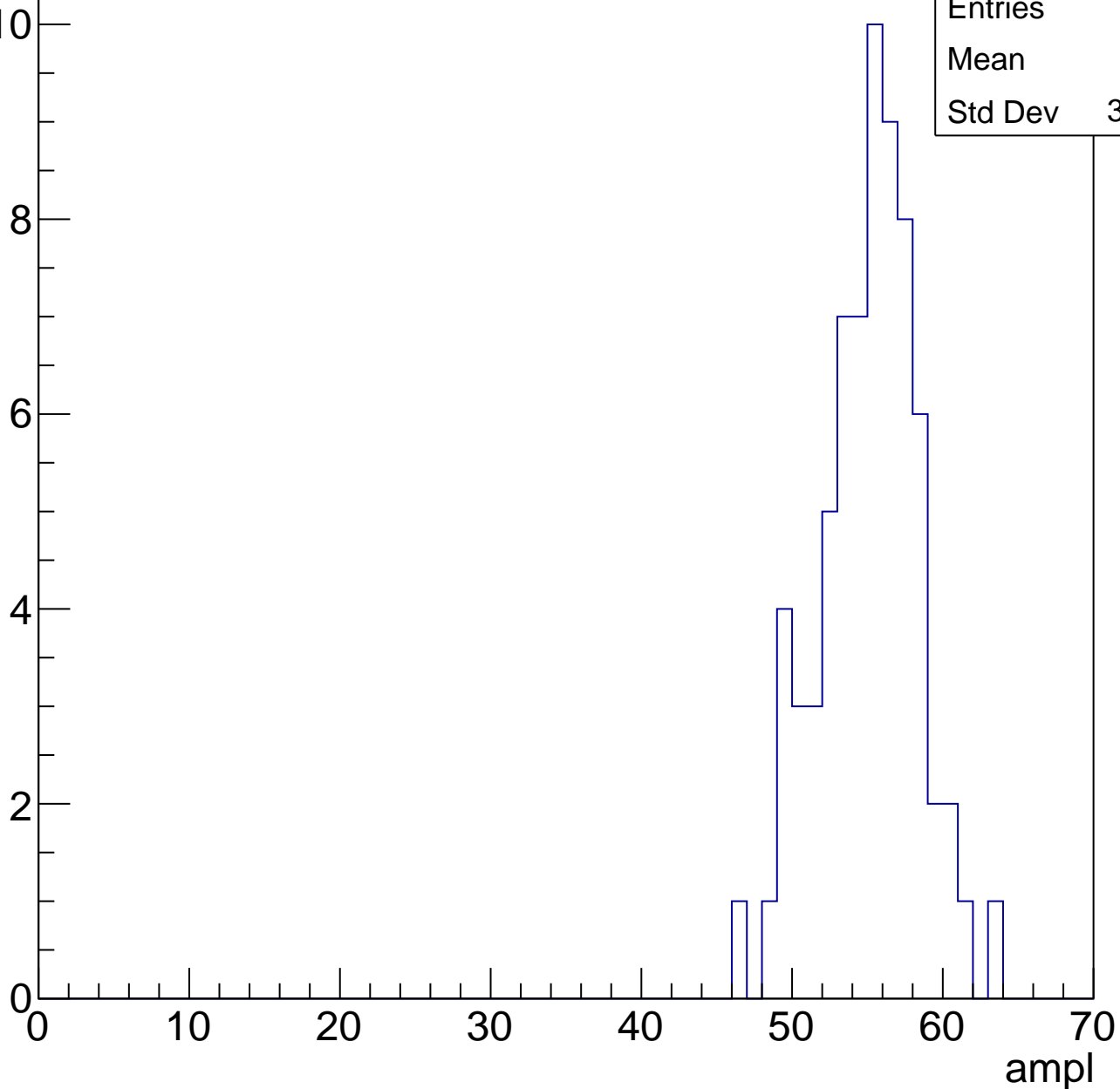
Entries	54
Mean	48.11
Std Dev	3.281

B0L102S, U5-ch13, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

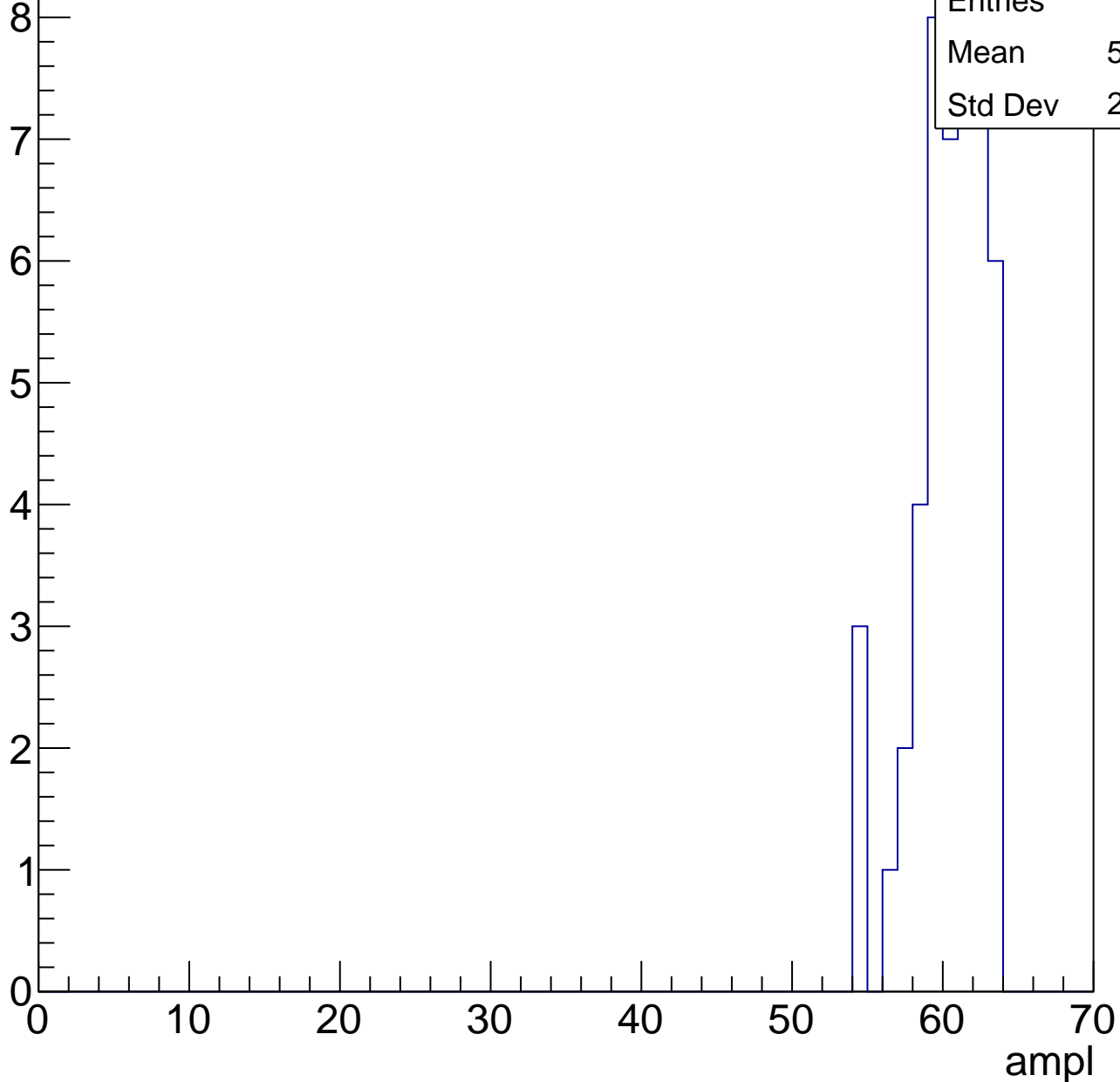
Entries	70
Mean	54.6
Std Dev	3.258



B0L102S, U5-ch13, adc5

calib_packv5_042523_0143.root, FC#12, port B1

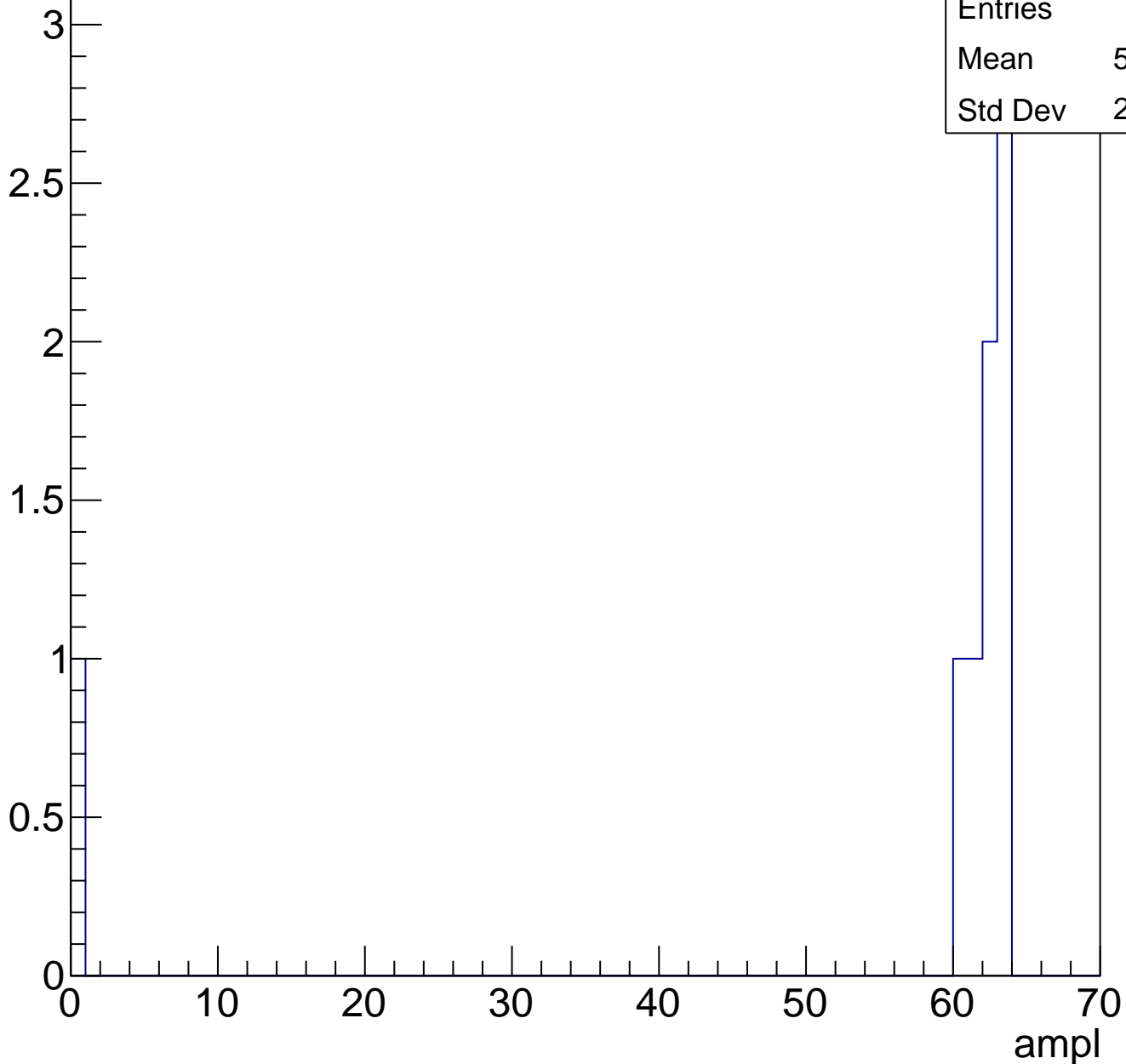
Entry



B0L102S, U5-ch13, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	8
Mean	54.25
Std Dev	20.53

B0L102S, U5-ch13, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch14, adc0

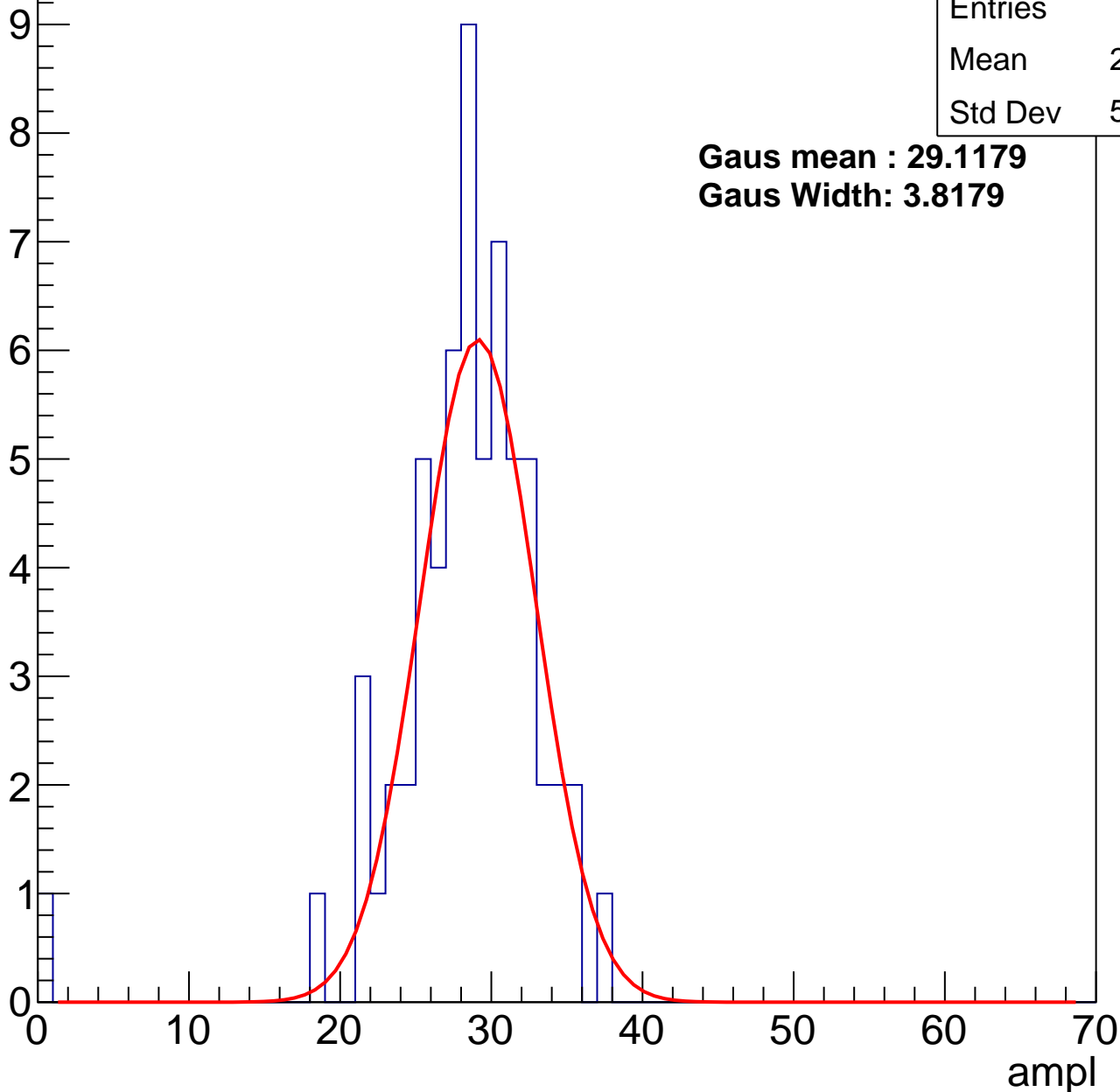
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	27.79
Std Dev	5.152

Gaus mean : 29.1179

Gaus Width: 3.8179



B0L102S, U5-ch14, adc1

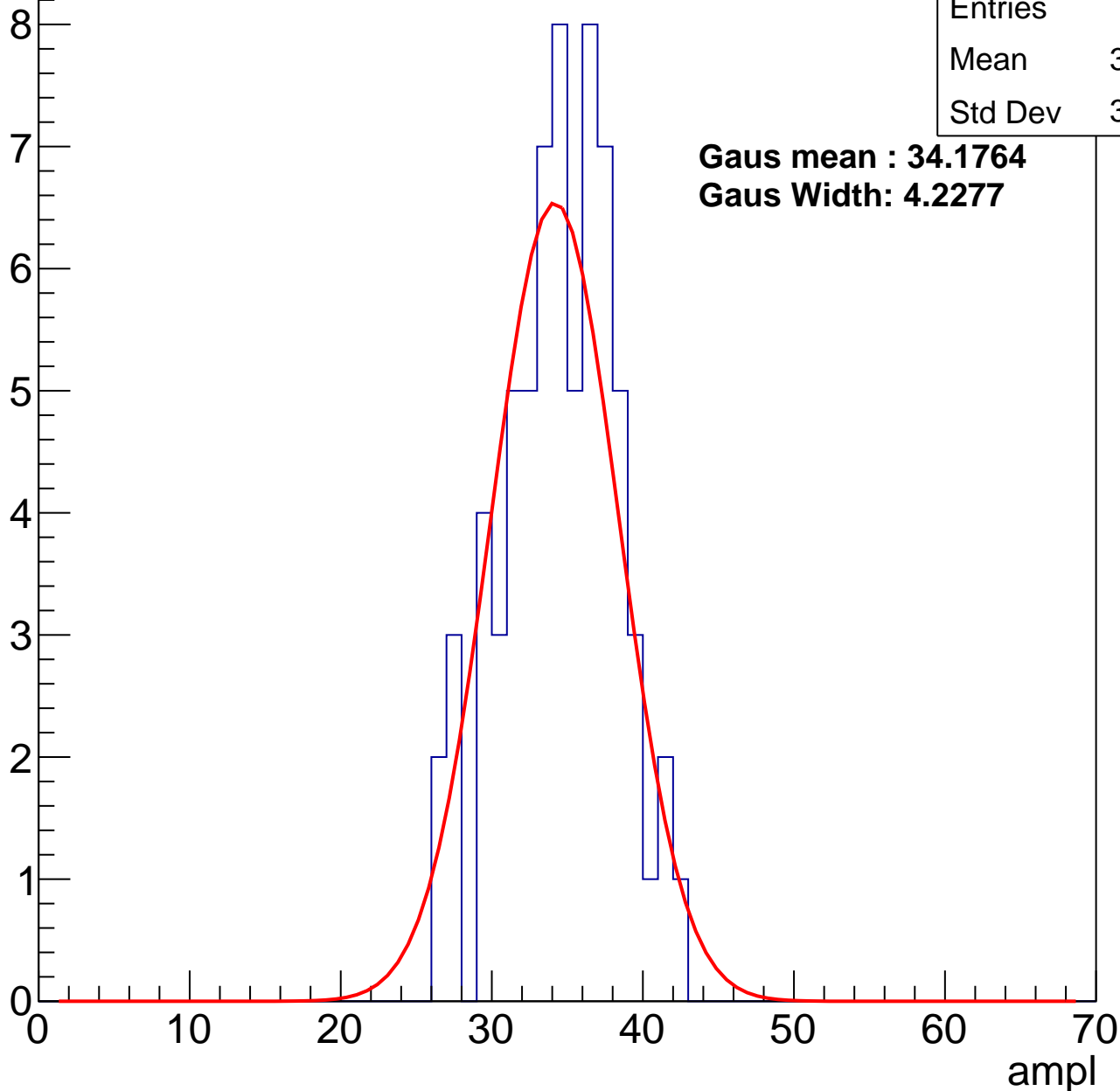
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	34.06
Std Dev	3.706

Gaus mean : 34.1764

Gaus Width: 4.2277



B0L102S, U5-ch14, adc2

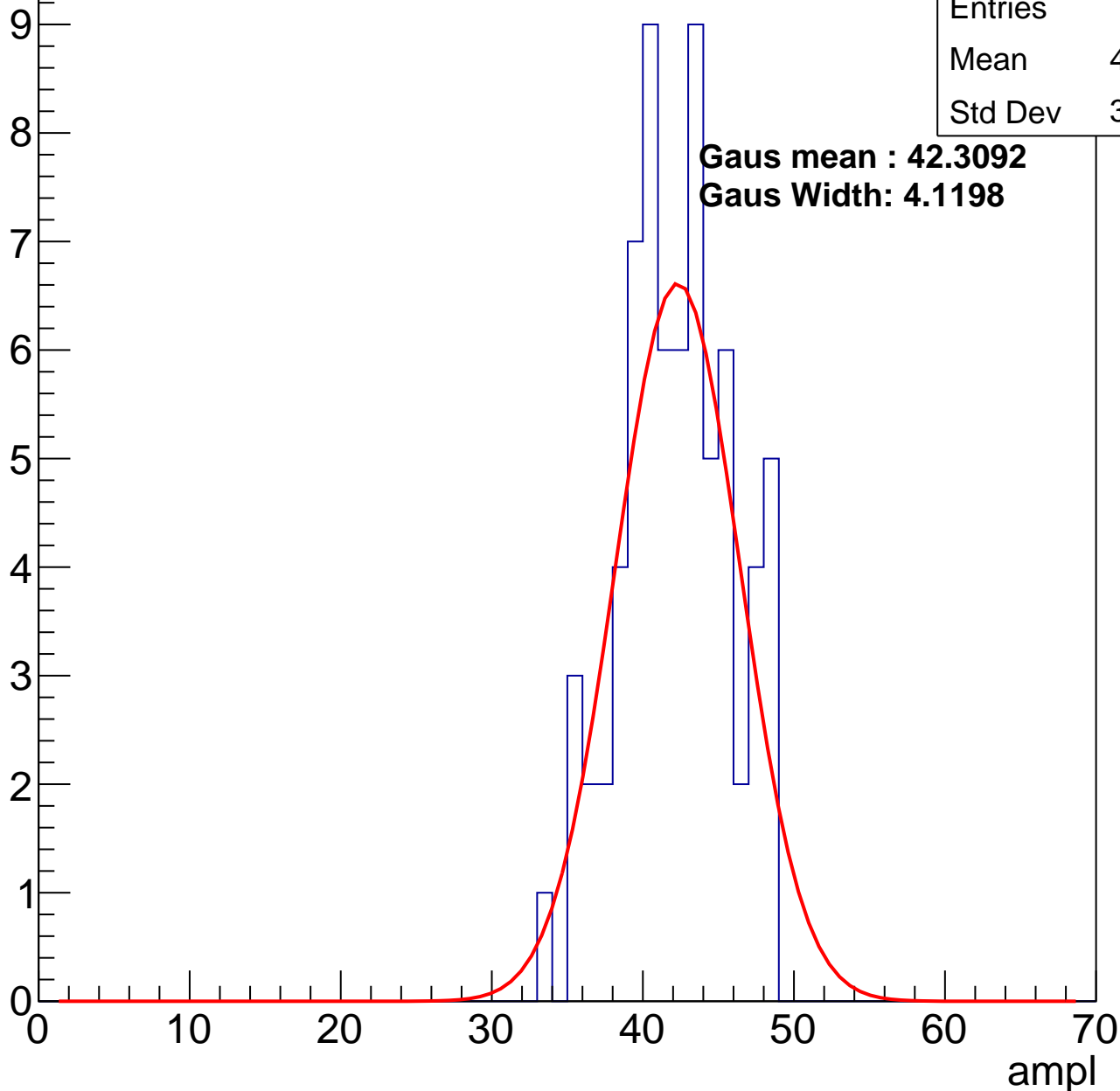
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.75
Std Dev	3.579

Gaus mean : 42.3092

Gaus Width: 4.1198

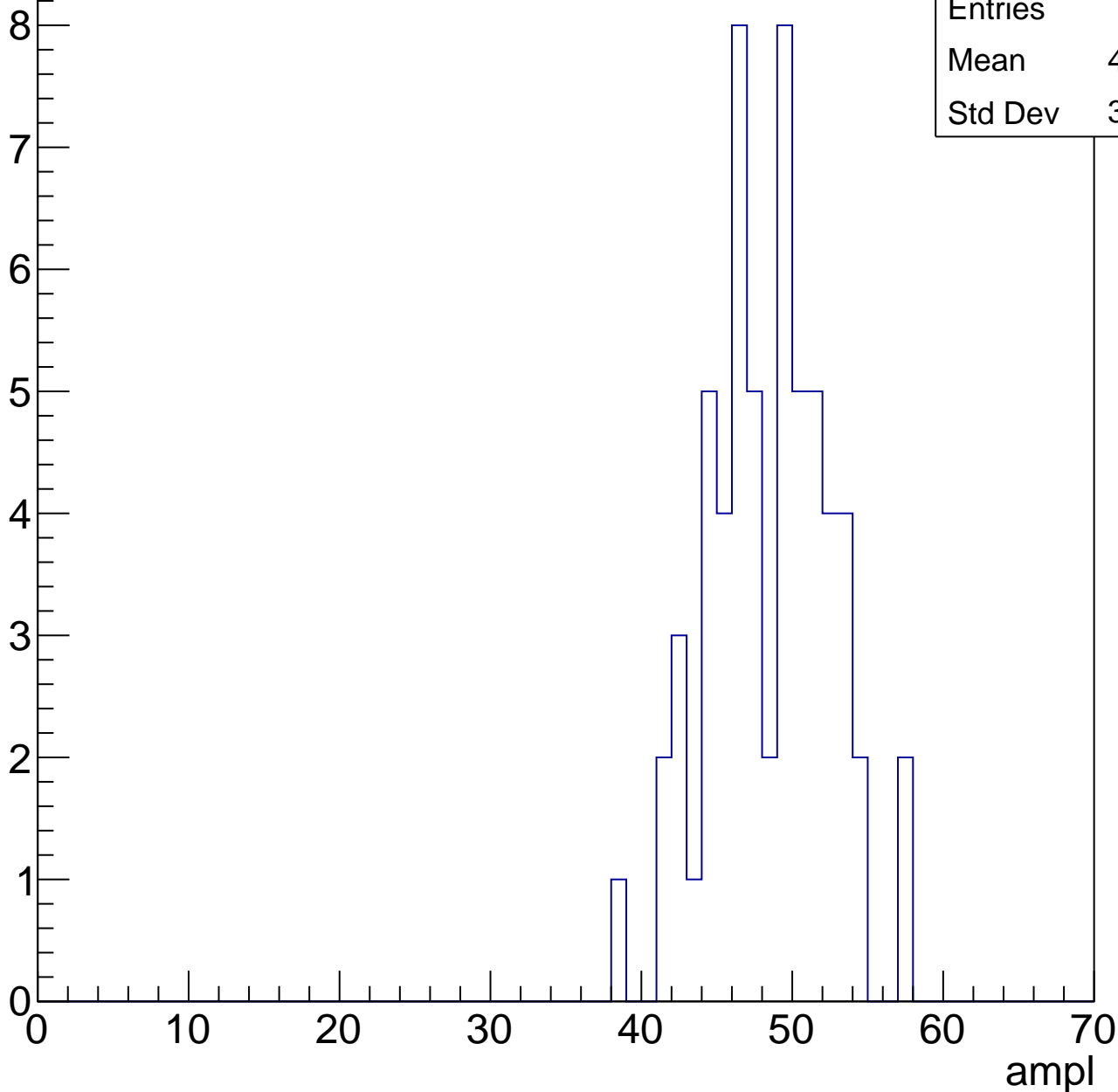


B0L102S, U5-ch14, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	47.98
Std Dev	3.957

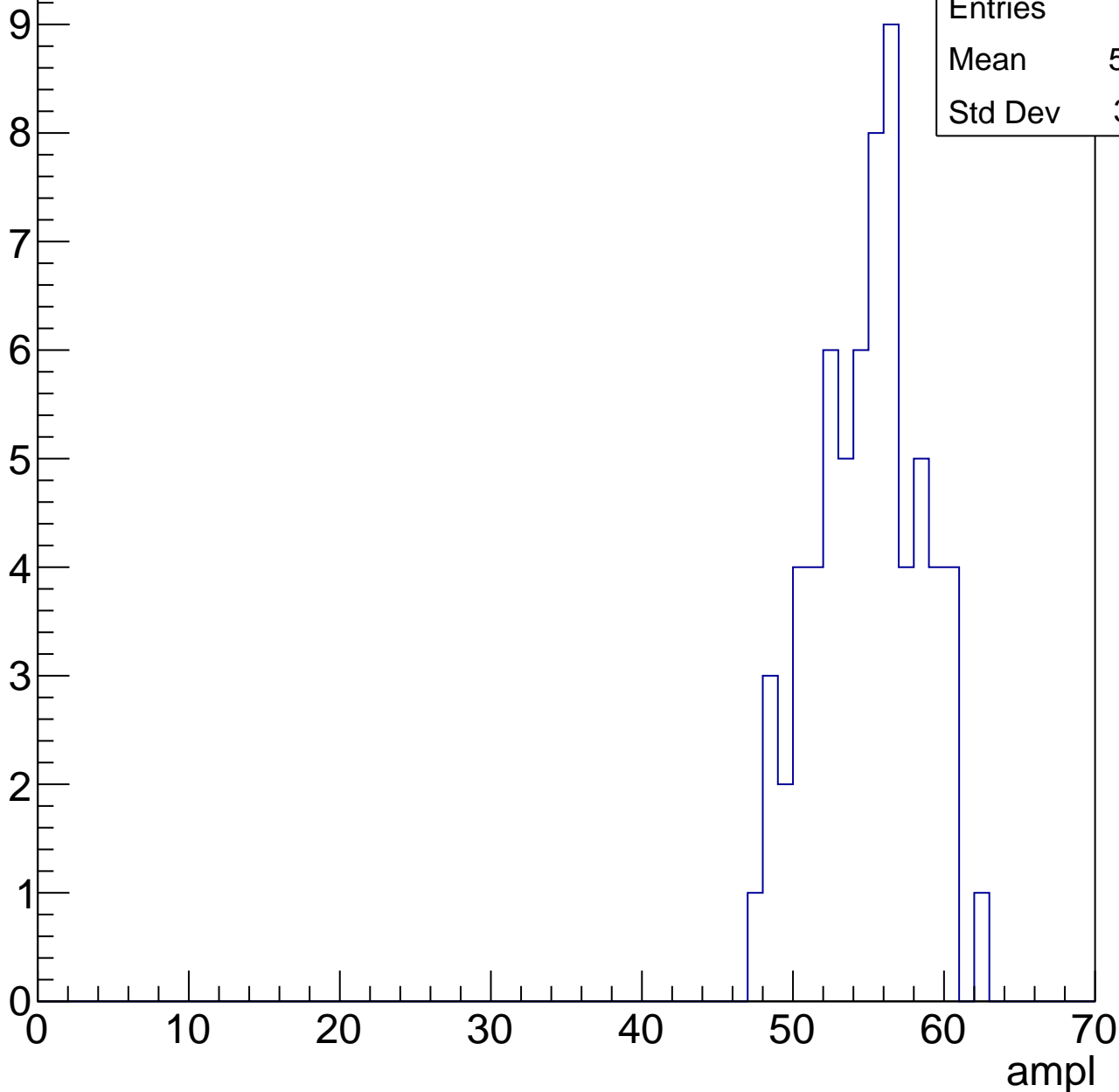


B0L102S, U5-ch14, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	54.45
Std Dev	3.461

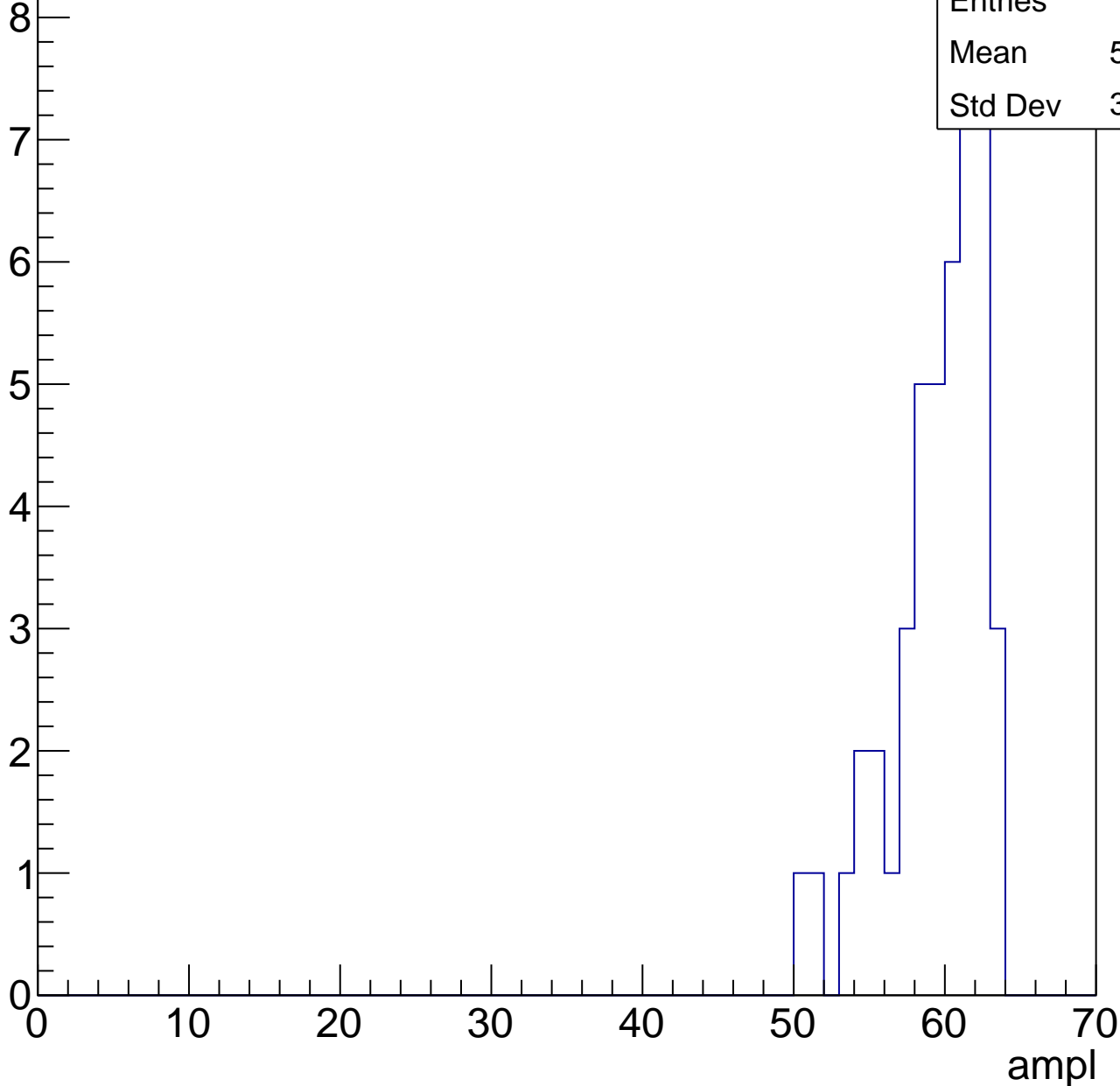


B0L102S, U5-ch14, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	59.07
Std Dev	3.117

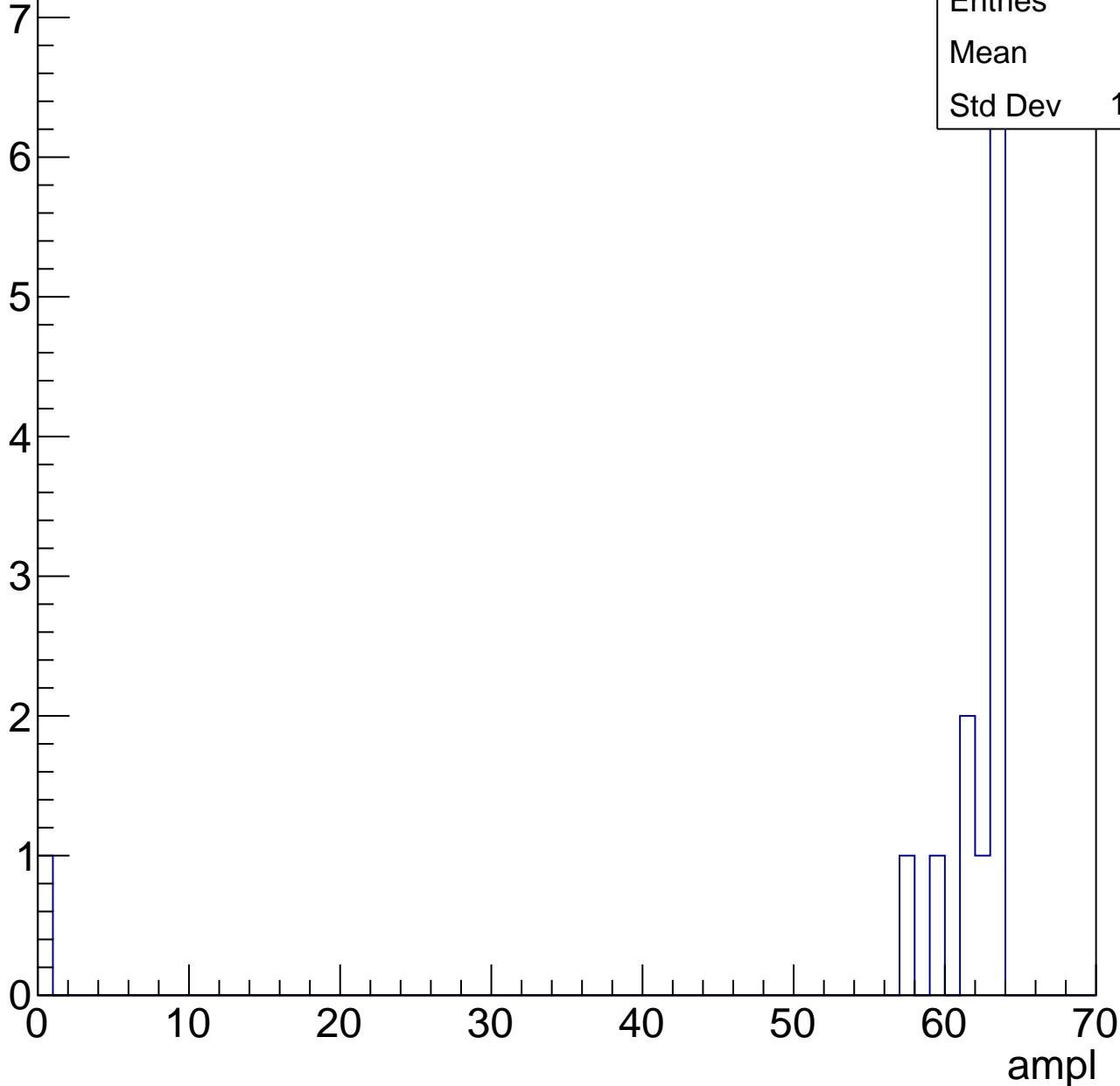


B0L102S, U5-ch14, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	13
Mean	57
Std Dev	16.55



B0L102S, U5-ch14, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch15, adc0

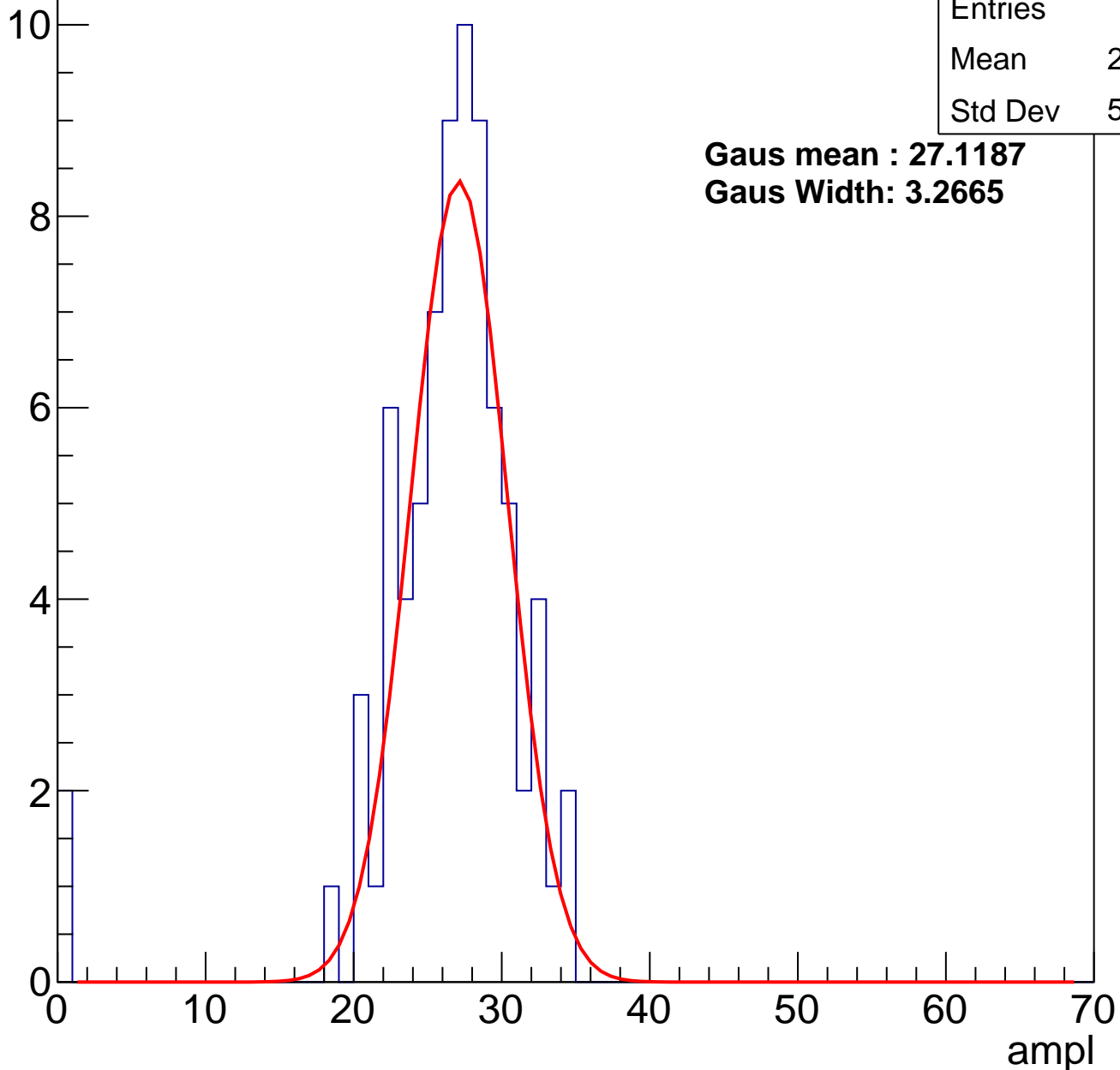
calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	25.83
Std Dev	5.414

Gaus mean : 27.1187

Gaus Width: 3.2665

Entry



B0L102S, U5-ch15, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	84
Mean	34.9
Std Dev	3.538

Gaus mean : 34.5513

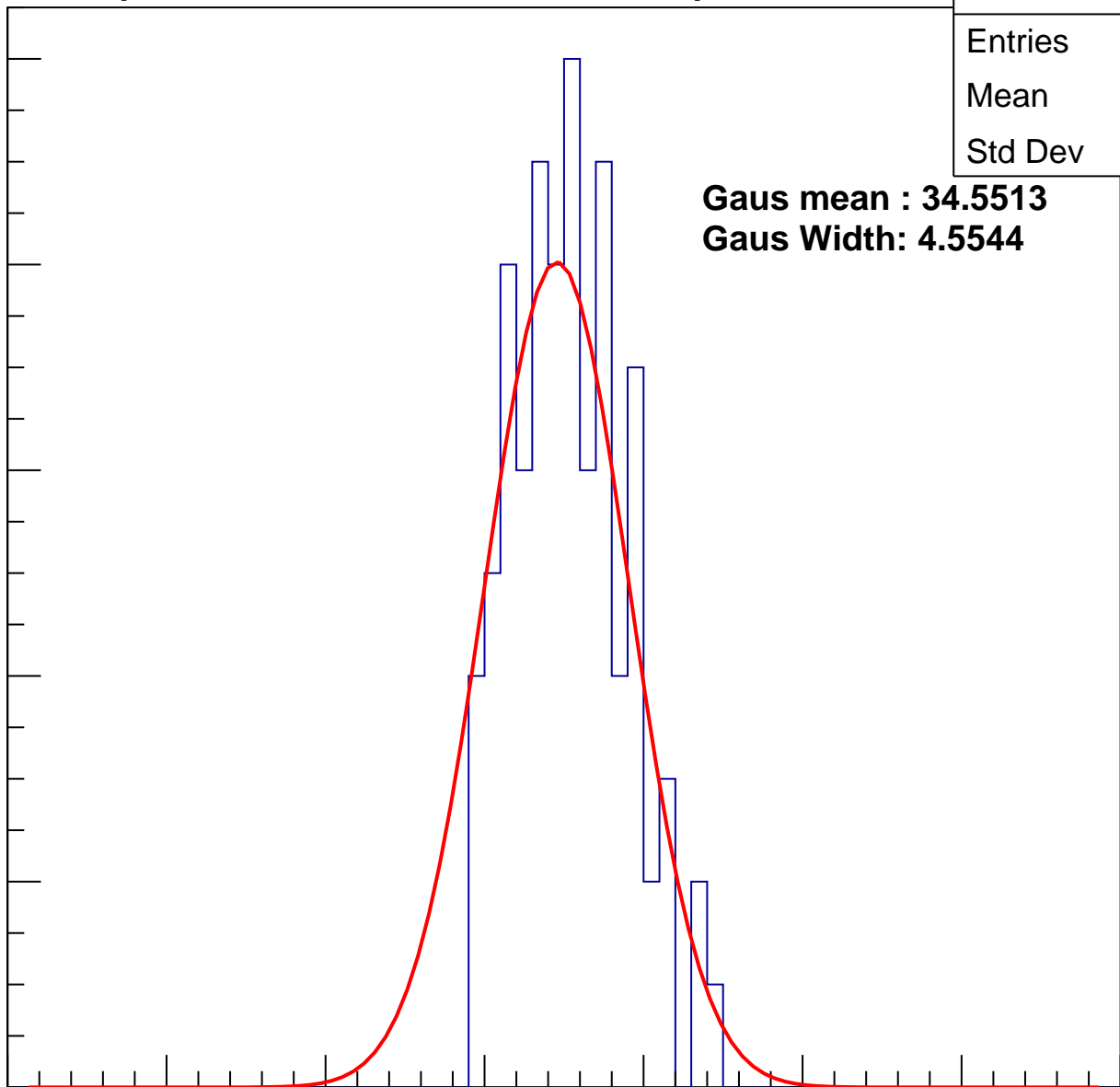
Gaus Width: 4.5544

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

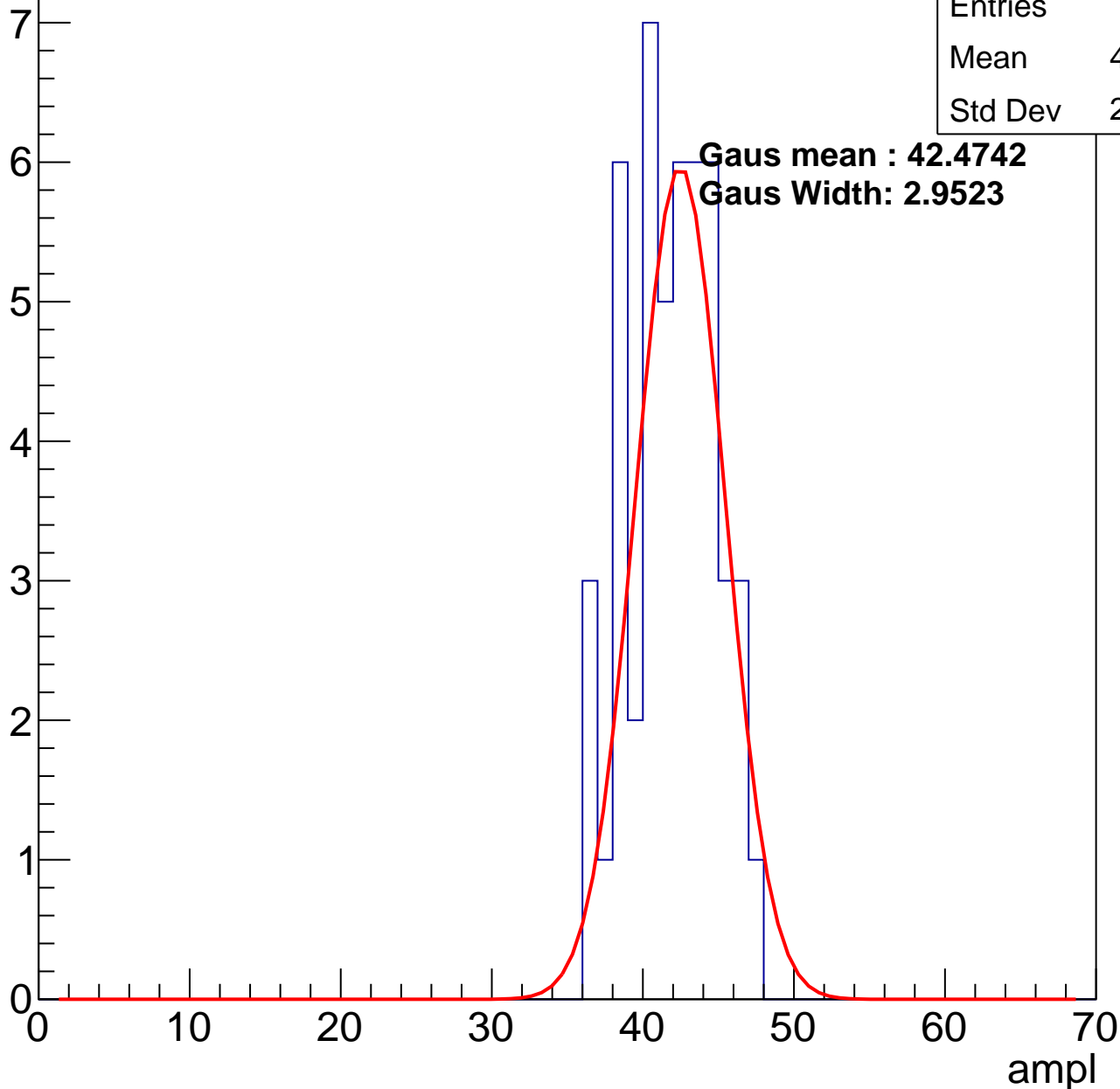


B0L102S, U5-ch15, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	41.43
Std Dev	2.836

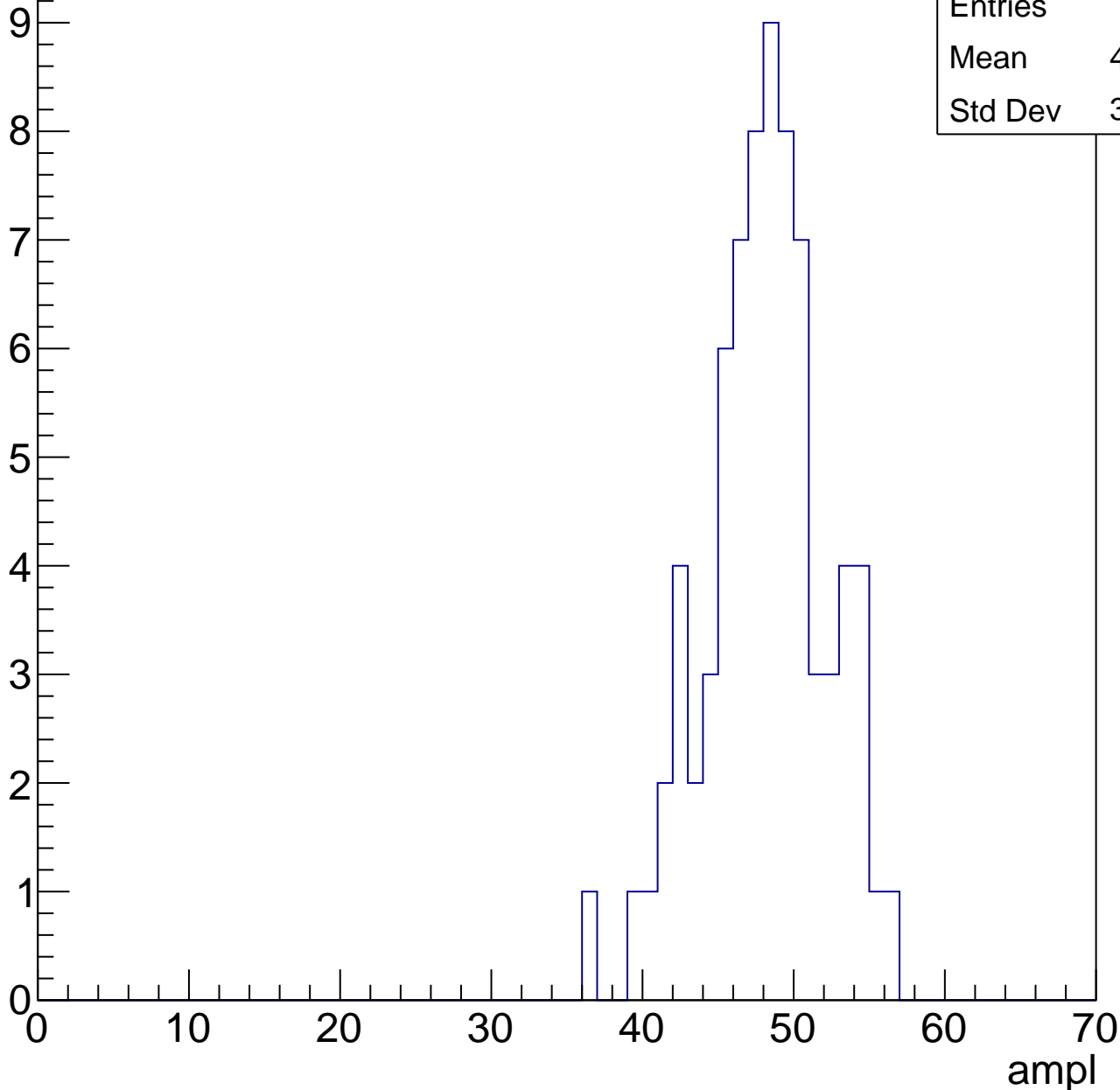


B0L102S, U5-ch15, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	47.64
Std Dev	3.962

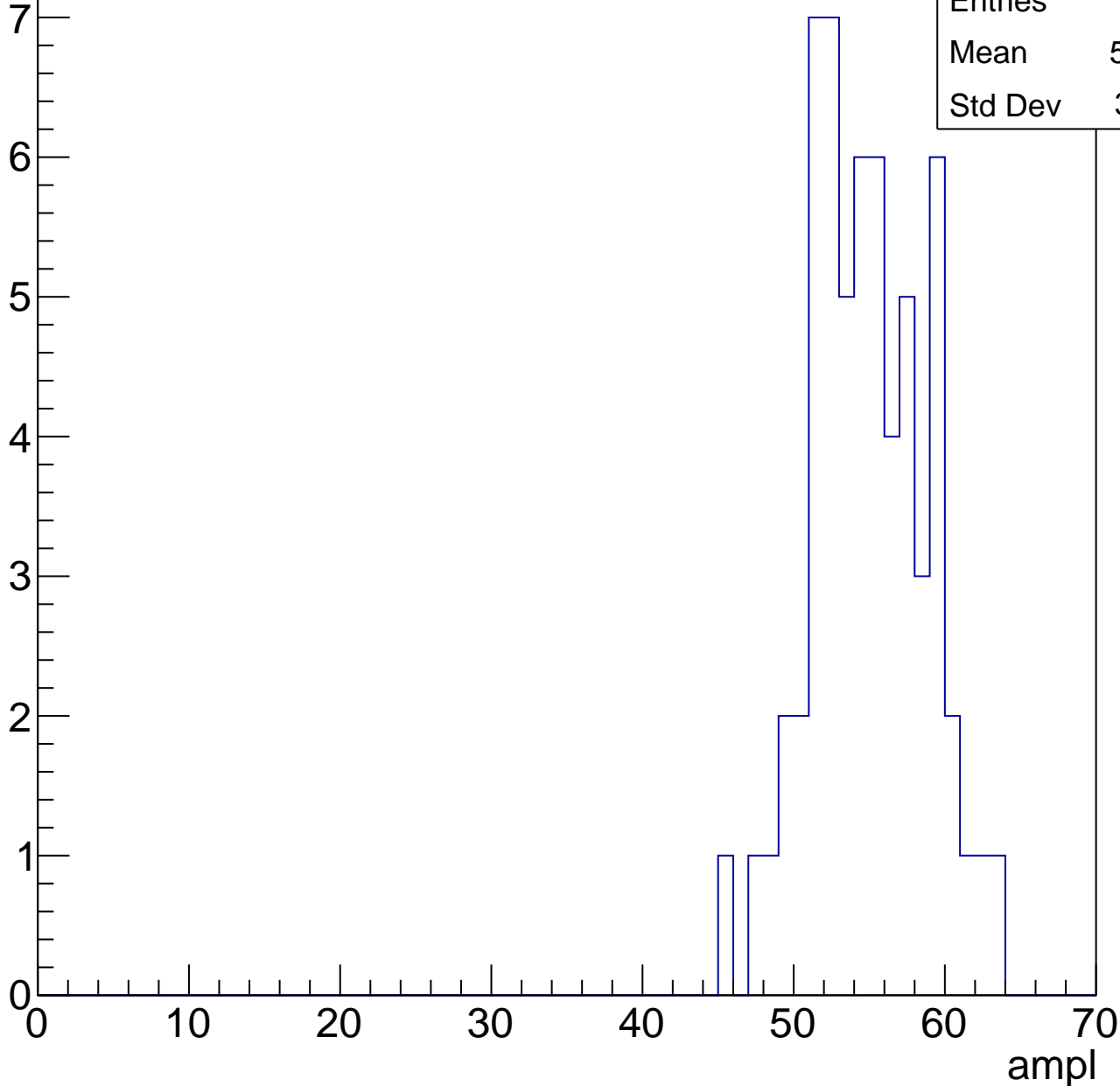


B0L102S, U5-ch15, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	54.44
Std Dev	3.761

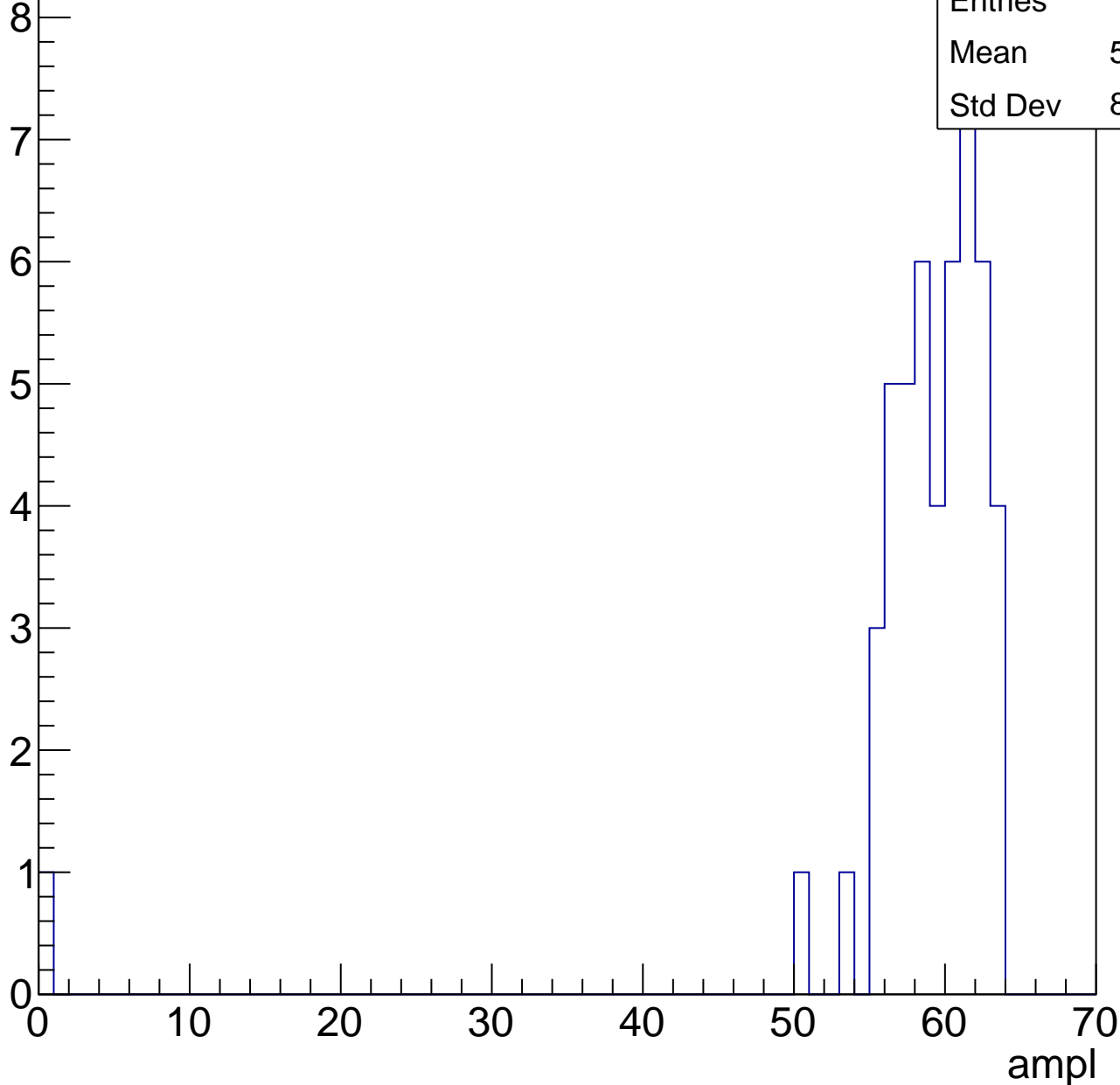


B0L102S, U5-ch15, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

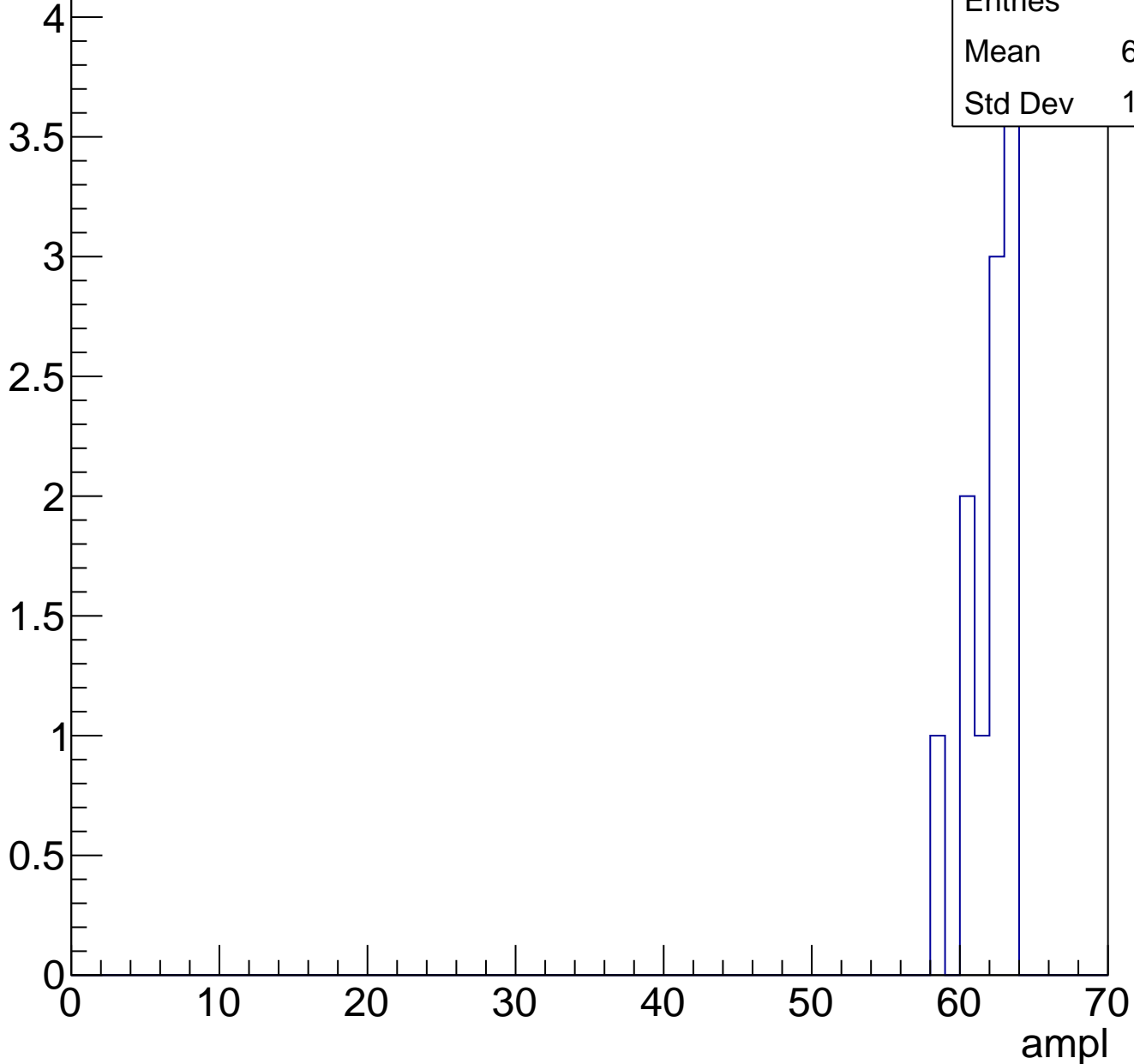
Entries	50
Mean	57.78
Std Dev	8.716



B0L102S, U5-ch15, adc6

calib_packv5_042523_0143.root, FC#12, port B1

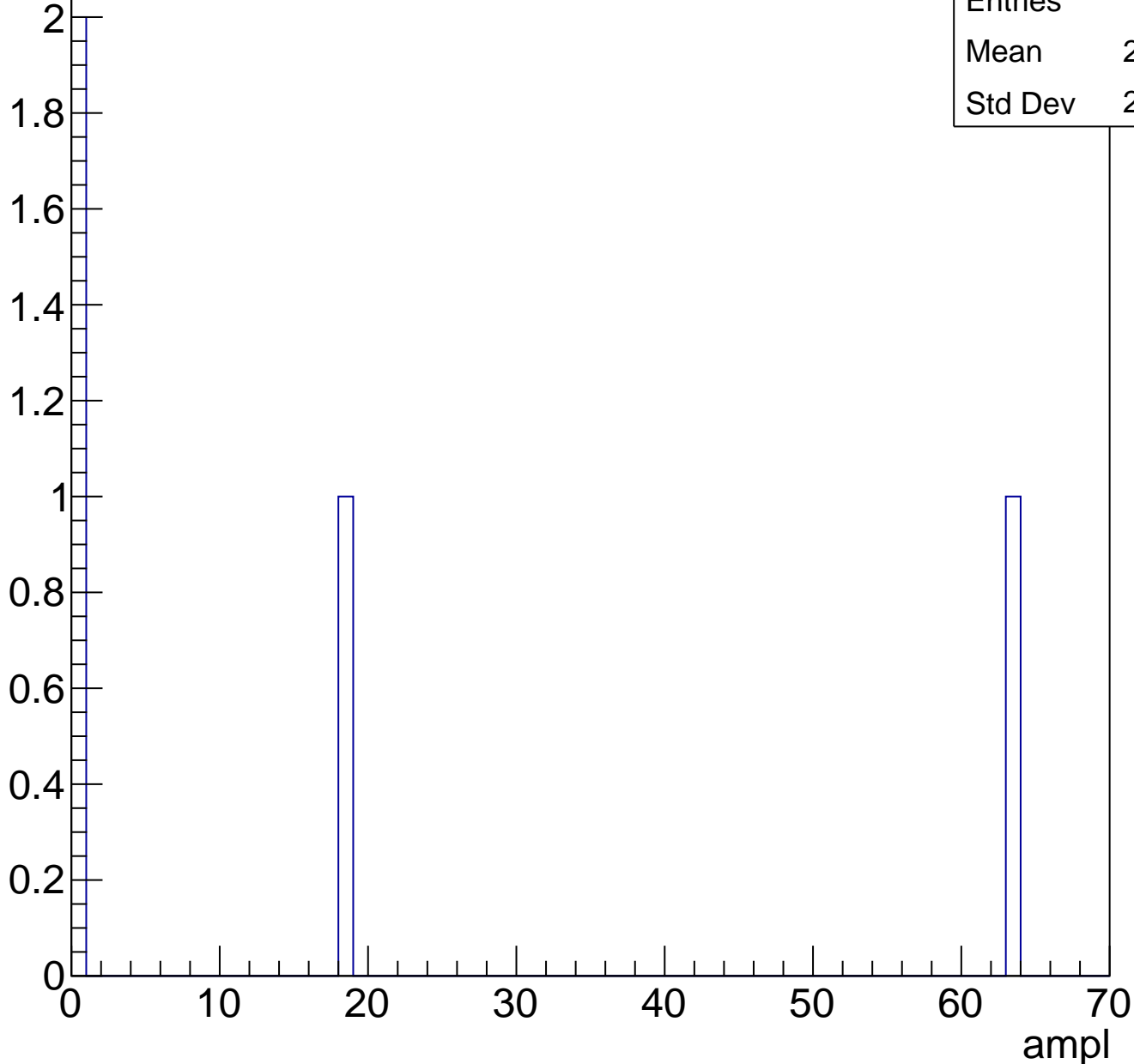
Entry



B0L102S, U5-ch15, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	4
Mean	20.25
Std Dev	25.75

B0L102S, U5-ch16, adc0

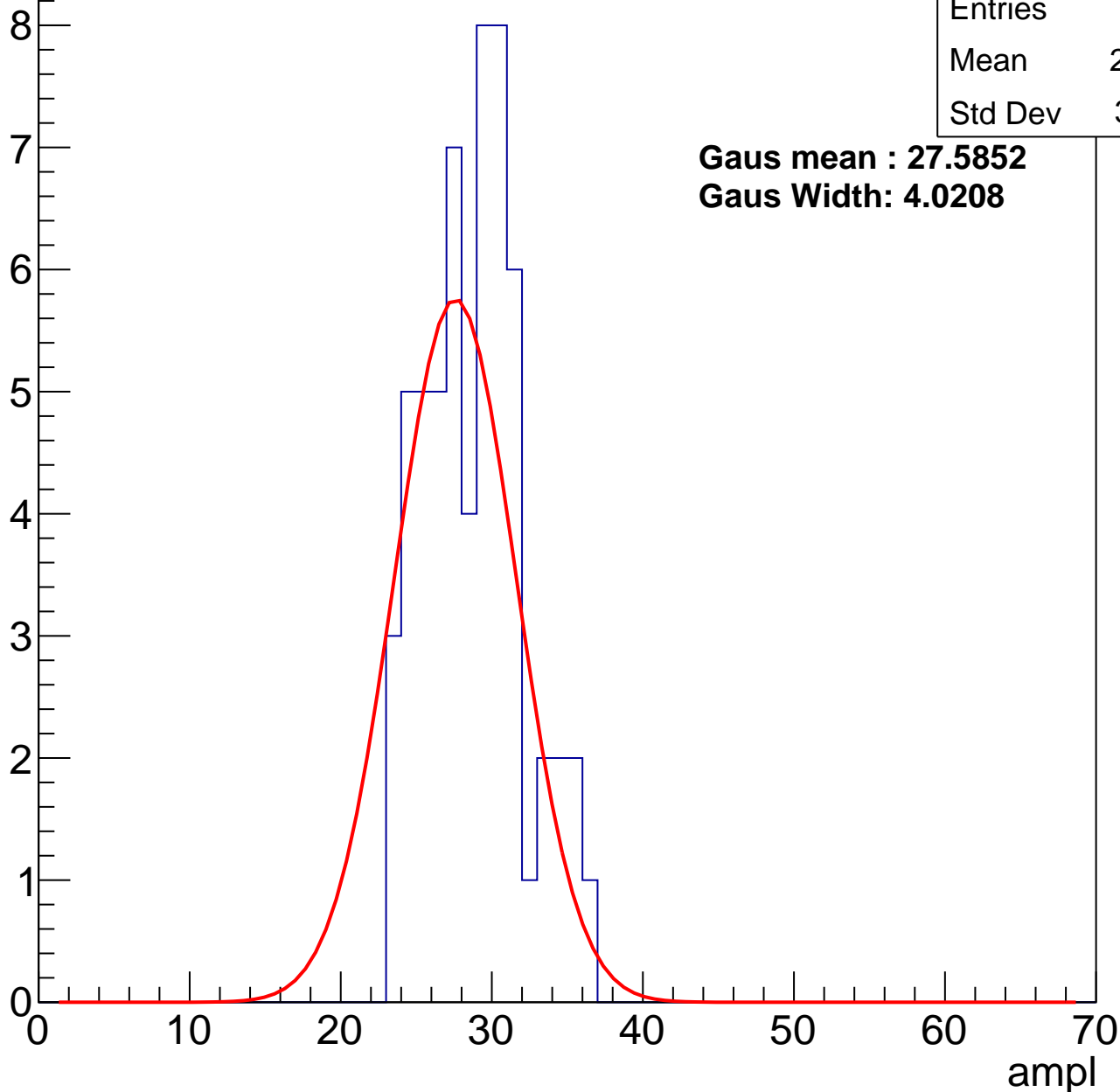
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	28.39
Std Dev	3.221

Gaus mean : 27.5852

Gaus Width: 4.0208



B0L102S, U5-ch16, adc1

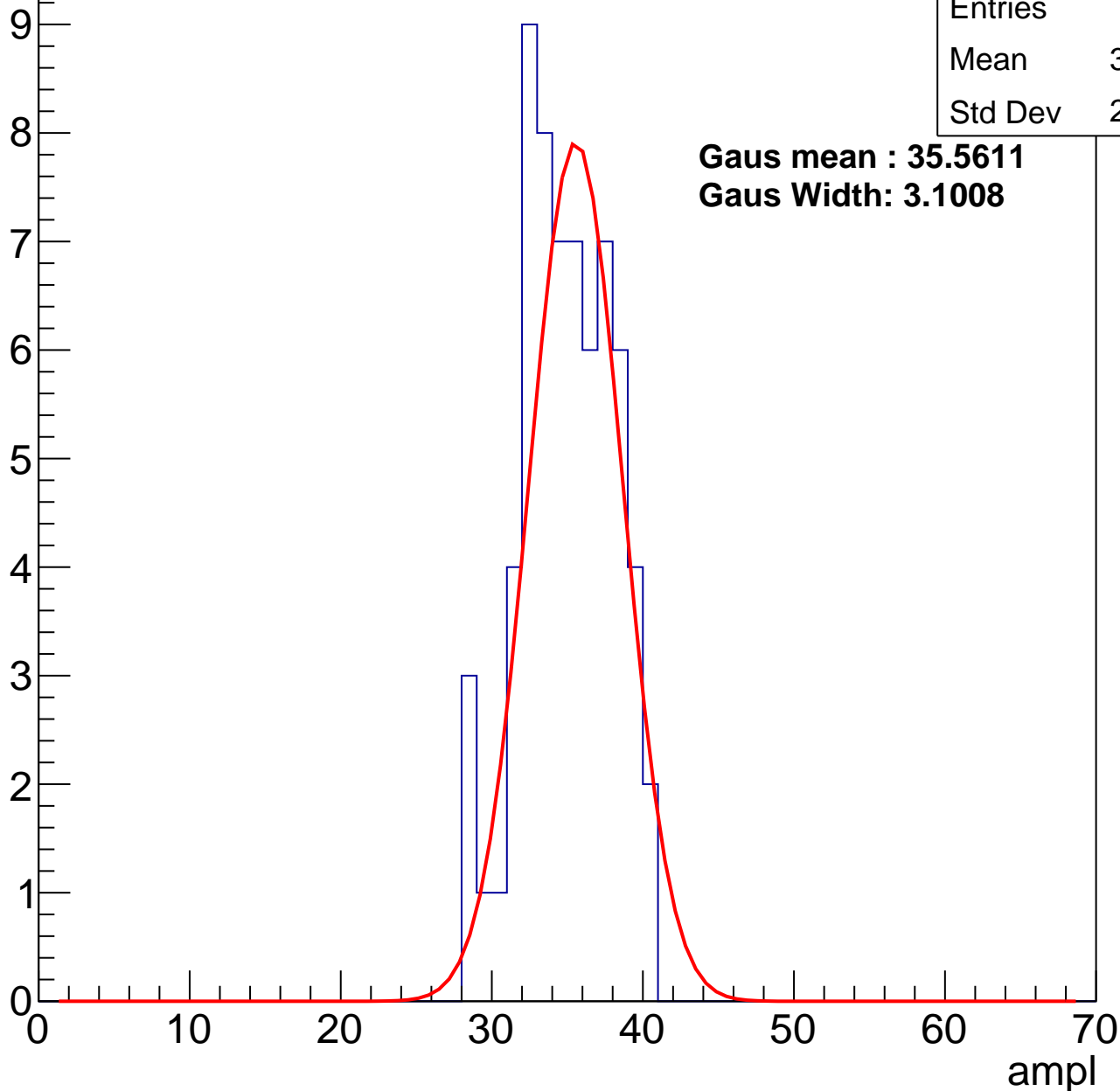
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	34.48
Std Dev	2.977

Gaus mean : 35.5611

Gaus Width: 3.1008



B0L102S, U5-ch16, adc2

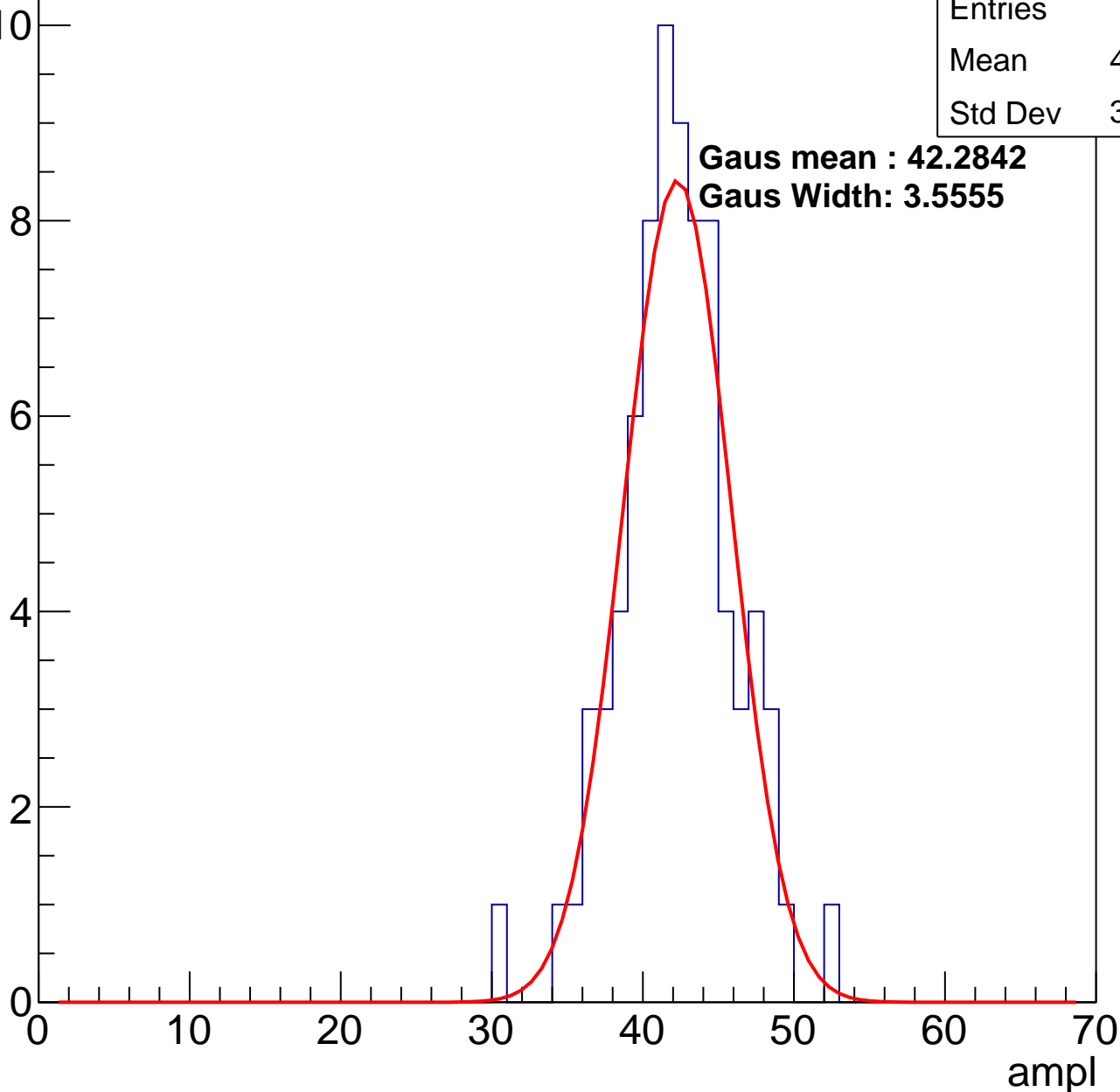
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	41.78
Std Dev	3.716

Gaus mean : 42.2842

Gaus Width: 3.5555

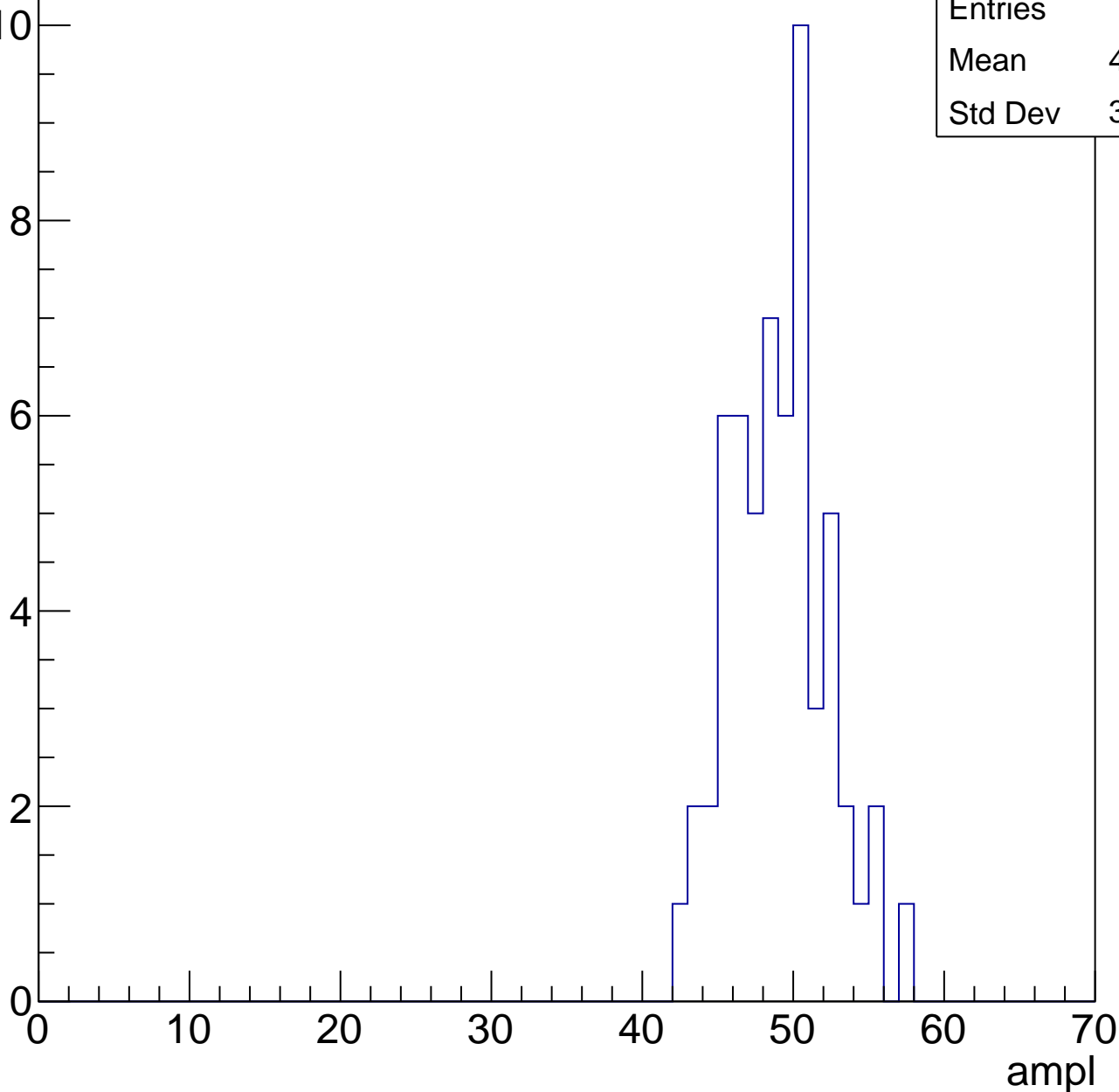


B0L102S, U5-ch16, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	48.59
Std Dev	3.184



B0L102S, U5-ch16, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

Entries 72

Mean 54.94

Std Dev 3.32

8

6

4

2

0

0

10

20

30

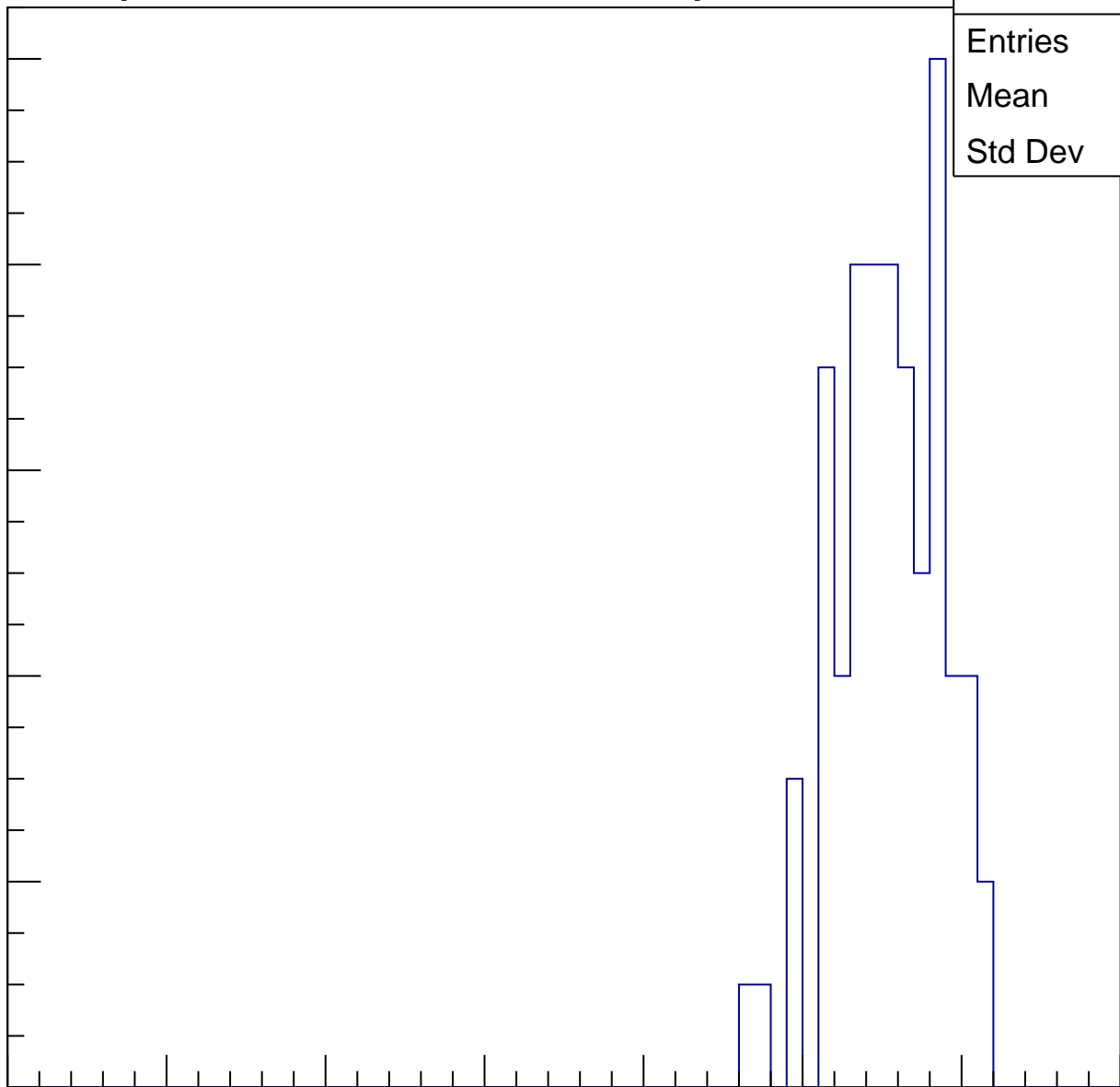
40

50

60

70

ampl

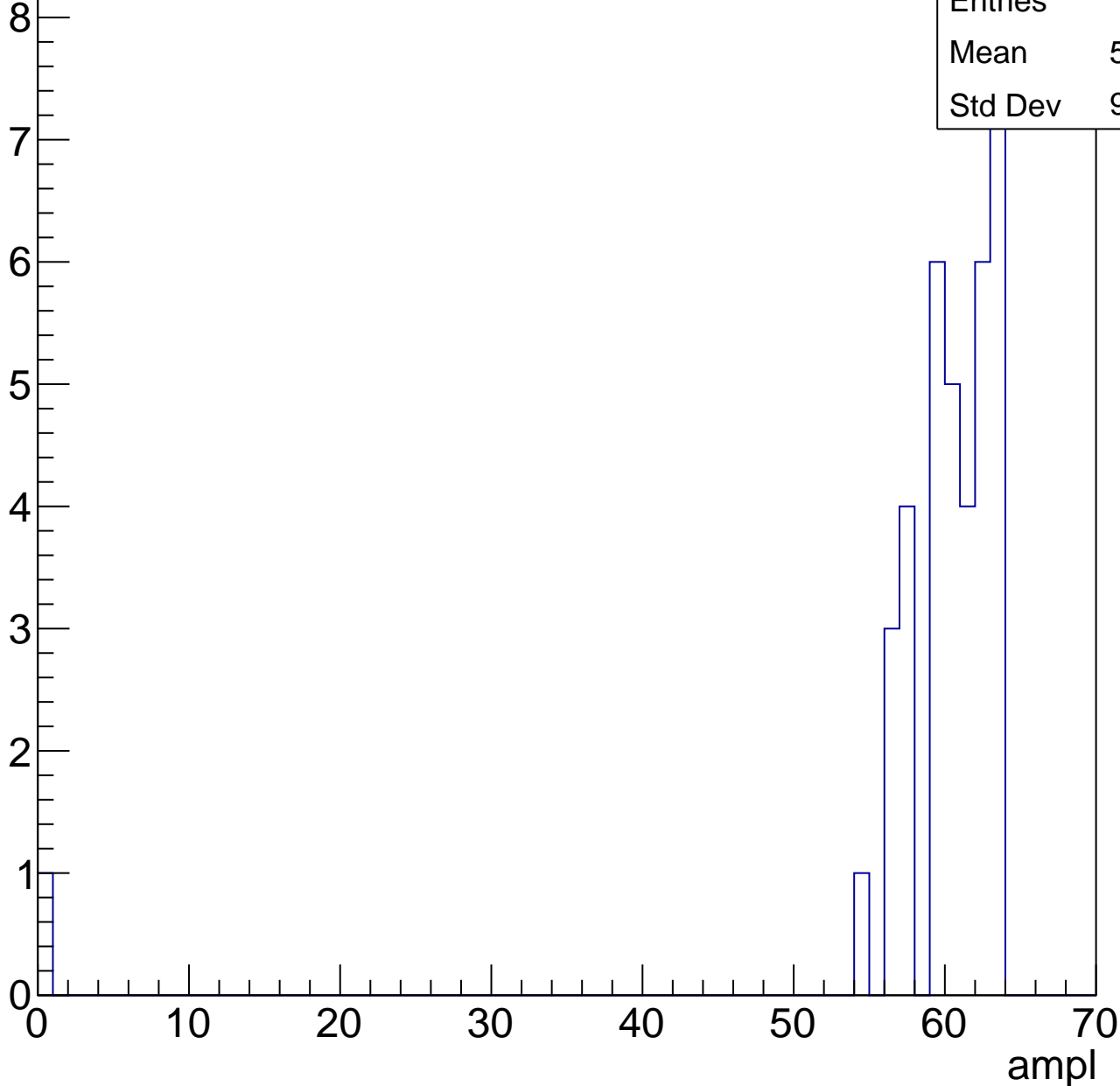


B0L102S, U5-ch16, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

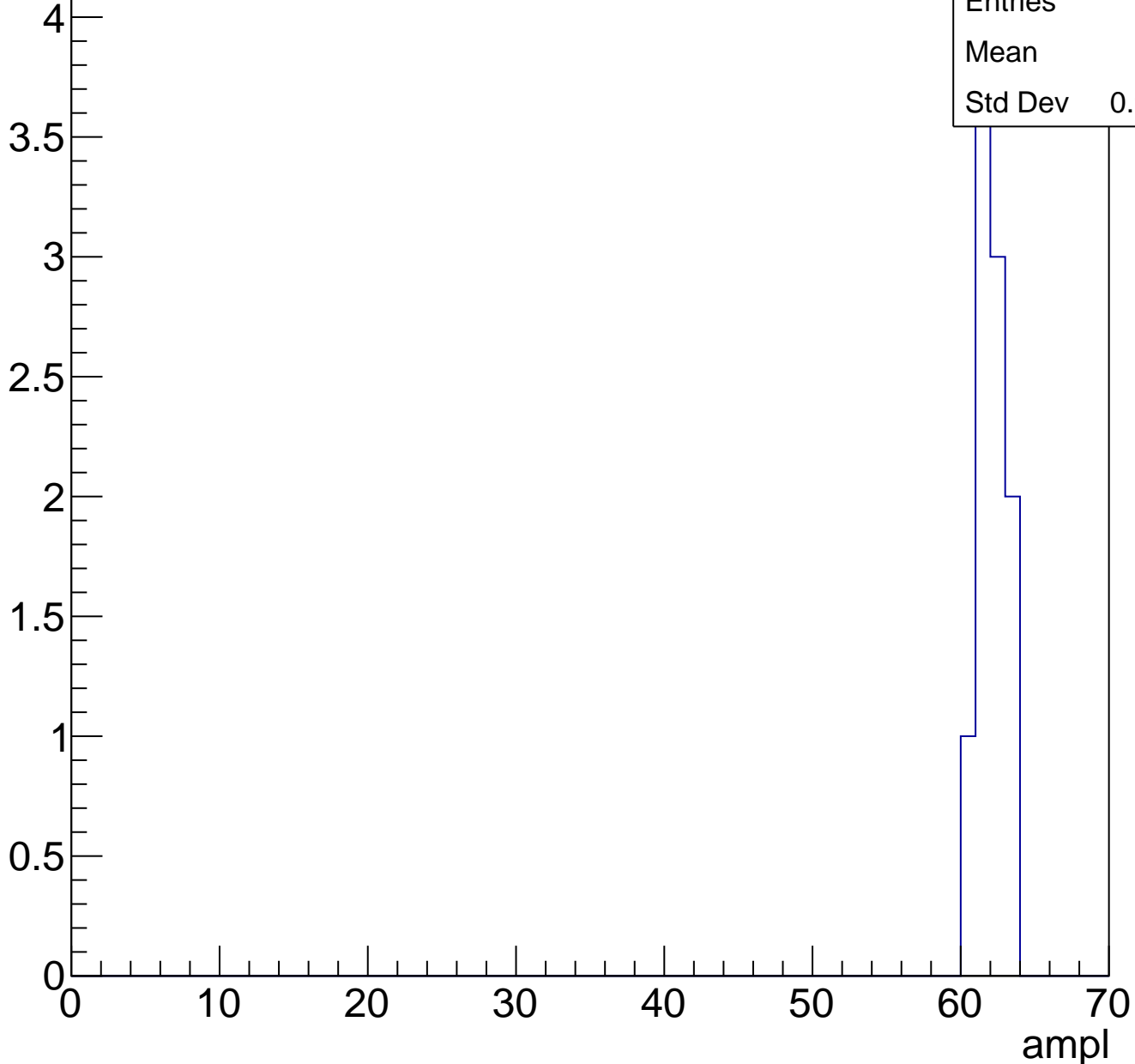
Entries	38
Mean	58.53
Std Dev	9.925



B0L102S, U5-ch16, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

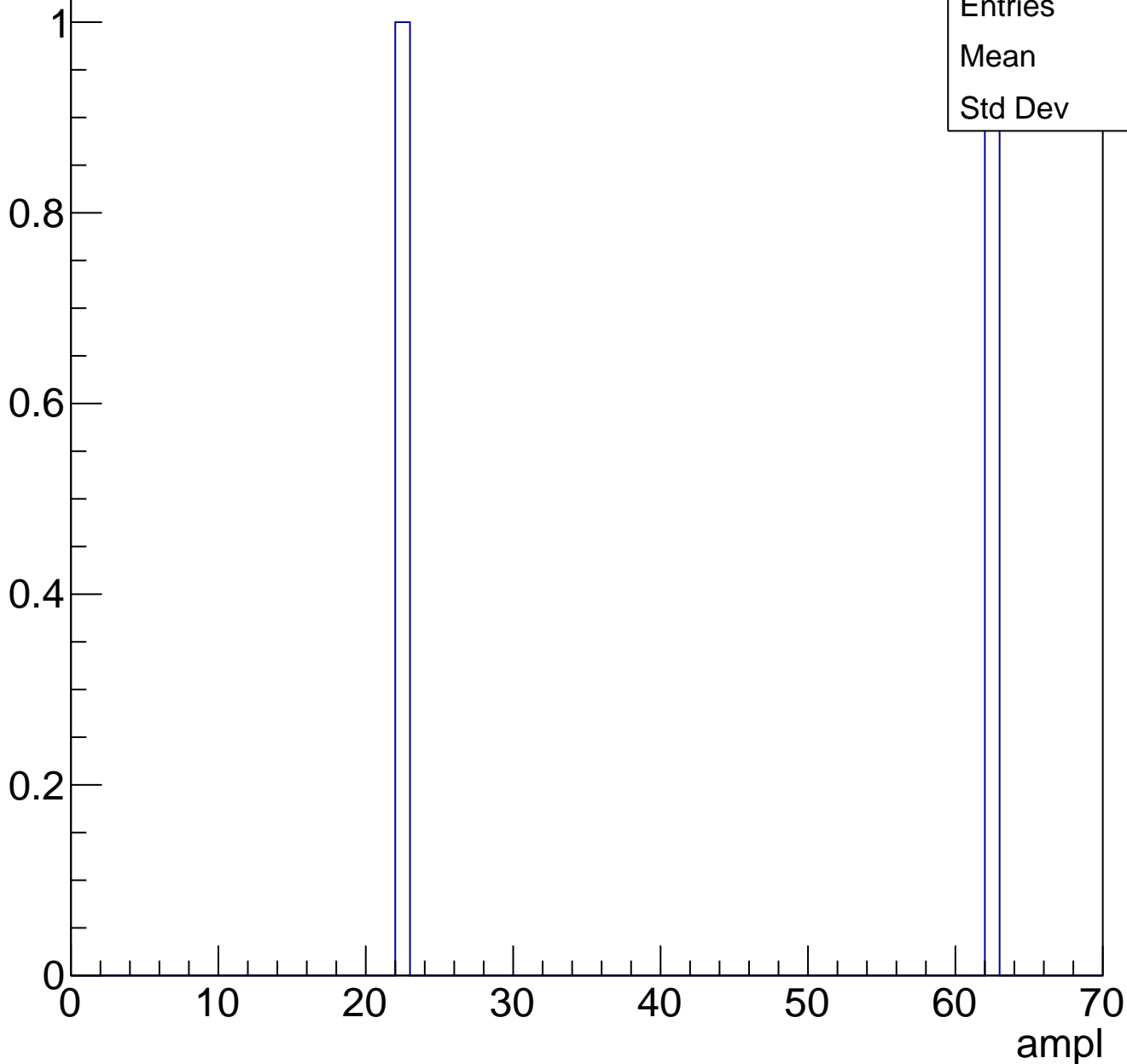


Entries	10
Mean	61.6
Std Dev	0.9165

B0L102S, U5-ch16, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch17, adc0

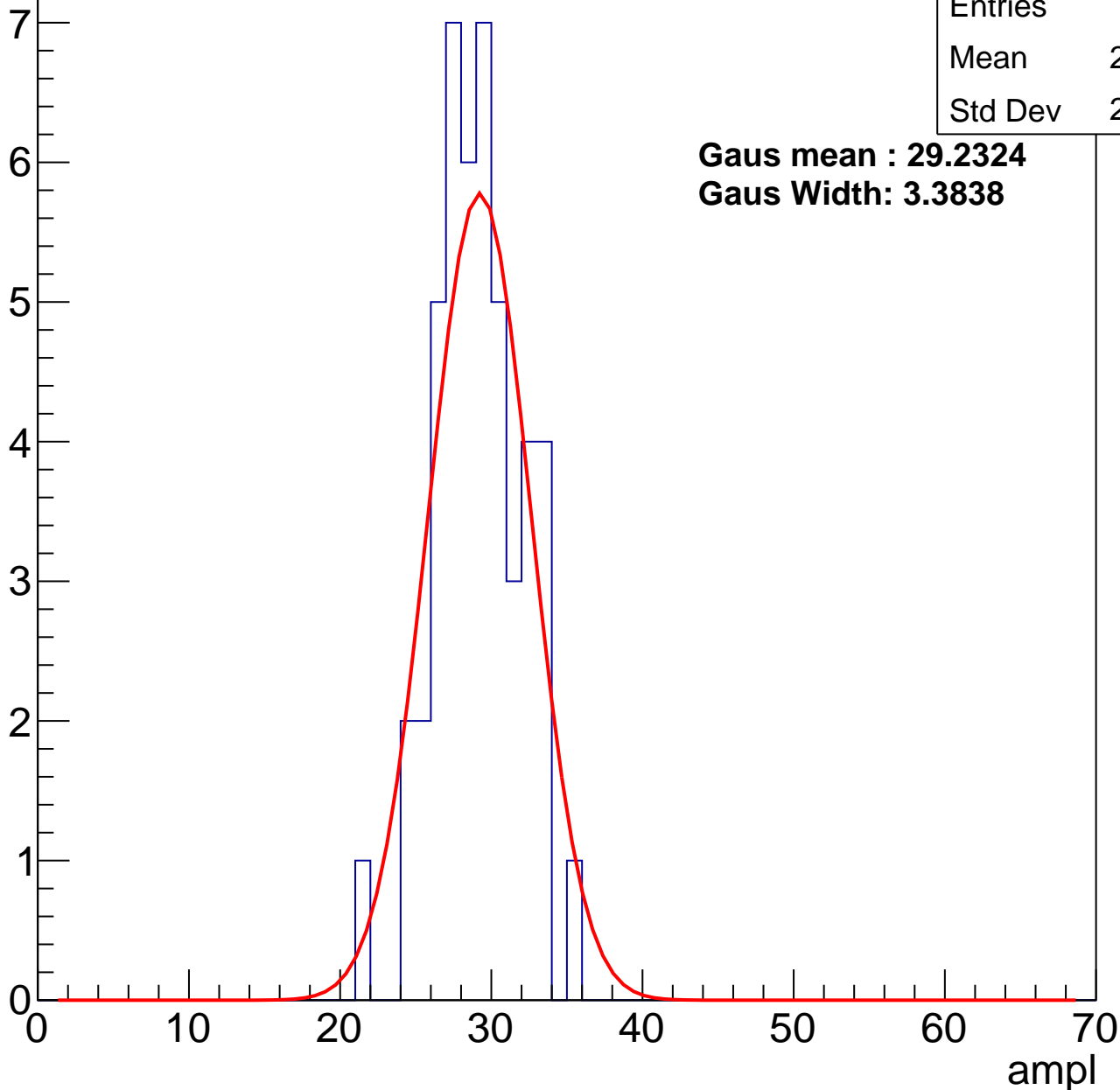
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	28.66
Std Dev	2.815

Gaus mean : 29.2324

Gaus Width: 3.3838



B0L102S, U5-ch17, adc1

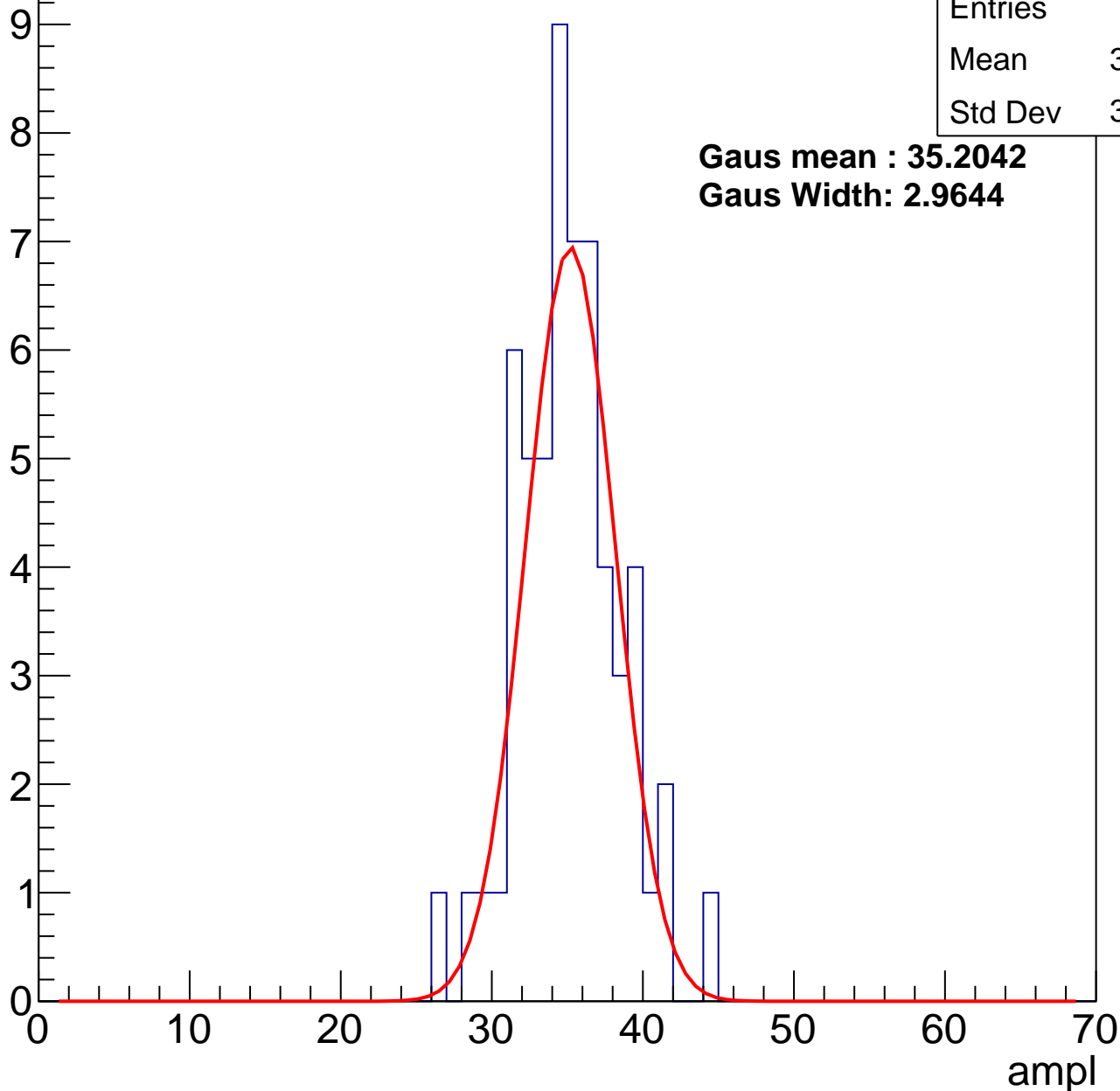
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	34.67
Std Dev	3.339

Gaus mean : 35.2042

Gaus Width: 2.9644



B0L102S, U5-ch17, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	77
Mean	40.9
Std Dev	3.677

Gaus mean : 41.0486

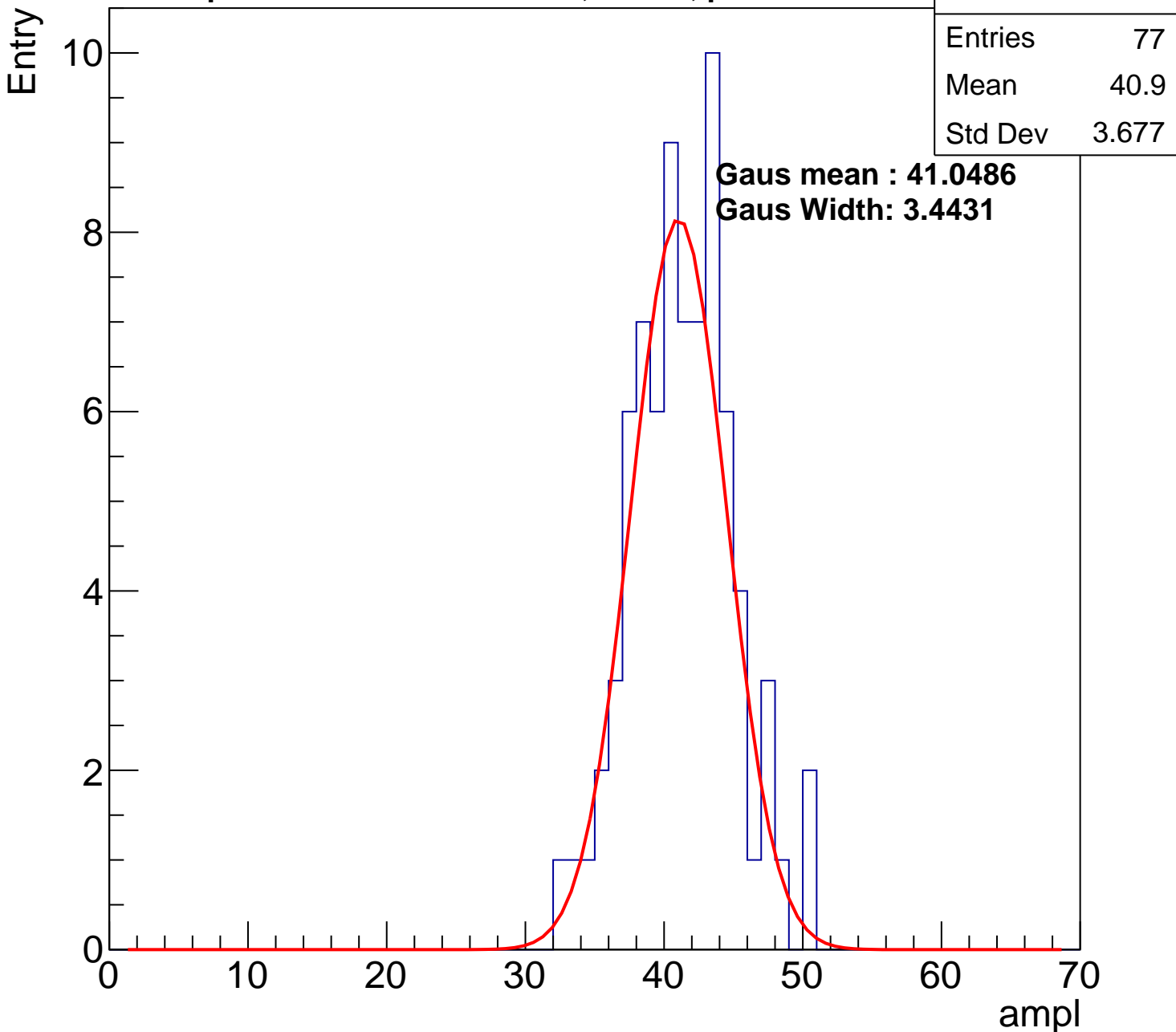
Gaus Width: 3.4431

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

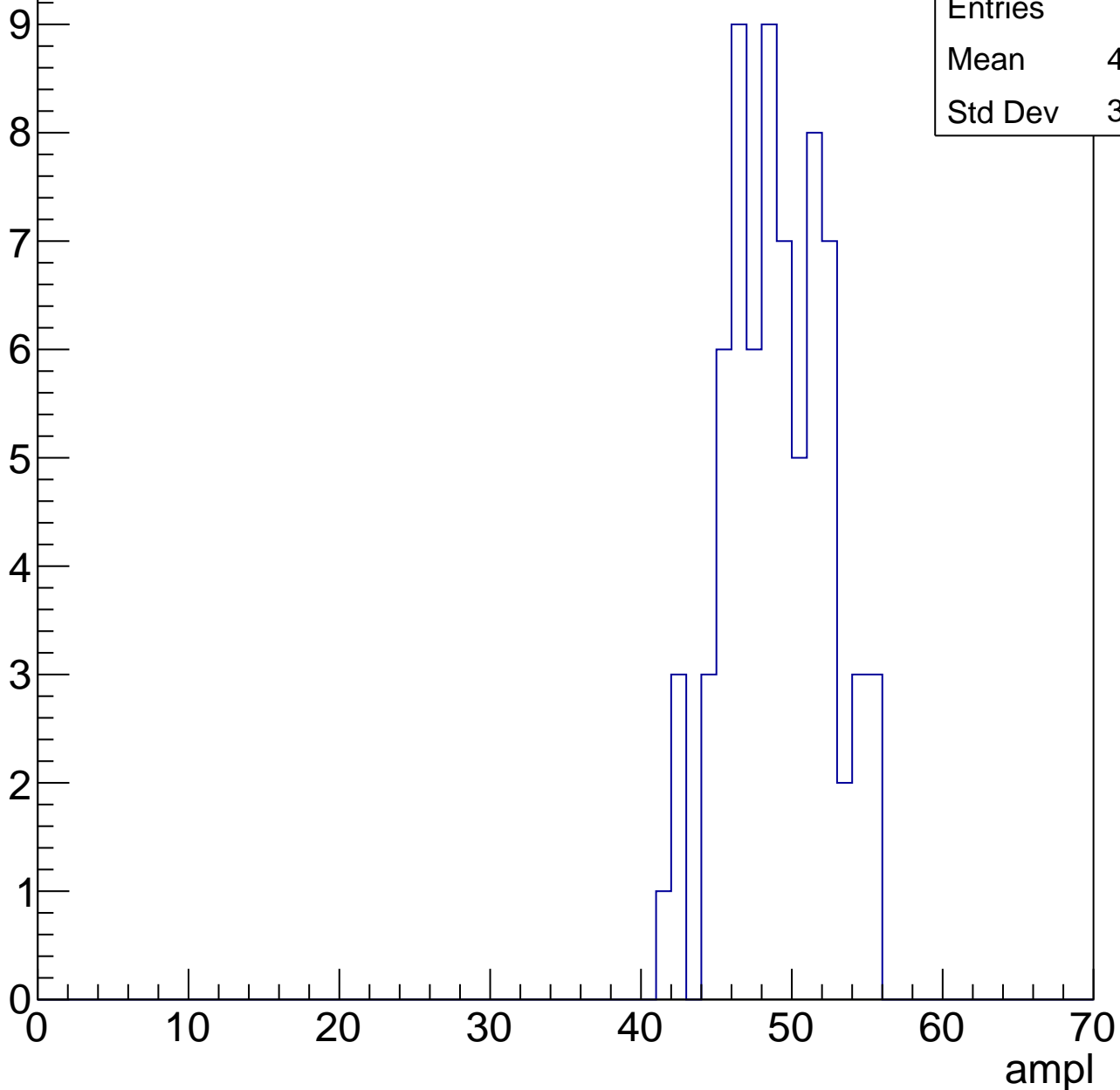
ampl



B0L102S, U5-ch17, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



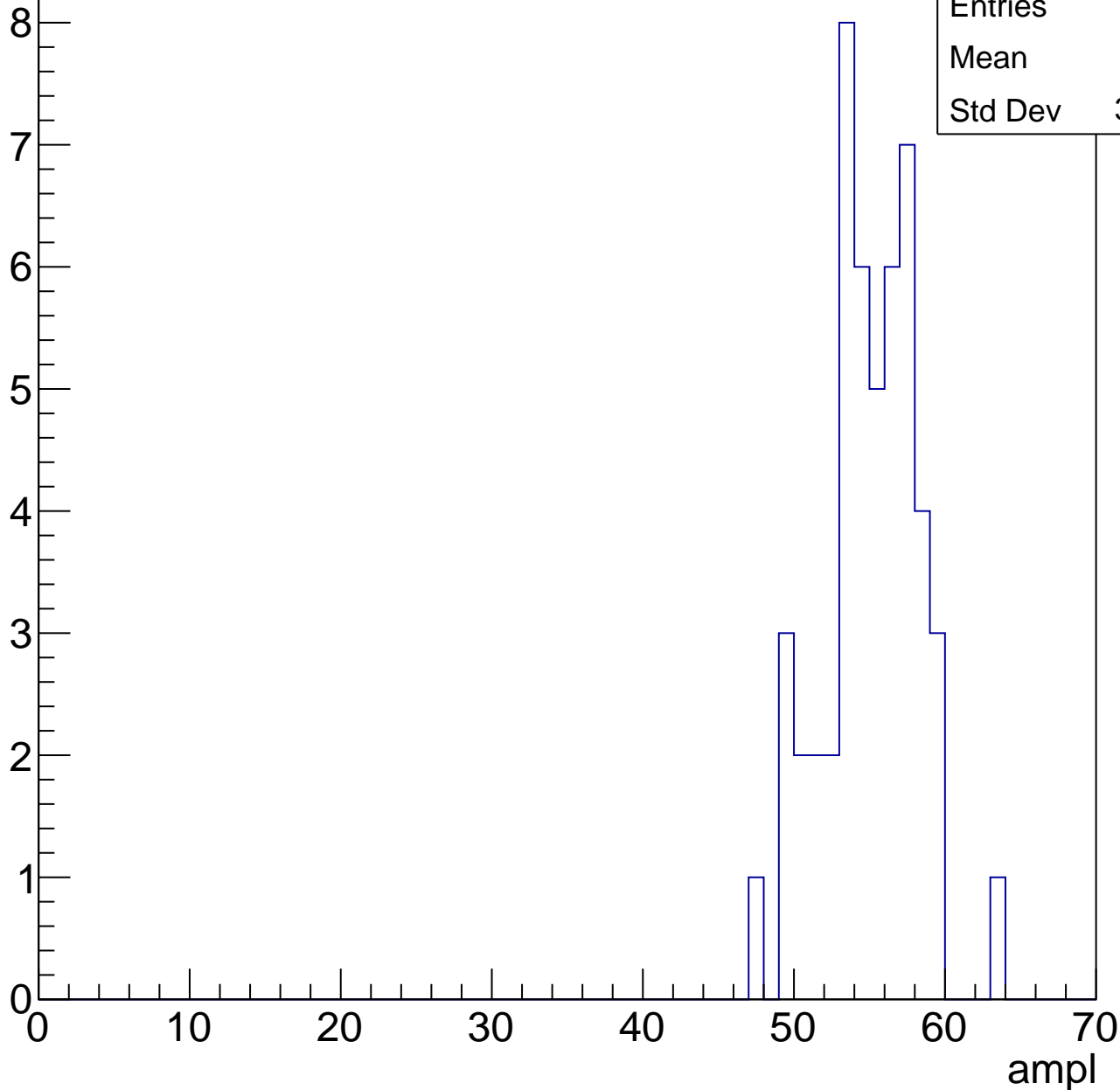
Entries	72
Mean	48.54
Std Dev	3.325

B0L102S, U5-ch17, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	54.6
Std Dev	3.111

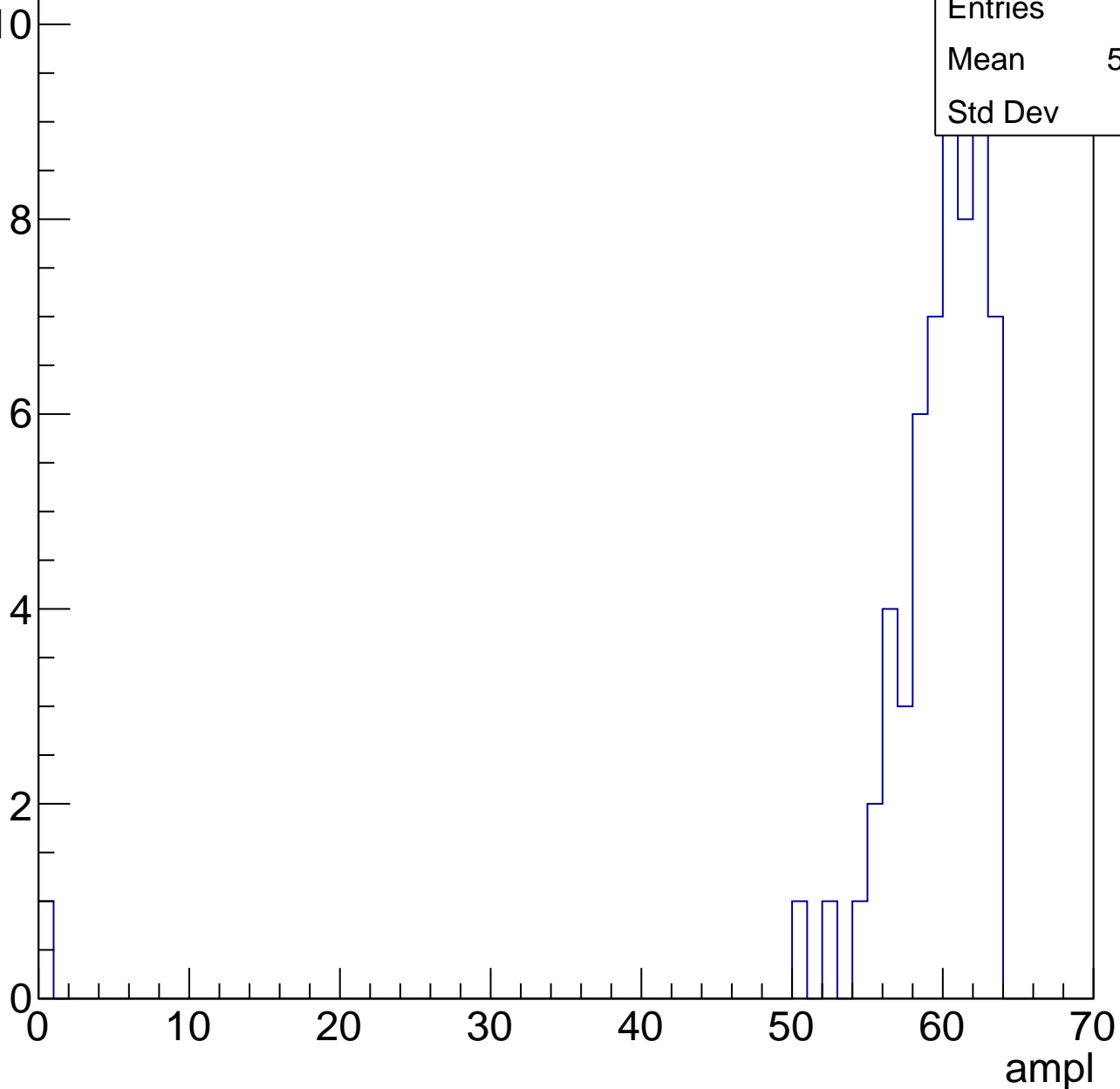


B0L102S, U5-ch17, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	58.54
Std Dev	8.05



B0L102S, U5-ch17, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	62
Std Dev	1

B0L102S, U5-ch17, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L102S, U5-ch18, adc0

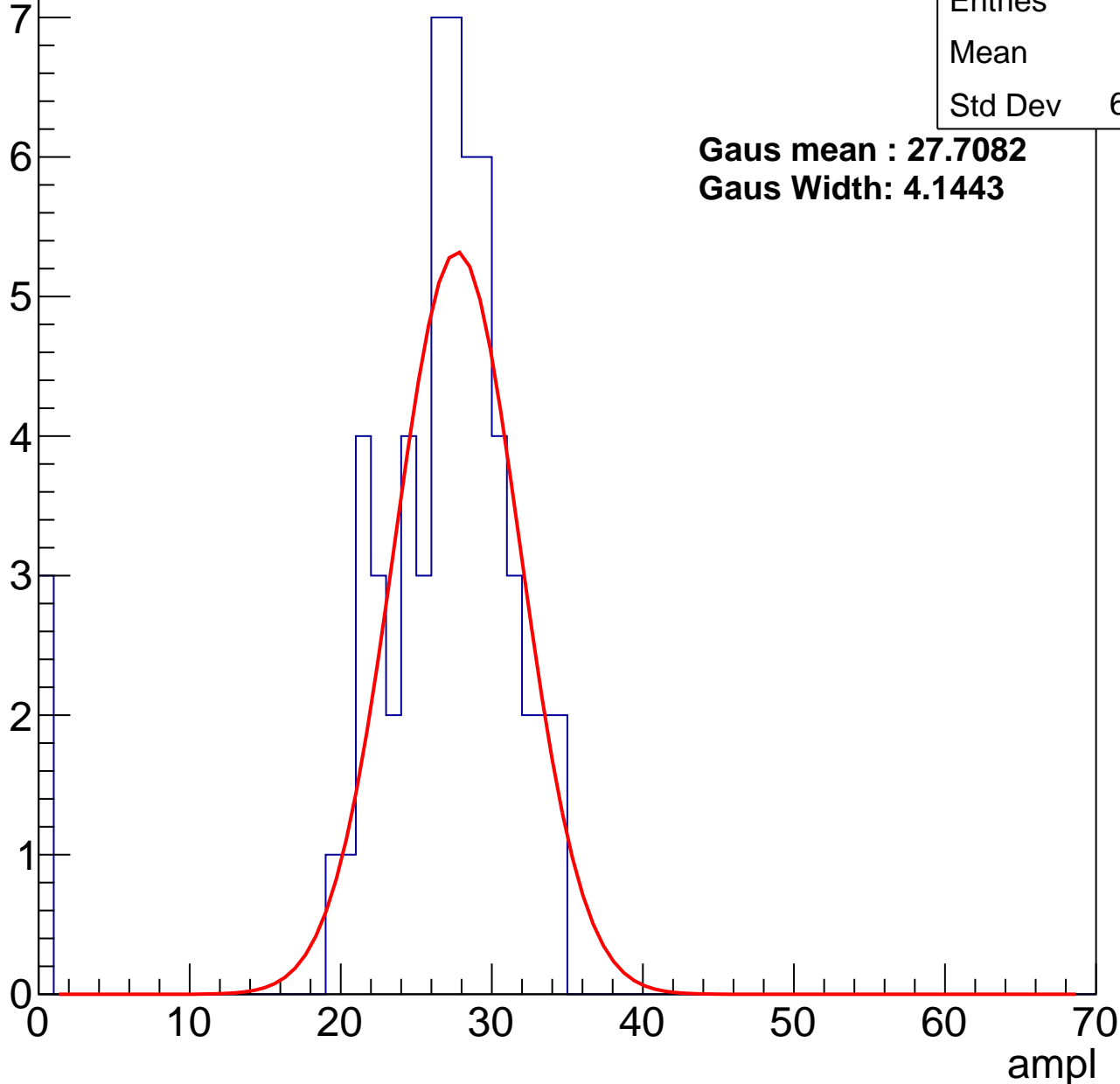
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	25.5
Std Dev	6.837

Gaus mean : 27.7082

Gaus Width: 4.1443



B0L102S, U5-ch18, adc1

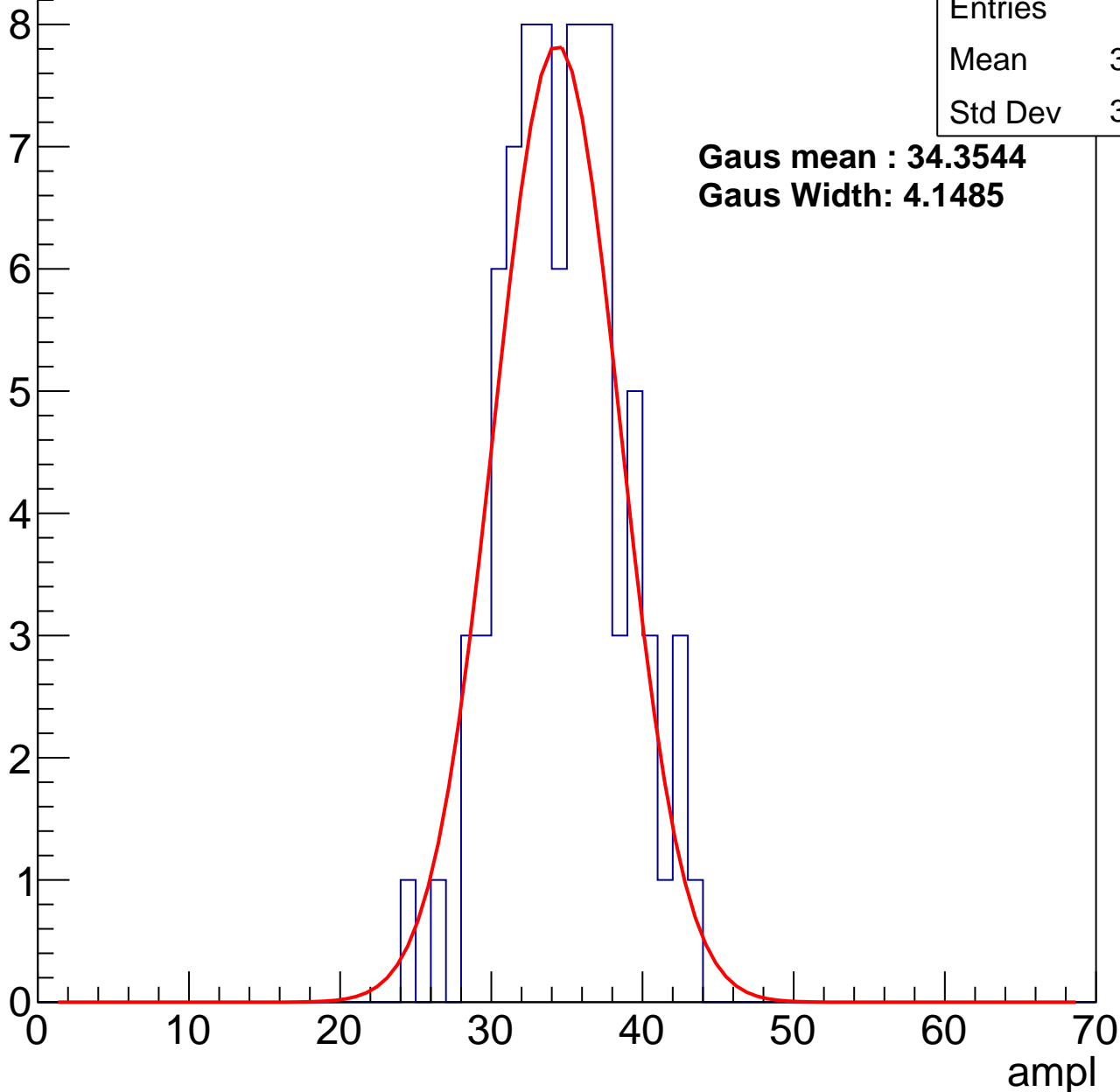
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	34.28
Std Dev	3.888

Gaus mean : 34.3544

Gaus Width: 4.1485



B0L102S, U5-ch18, adc2

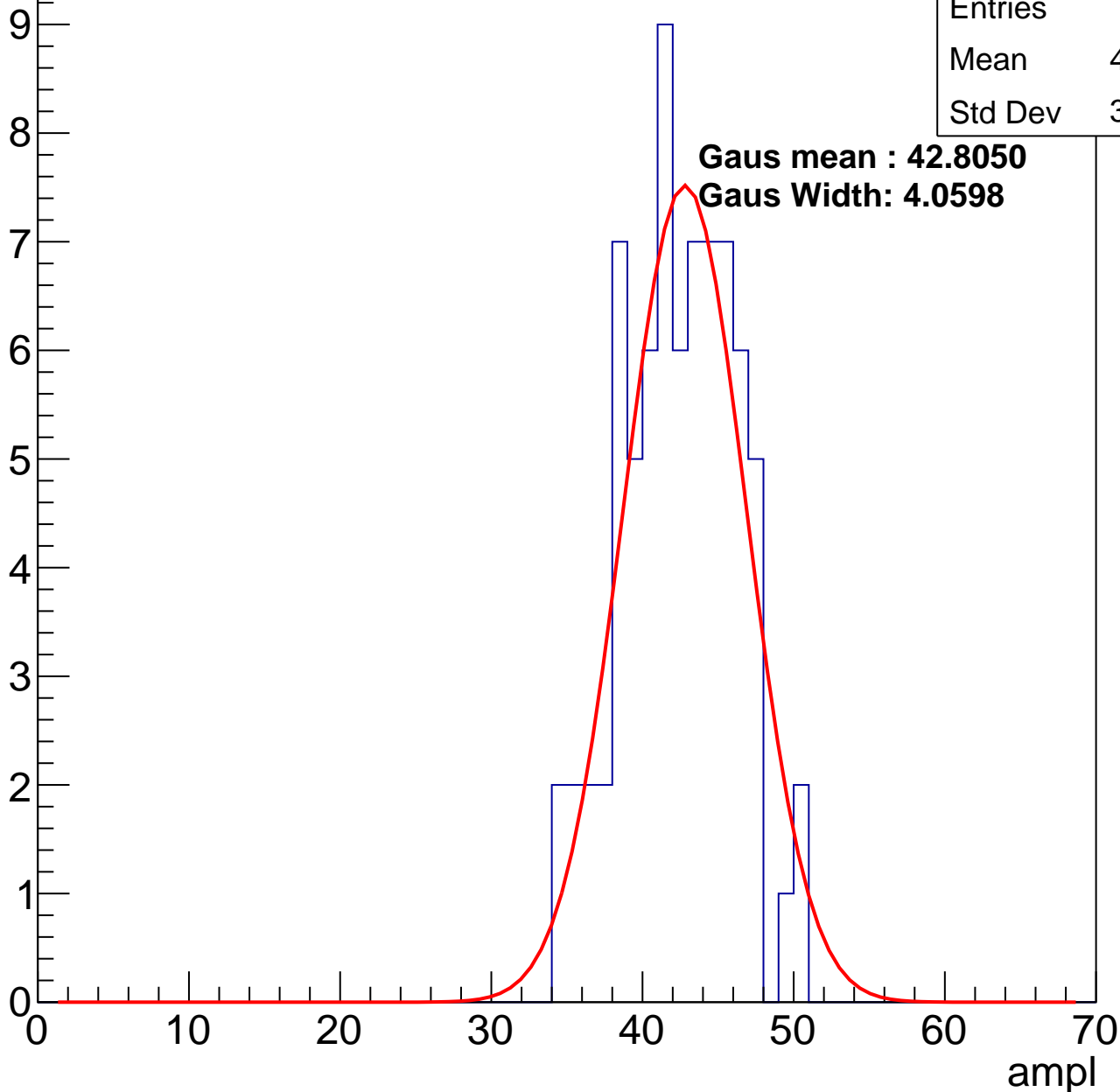
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	41.97
Std Dev	3.688

Gaus mean : 42.8050

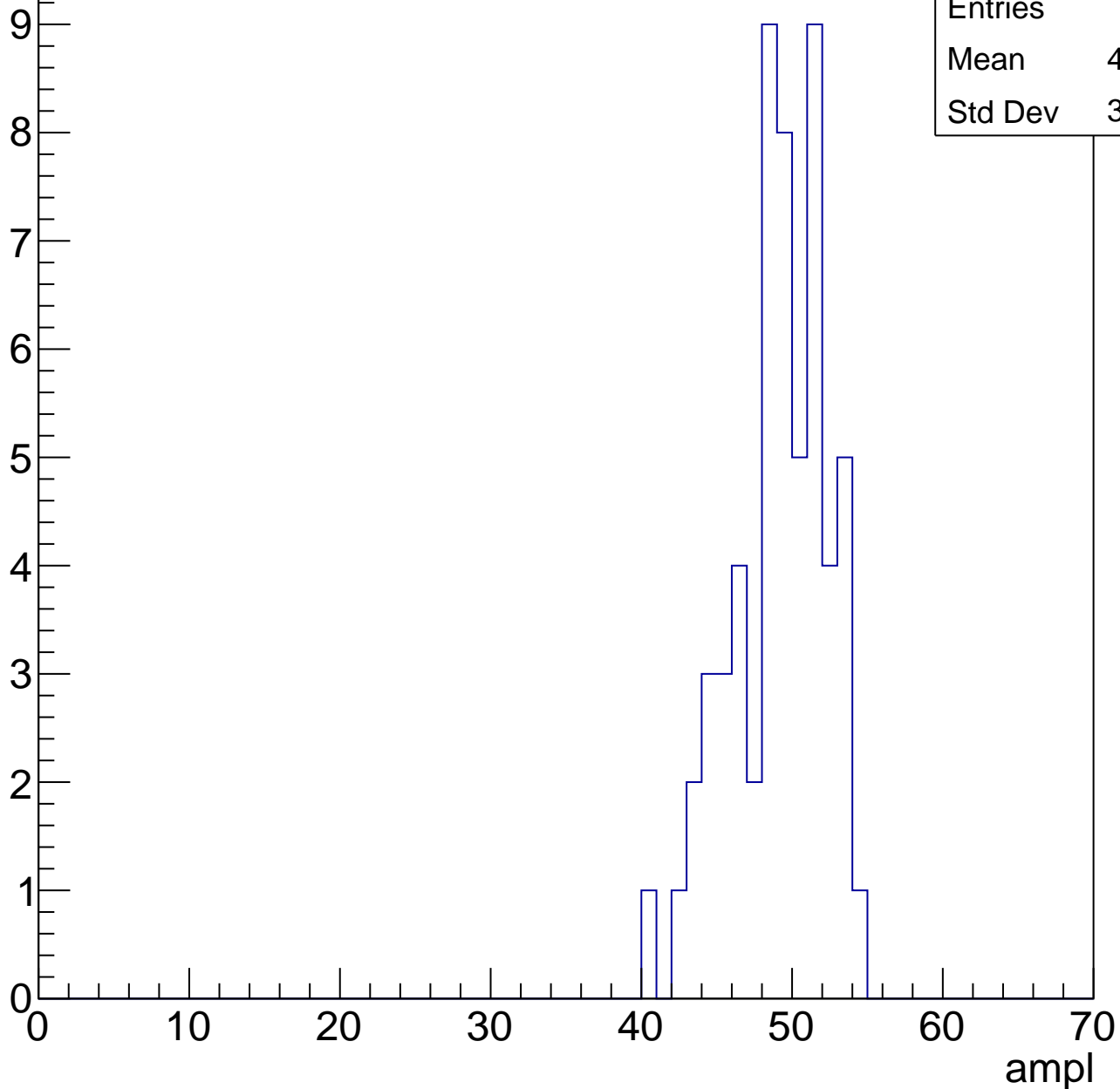
Gaus Width: 4.0598



B0L102S, U5-ch18, adc3

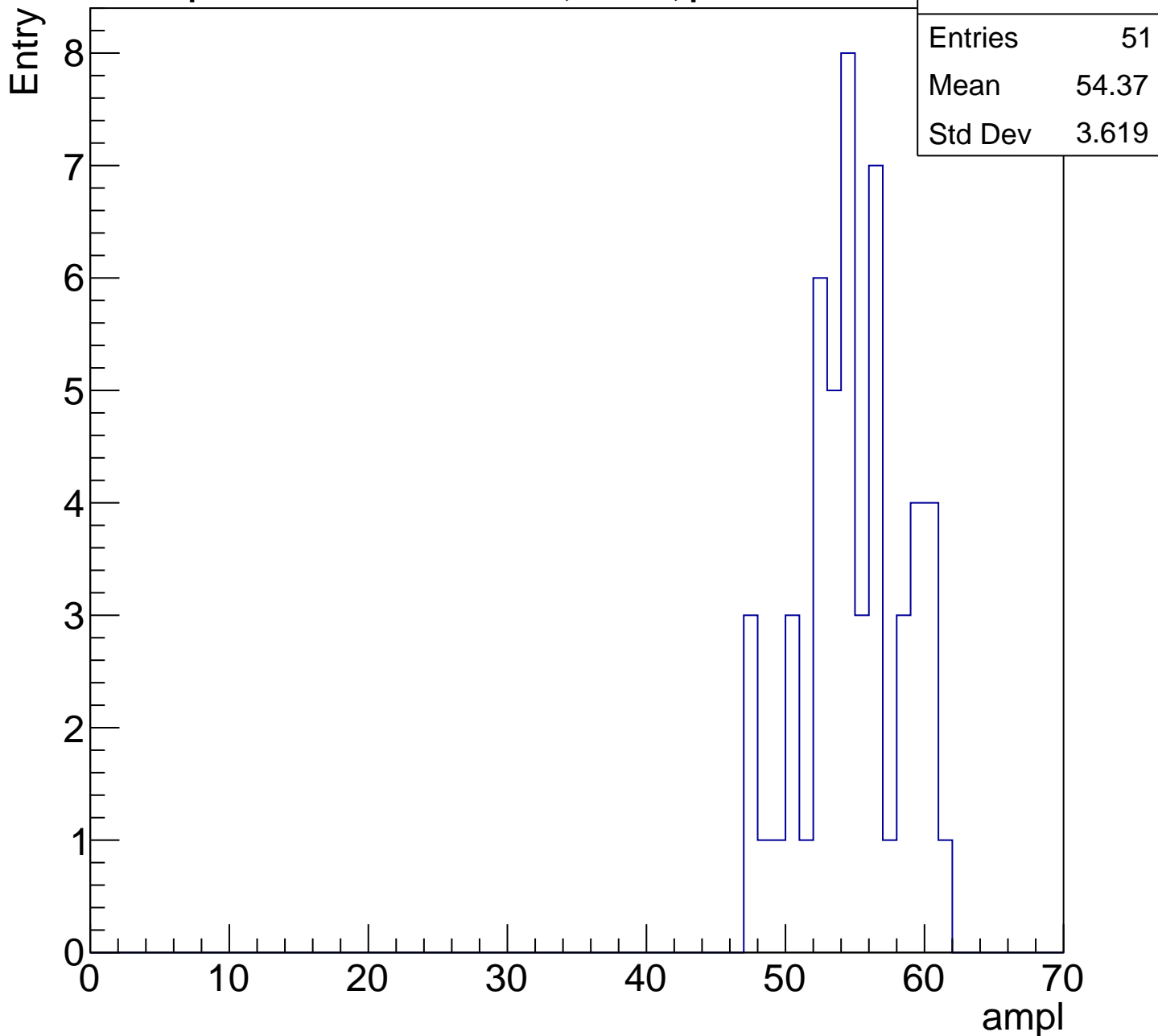
calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch18, adc4

calib_packv5_042523_0143.root, FC#12, port B1

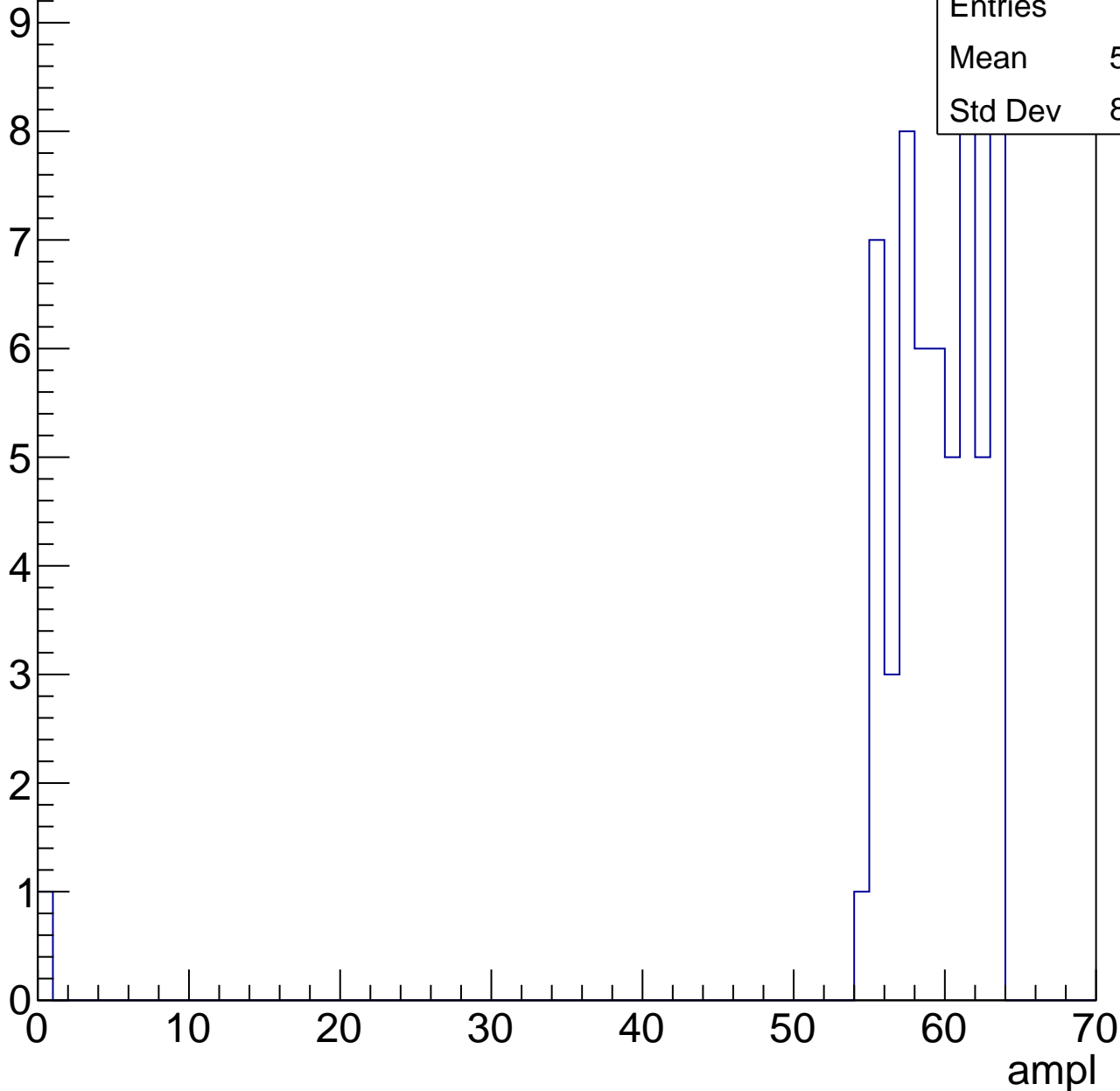


B0L102S, U5-ch18, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

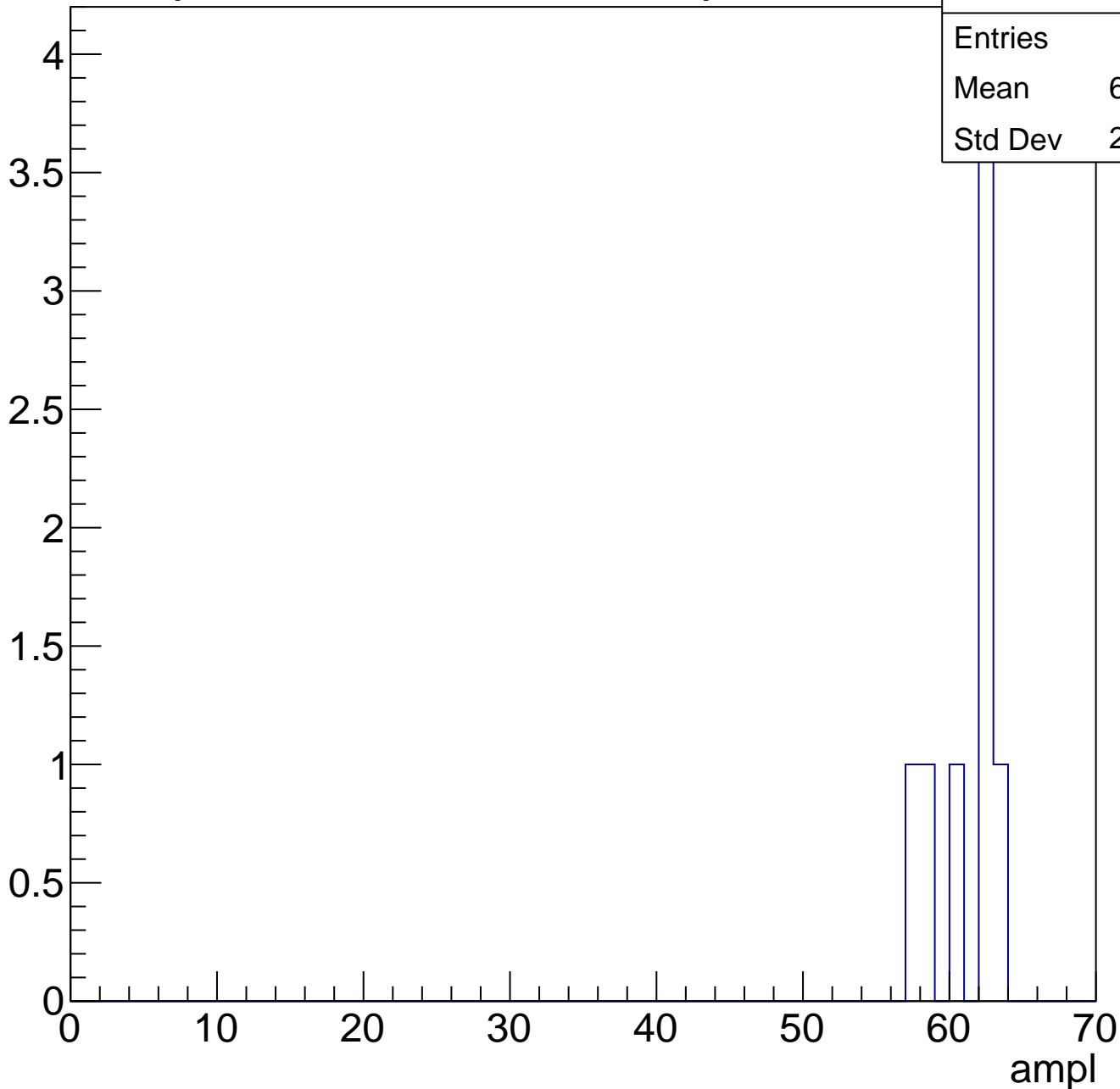
Entries	60
Mean	58.18
Std Dev	8.034



B0L102S, U5-ch18, adc6

calib_packv5_042523_0143.root, FC#12, port B1

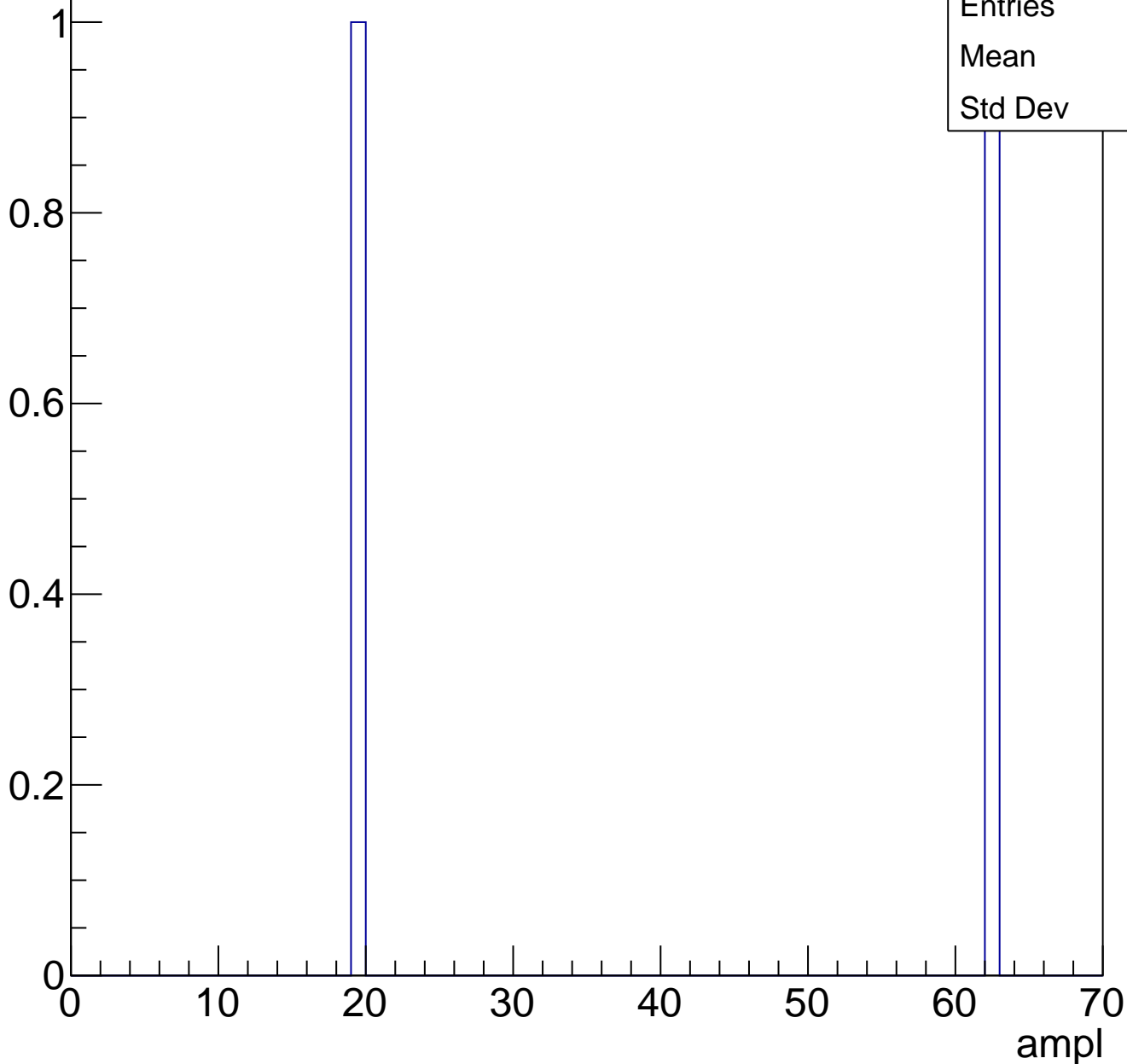
Entry



B0L102S, U5-ch18, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch19, adc0

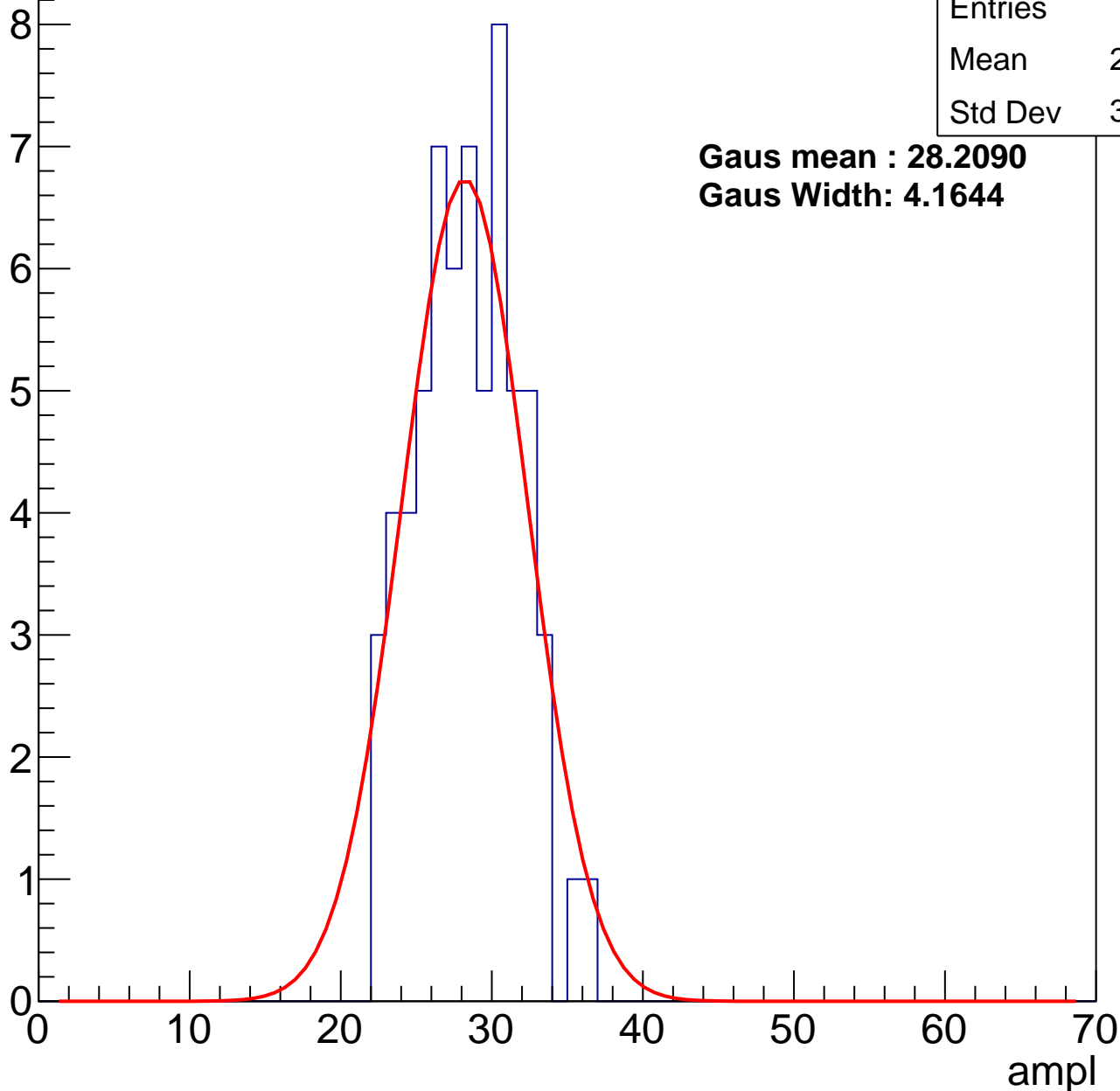
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	27.95
Std Dev	3.309

Gaus mean : 28.2090

Gaus Width: 4.1644



B0L102S, U5-ch19, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	34.19
Std Dev	3.823

Gaus mean : 34.8676

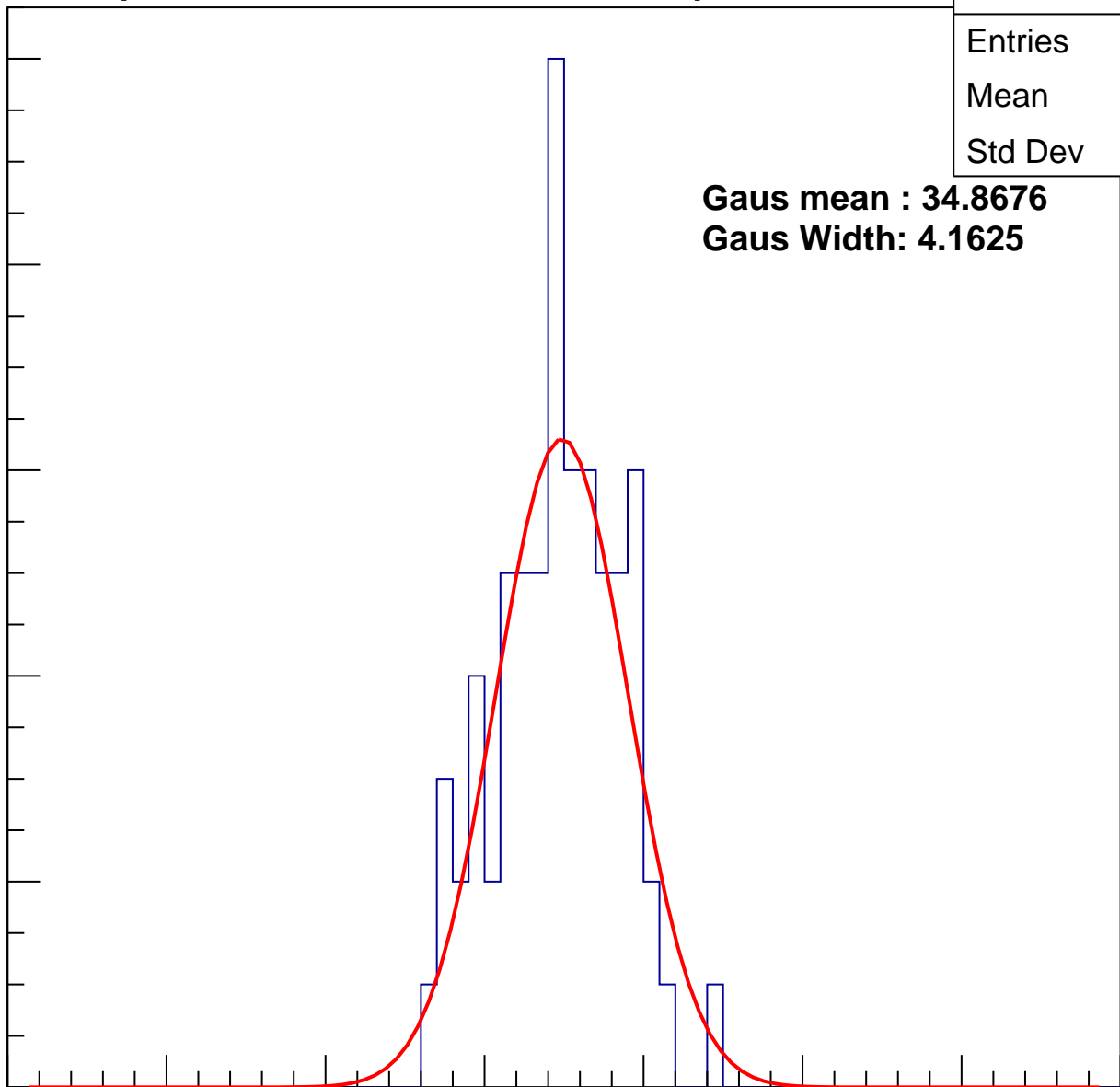
Gaus Width: 4.1625

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch19, adc2

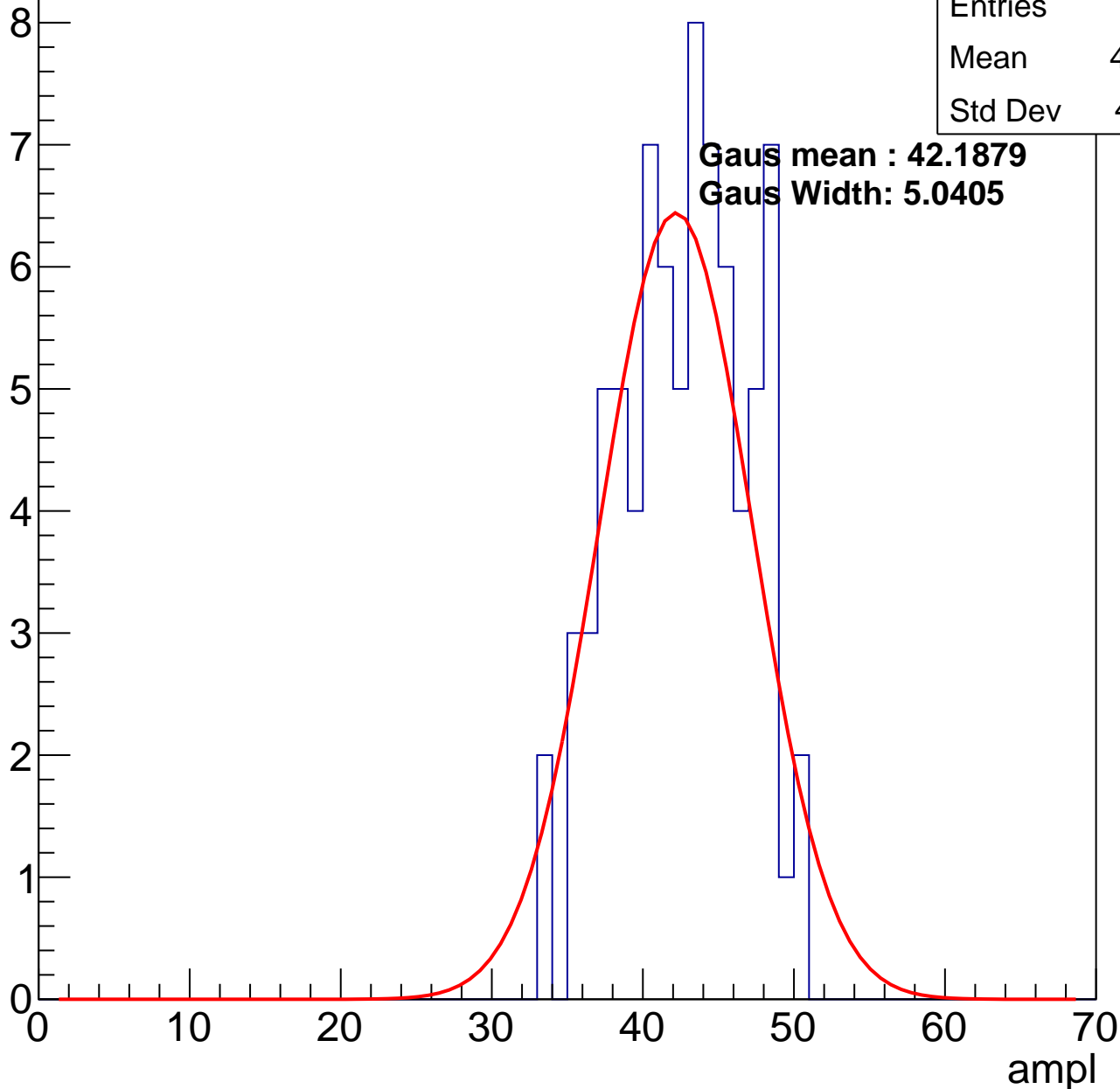
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	42.15
Std Dev	4.181

Gaus mean : 42.1879

Gaus Width: 5.0405

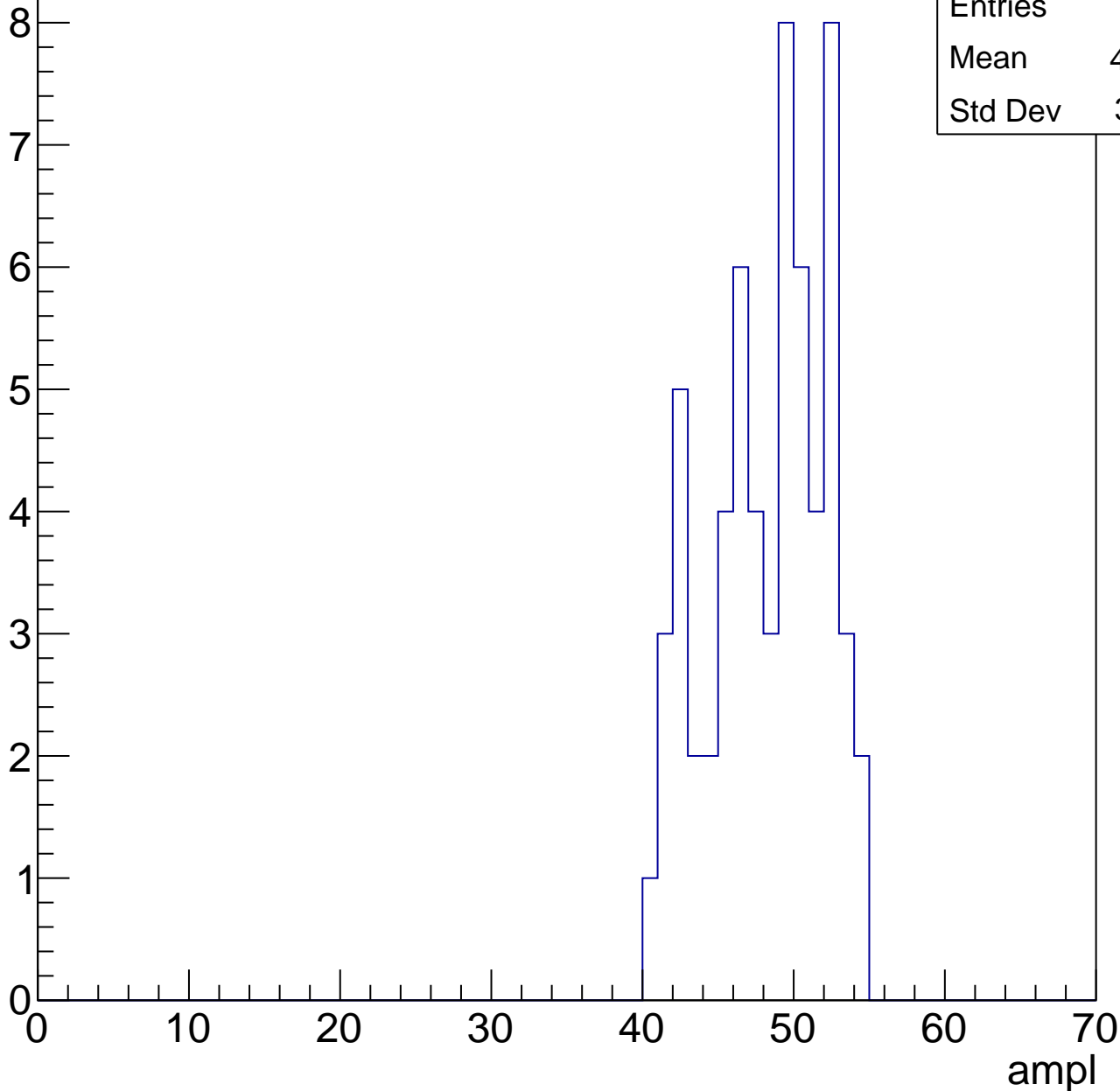


B0L102S, U5-ch19, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	47.77
Std Dev	3.791

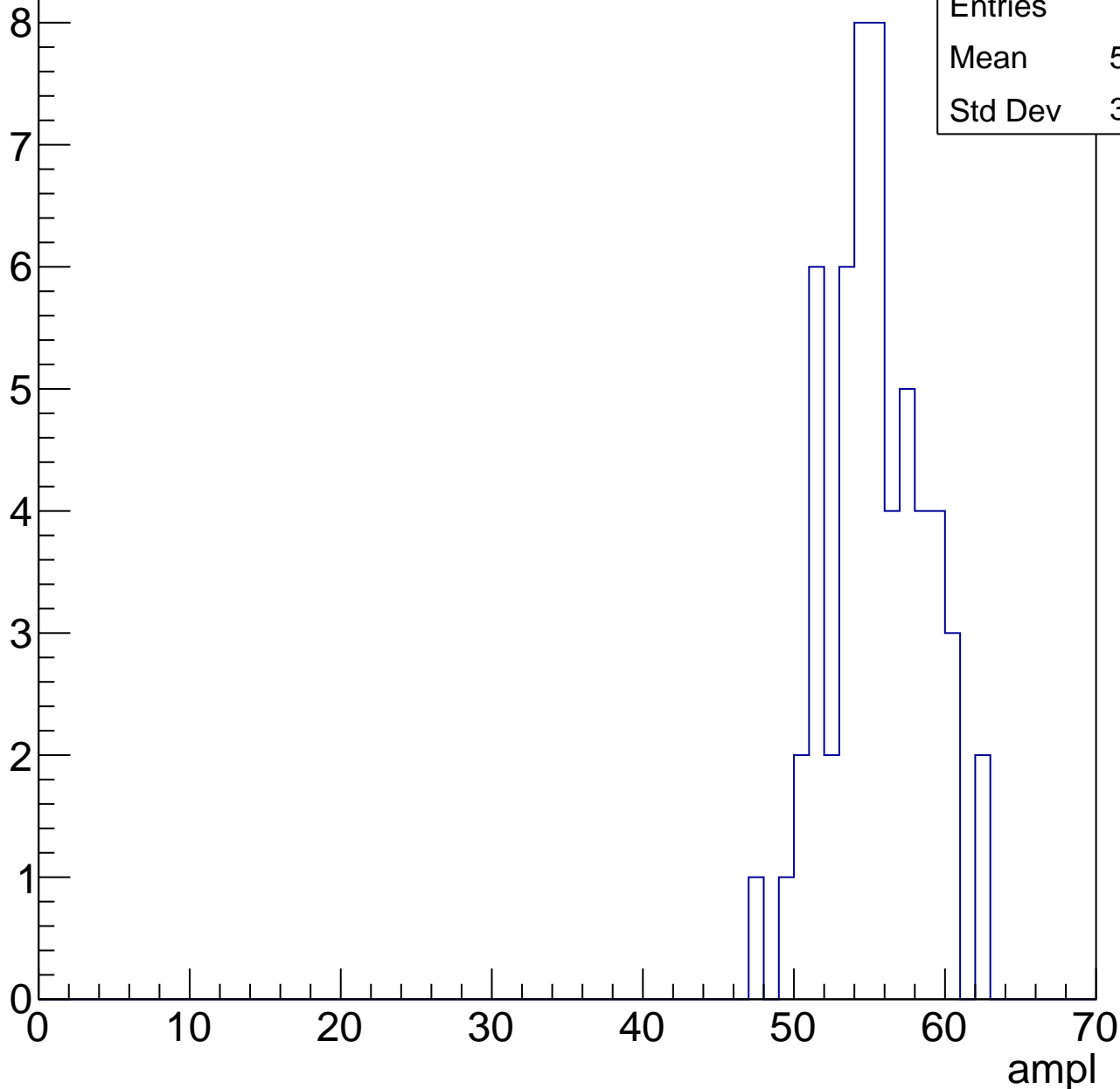


B0L102S, U5-ch19, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	54.95
Std Dev	3.254

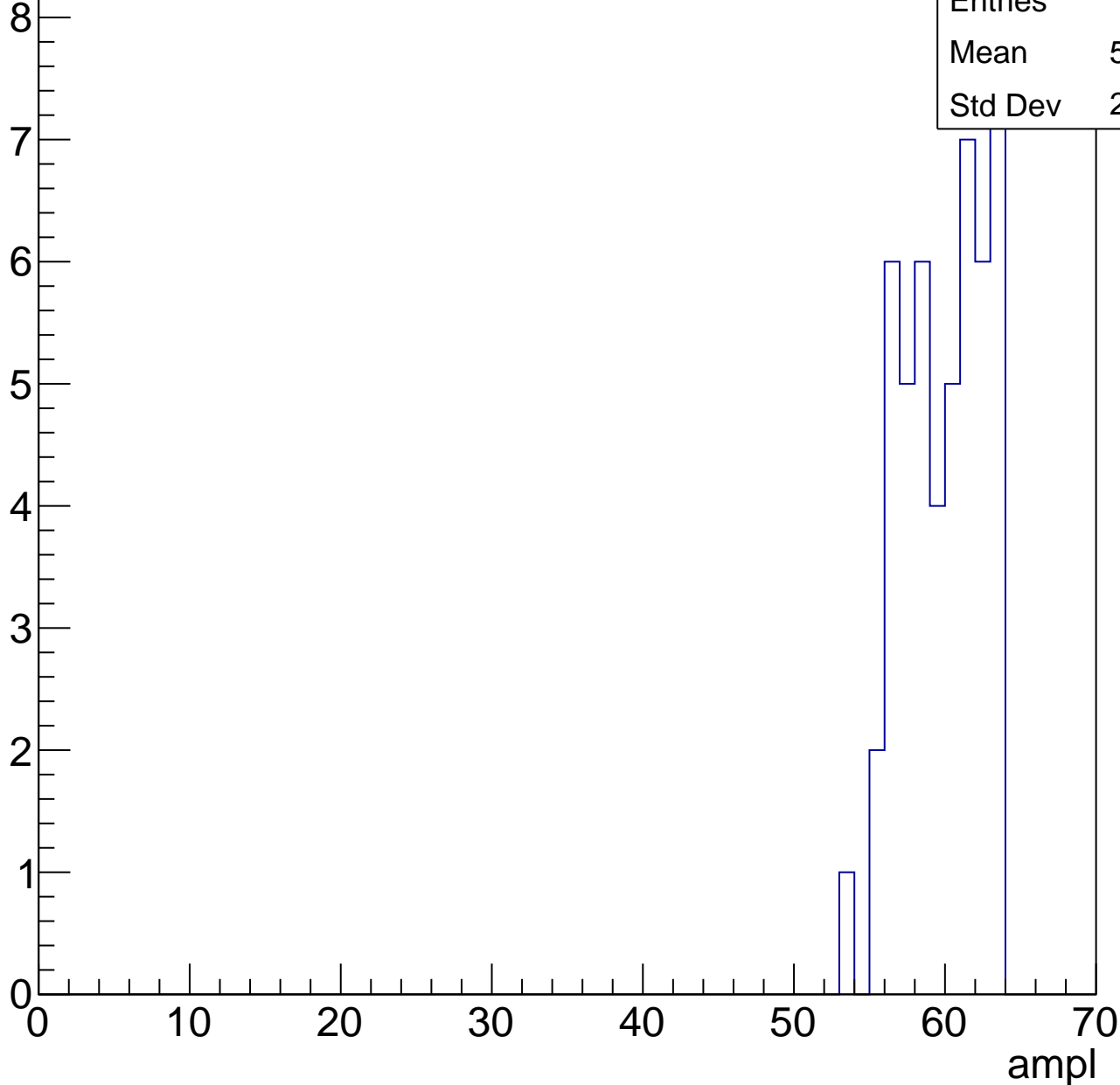


B0L102S, U5-ch19, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

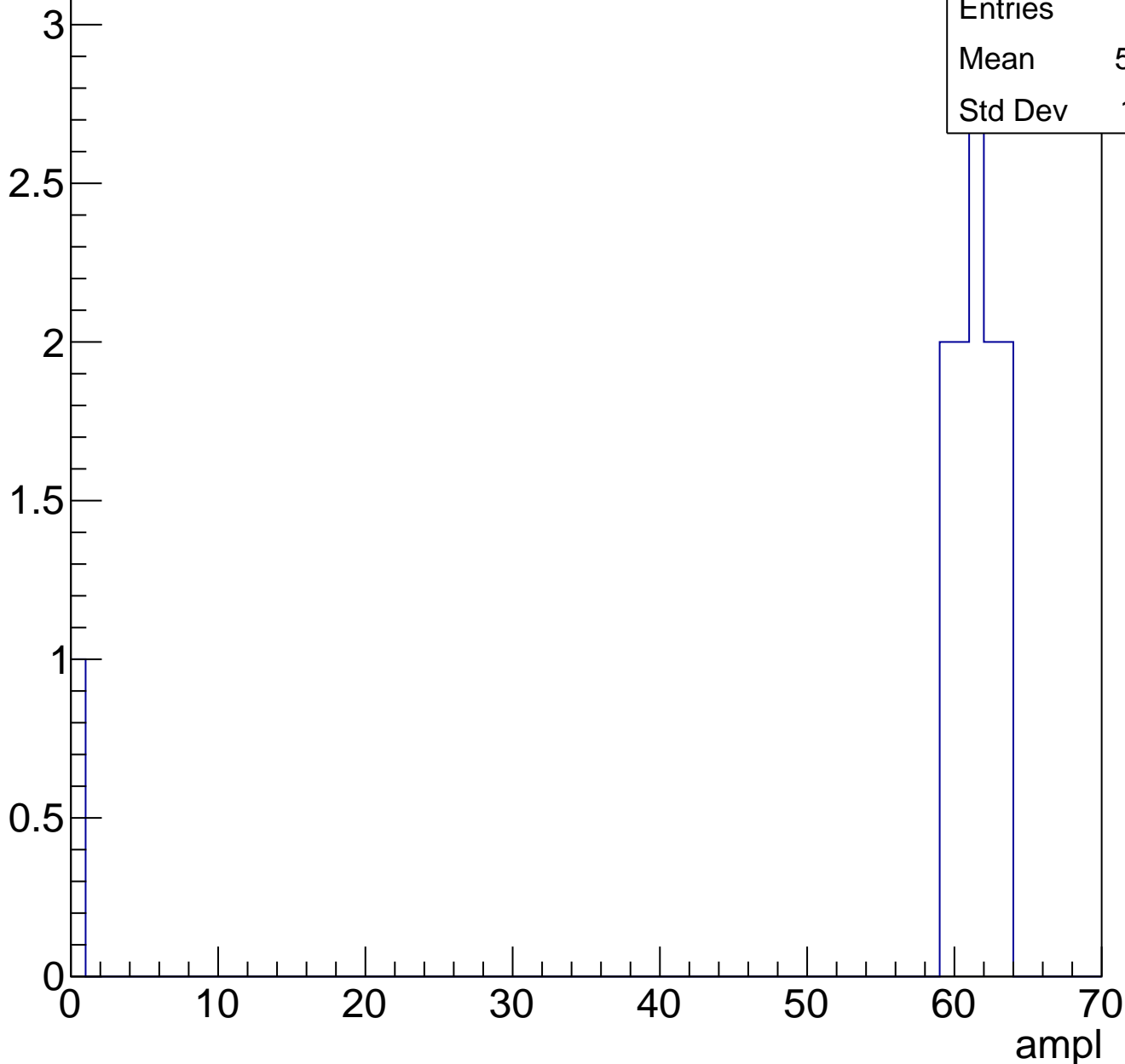
Entries	50
Mean	59.42
Std Dev	2.662



B0L102S, U5-ch19, adc6

calib_packv5_042523_0143.root, FC#12, port B1

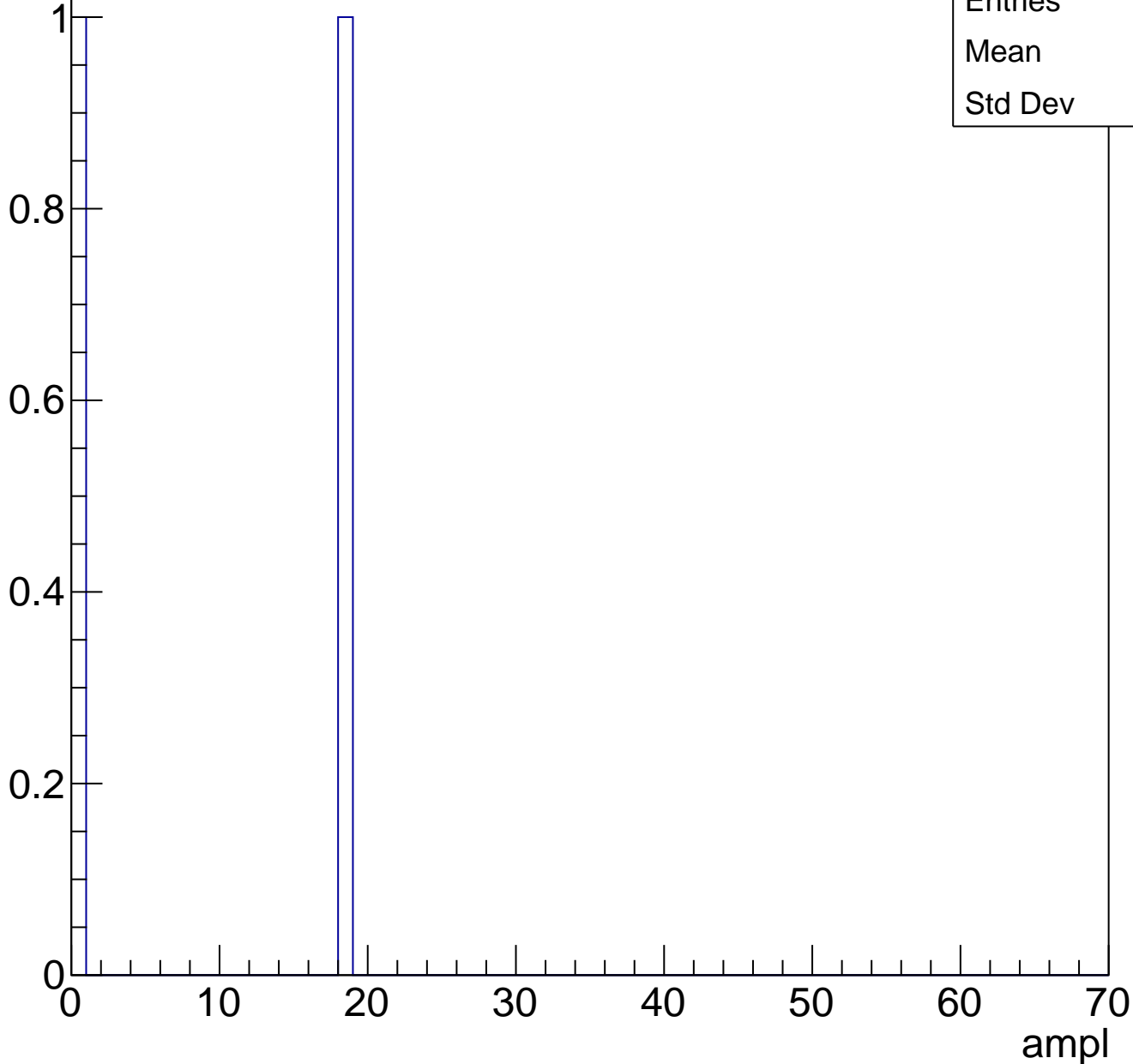
Entry



B0L102S, U5-ch19, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	9
Std Dev	9

B0L102S, U5-ch20, adc0

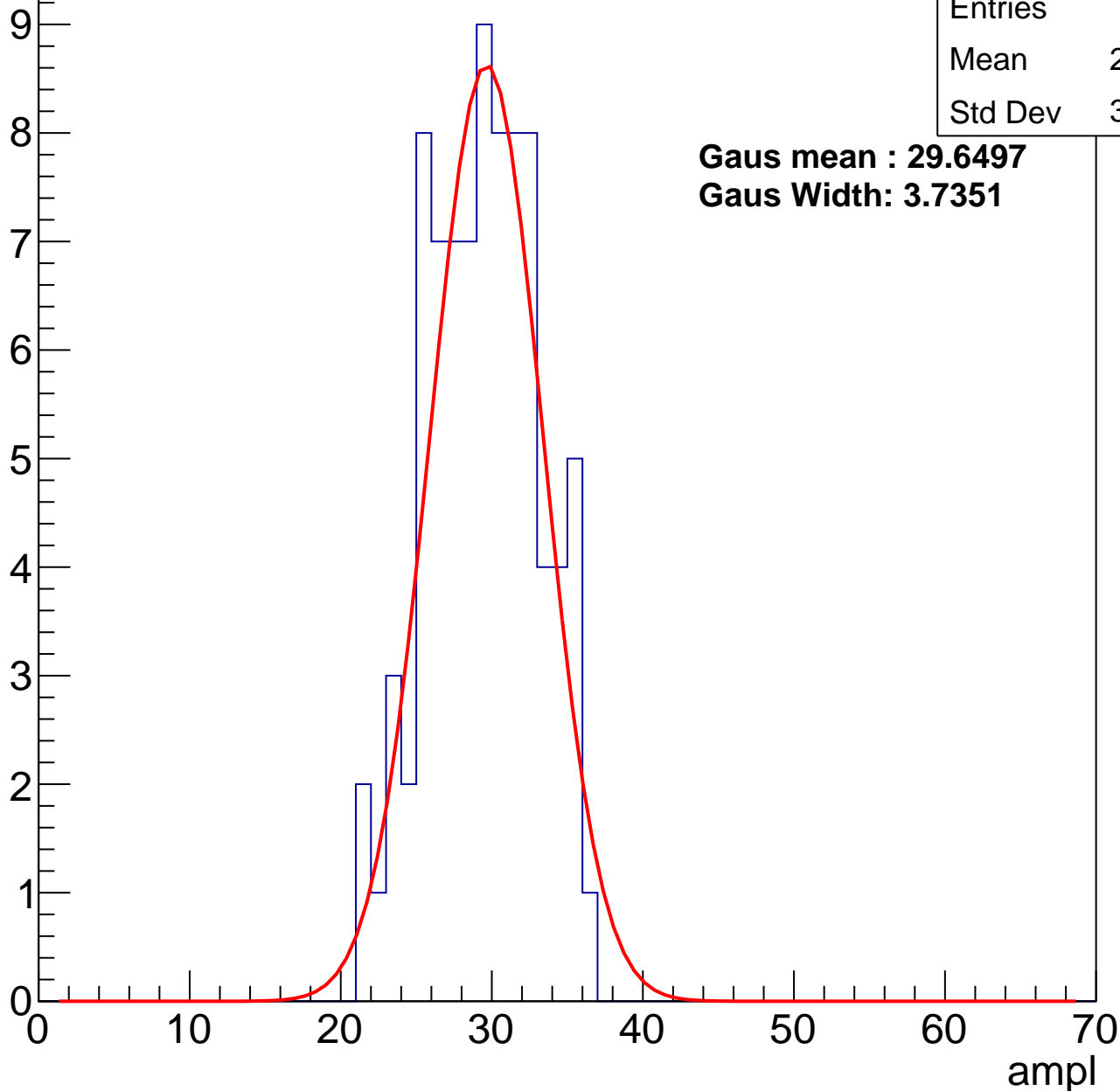
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	28.95
Std Dev	3.562

Gaus mean : 29.6497

Gaus Width: 3.7351



B0L102S, U5-ch20, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	46
Mean	36.17
Std Dev	2.892

Gaus mean : 37.0494

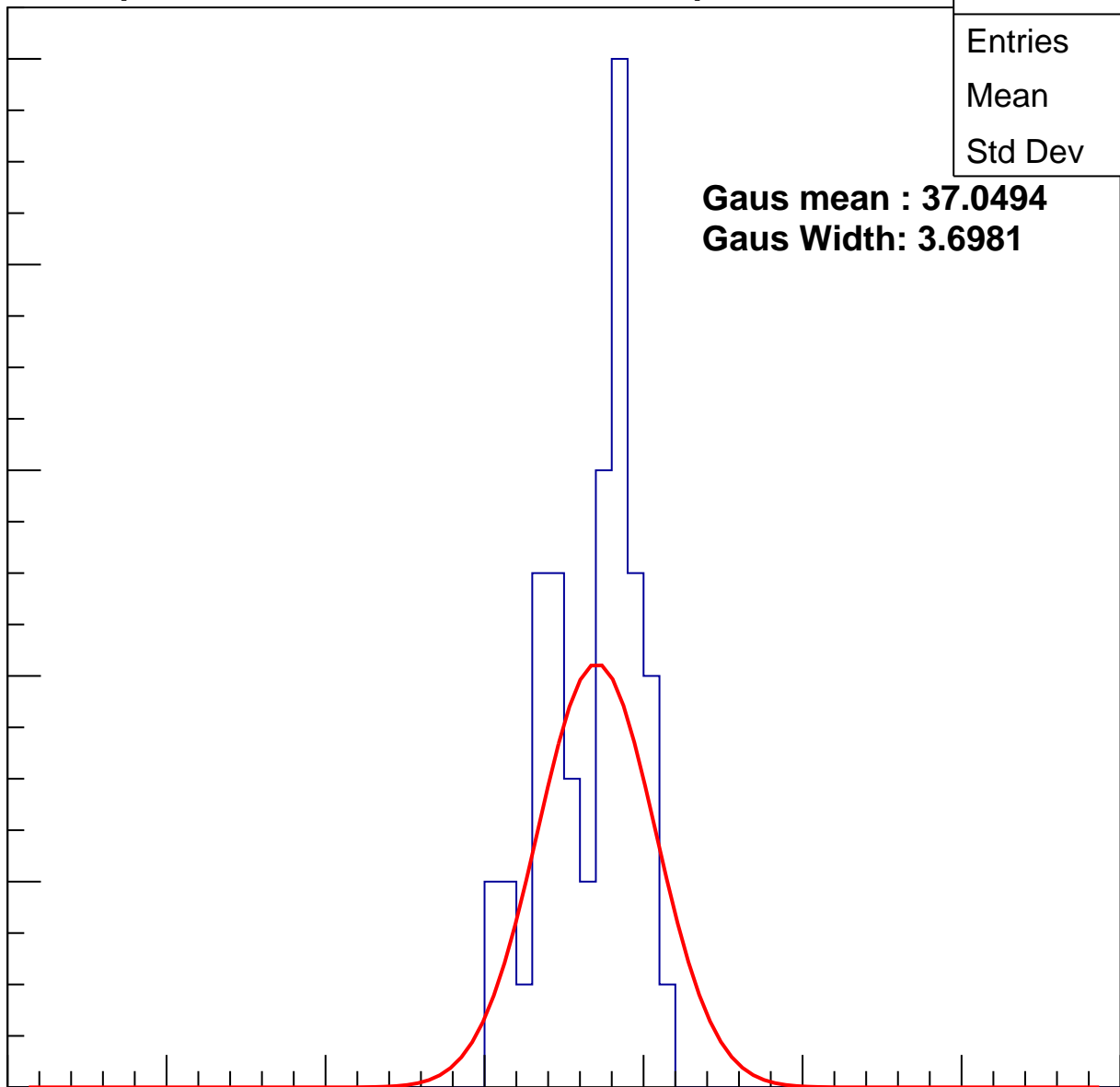
Gaus Width: 3.6981

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch20, adc2

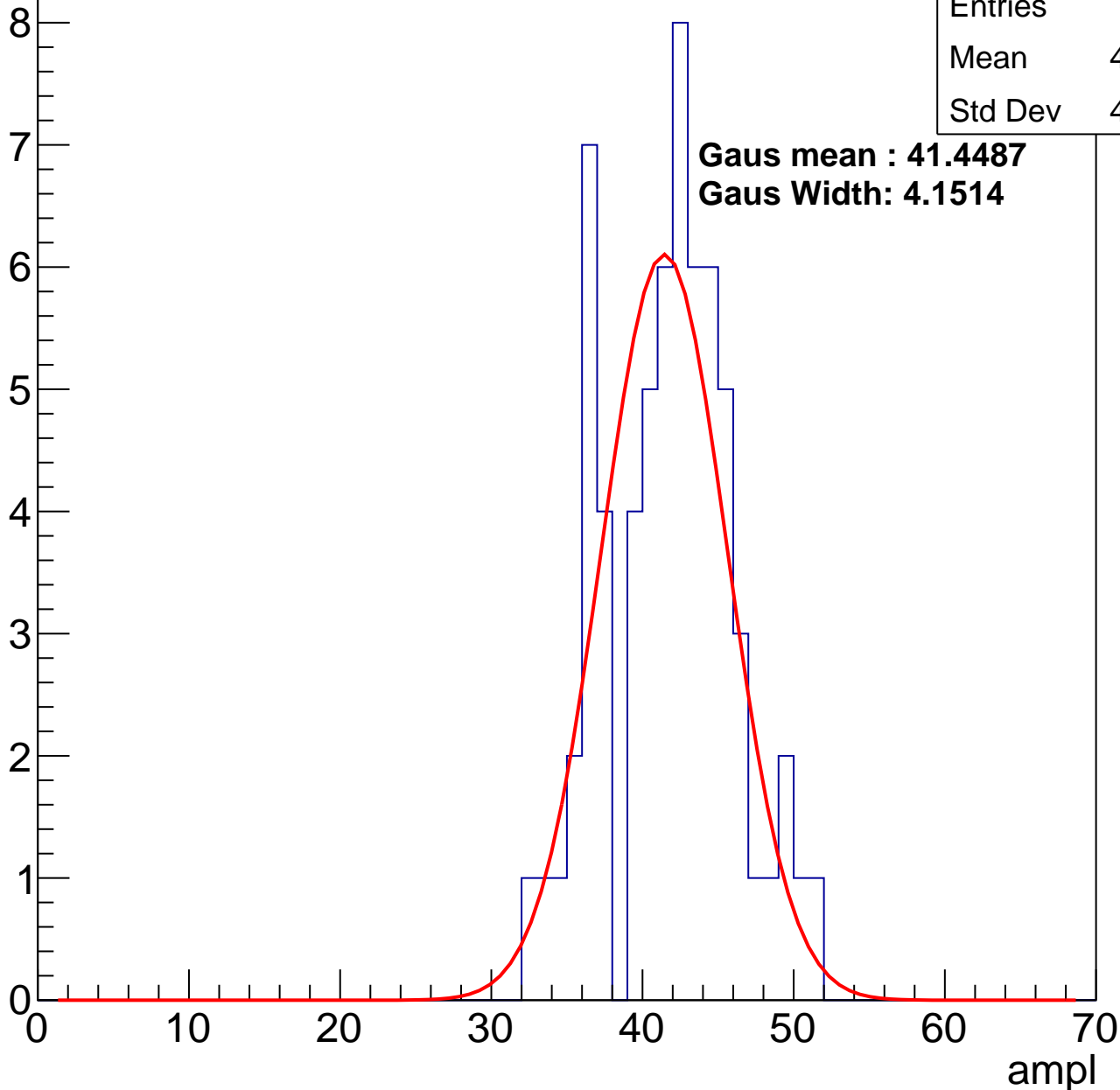
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.32
Std Dev	4.214

Gaus mean : 41.4487

Gaus Width: 4.1514

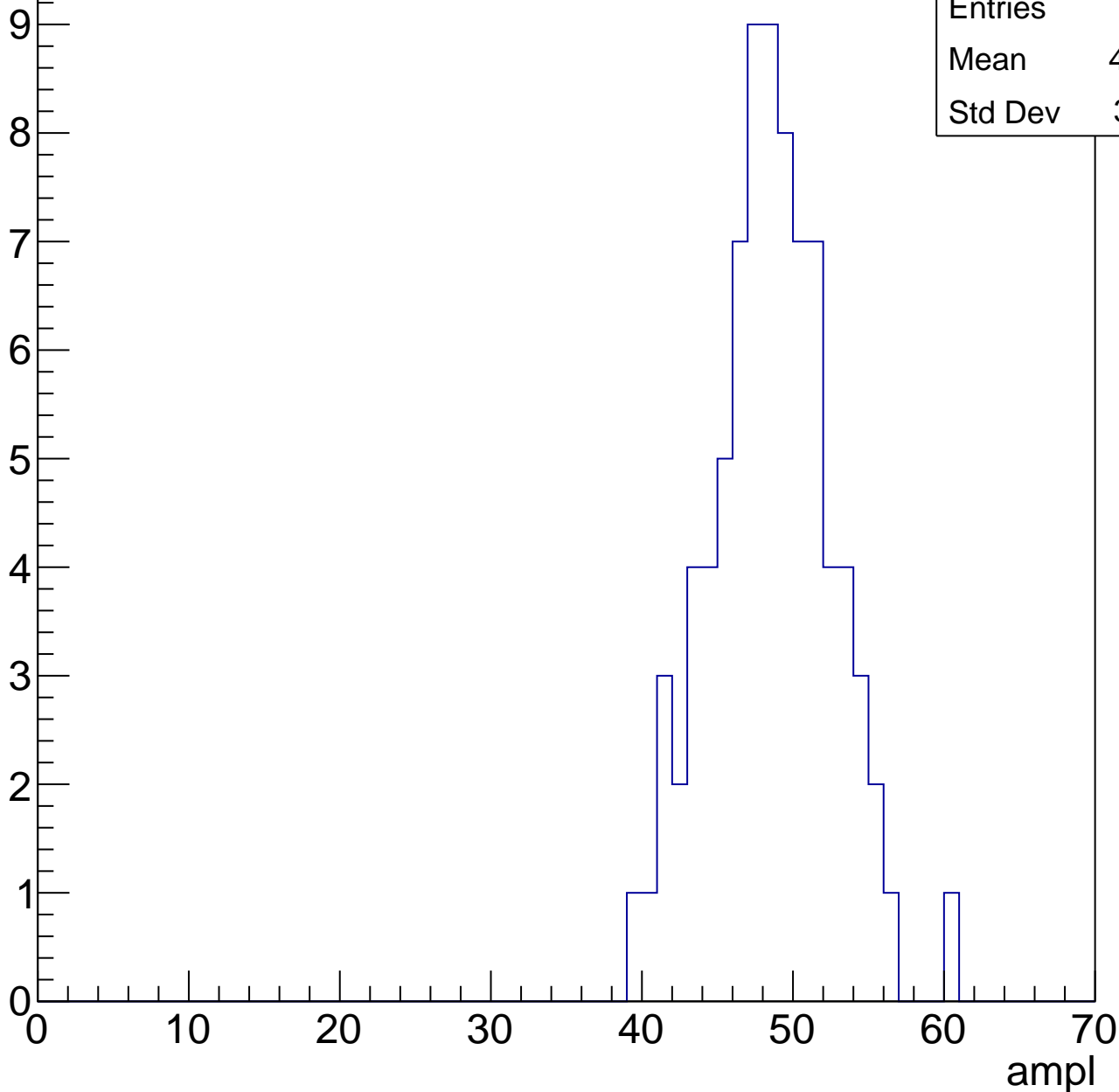


B0L102S, U5-ch20, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	48.09
Std Dev	3.961

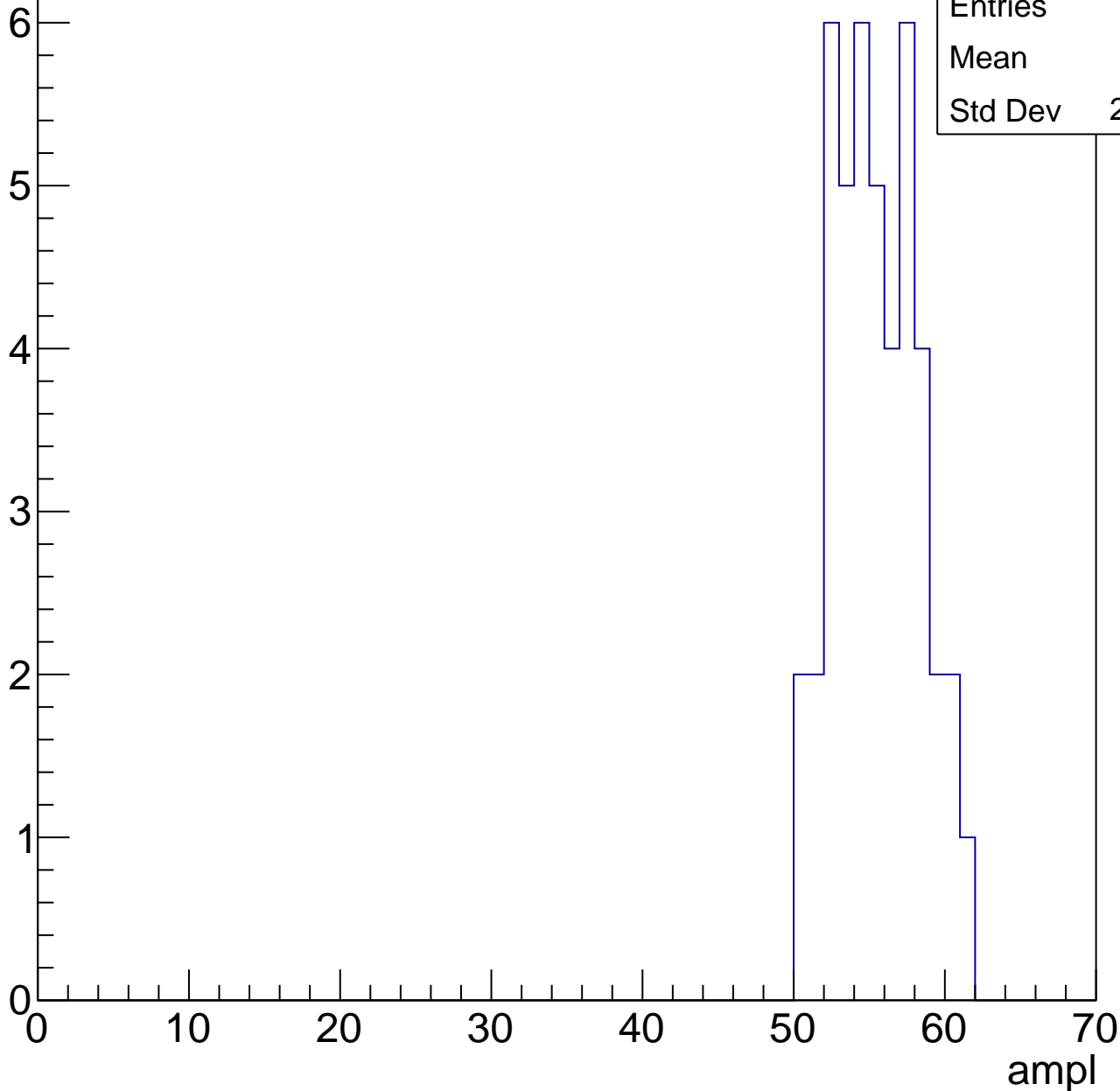


B0L102S, U5-ch20, adc4

calib_packv5_042523_0143.root, FC#12, port B1

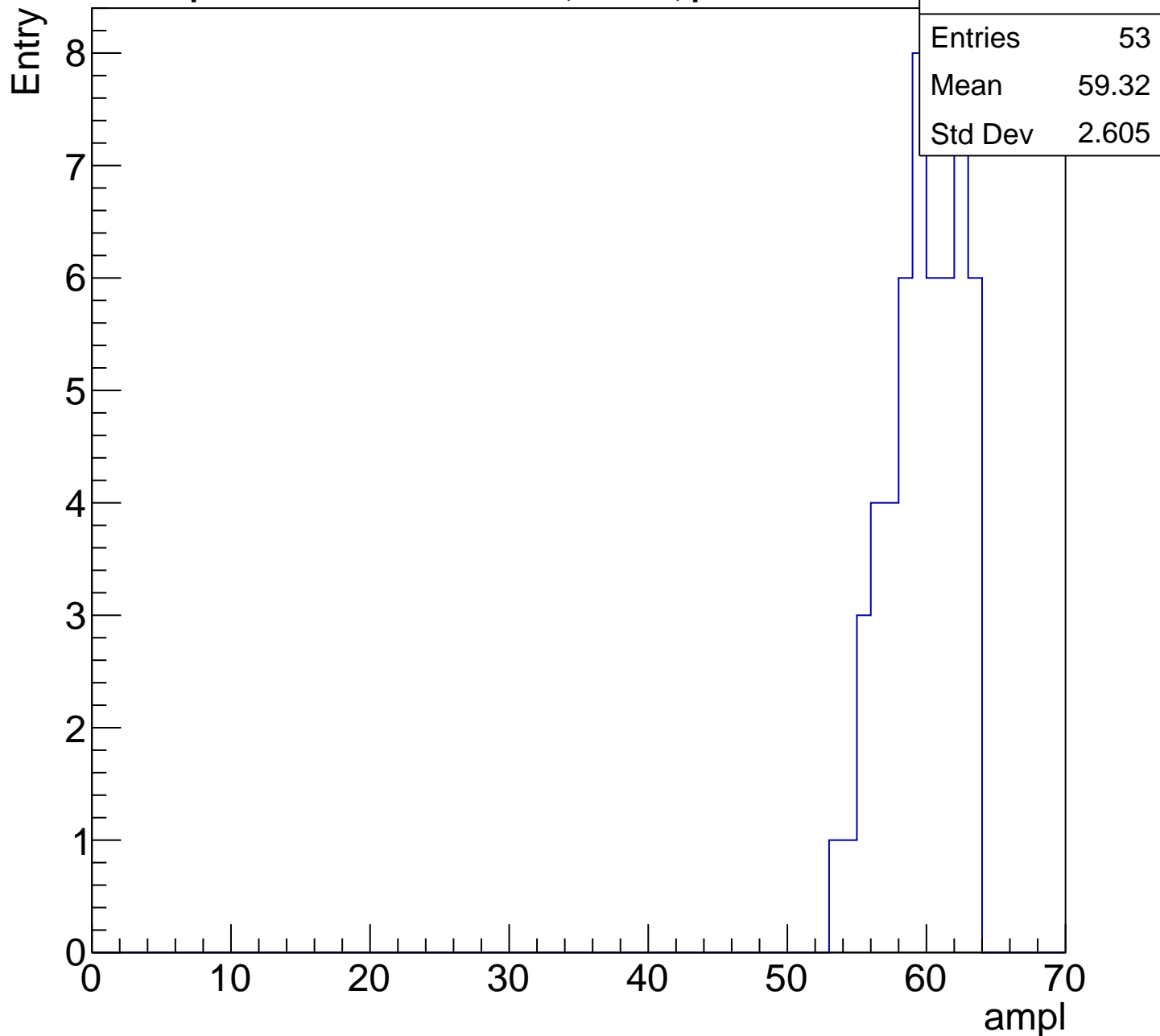
Entry

Entries	45
Mean	55
Std Dev	2.765



B0L102S, U5-ch20, adc5

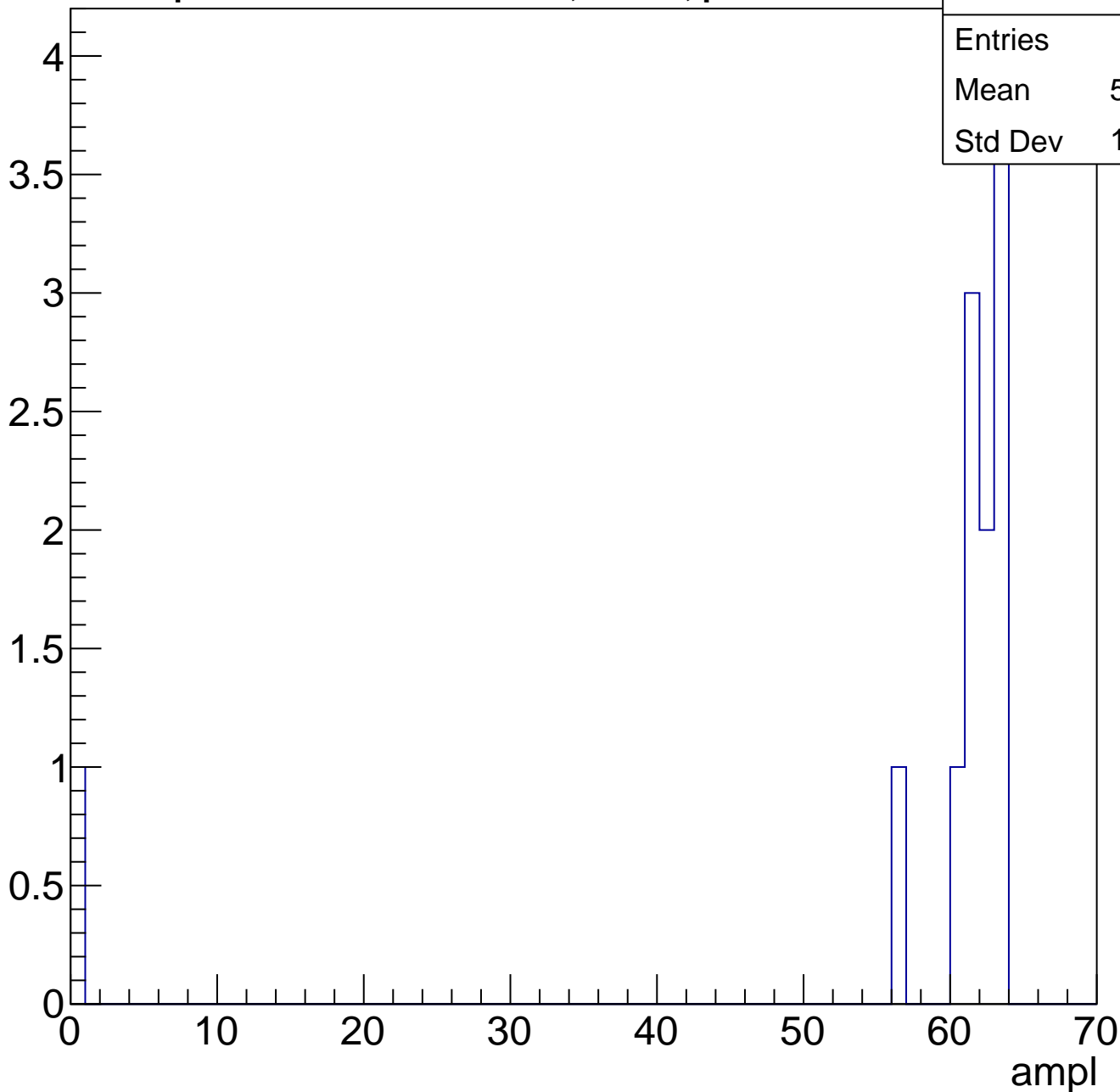
calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch20, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	12
Mean	56.25
Std Dev	17.06

B0L102S, U5-ch20, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch21, adc0

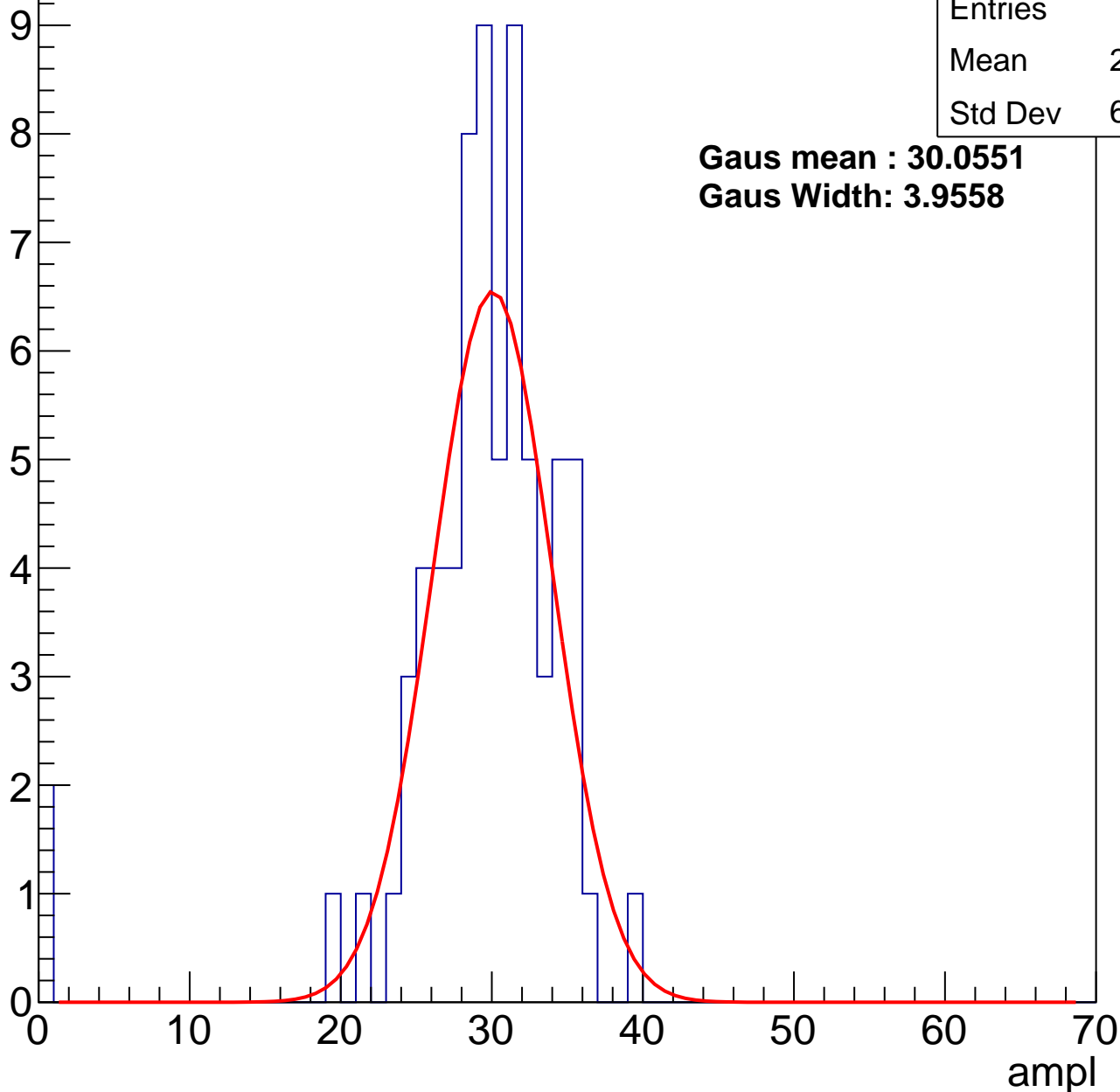
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	28.73
Std Dev	6.125

Gaus mean : 30.0551

Gaus Width: 3.9558



B0L102S, U5-ch21, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	82
Mean	37.41
Std Dev	3.882

Gaus mean : 37.9695

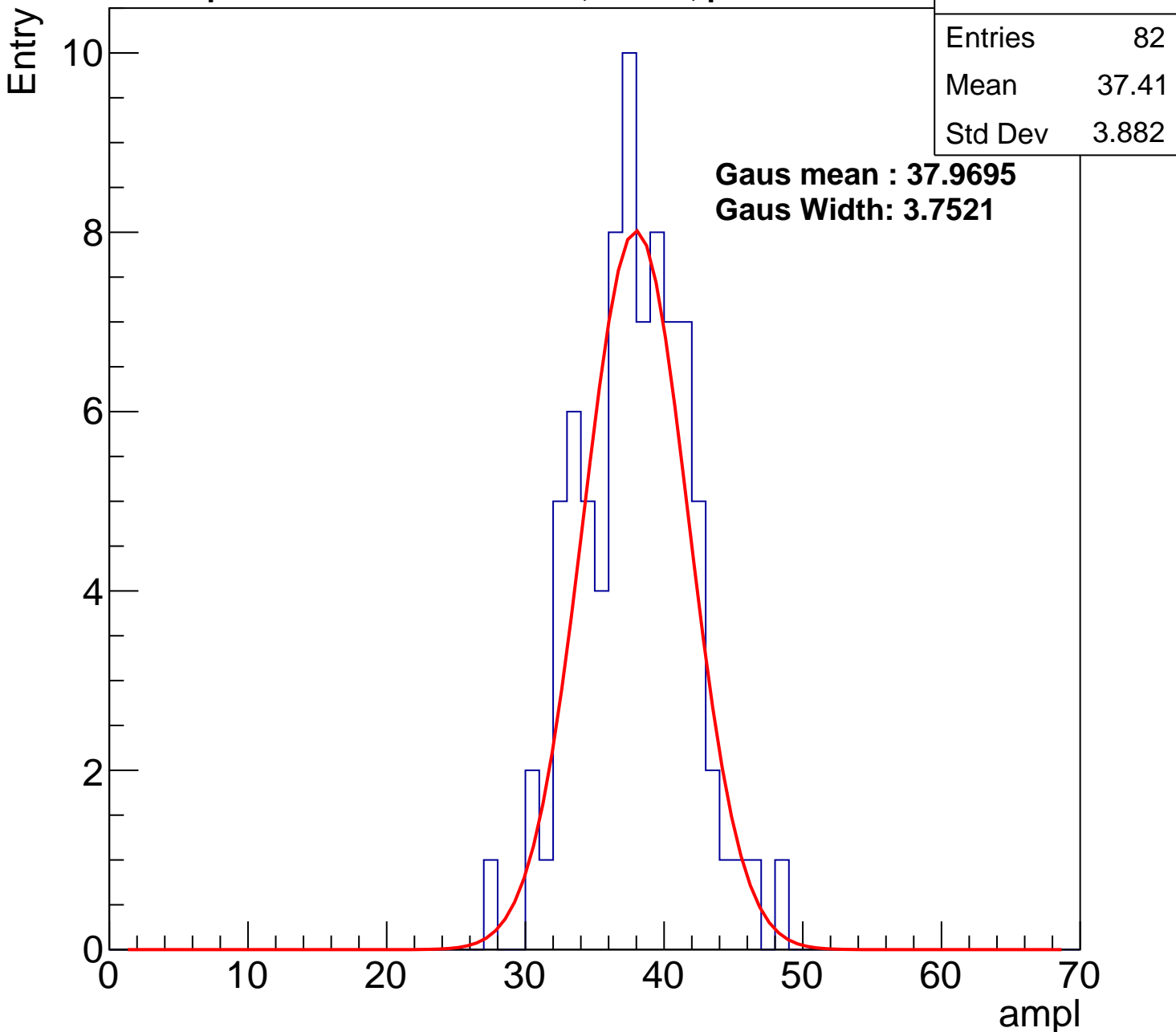
Gaus Width: 3.7521

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch21, adc2

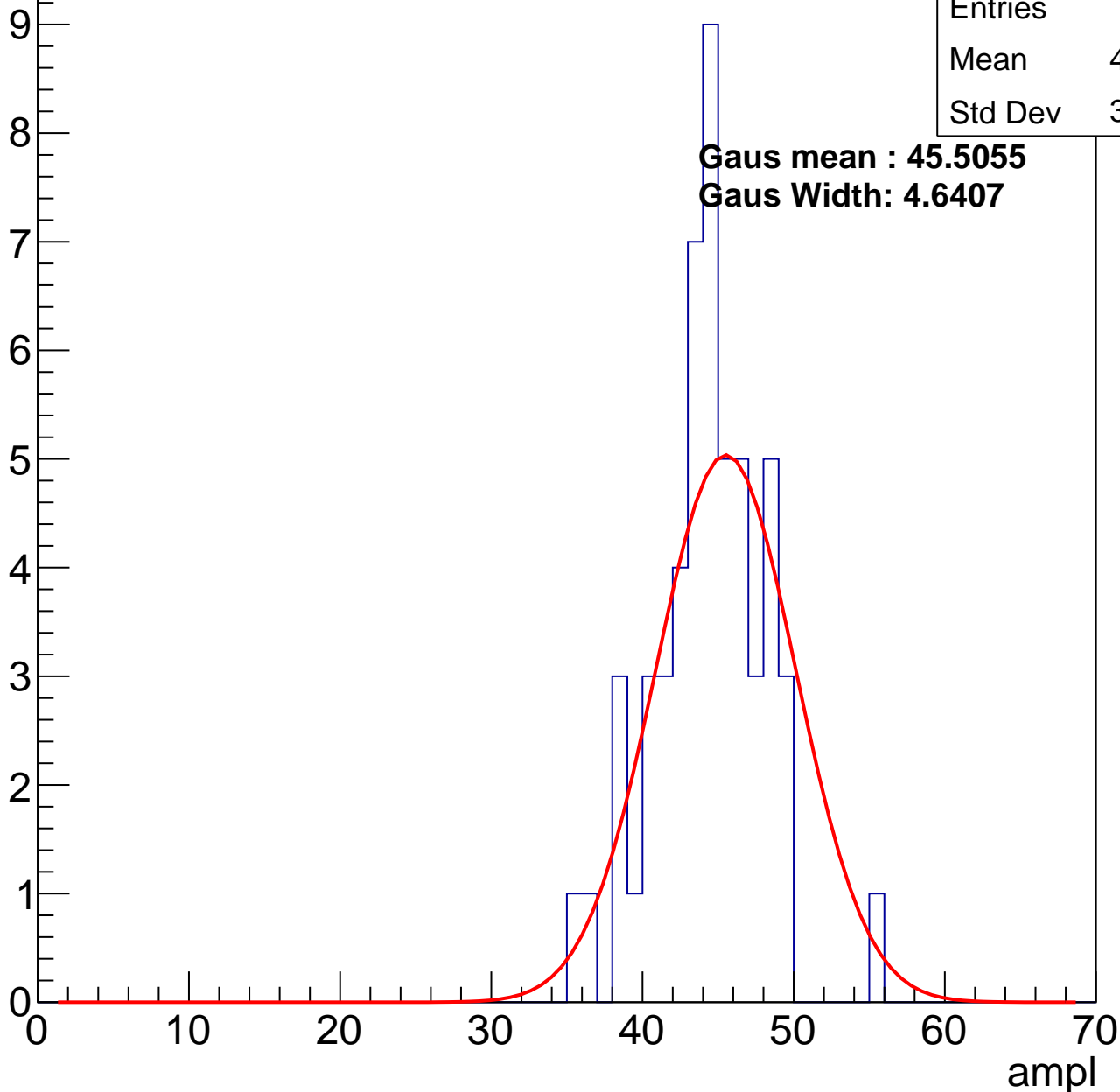
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	43.89
Std Dev	3.609

Gaus mean : 45.5055

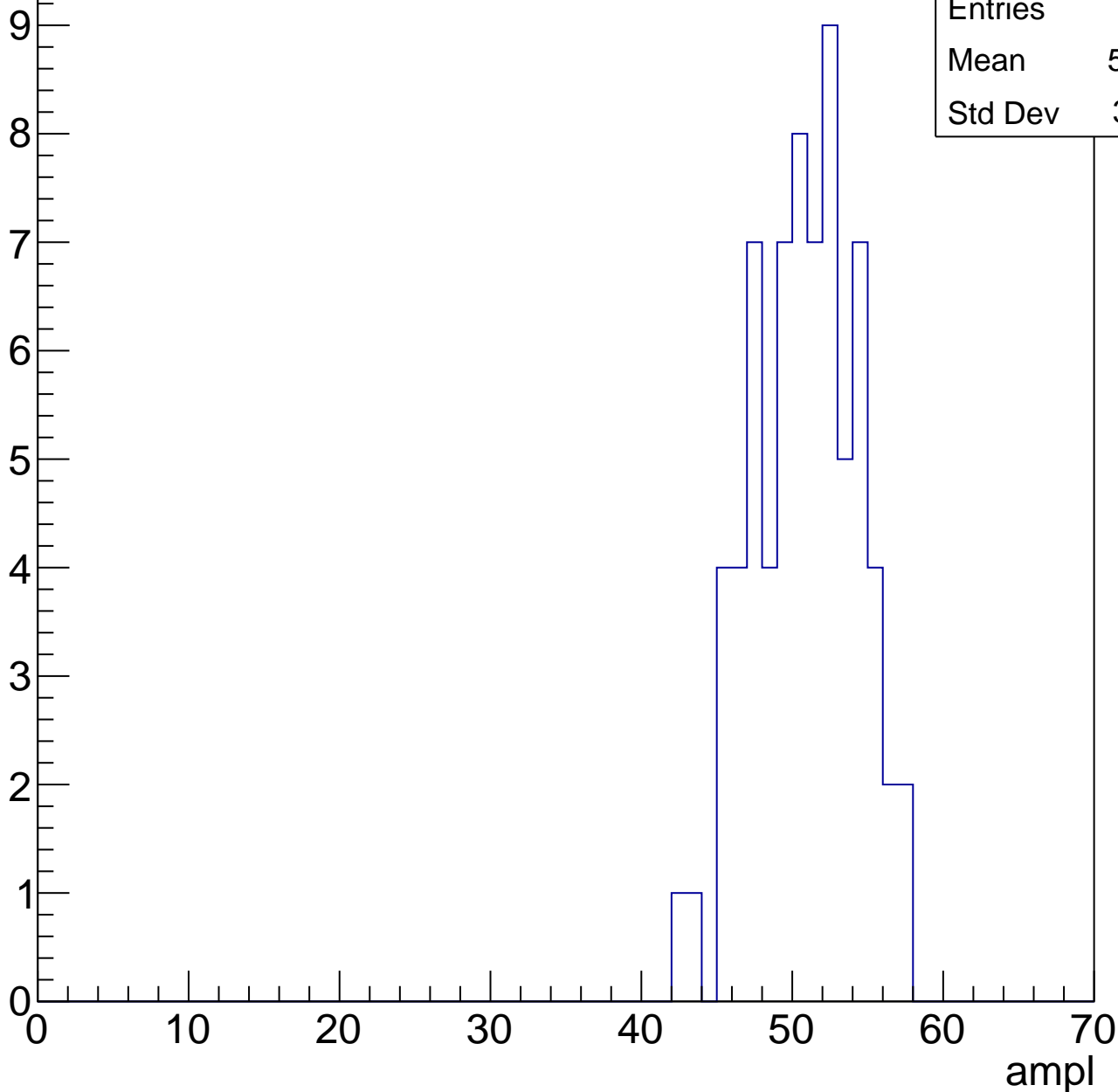
Gaus Width: 4.6407



B0L102S, U5-ch21, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

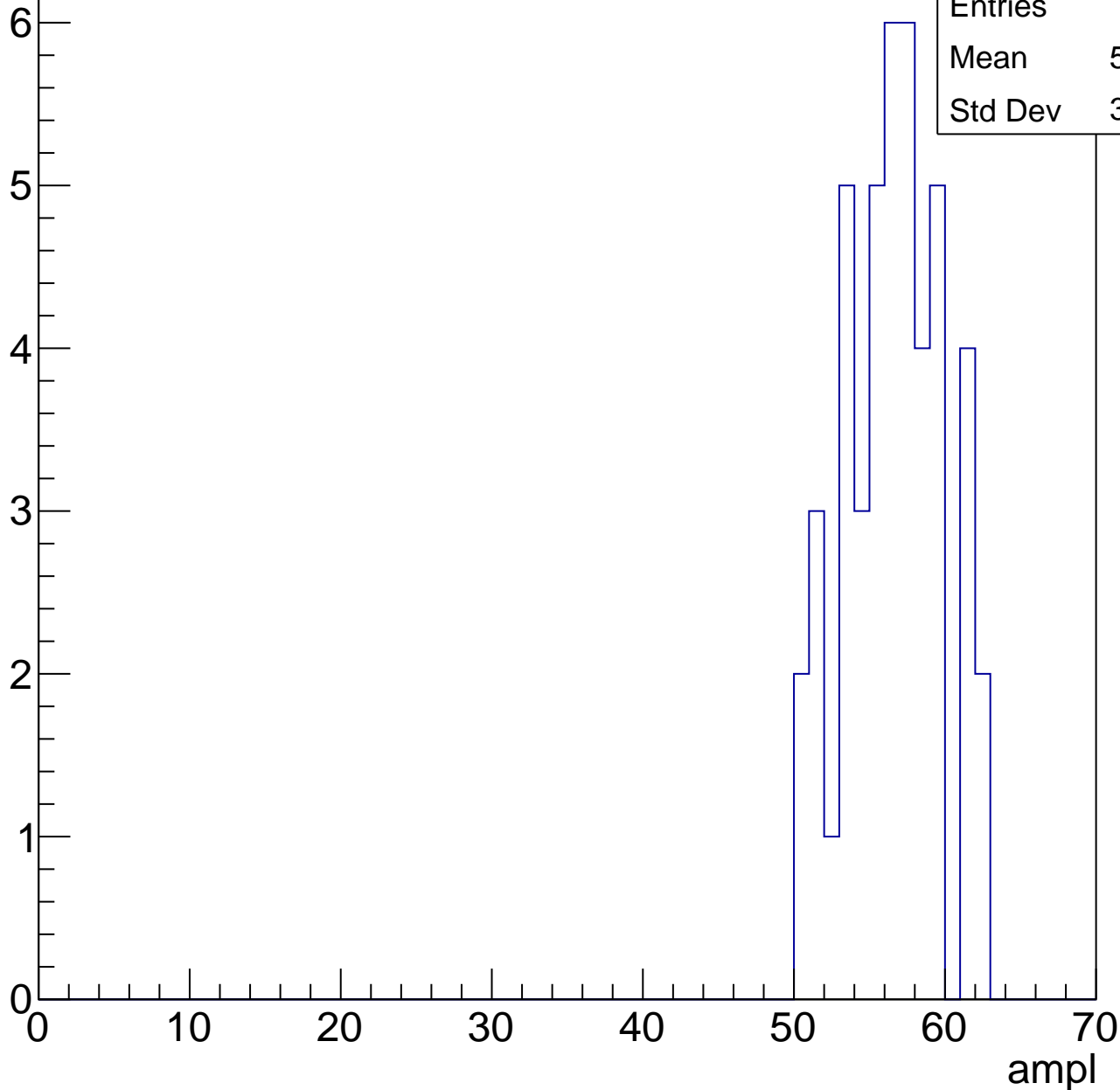


Entries	72
Mean	50.38
Std Dev	3.381

B0L102S, U5-ch21, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	46
Mean	56.09
Std Dev	3.175

B0L102S, U5-ch21, adc5

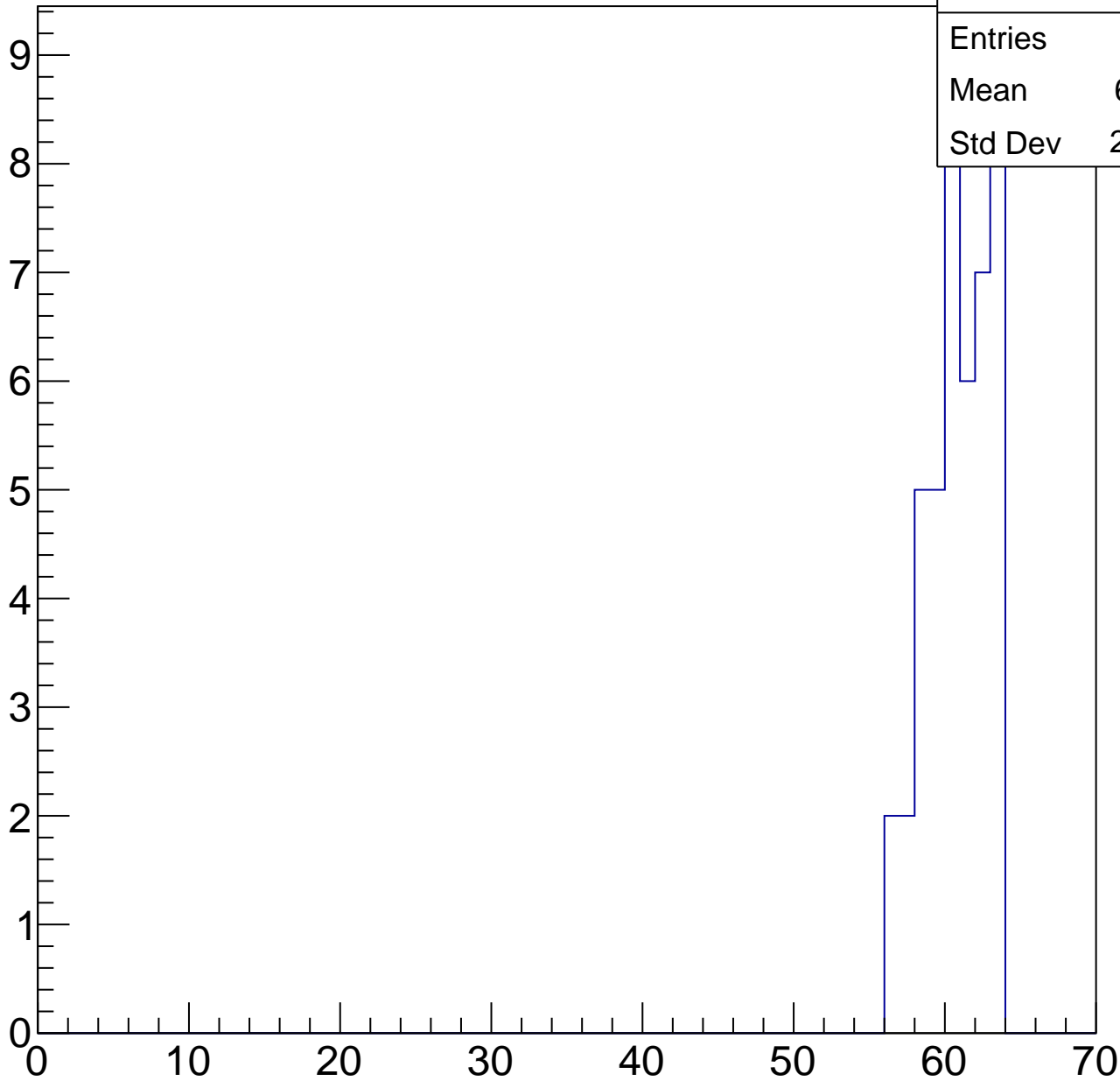
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	60.41
Std Dev	2.037

ampl



B0L102S, U5-ch21, adc6

calib_packv5_042523_0143.root, FC#12, port B1

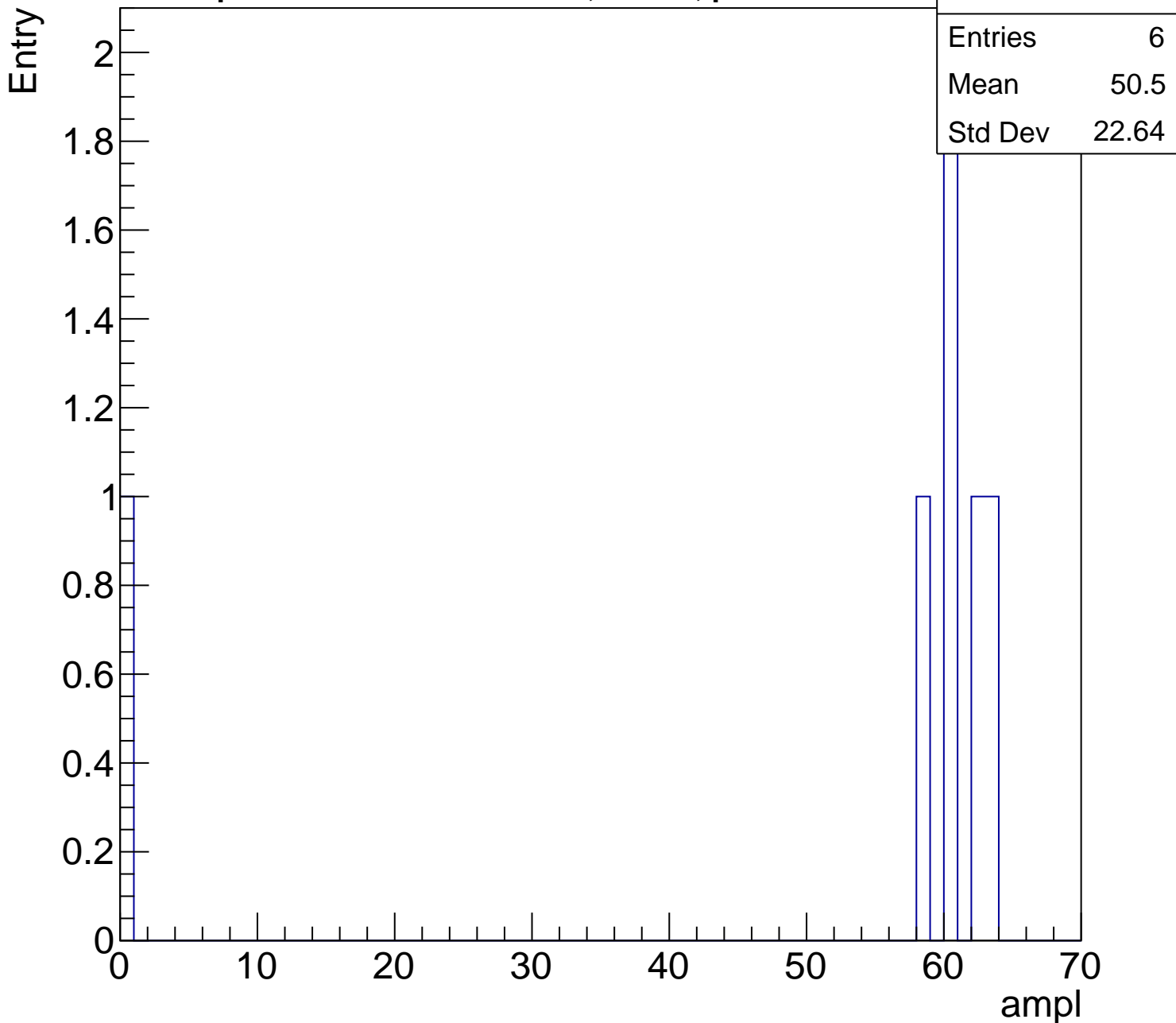
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.5
Std Dev	22.64

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch21, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch22, adc0

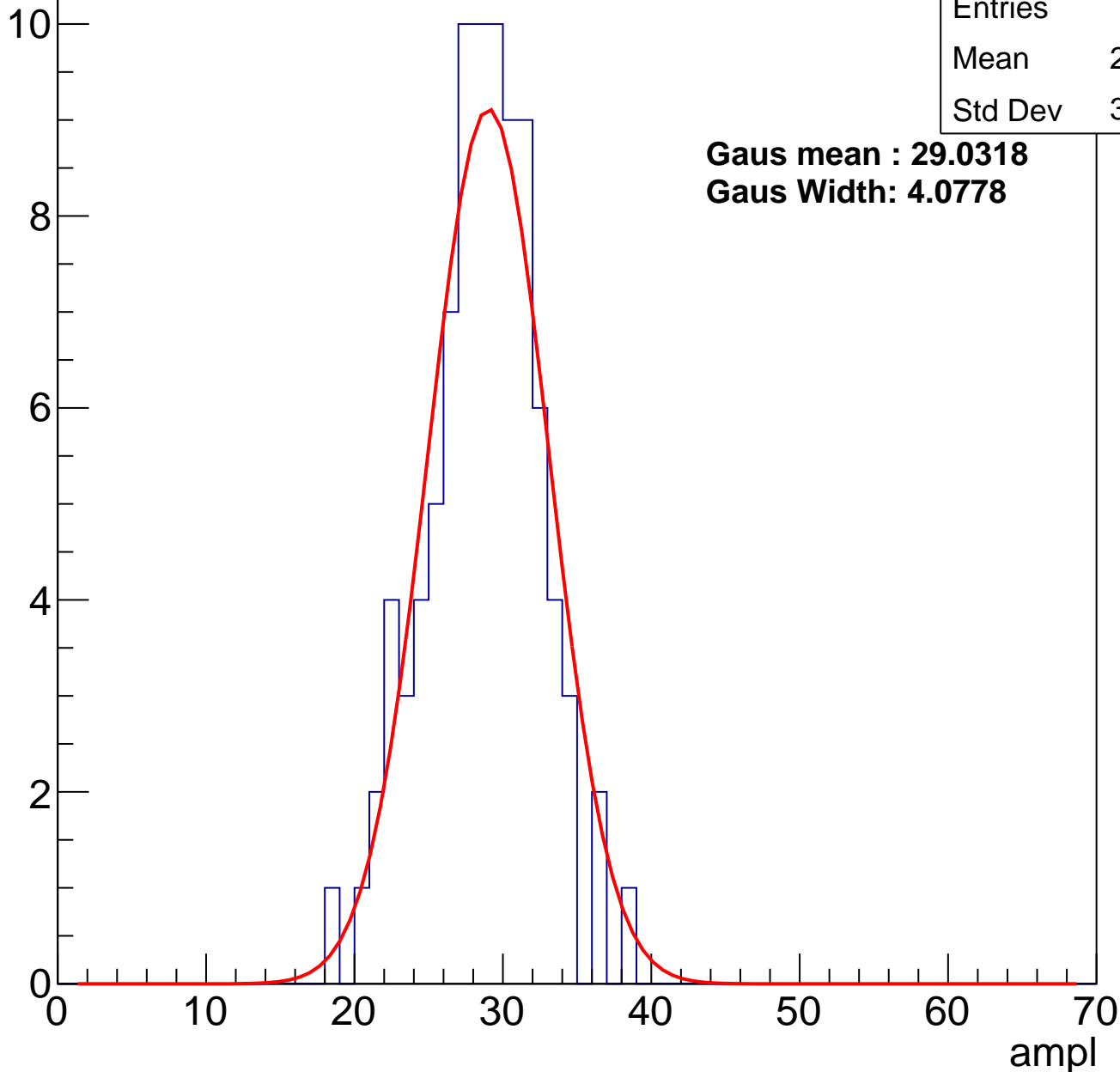
calib_packv5_042523_0143.root, FC#12, port B1

Entries	91
Mean	28.19
Std Dev	3.756

Gaus mean : 29.0318

Gaus Width: 4.0778

Entry



B0L102S, U5-ch22, adc1

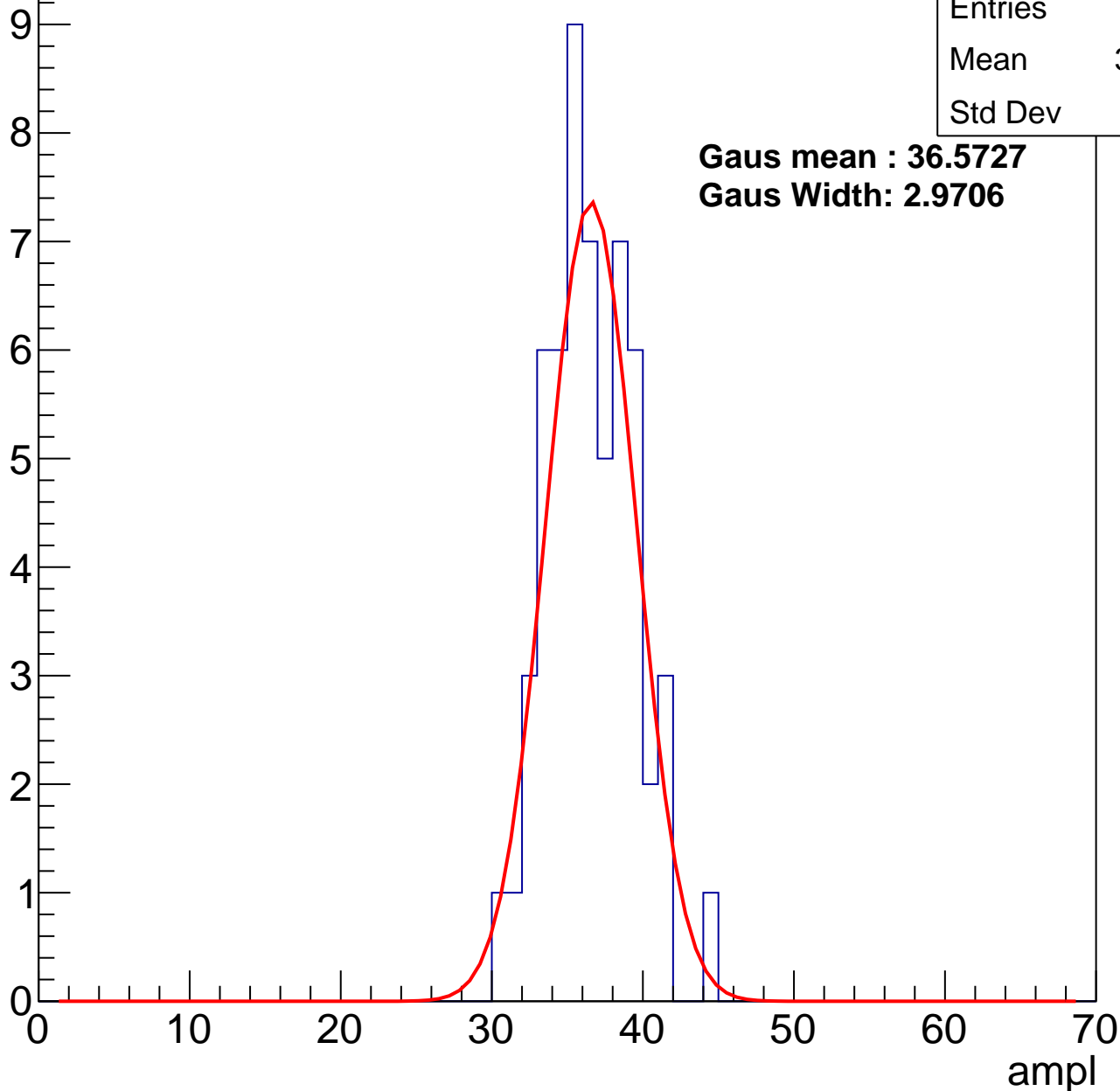
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	36.11
Std Dev	2.82

Gaus mean : 36.5727

Gaus Width: 2.9706



B0L102S, U5-ch22, adc2

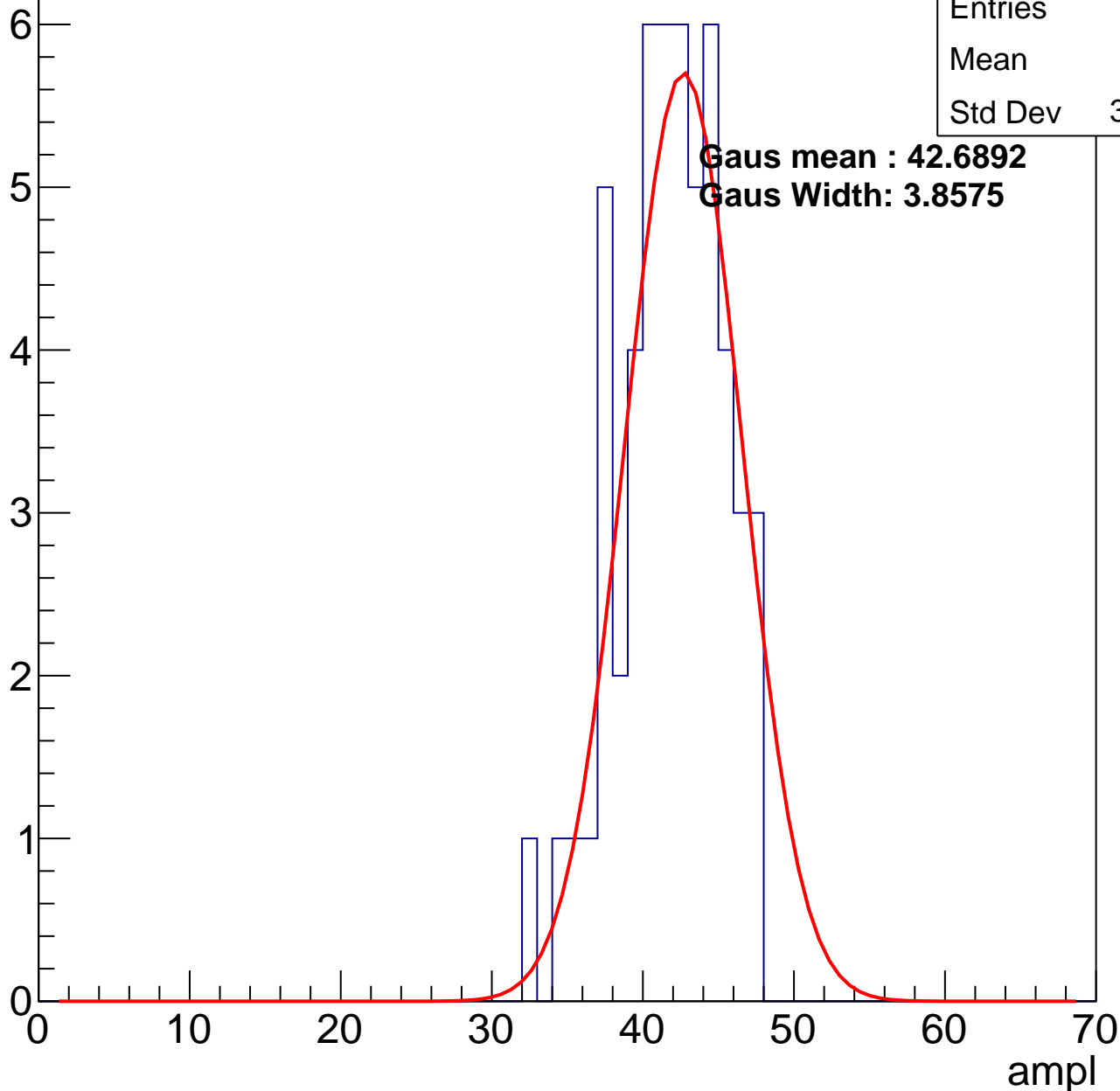
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	41.3
Std Dev	3.424

Gaus mean : 42.6892

Gaus Width: 3.8575

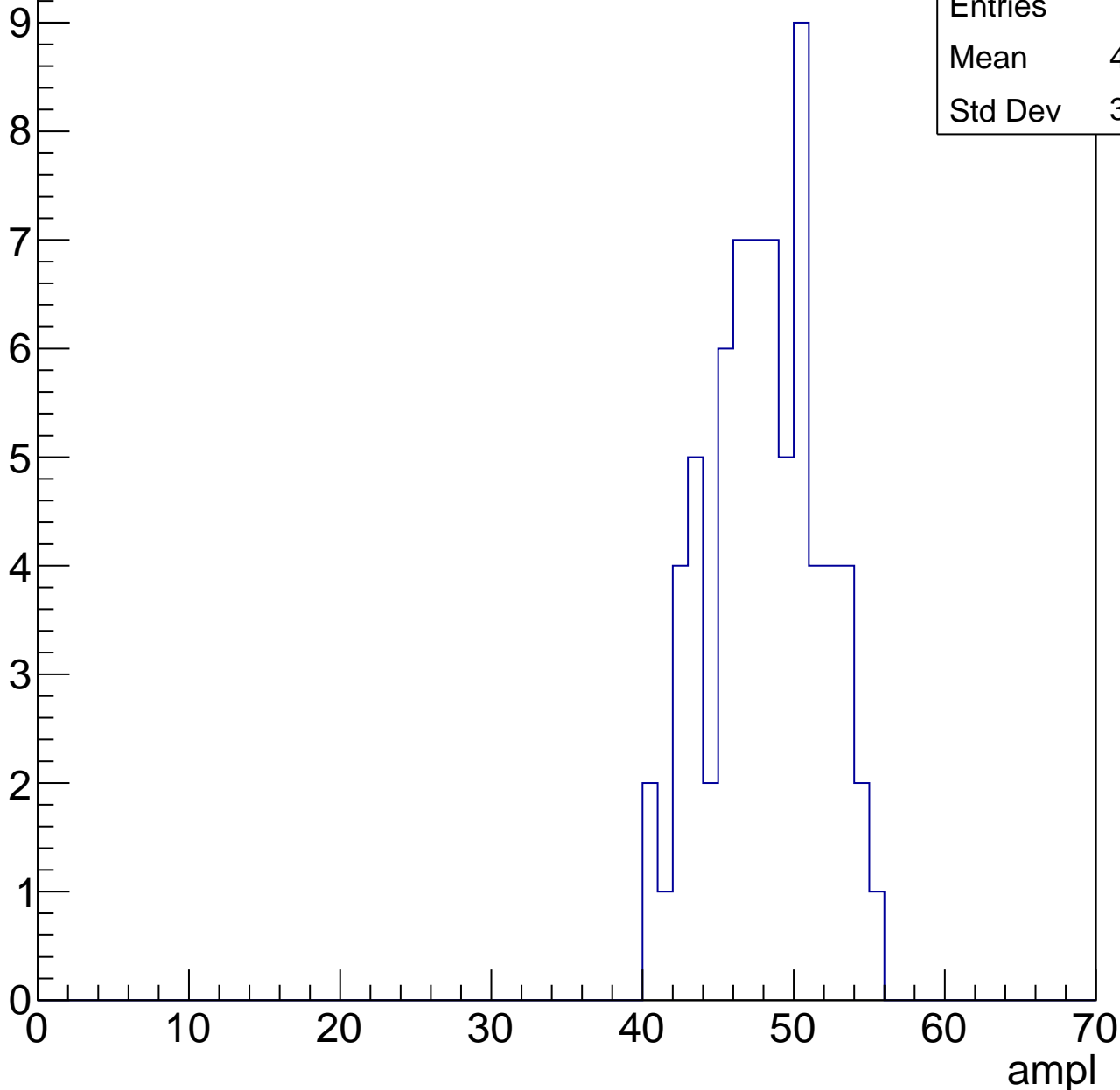


B0L102S, U5-ch22, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	47.59
Std Dev	3.619

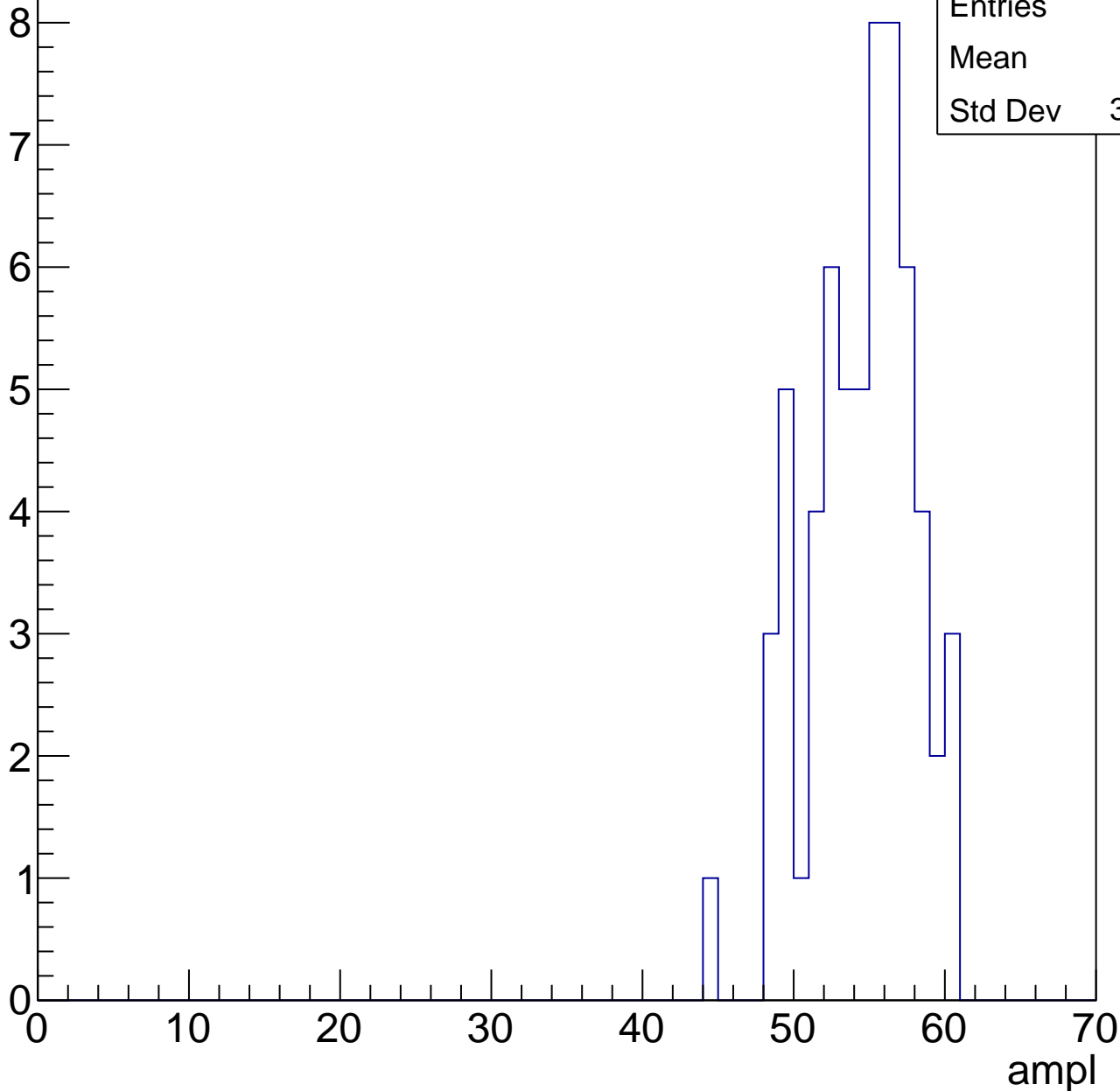


B0L102S, U5-ch22, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

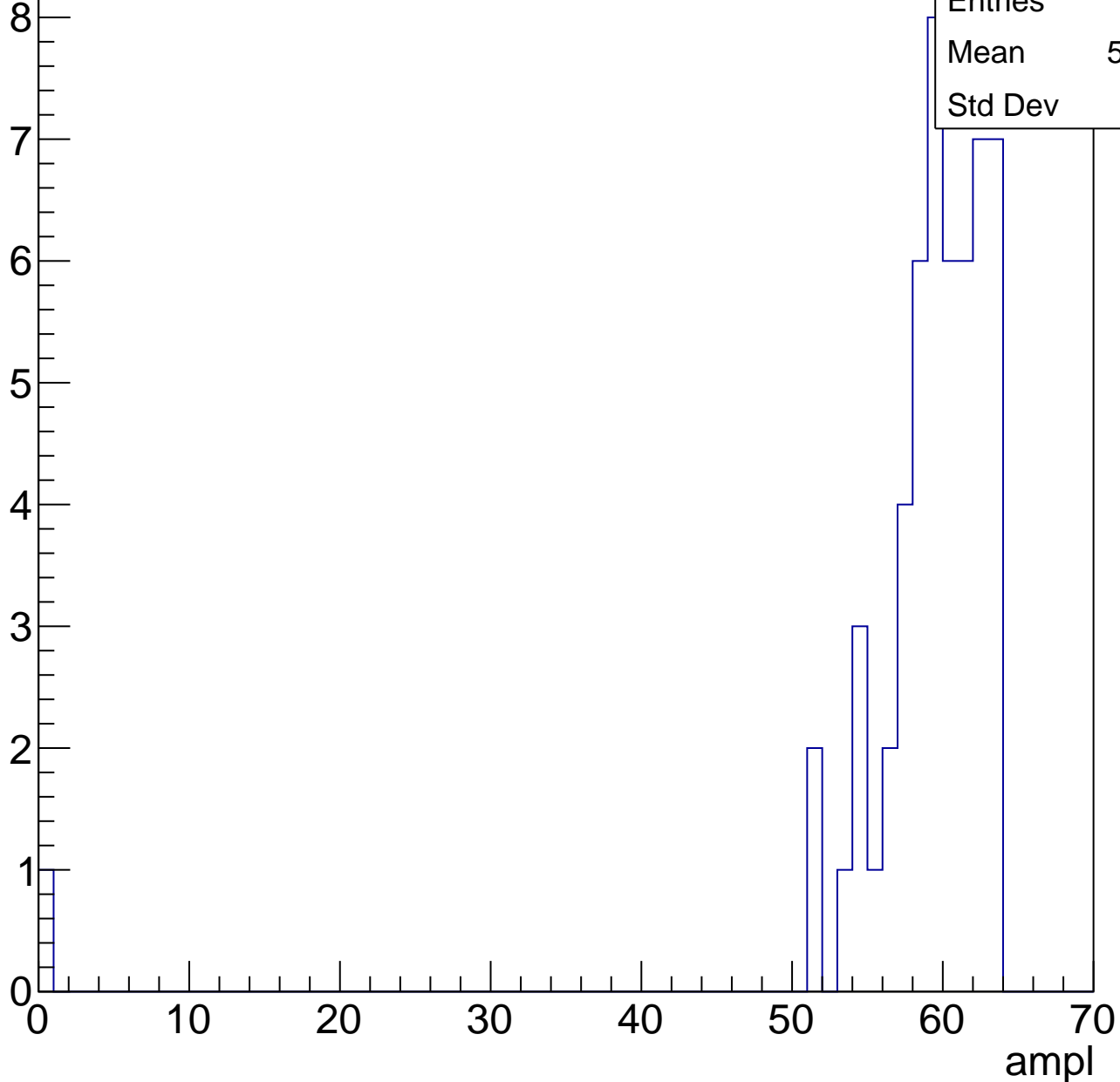
Entries	61
Mean	54
Std Dev	3.459



B0L102S, U5-ch22, adc5

calib_packv5_042523_0143.root, FC#12, port B1

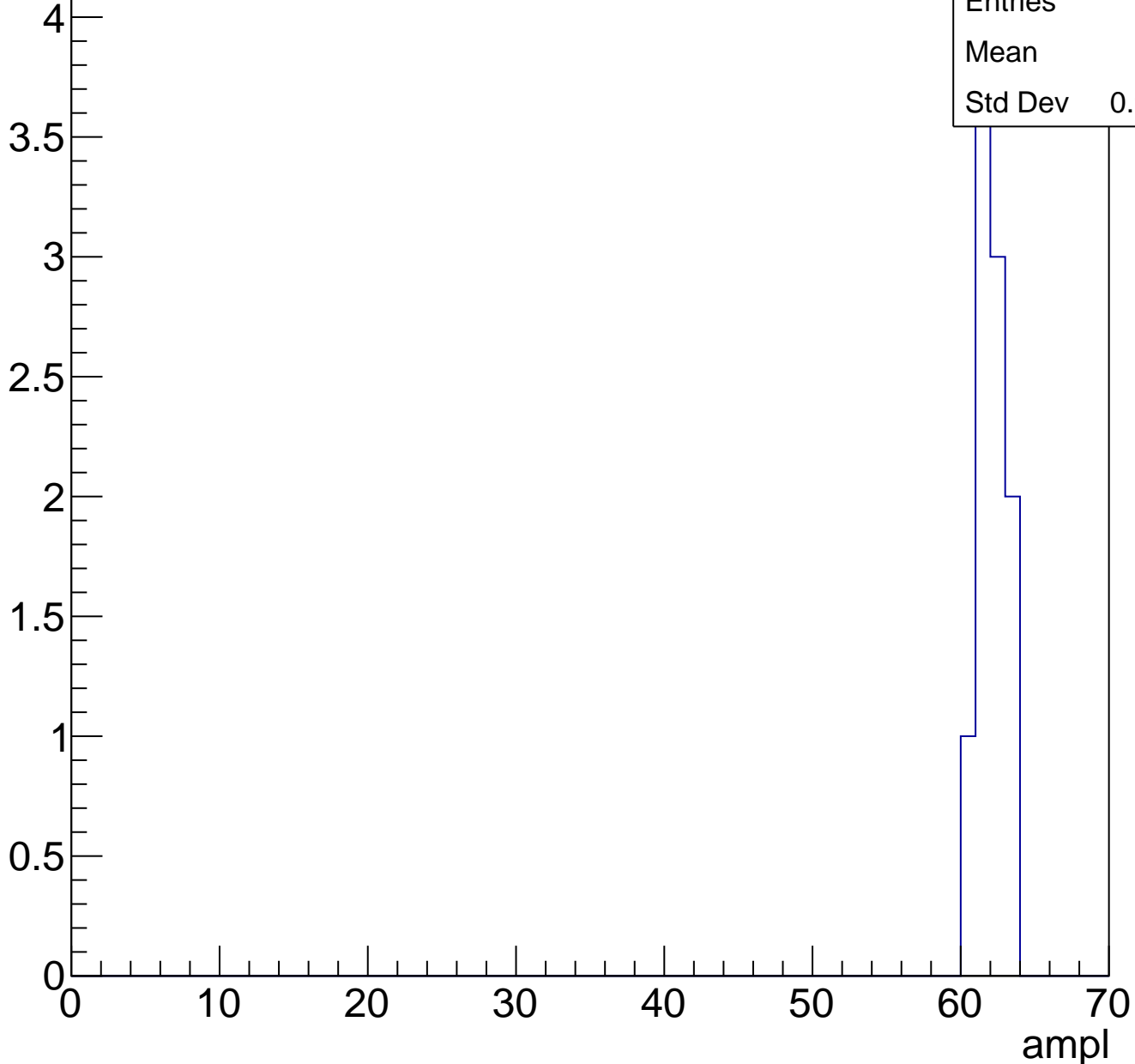
Entry



B0L102S, U5-ch22, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	10
Mean	61.6
Std Dev	0.9165

B0L102S, U5-ch22, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch23, adc0

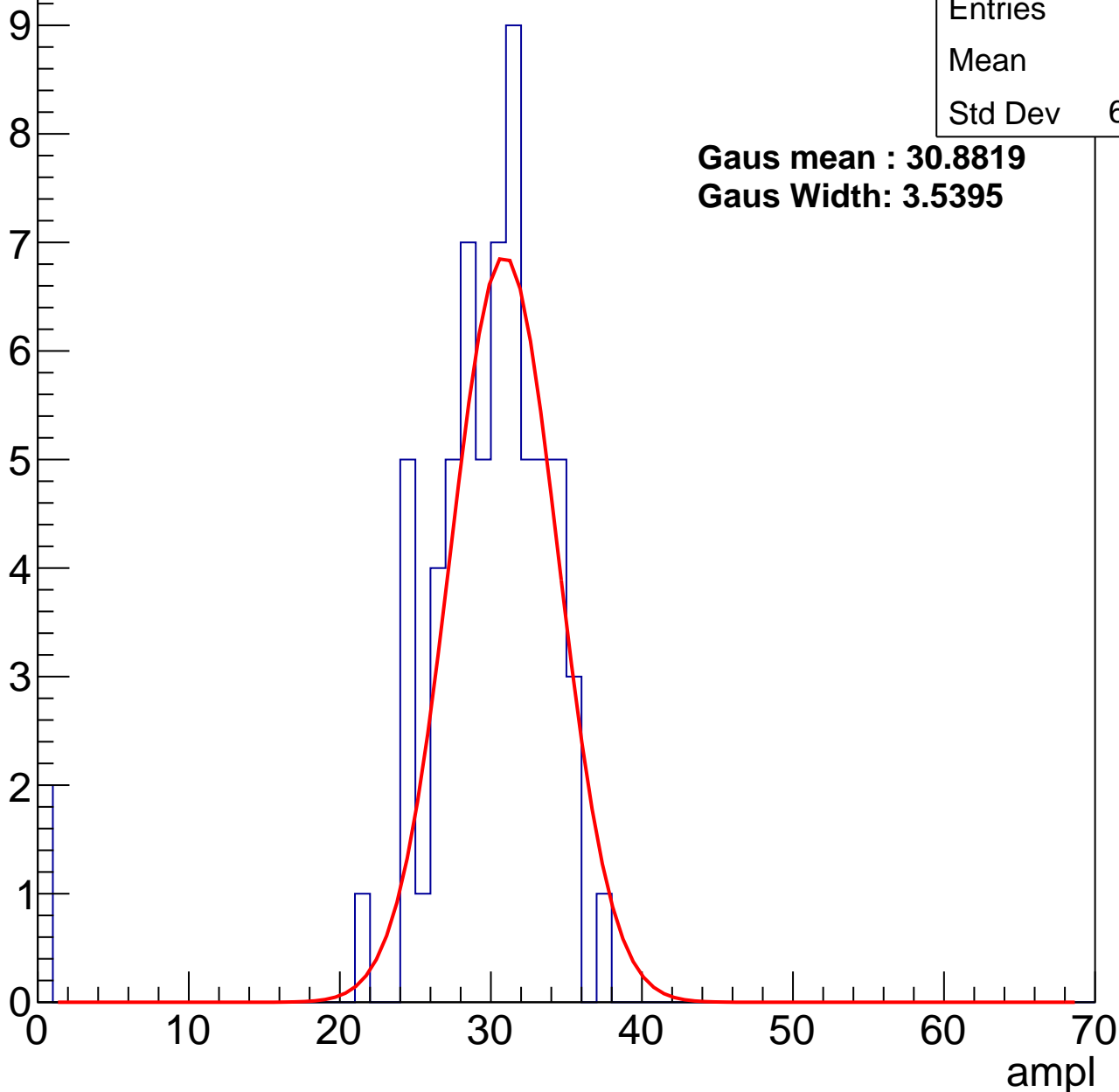
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	28.8
Std Dev	6.092

Gaus mean : 30.8819

Gaus Width: 3.5395



B0L102S, U5-ch23, adc1

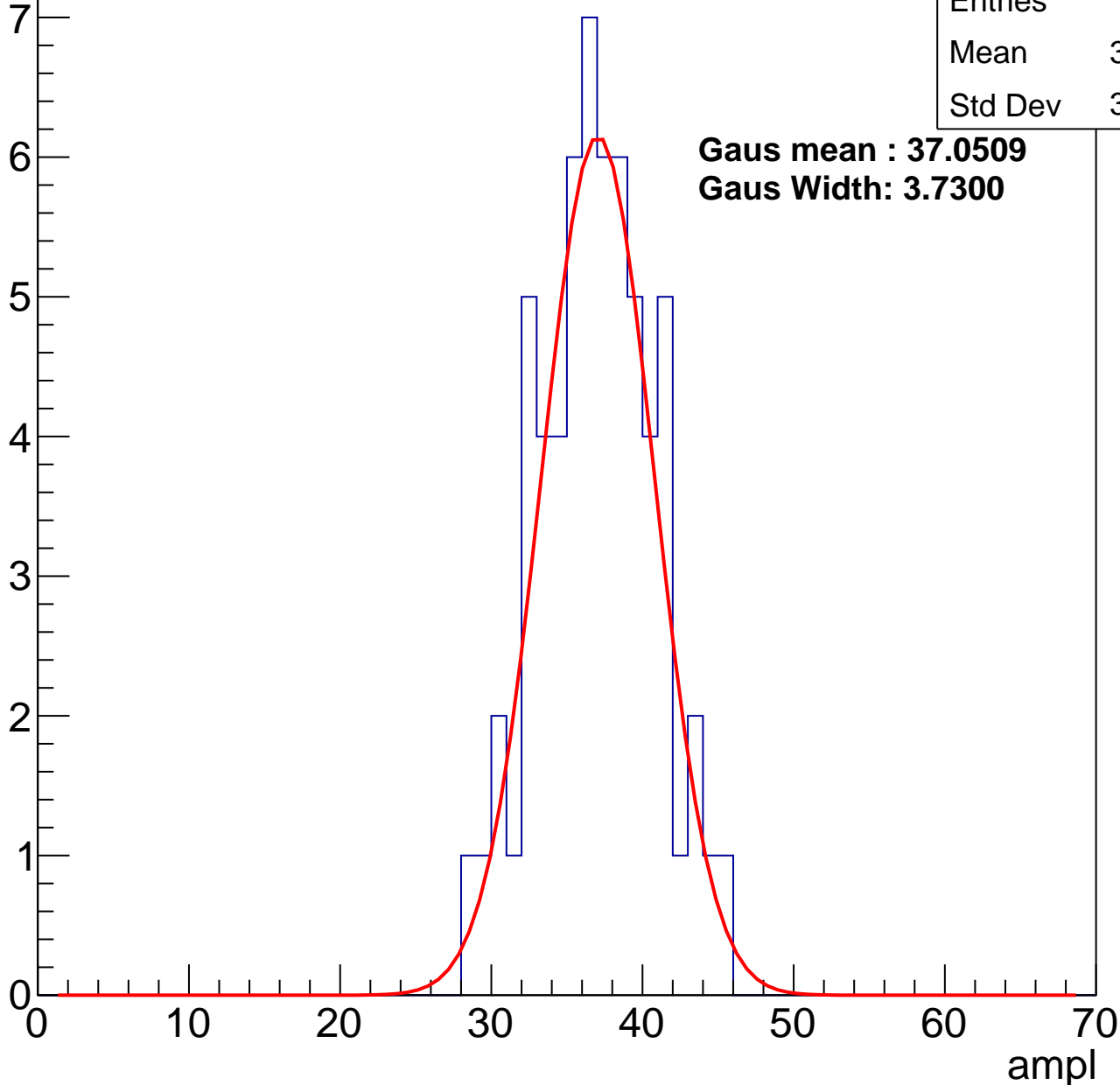
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	36.53
Std Dev	3.753

Gaus mean : 37.0509

Gaus Width: 3.7300



B0L102S, U5-ch23, adc2

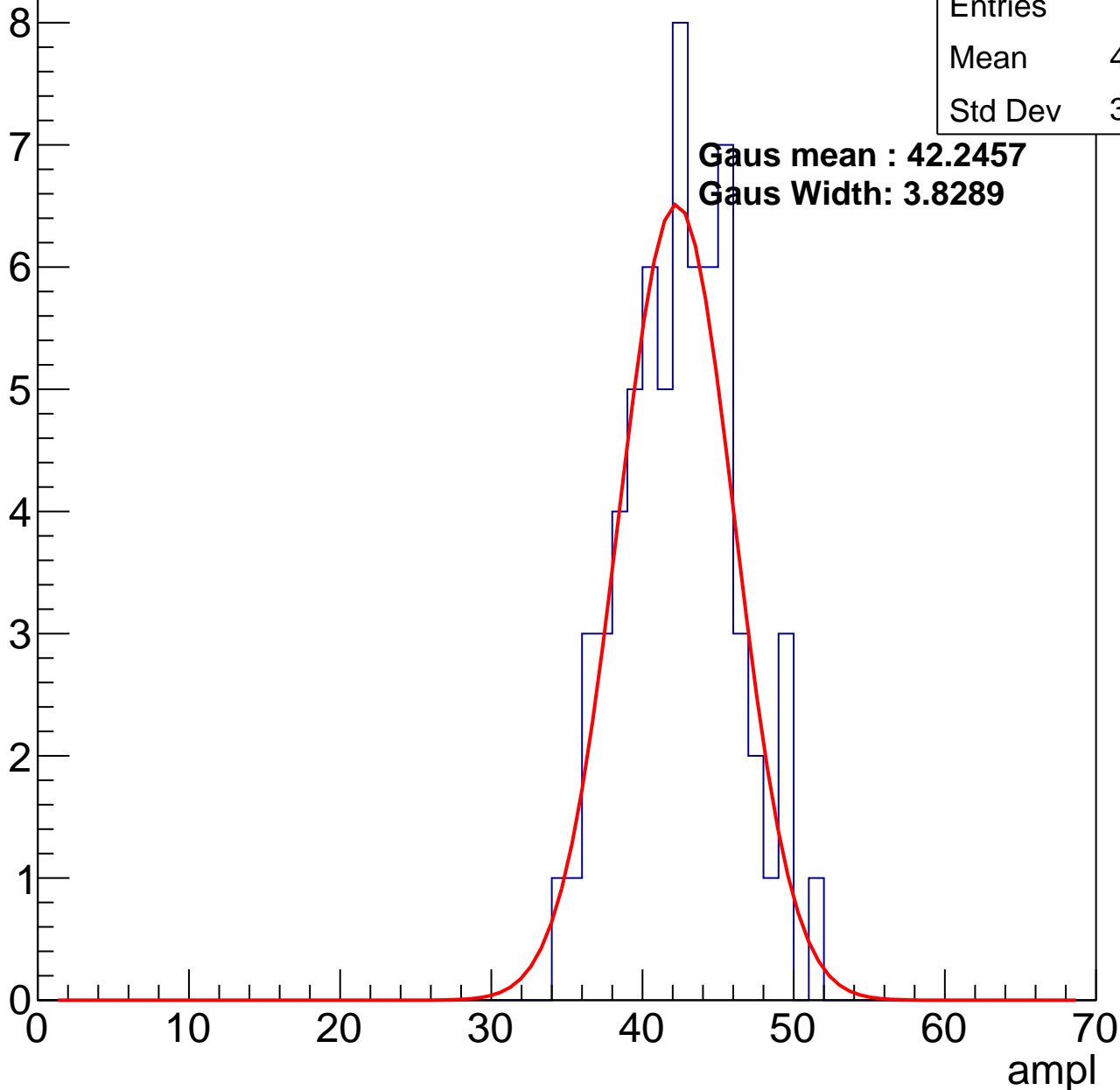
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.02
Std Dev	3.698

Gaus mean : 42.2457

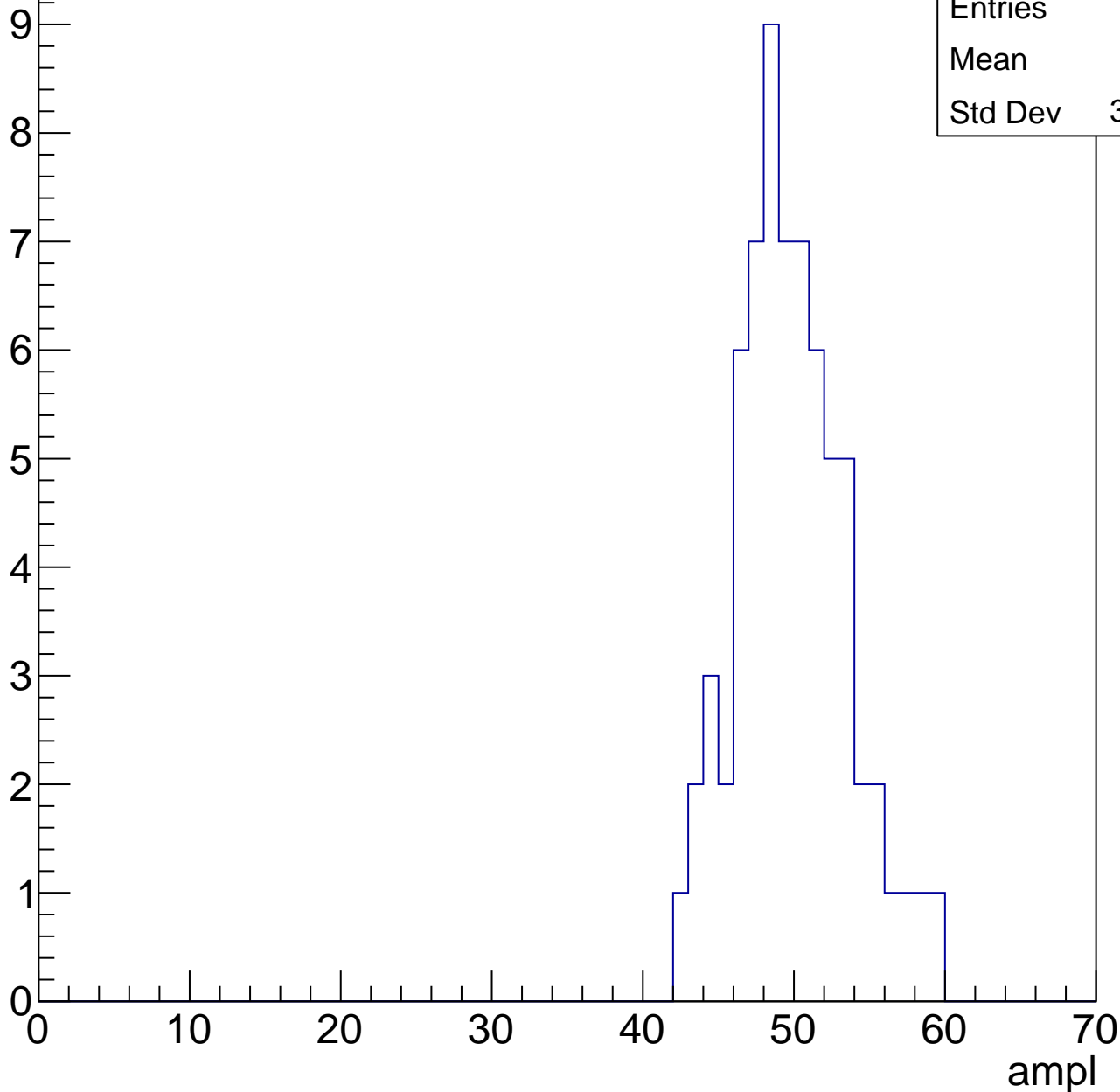
Gaus Width: 3.8289



B0L102S, U5-ch23, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



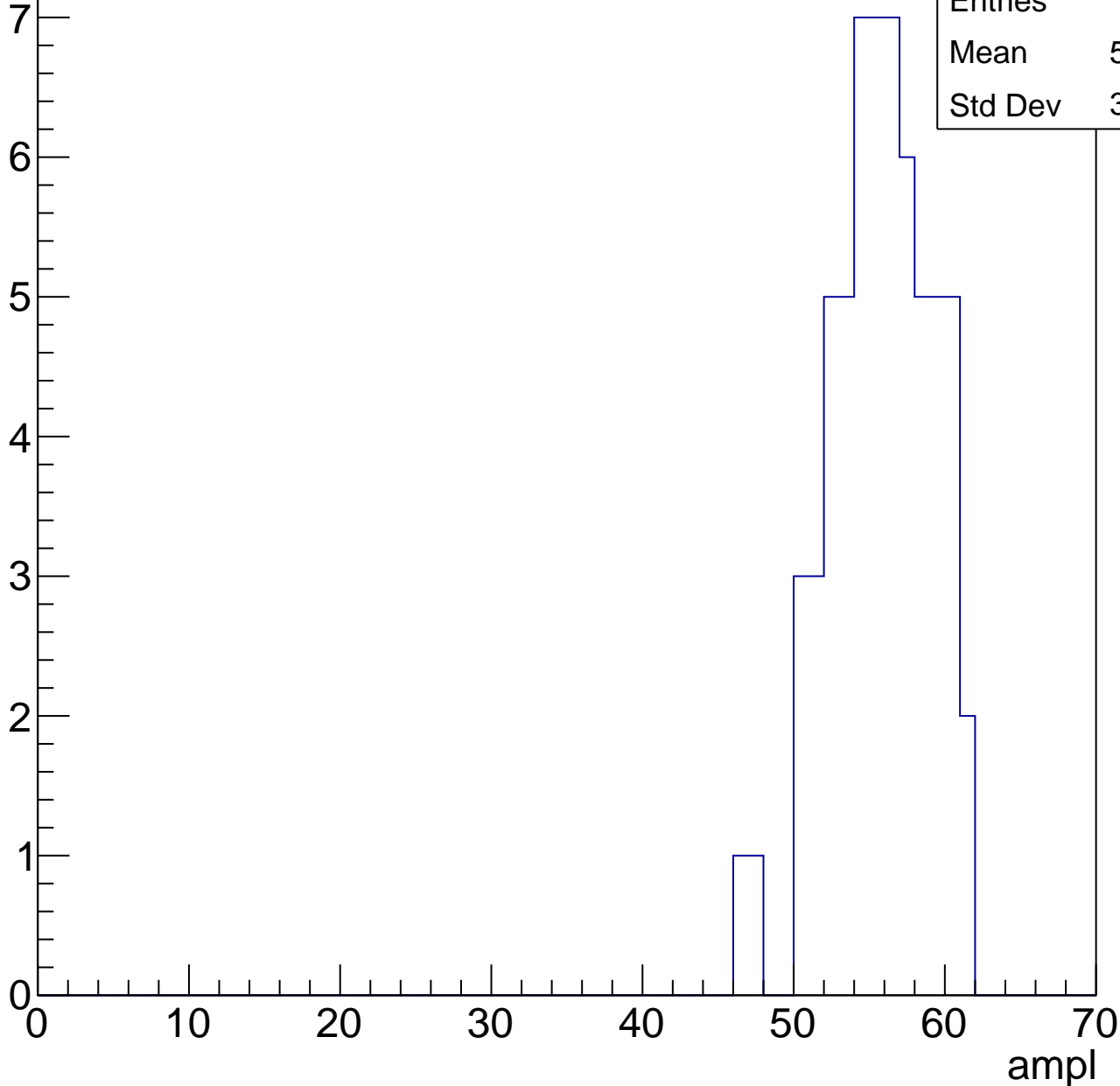
Entries	68
Mean	49.4
Std Dev	3.598

B0L102S, U5-ch23, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	55.24
Std Dev	3.334

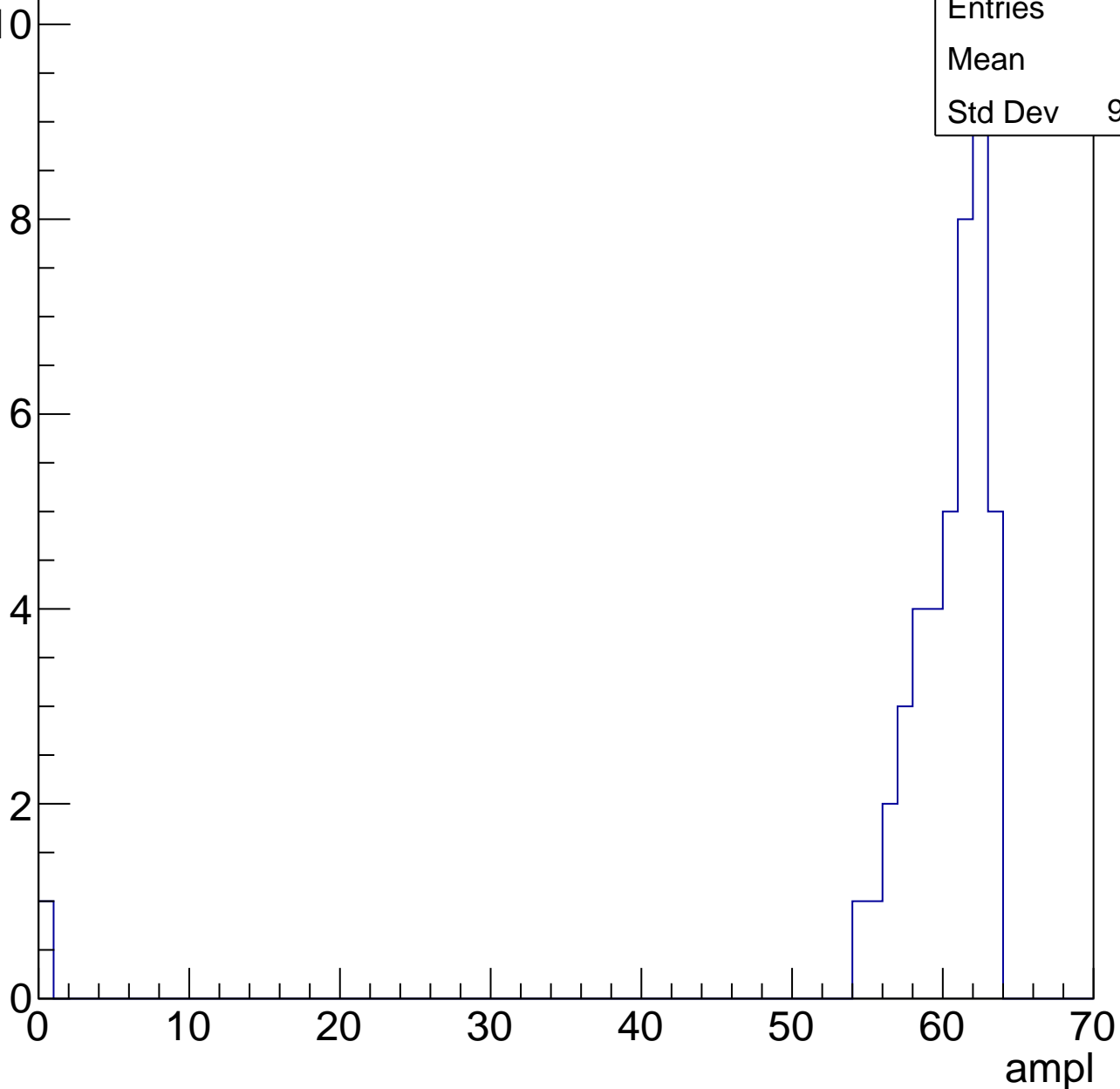


B0L102S, U5-ch23, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.7
Std Dev	9.243



B0L102S, U5-ch23, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch23, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch24, adc0

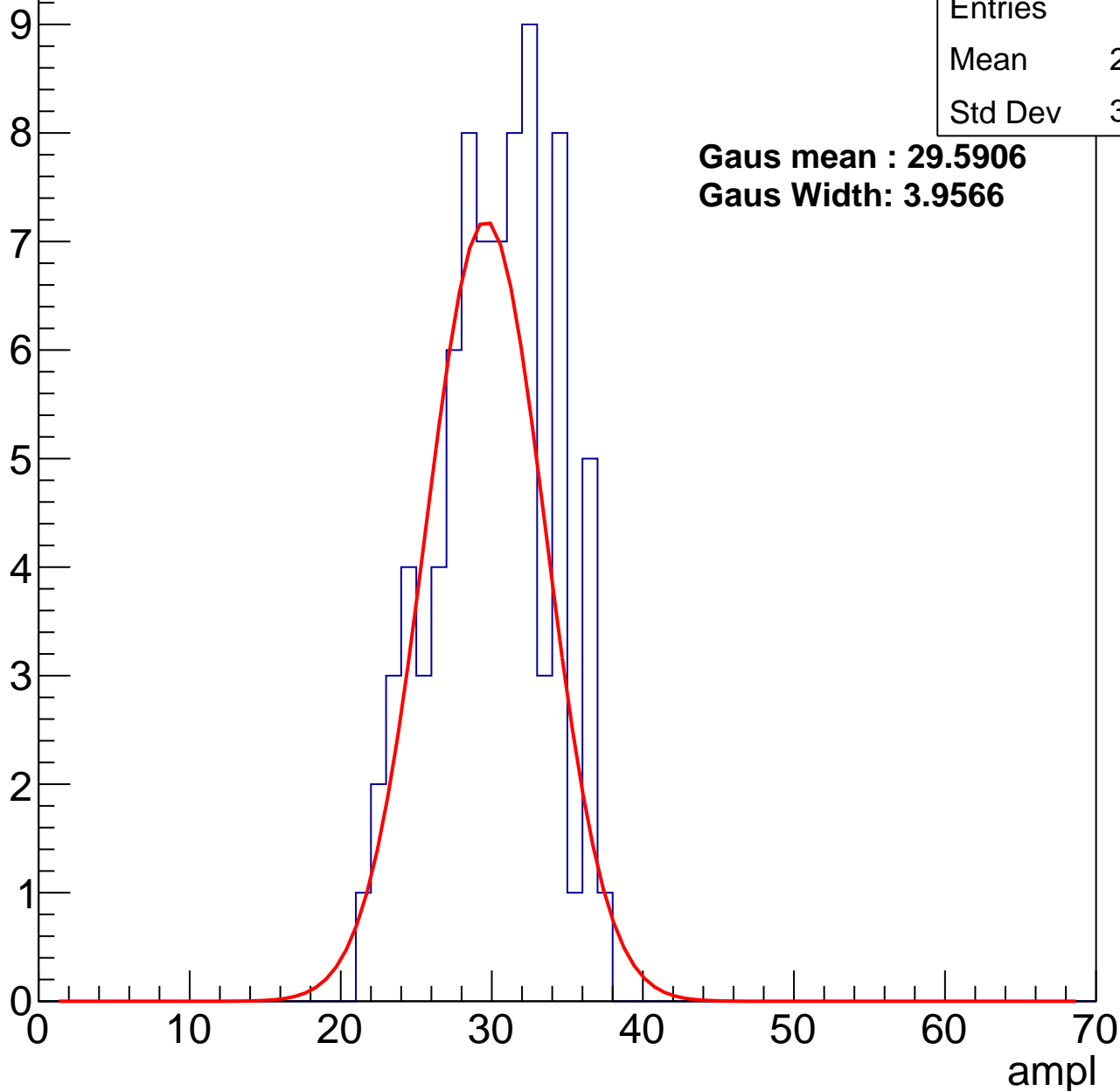
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	29.59
Std Dev	3.827

Gaus mean : 29.5906

Gaus Width: 3.9566



B0L102S, U5-ch24, adc1

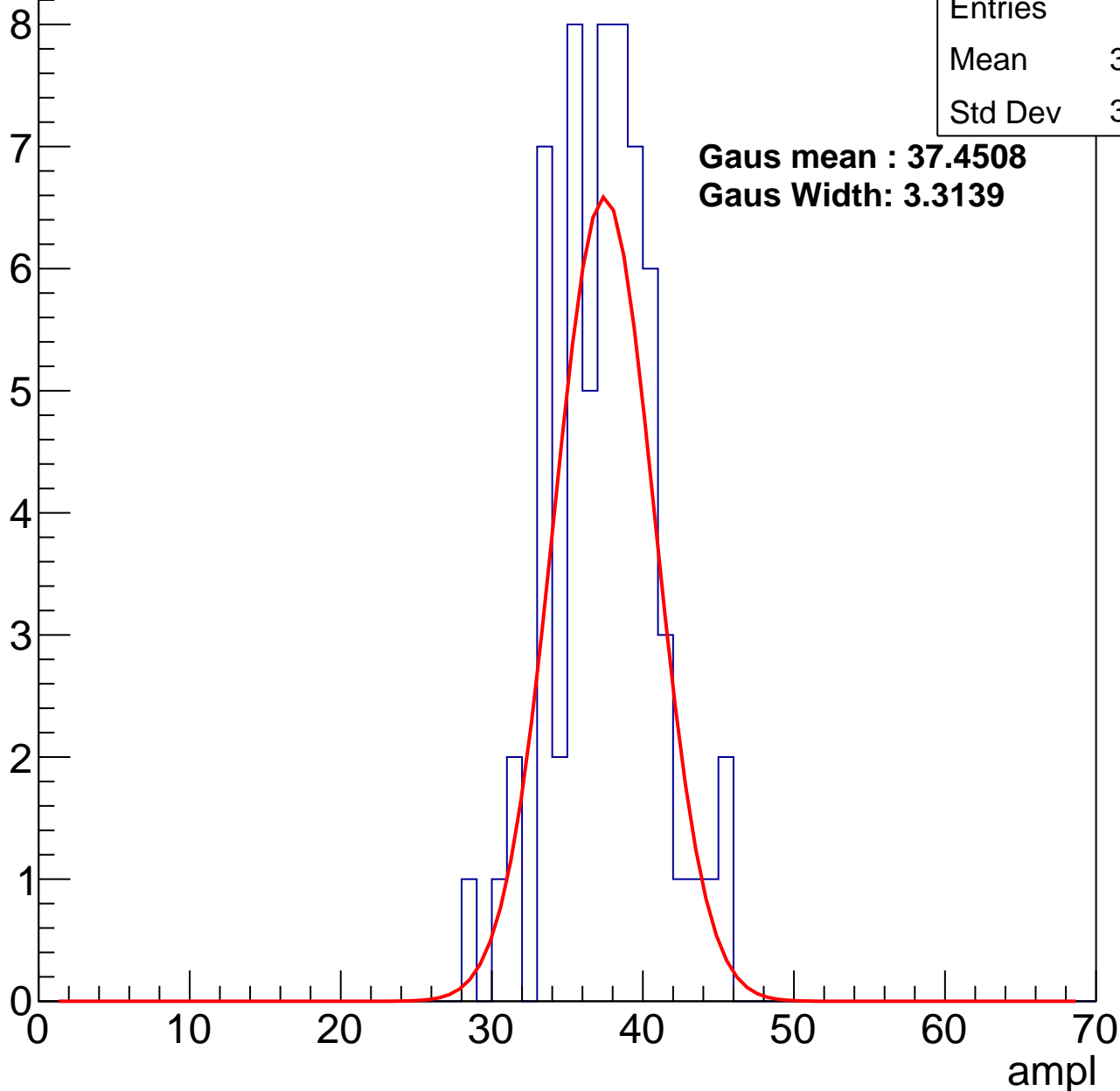
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	37.05
Std Dev	3.438

Gaus mean : 37.4508

Gaus Width: 3.3139



B0L102S, U5-ch24, adc2

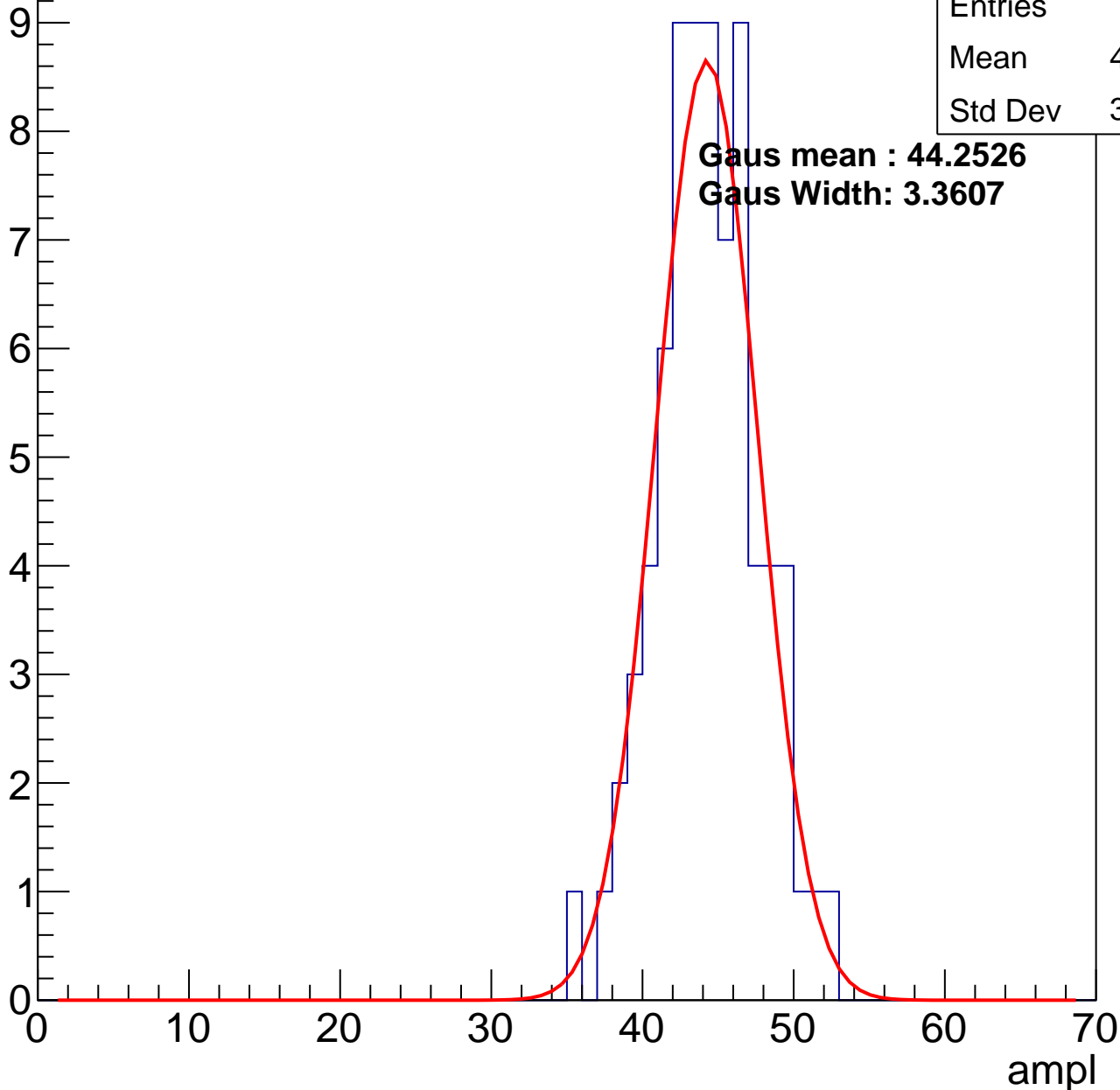
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	43.87
Std Dev	3.328

Gaus mean : 44.2526

Gaus Width: 3.3607

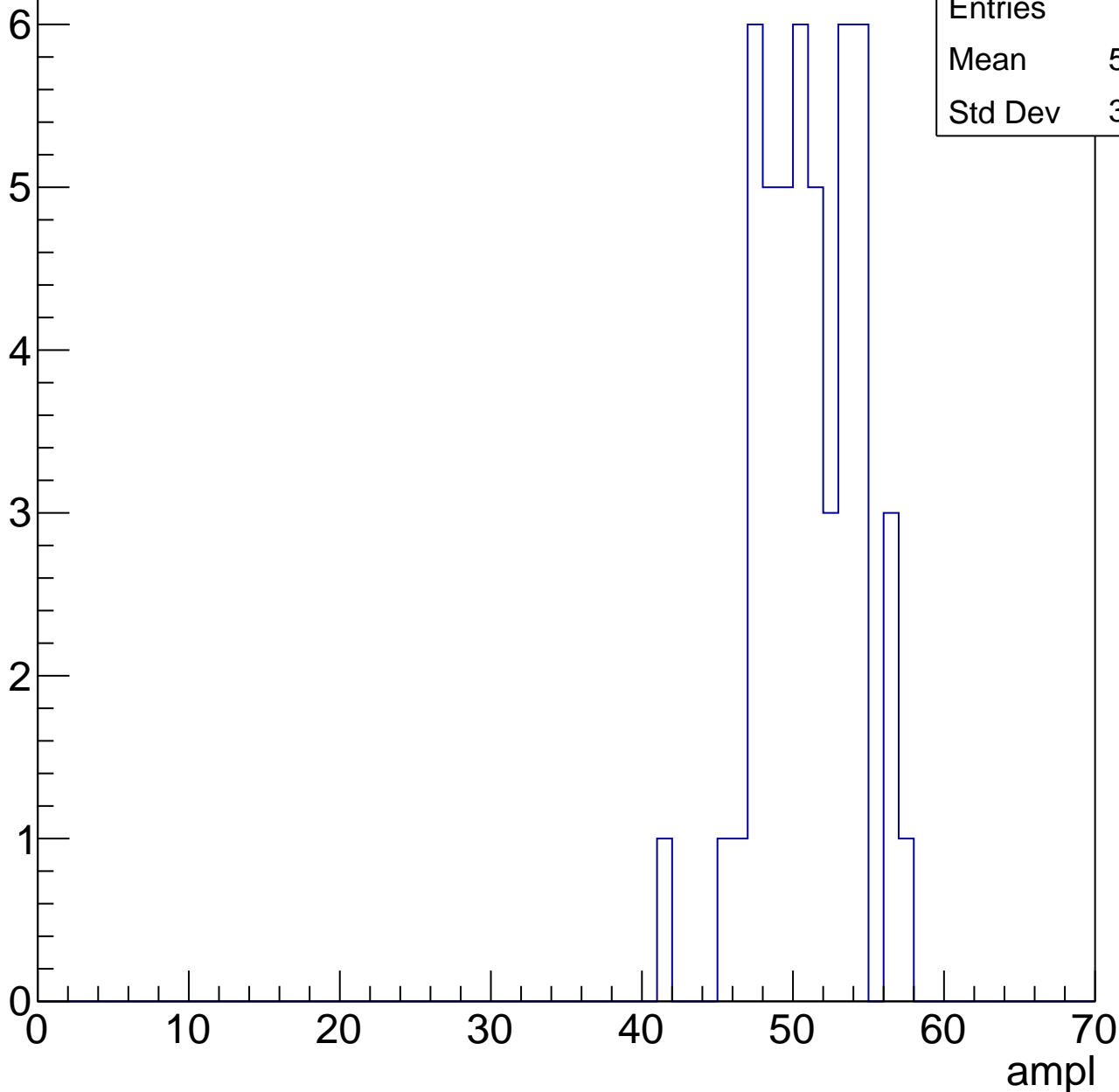


B0L102S, U5-ch24, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	50.55
Std Dev	3.227

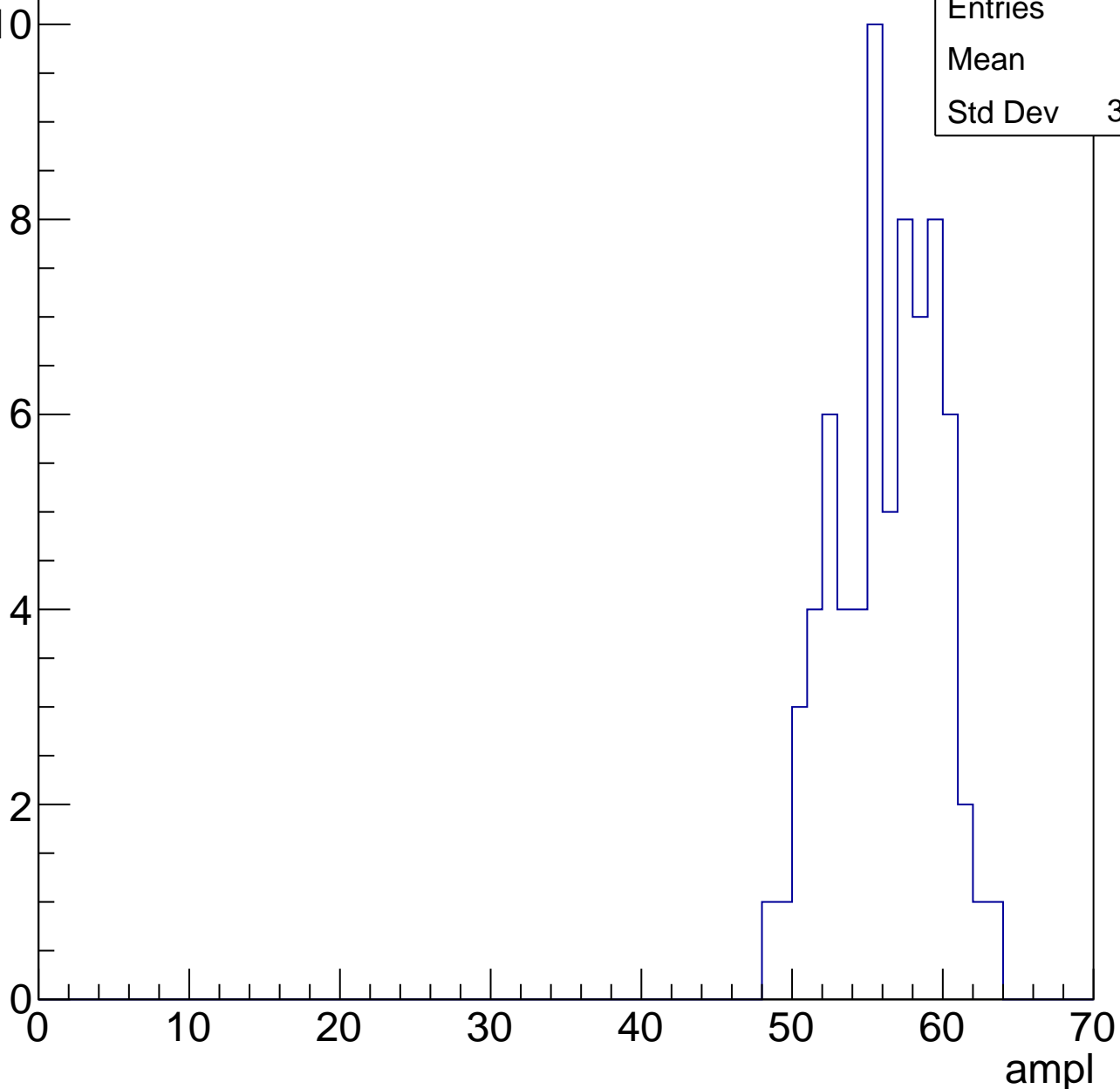


B0L102S, U5-ch24, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

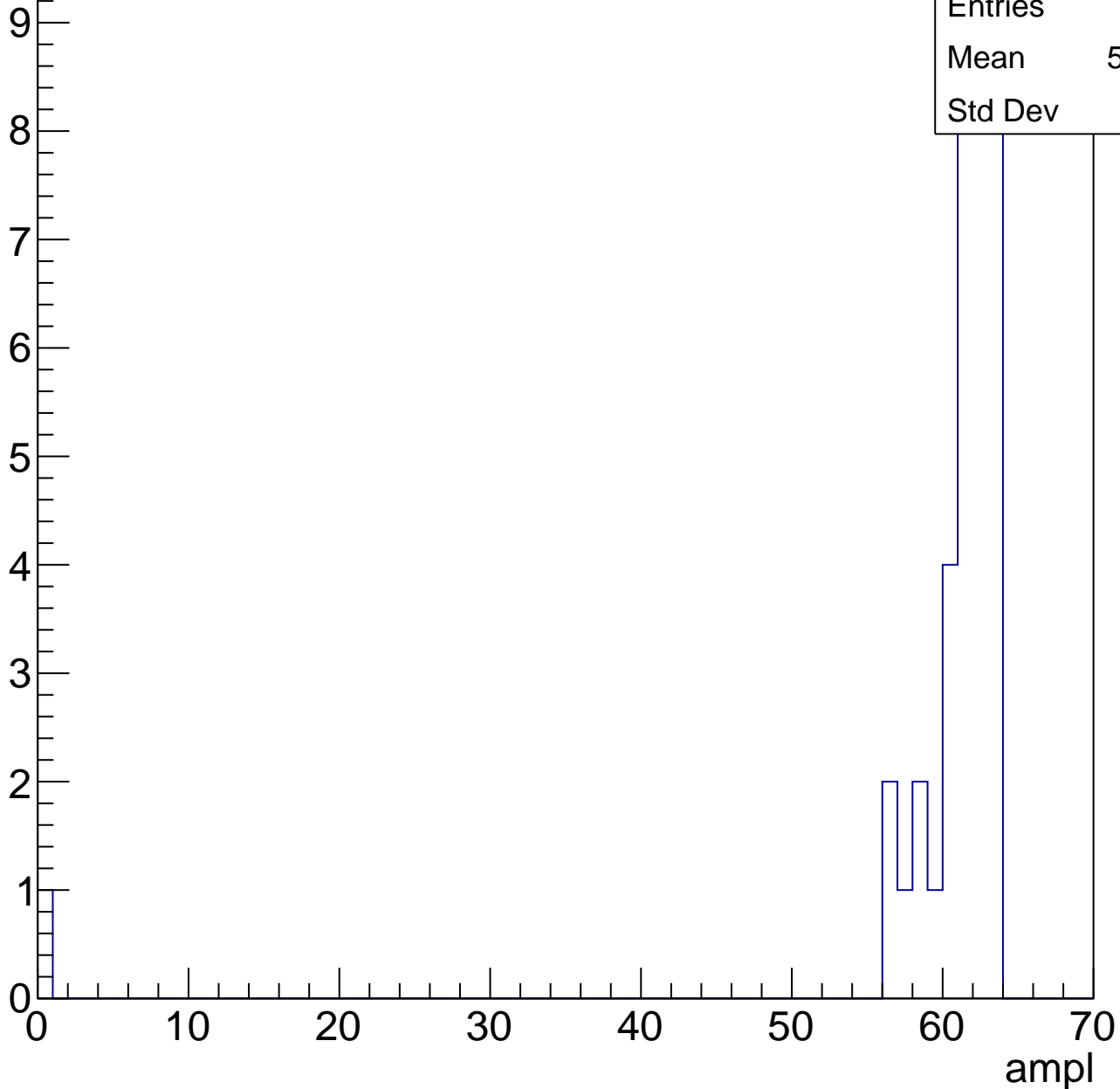
Entries	71
Mean	55.8
Std Dev	3.389



B0L102S, U5-ch24, adc5

calib_packv5_042523_0143.root, FC#12, port B1

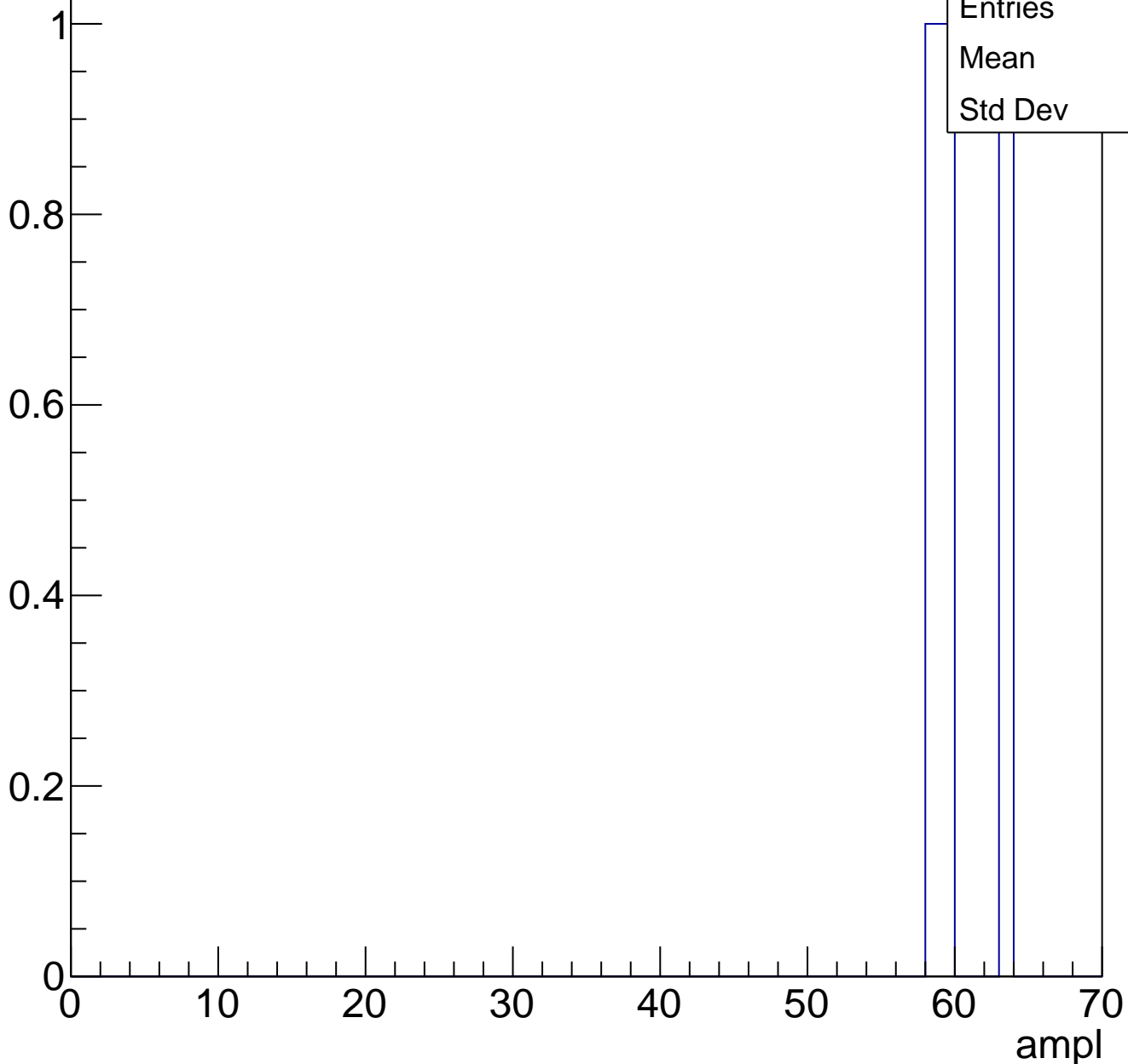
Entry



B0L102S, U5-ch24, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch24, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch25, adc0

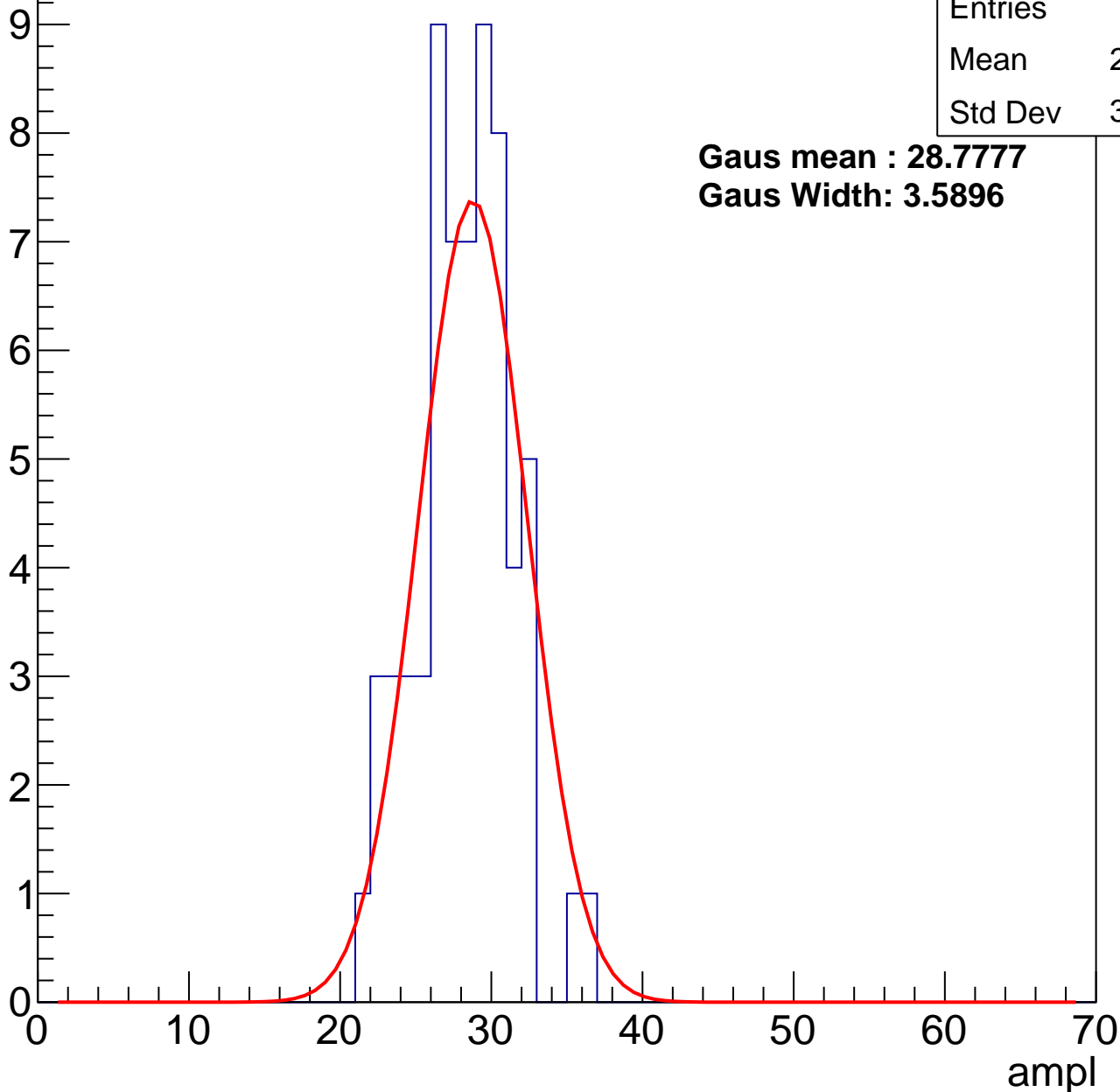
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	27.78
Std Dev	3.105

Gaus mean : 28.7777

Gaus Width: 3.5896



B0L102S, U5-ch25, adc1

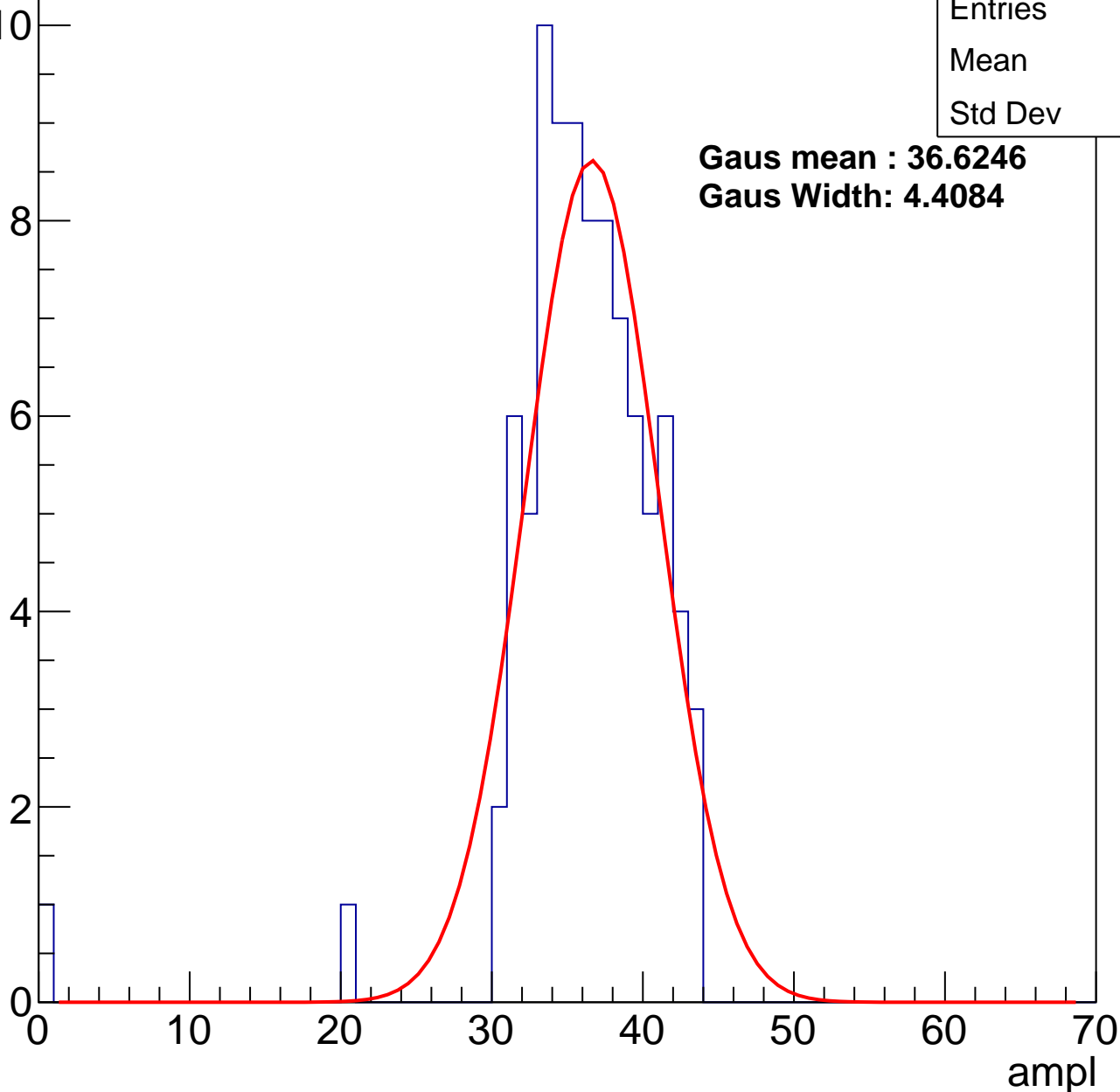
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	90
Mean	35.6
Std Dev	5.36

Gaus mean : 36.6246

Gaus Width: 4.4084



B0L102S, U5-ch25, adc2

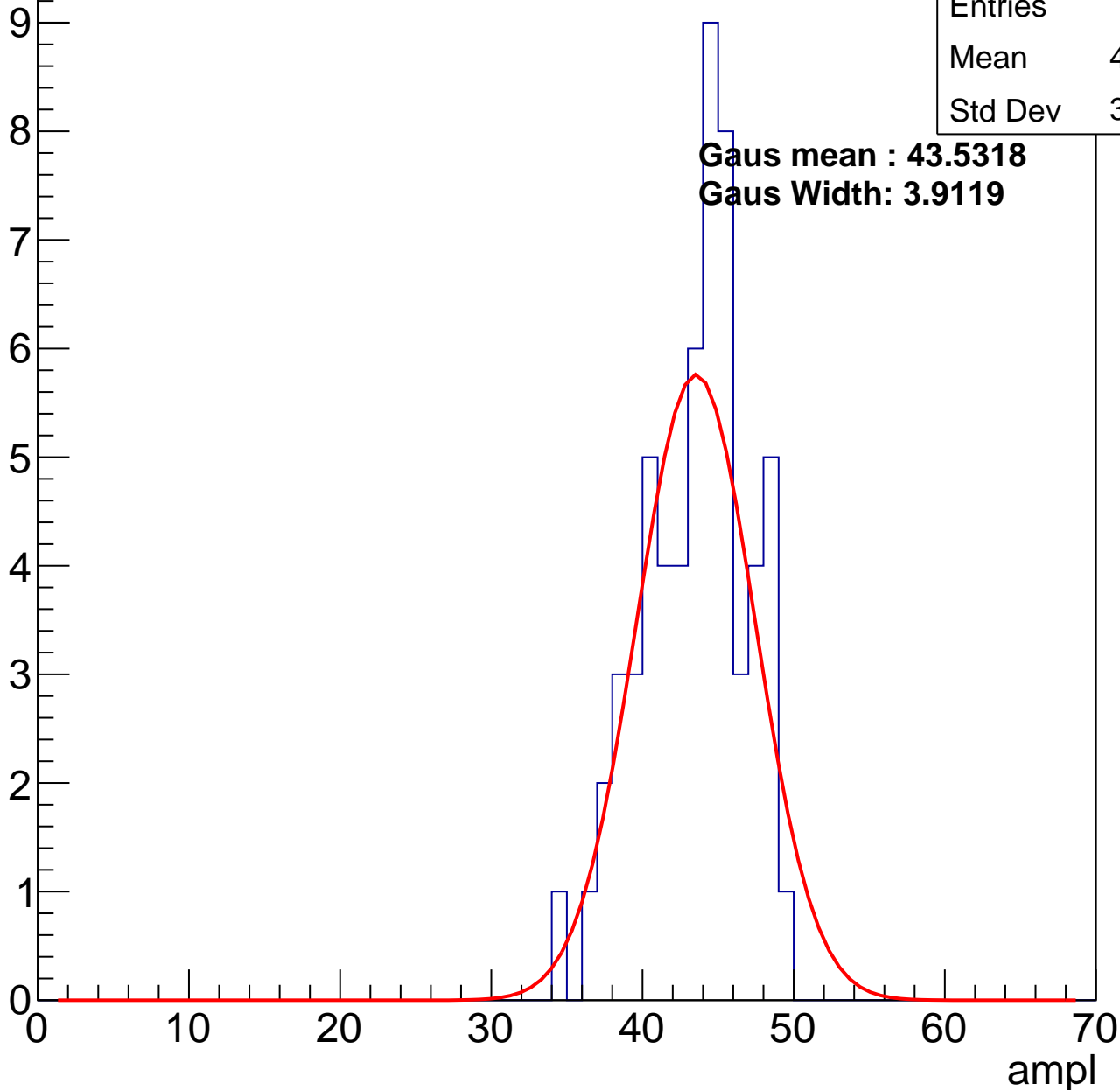
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.98
Std Dev	3.402

Gaus mean : 43.5318

Gaus Width: 3.9119

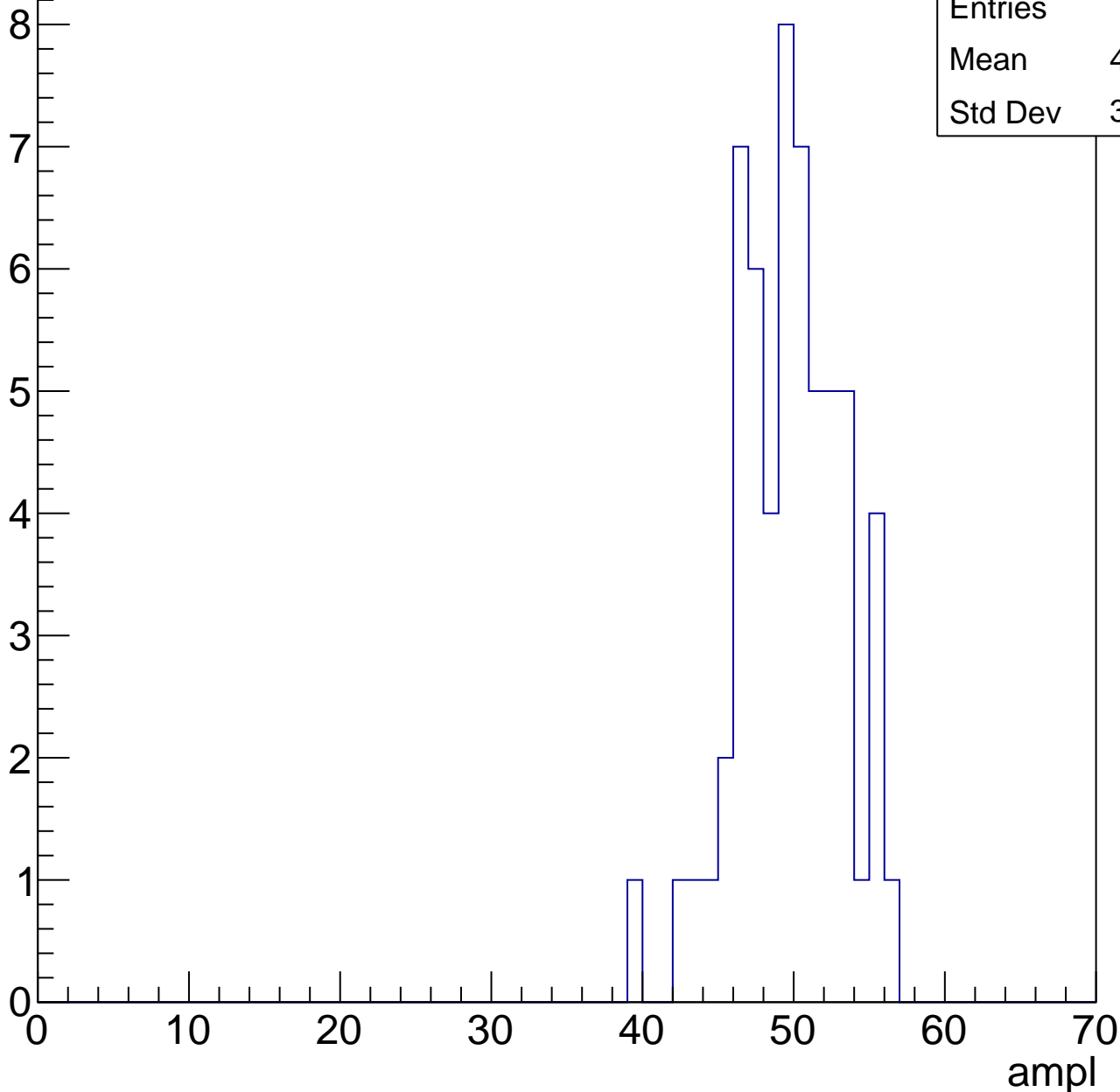


B0L102S, U5-ch25, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	49.25
Std Dev	3.442

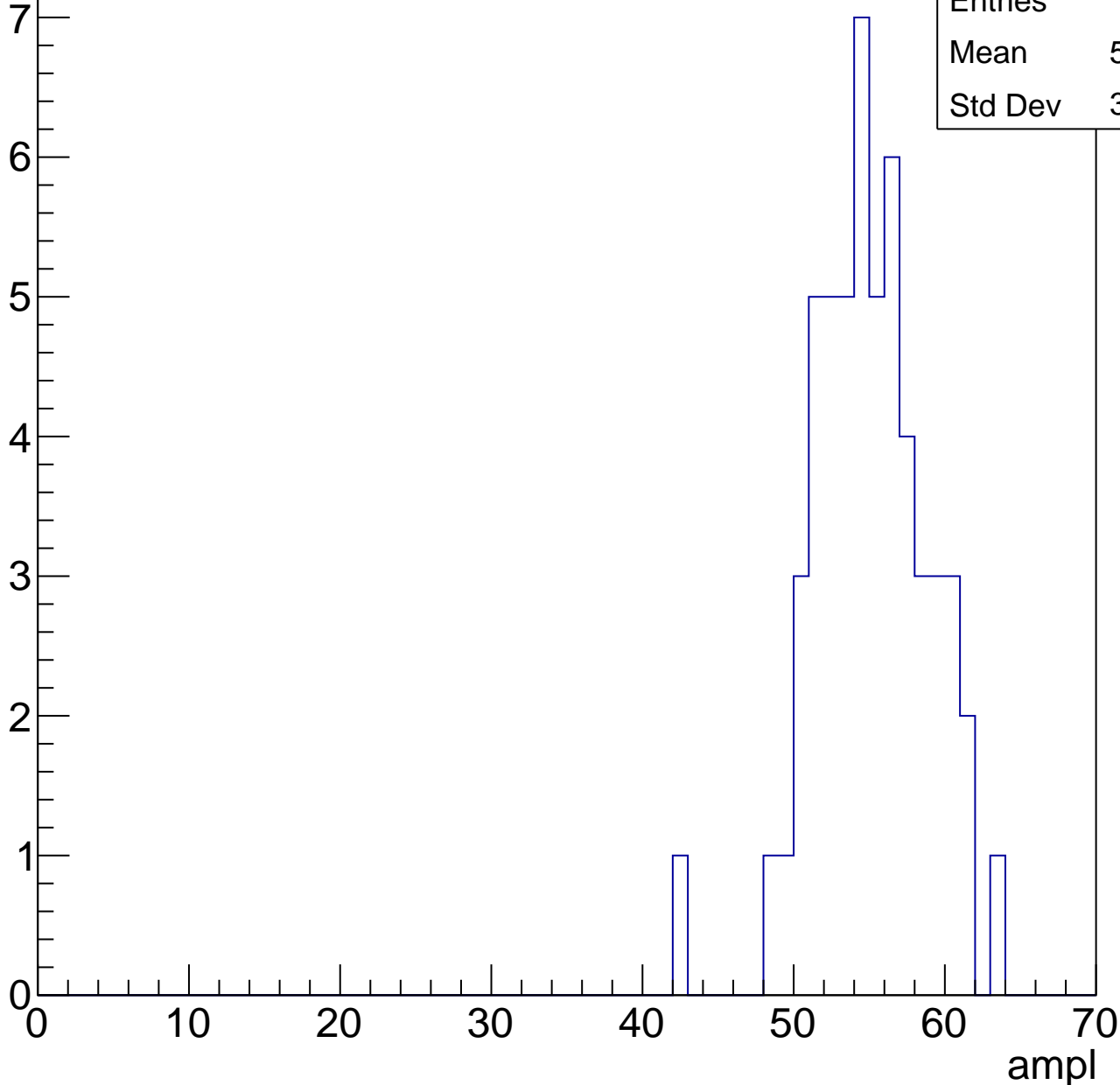


B0L102S, U5-ch25, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	54.58
Std Dev	3.764



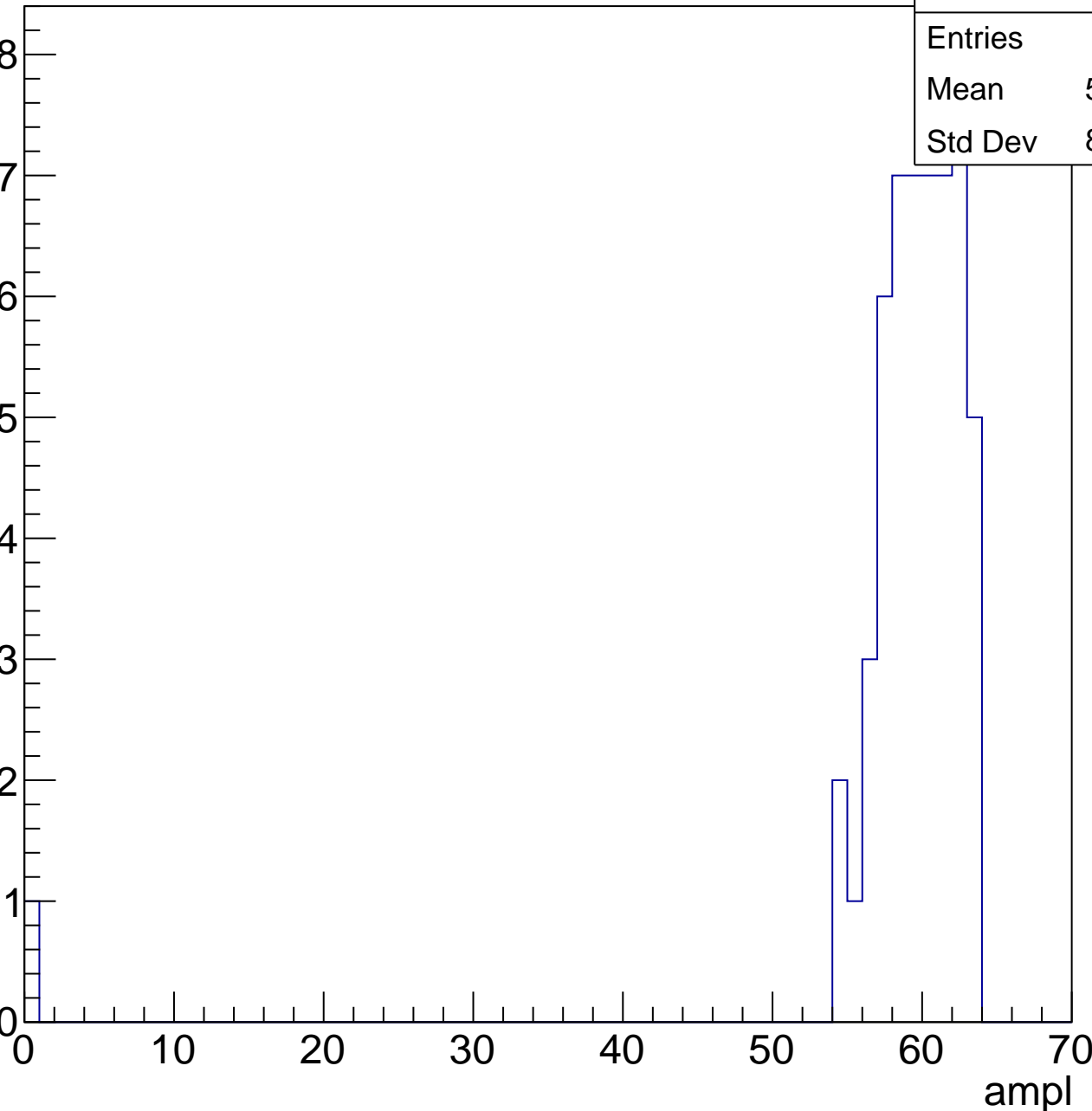
B0L102S, U5-ch25, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	58.33
Std Dev	8.353

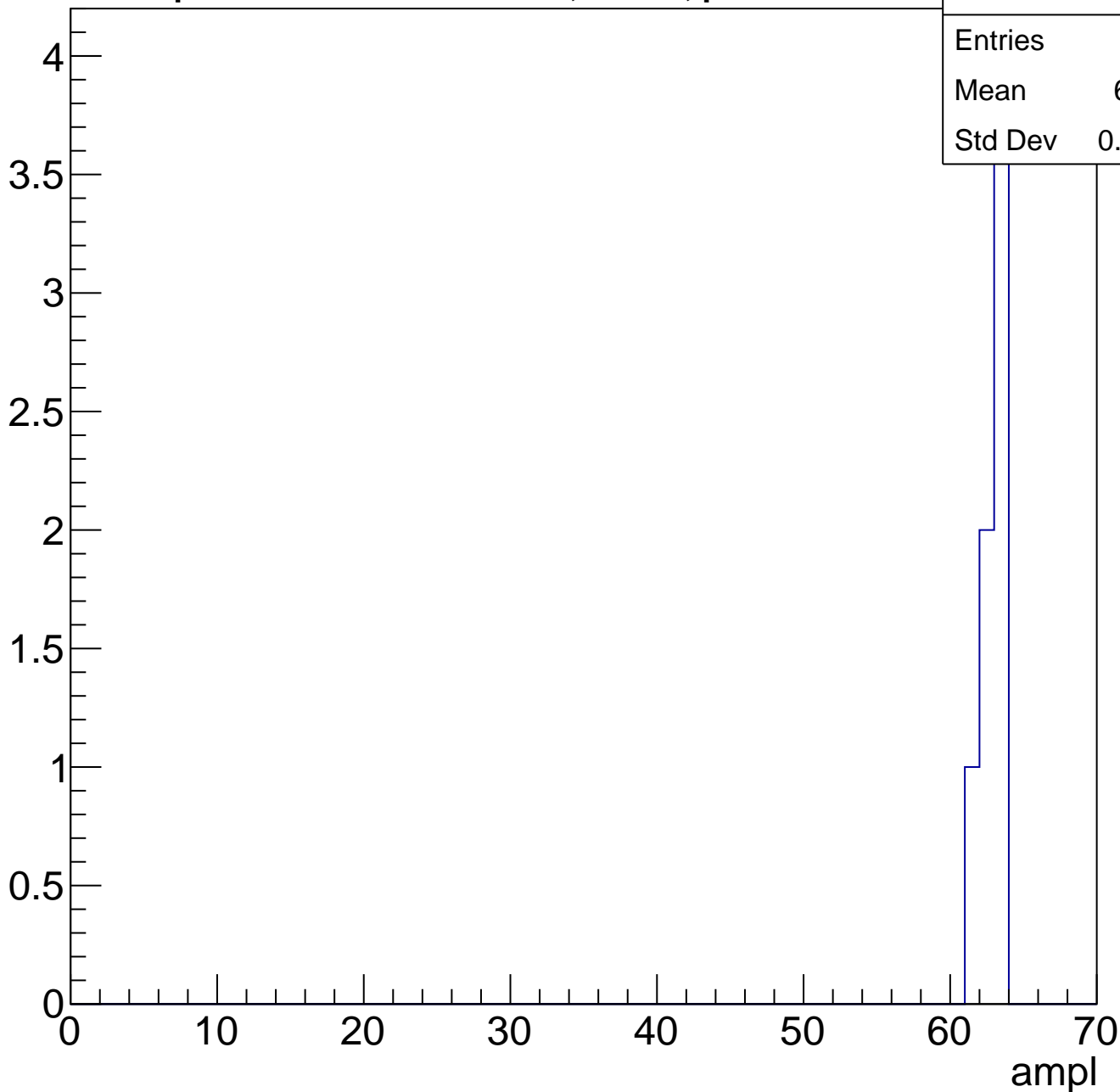
8
7
6
5
4
3
2
1
0



B0L102S, U5-ch25, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch25, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch26, adc0

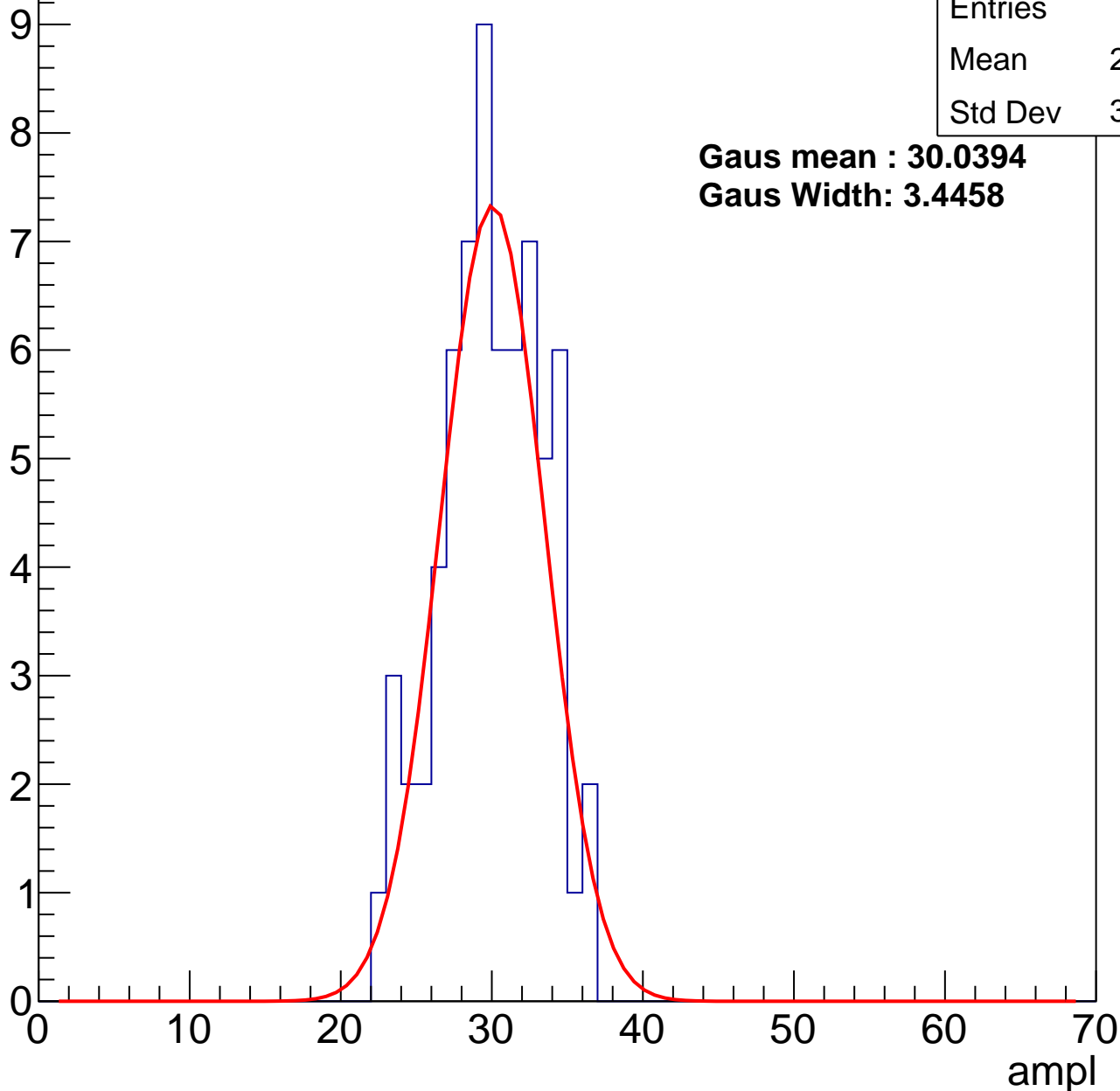
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	29.52
Std Dev	3.334

Gaus mean : 30.0394

Gaus Width: 3.4458



B0L102S, U5-ch26, adc1

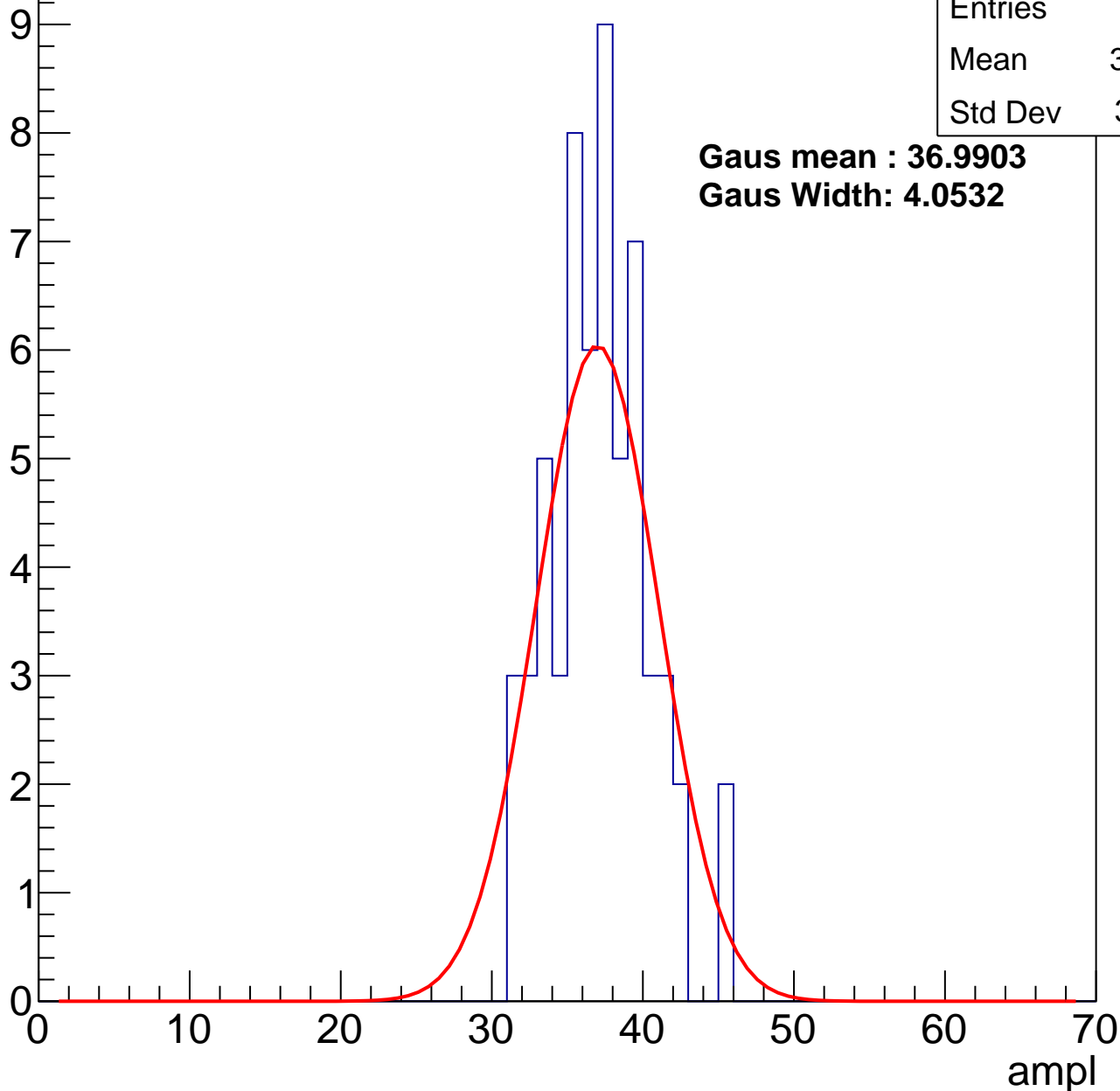
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	36.69
Std Dev	3.211

Gaus mean : 36.9903

Gaus Width: 4.0532

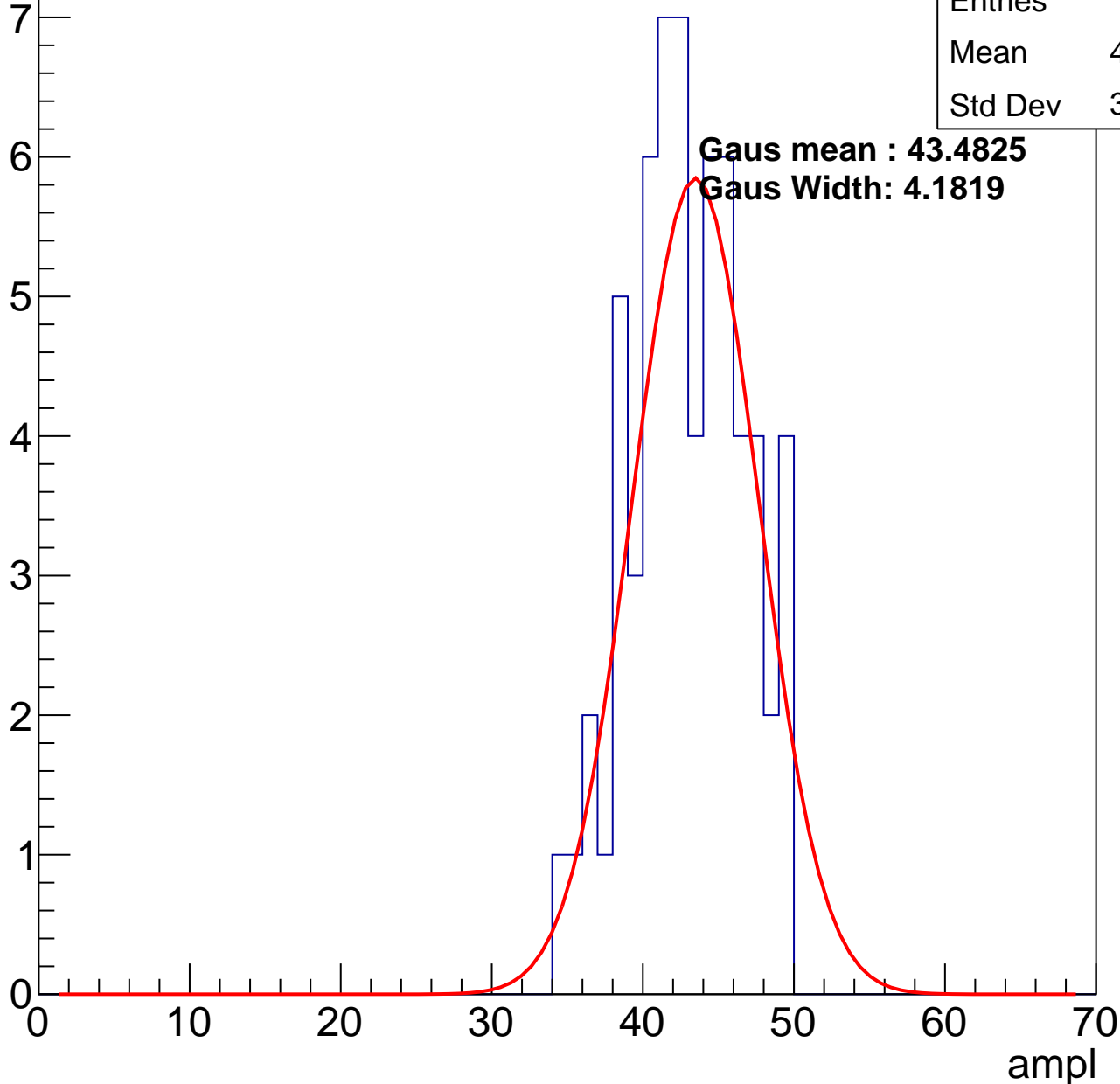


B0L102S, U5-ch26, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	42.48
Std Dev	3.668

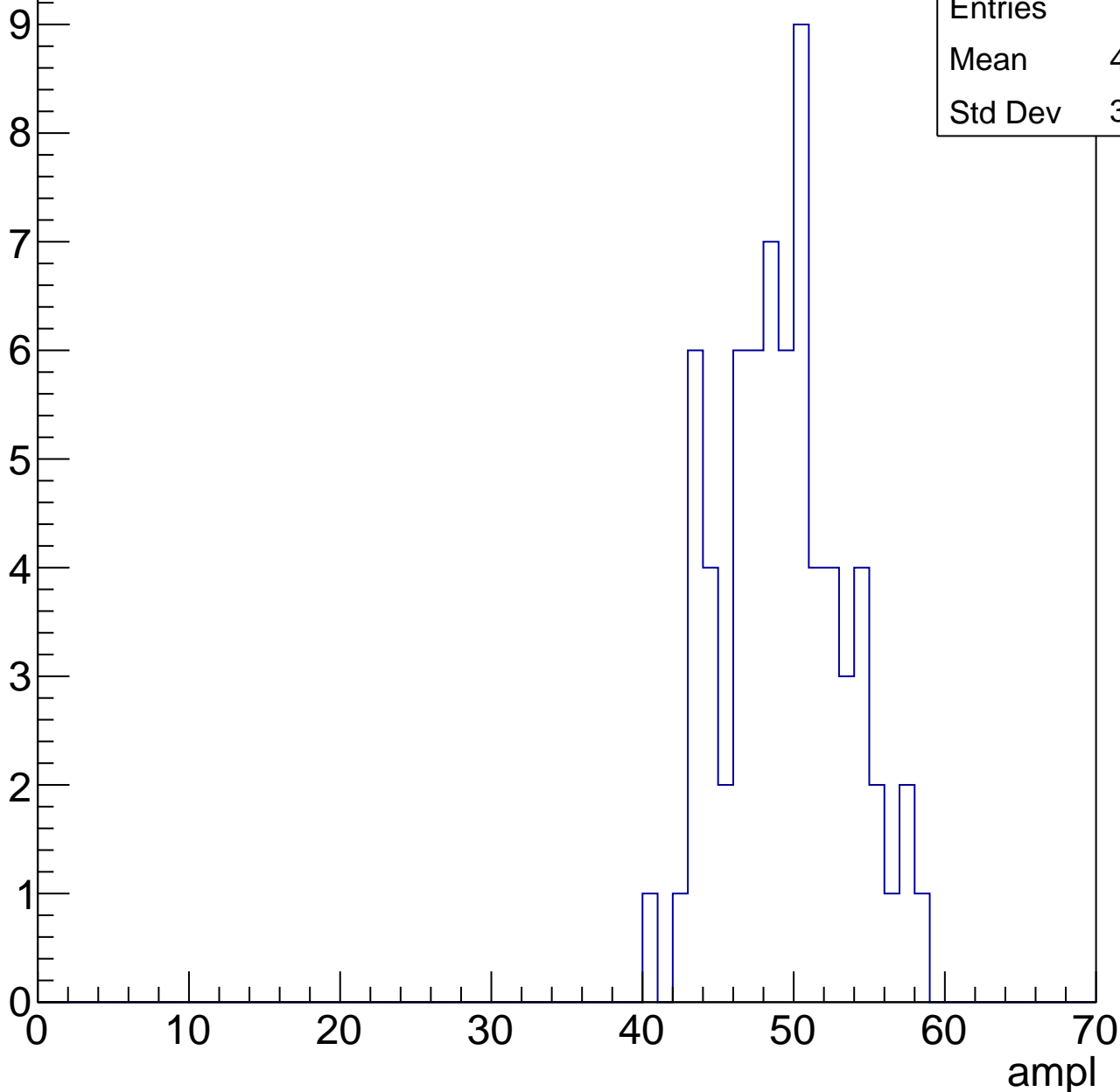


B0L102S, U5-ch26, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	48.83
Std Dev	3.993

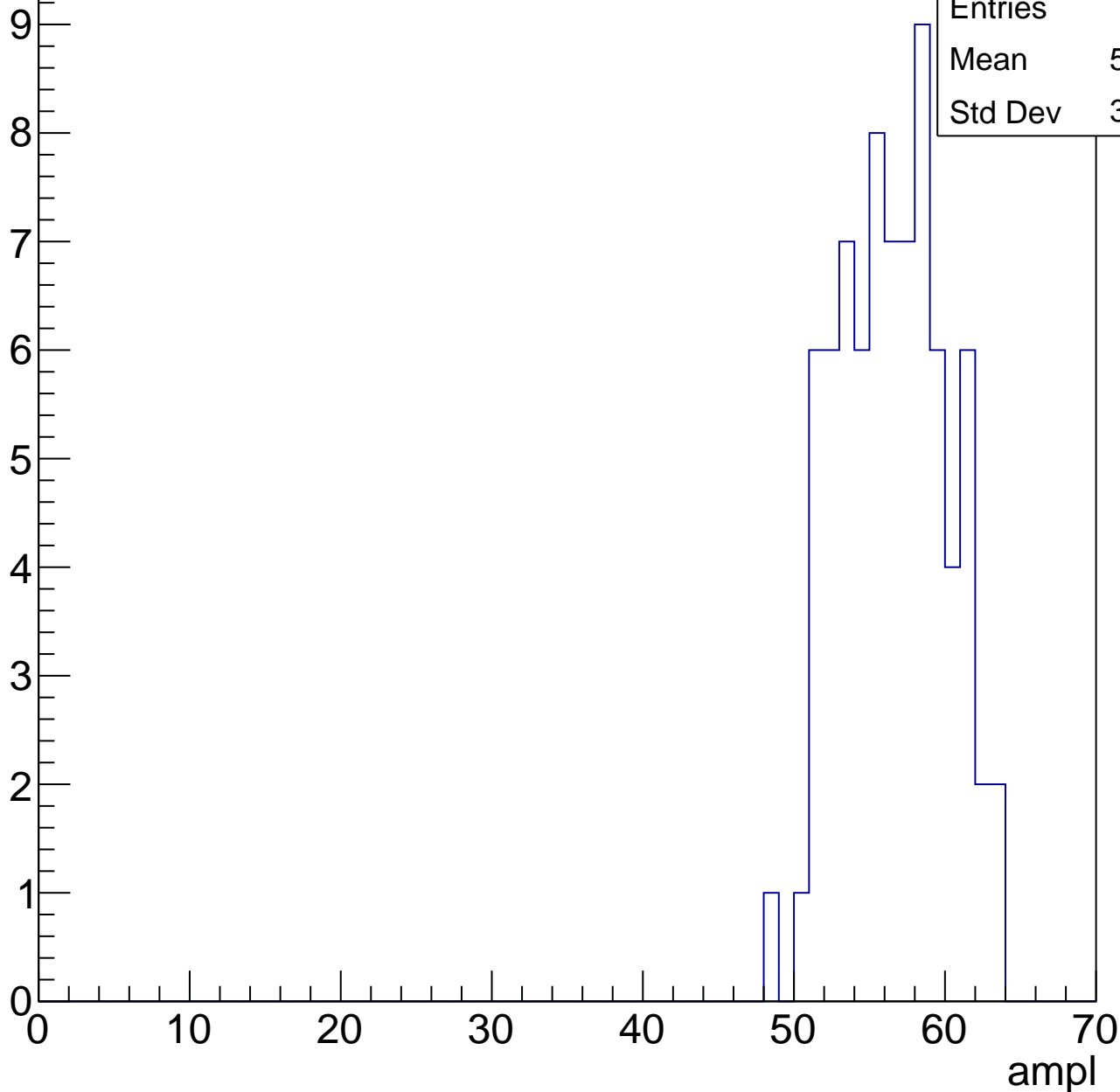


B0L102S, U5-ch26, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	56.08
Std Dev	3.437

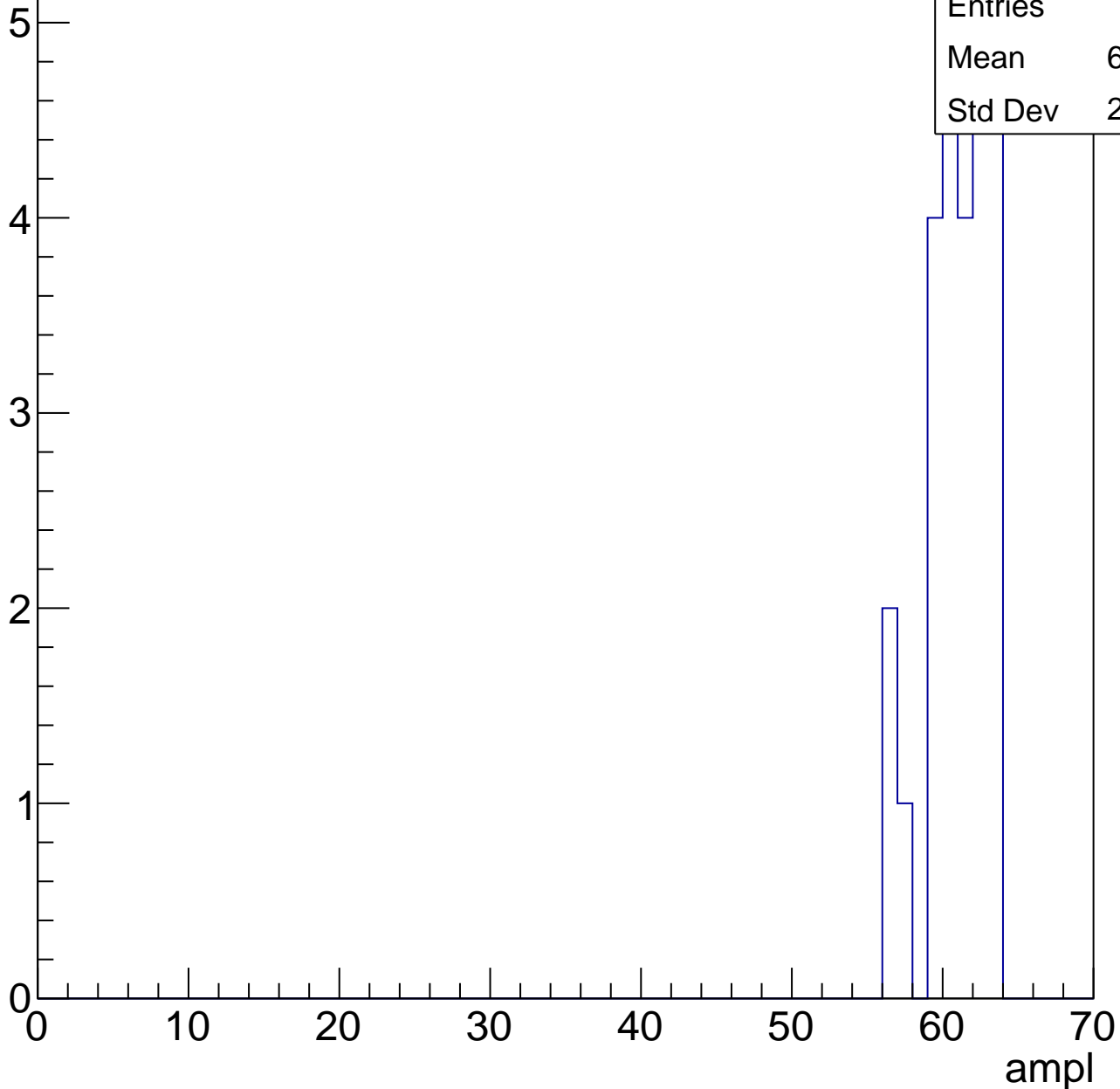


B0L102S, U5-ch26, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

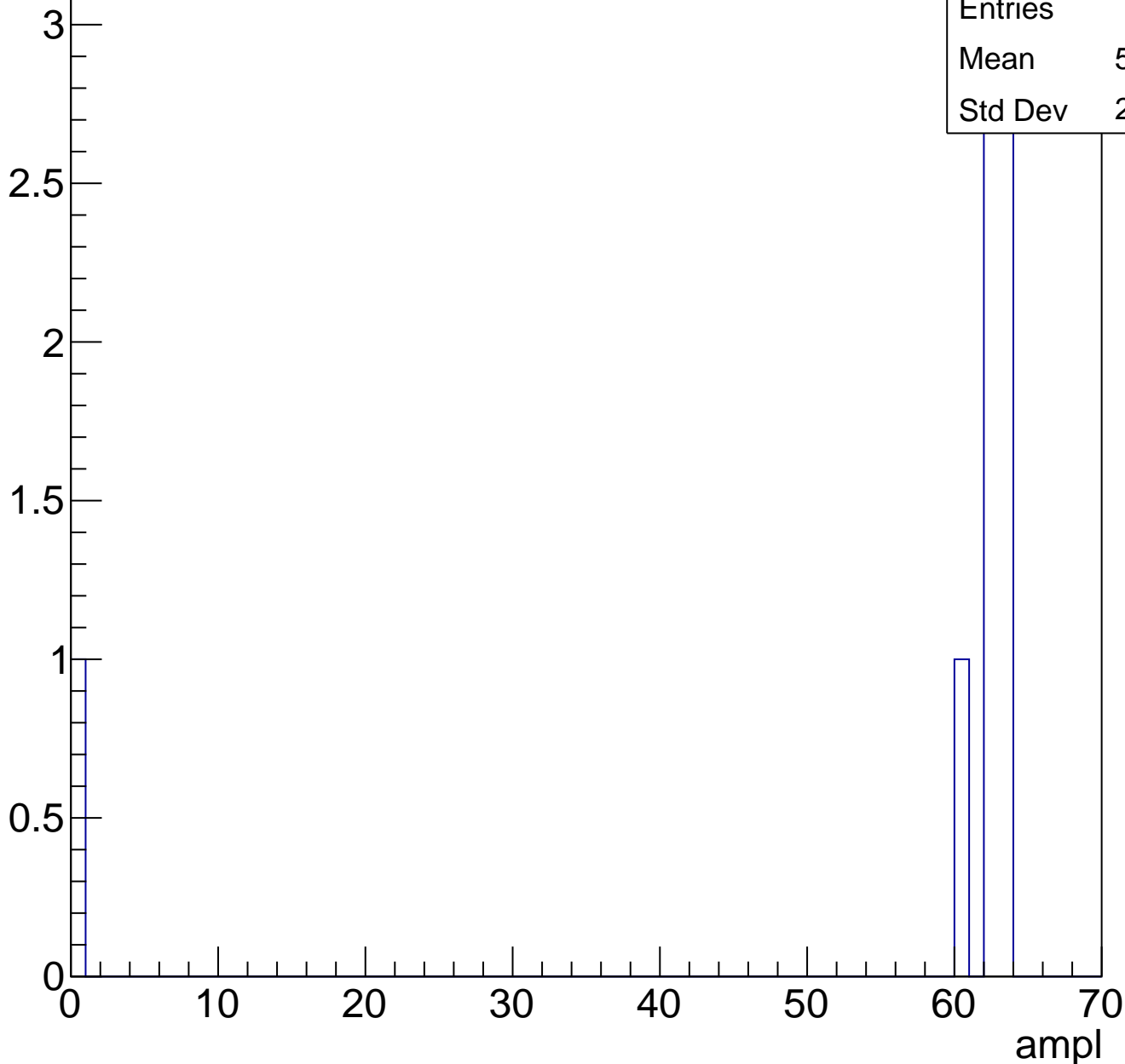
Entries	26
Mean	60.54
Std Dev	2.024



B0L102S, U5-ch26, adc6

calib_packv5_042523_0143.root, FC#12, port B1

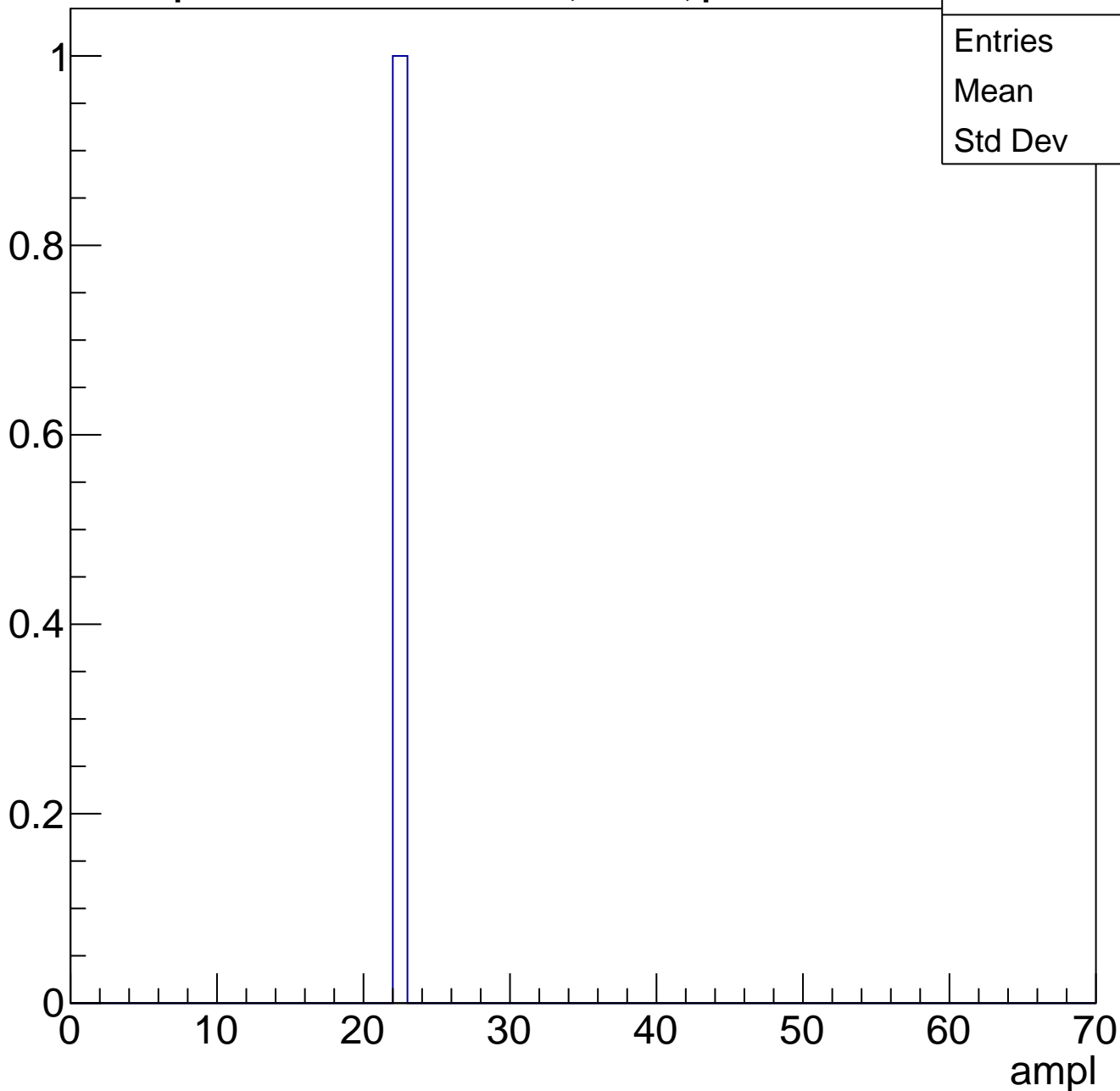
Entry



B0L102S, U5-ch26, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch27, adc0

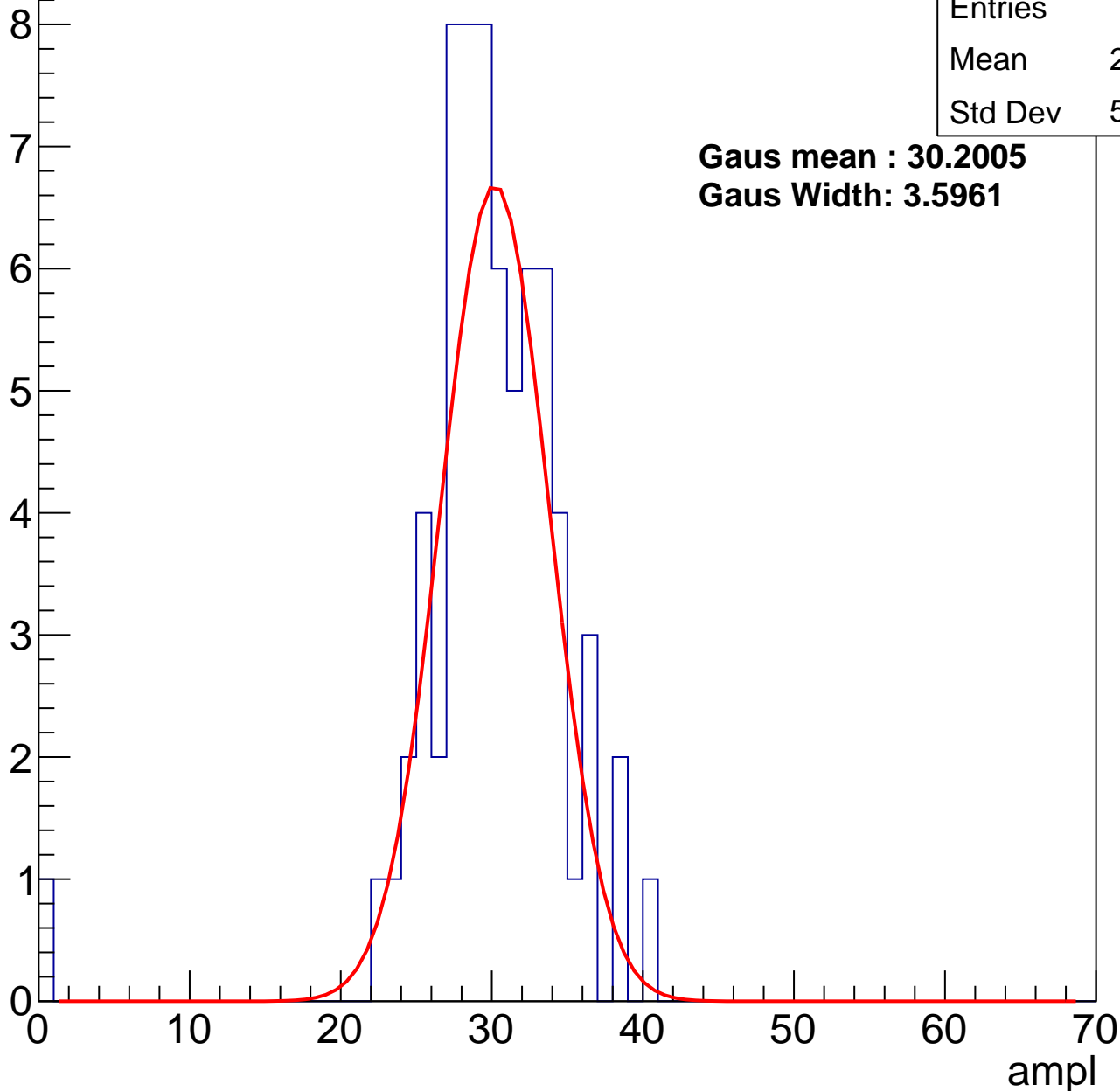
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	29.52
Std Dev	5.138

Gaus mean : 30.2005

Gaus Width: 3.5961



B0L102S, U5-ch27, adc1

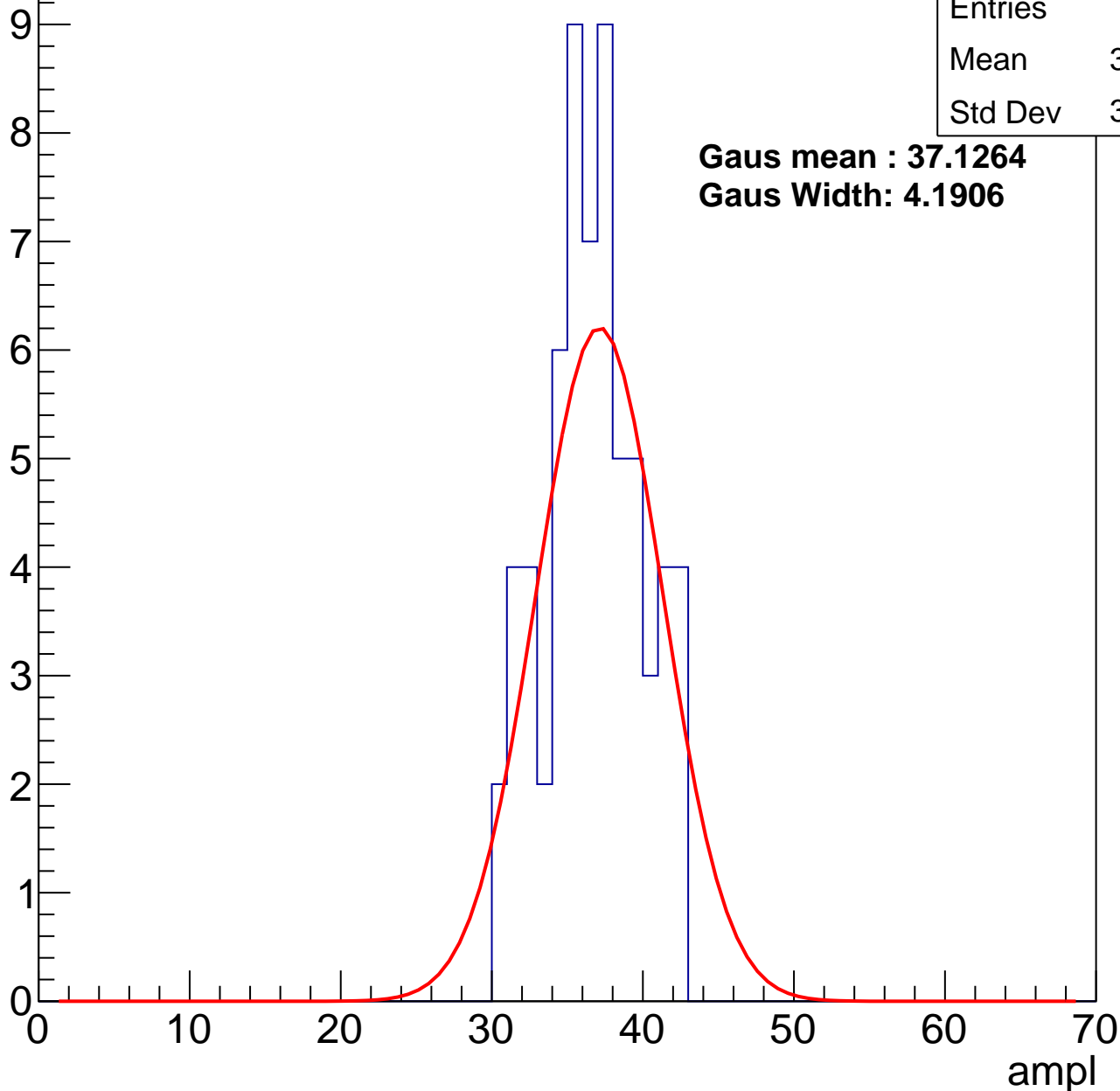
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	36.23
Std Dev	3.186

Gaus mean : 37.1264

Gaus Width: 4.1906



B0L102S, U5-ch27, adc2

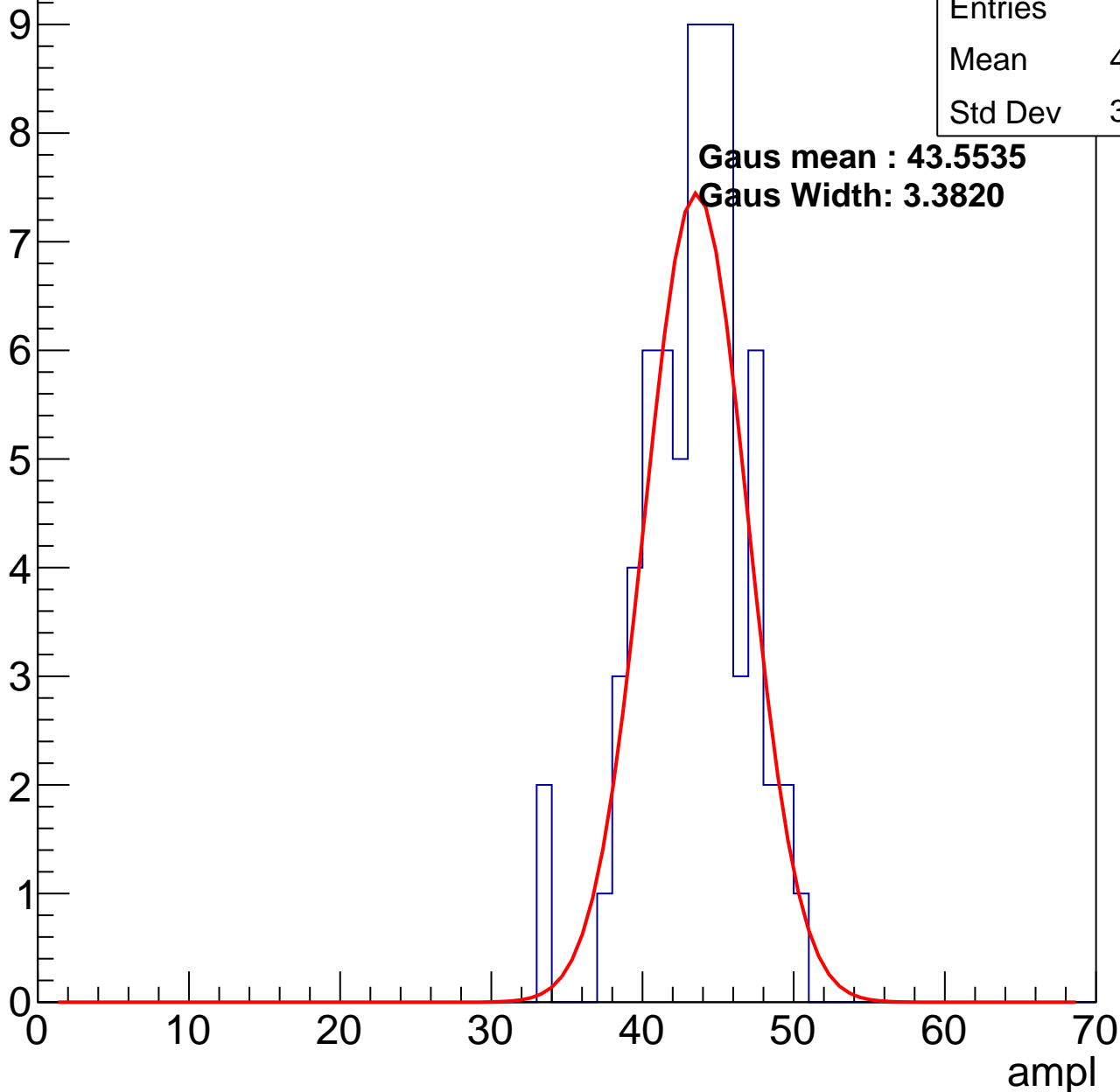
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.96
Std Dev	3.423

Gaus mean : 43.5535

Gaus Width: 3.3820

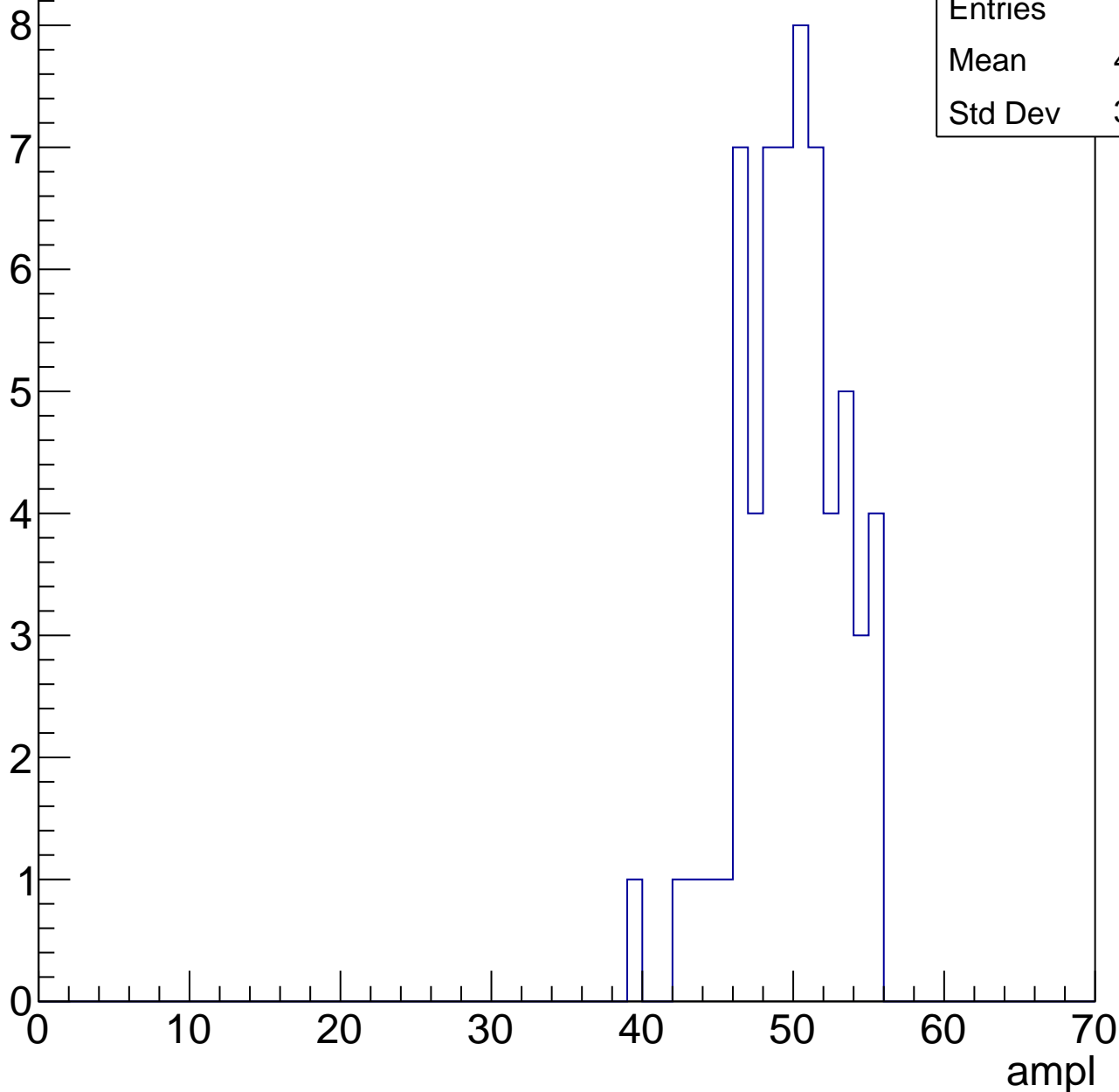


B0L102S, U5-ch27, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	49.41
Std Dev	3.321

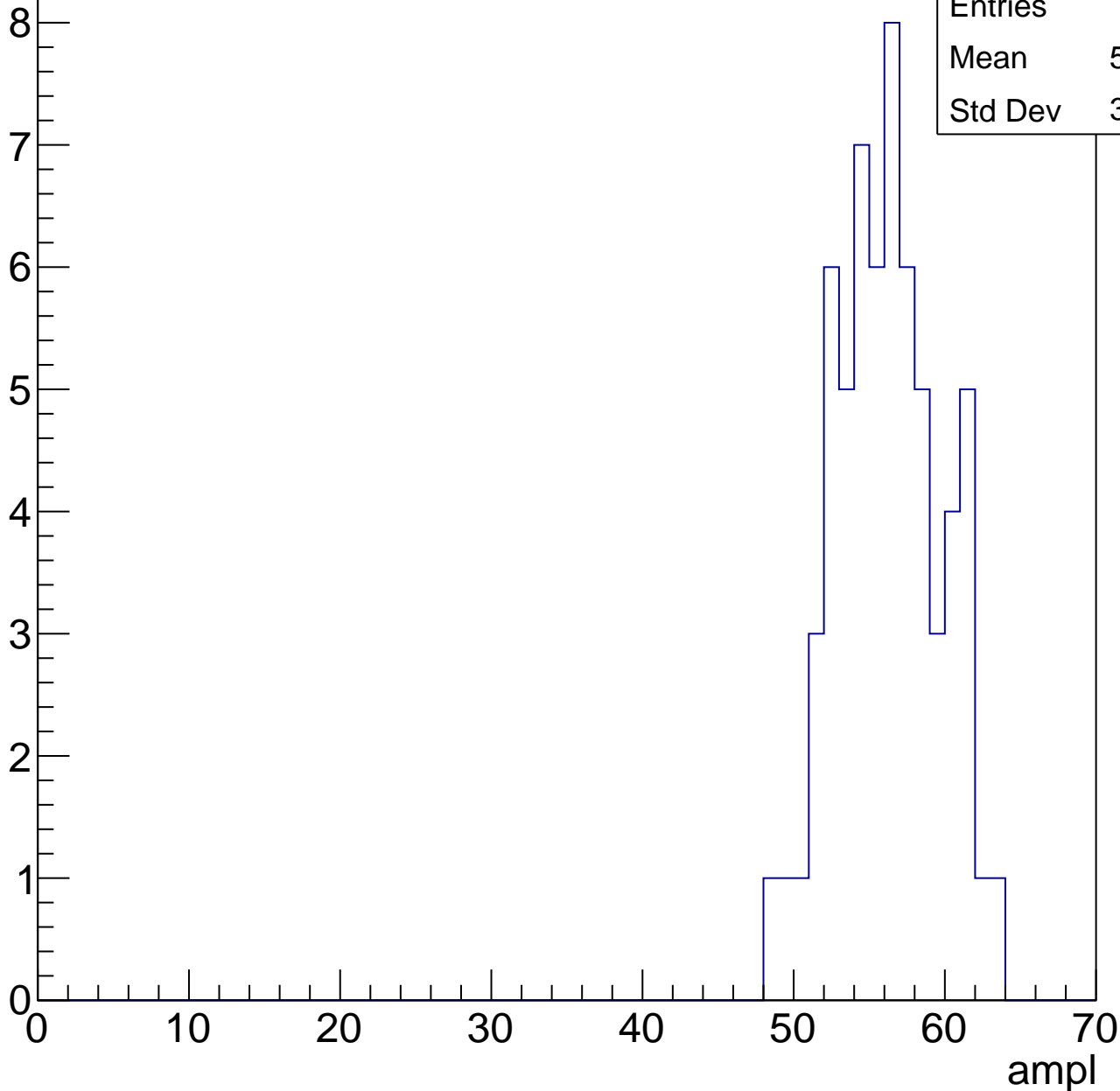


B0L102S, U5-ch27, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.75
Std Dev	3.385

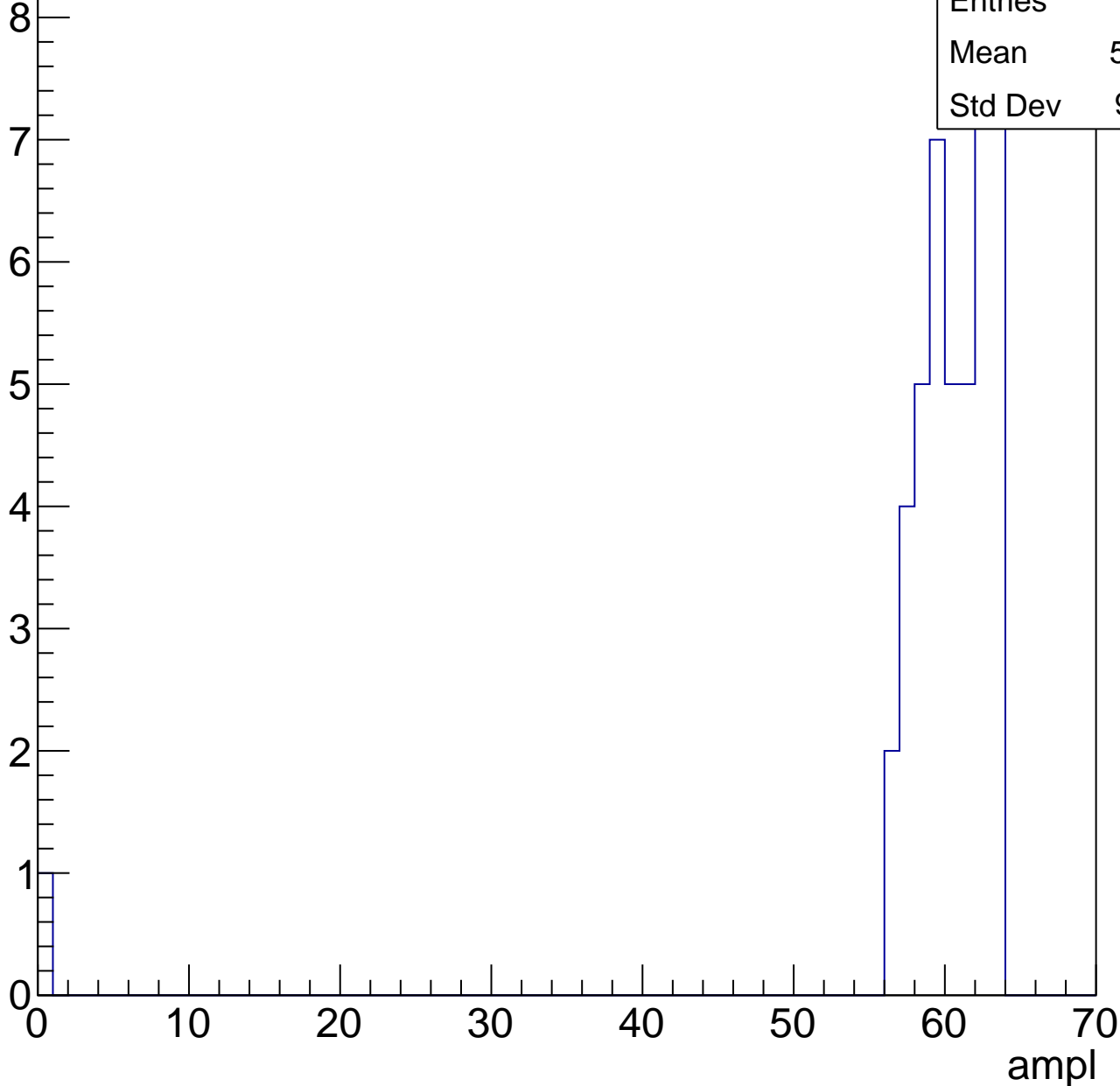


B0L102S, U5-ch27, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

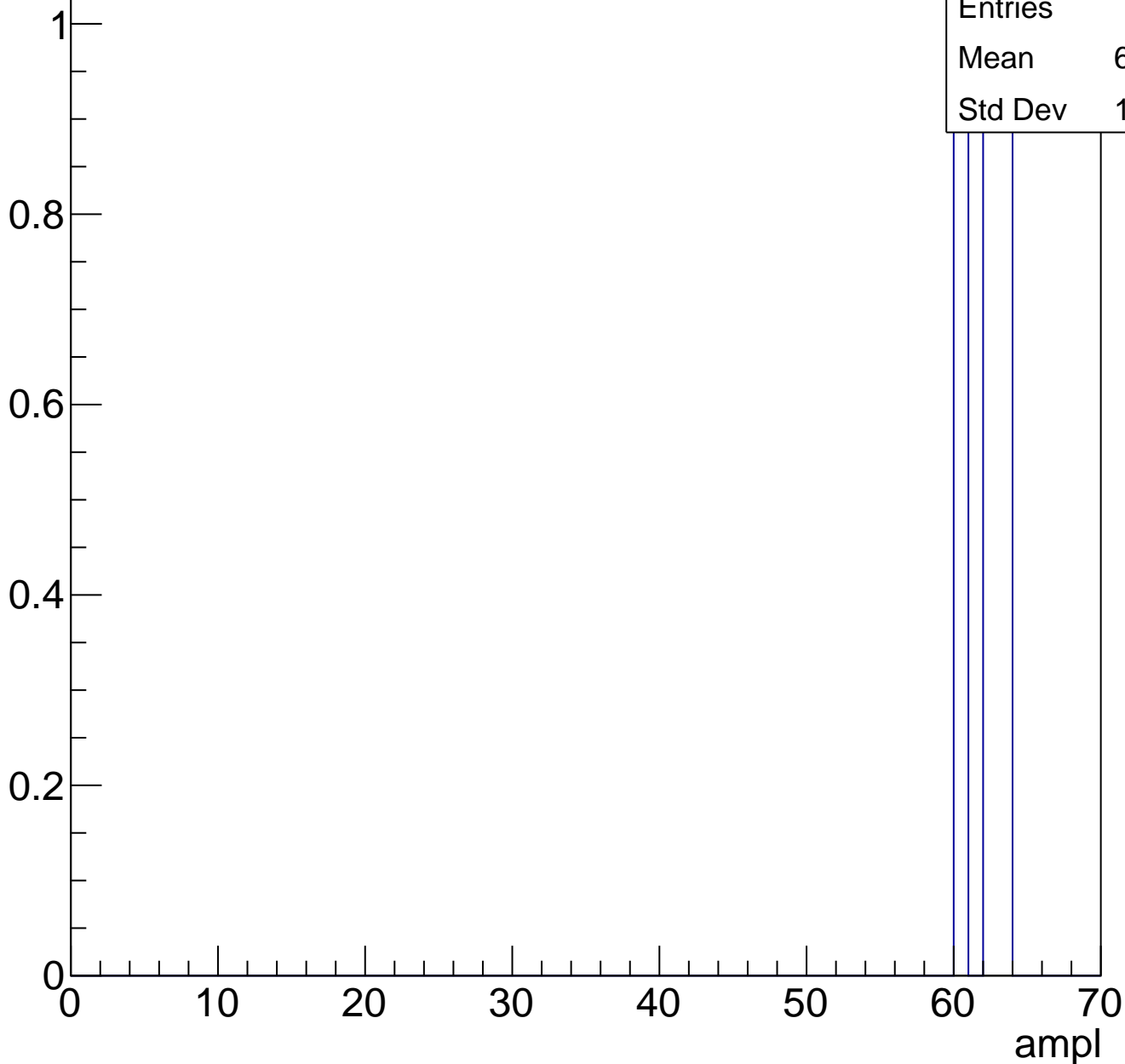
Entries	45
Mean	58.84
Std Dev	9.121



B0L102S, U5-ch27, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch27, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch28, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	66
Mean	27.67
Std Dev	4.752

Gaus mean : 28.3838

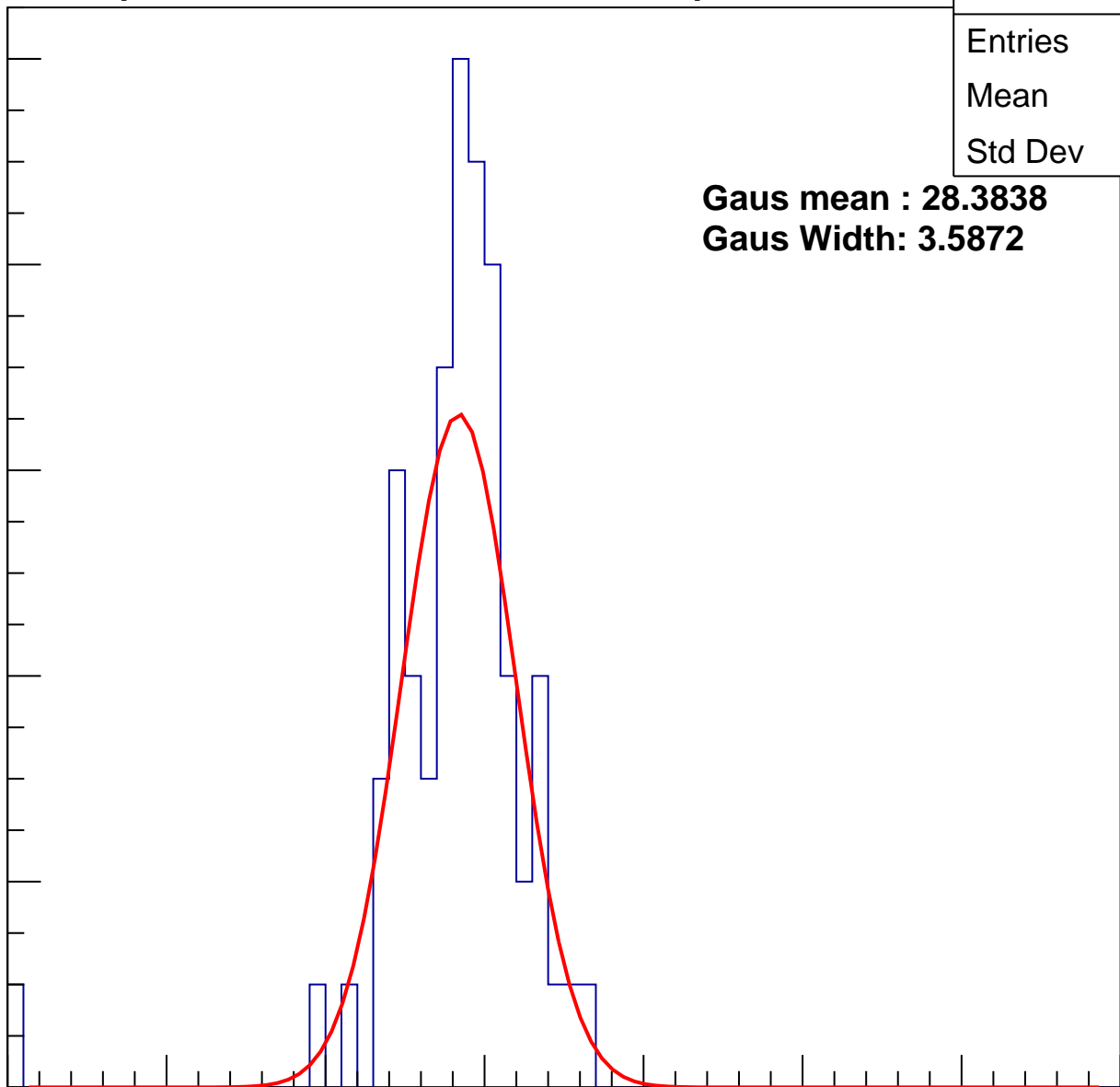
Gaus Width: 3.5872

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch28, adc1

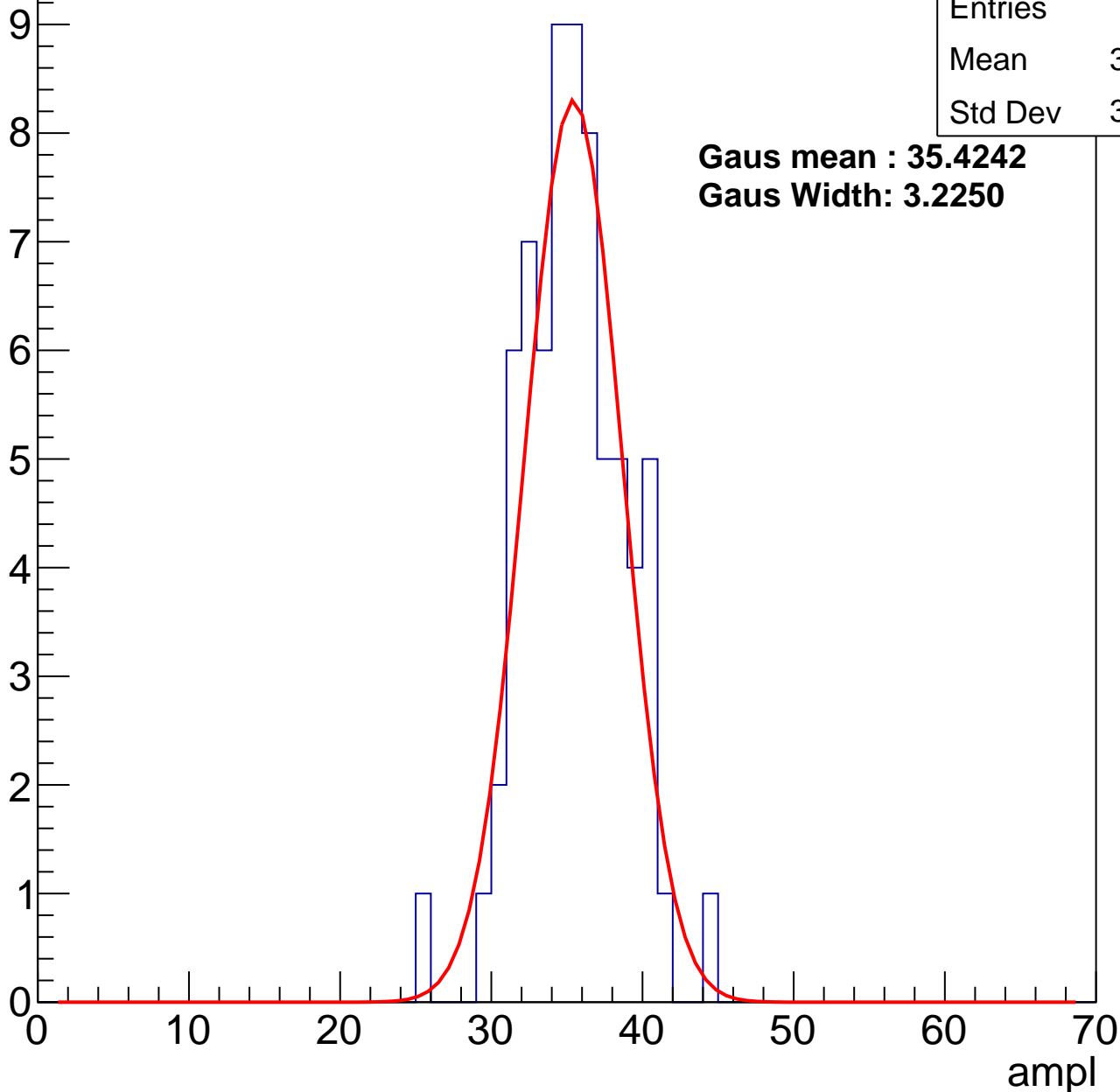
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	34.96
Std Dev	3.288

Gaus mean : 35.4242

Gaus Width: 3.2250



B0L102S, U5-ch28, adc2

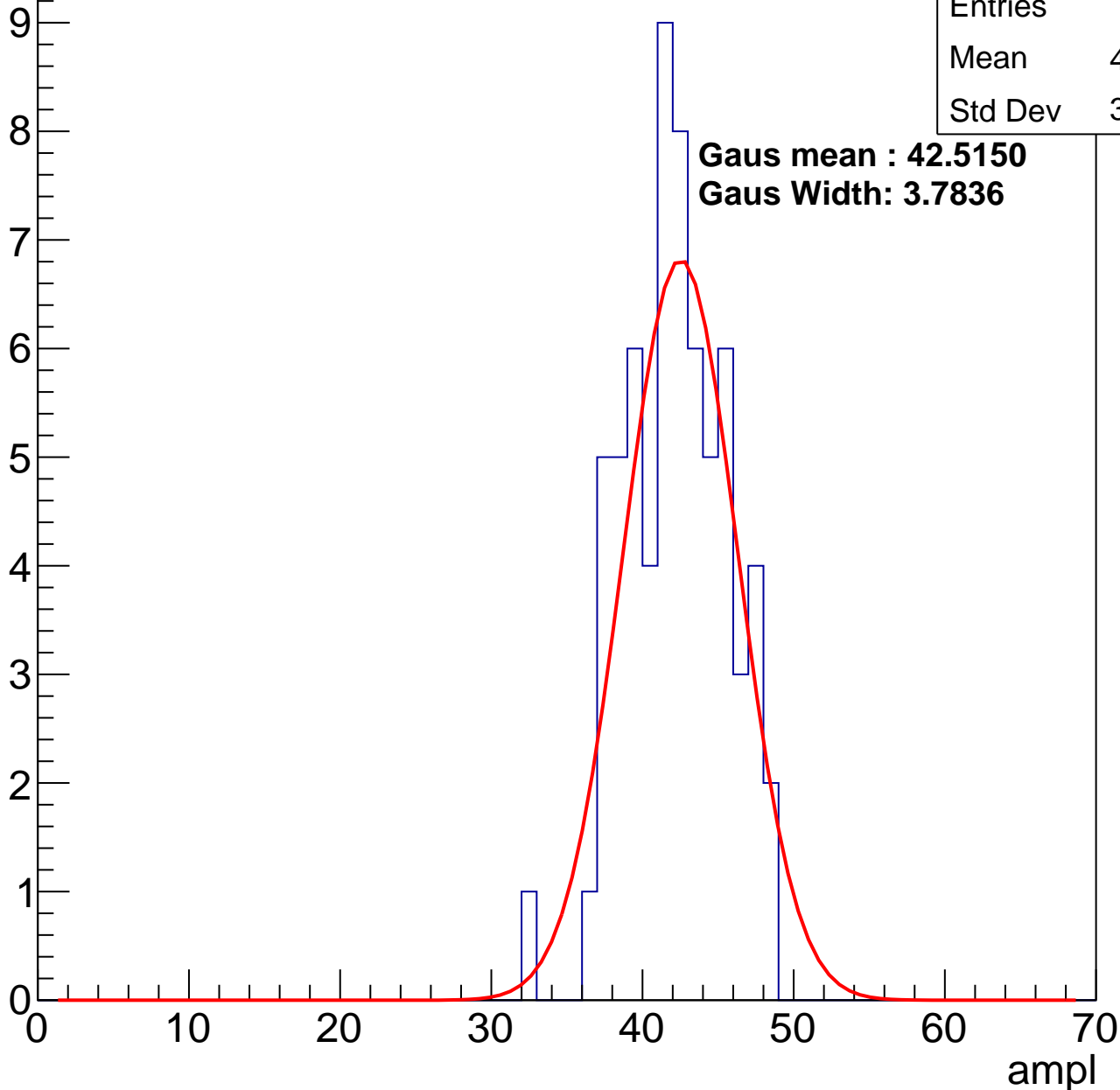
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.72
Std Dev	3.317

Gaus mean : 42.5150

Gaus Width: 3.7836

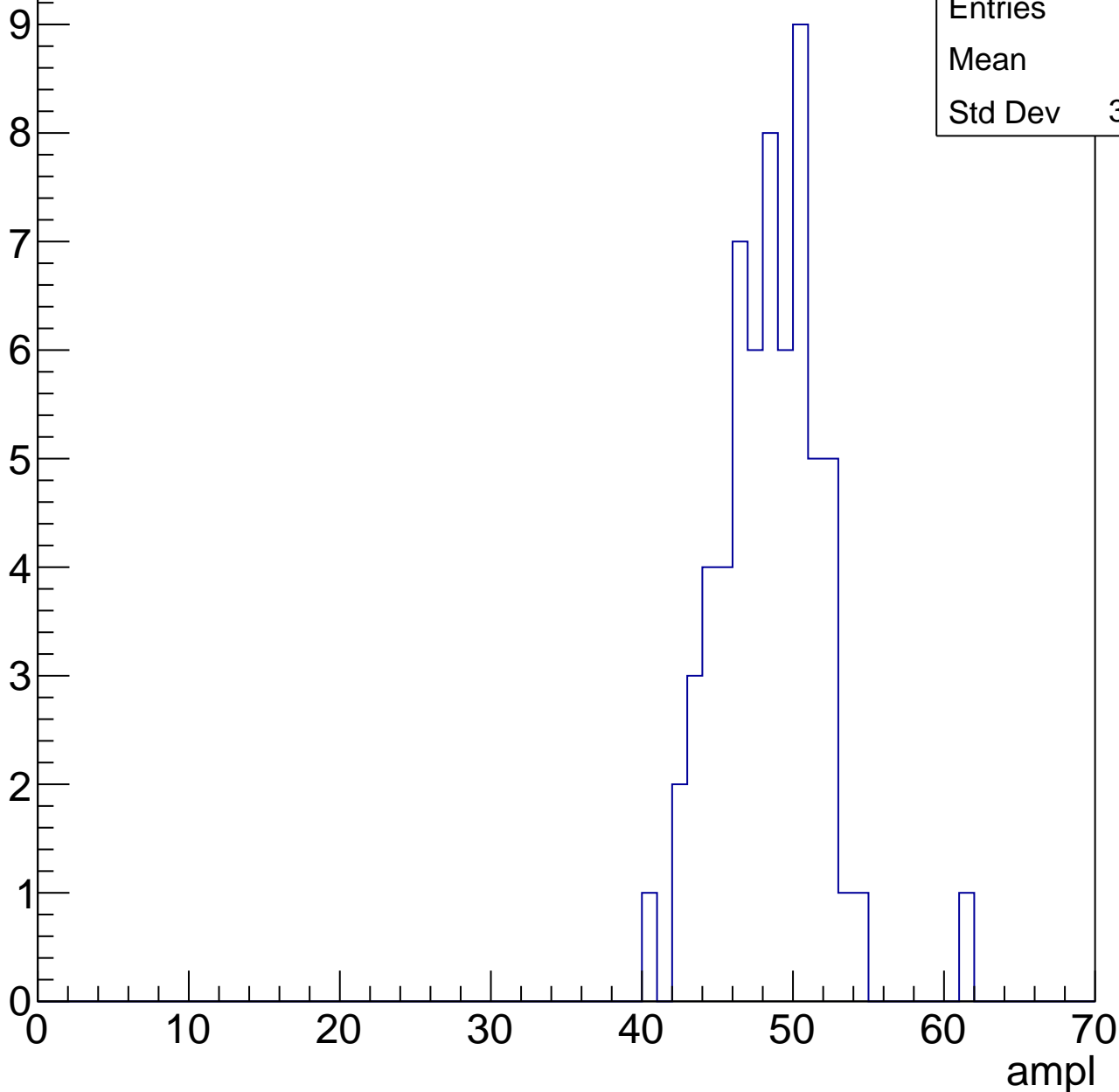


B0L102S, U5-ch28, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	48
Std Dev	3.432

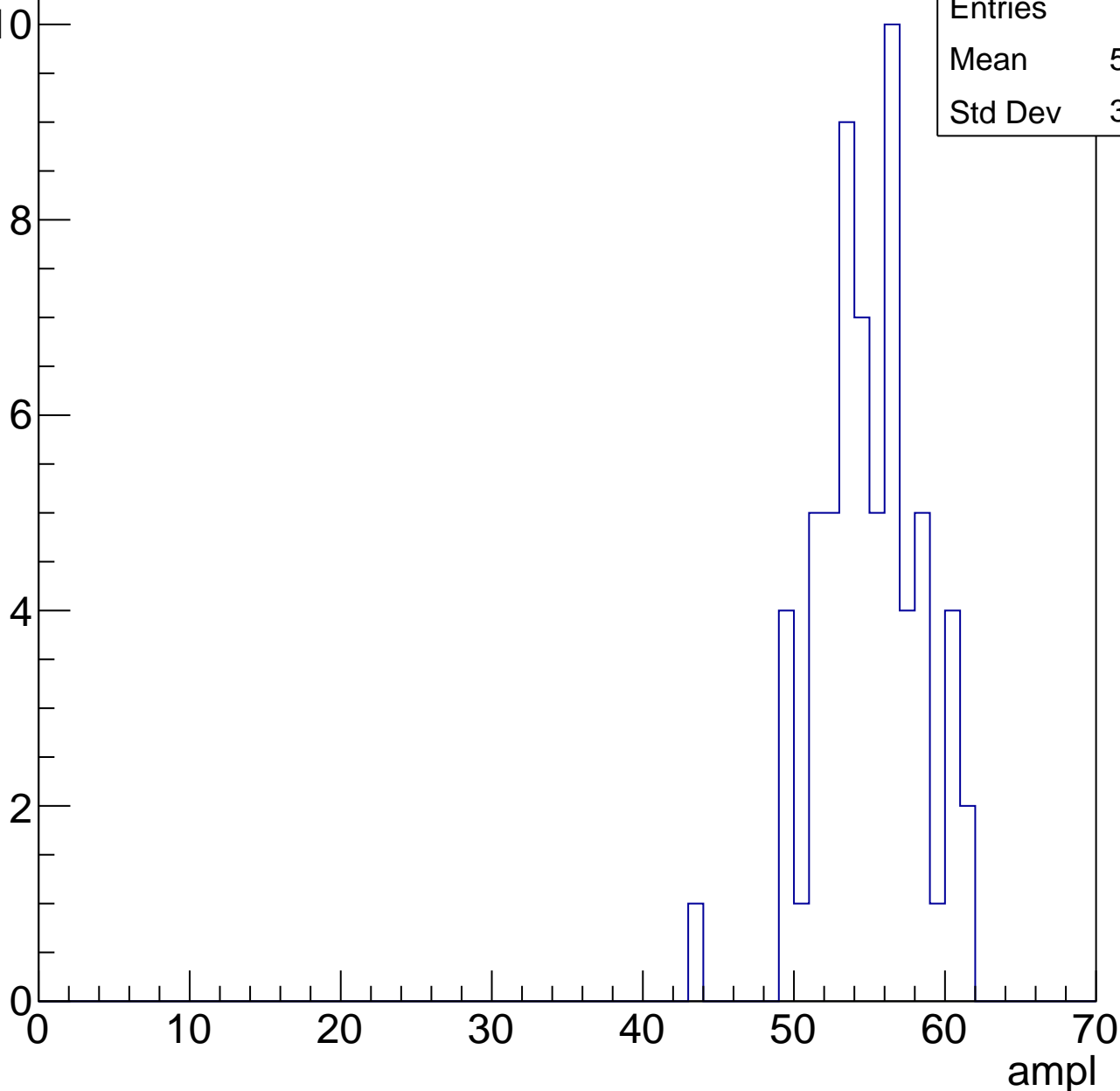


B0L102S, U5-ch28, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	54.49
Std Dev	3.389



B0L102S, U5-ch28, adc5

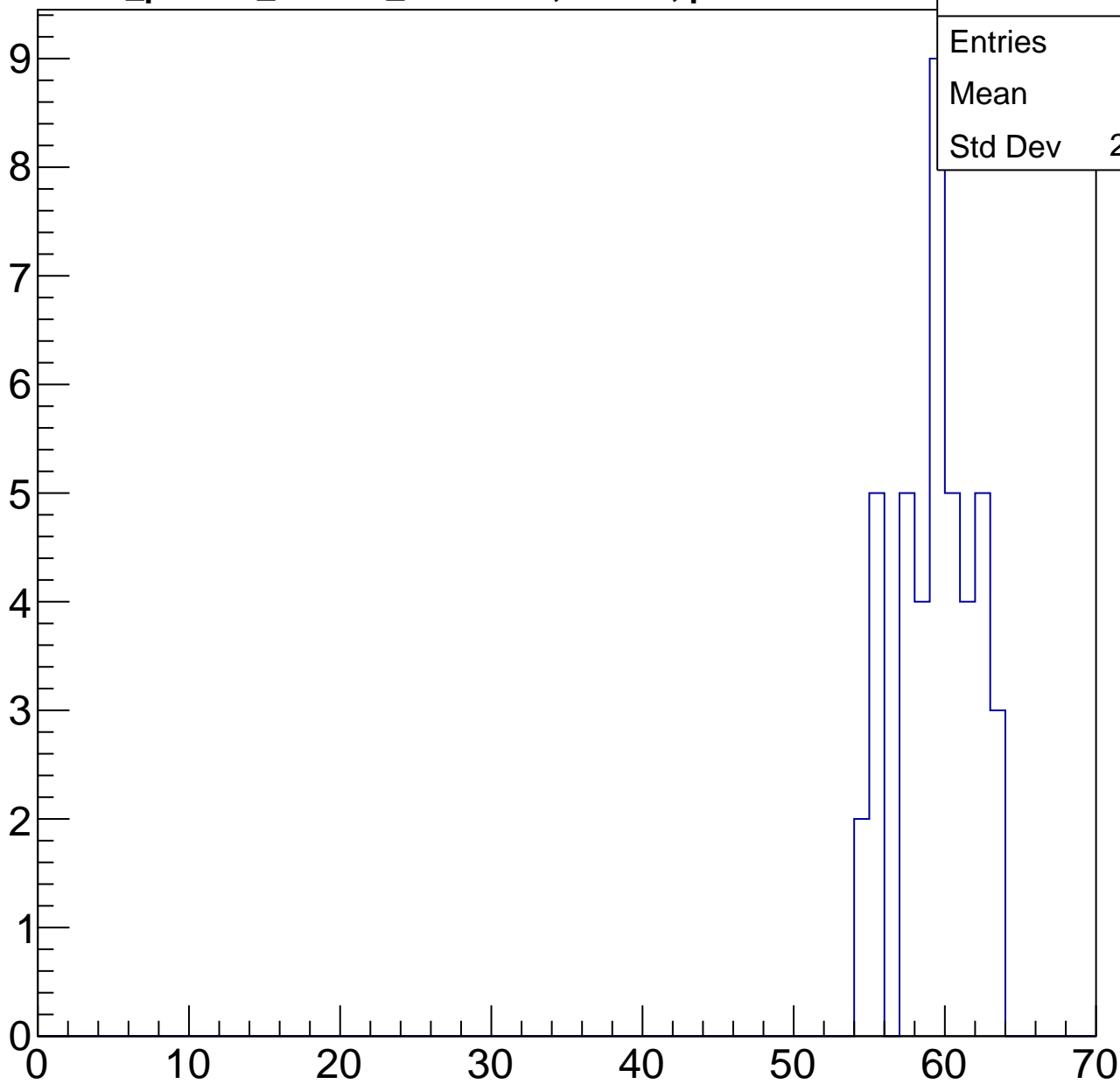
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	58.9
Std Dev	2.524

ampl

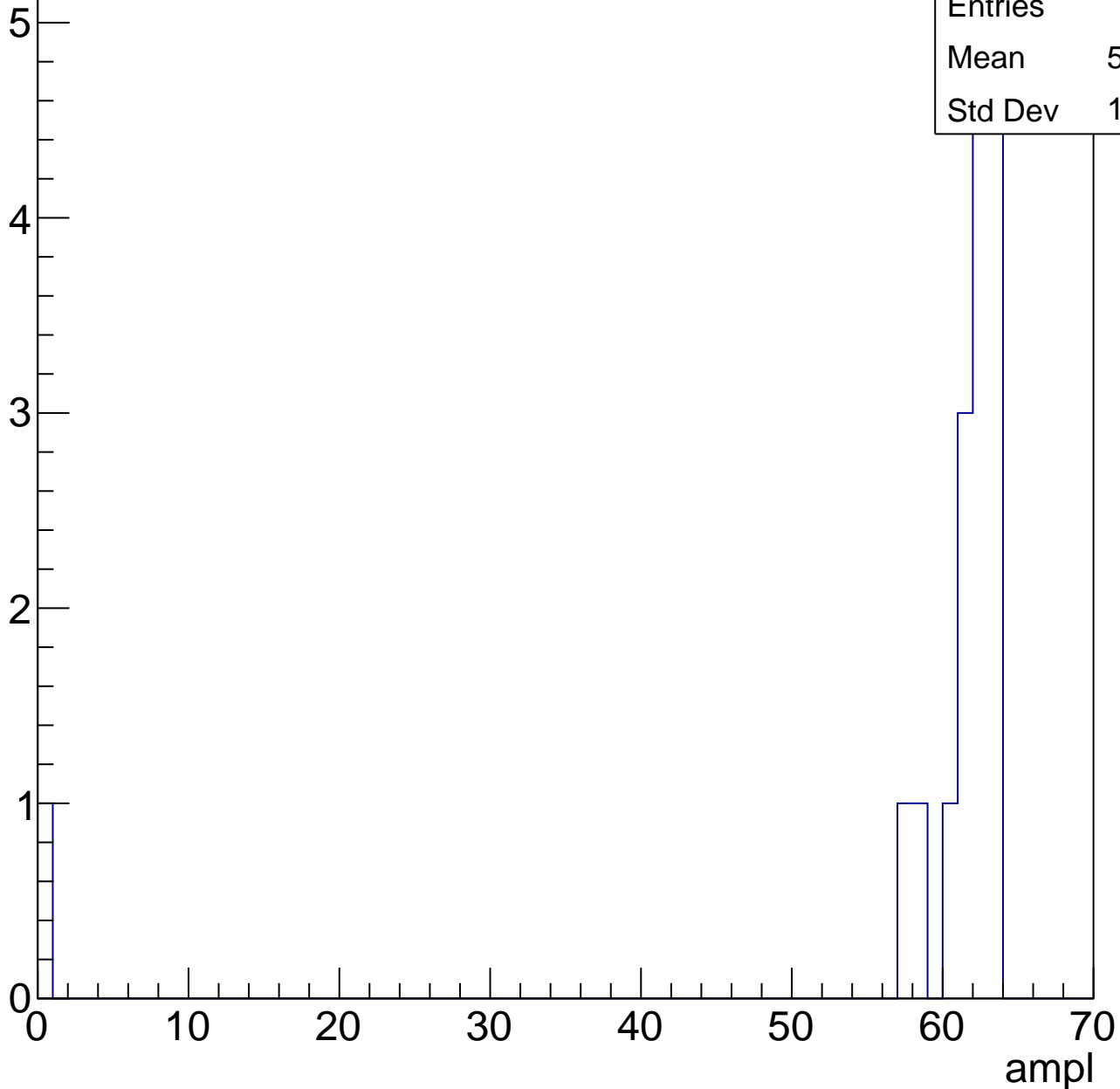


B0L102S, U5-ch28, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	17
Mean	57.82
Std Dev	14.55



B0L102S, U5-ch28, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch29, adc0

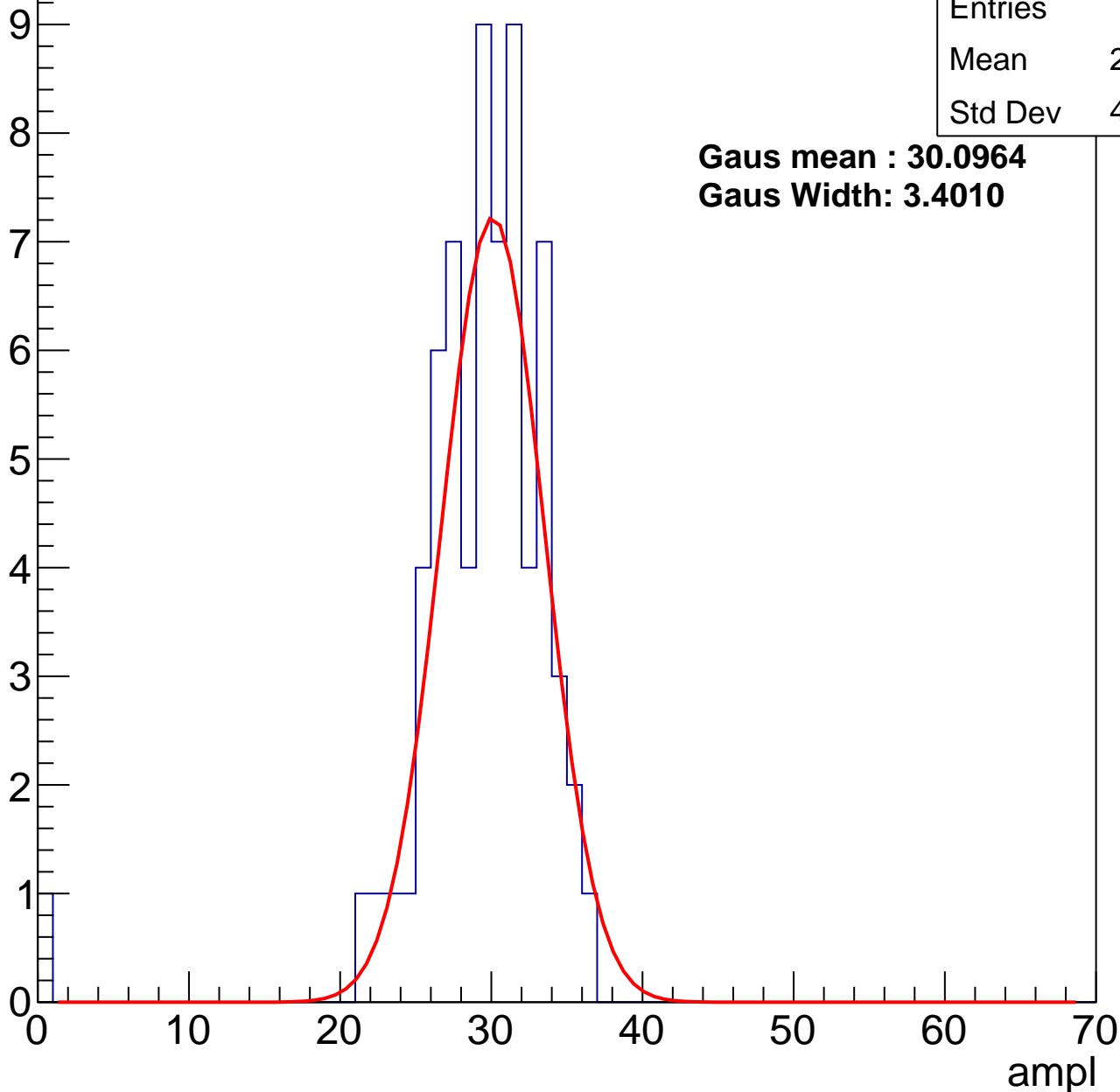
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.88
Std Dev	4.776

Gaus mean : 30.0964

Gaus Width: 3.4010



B0L102S, U5-ch29, adc1

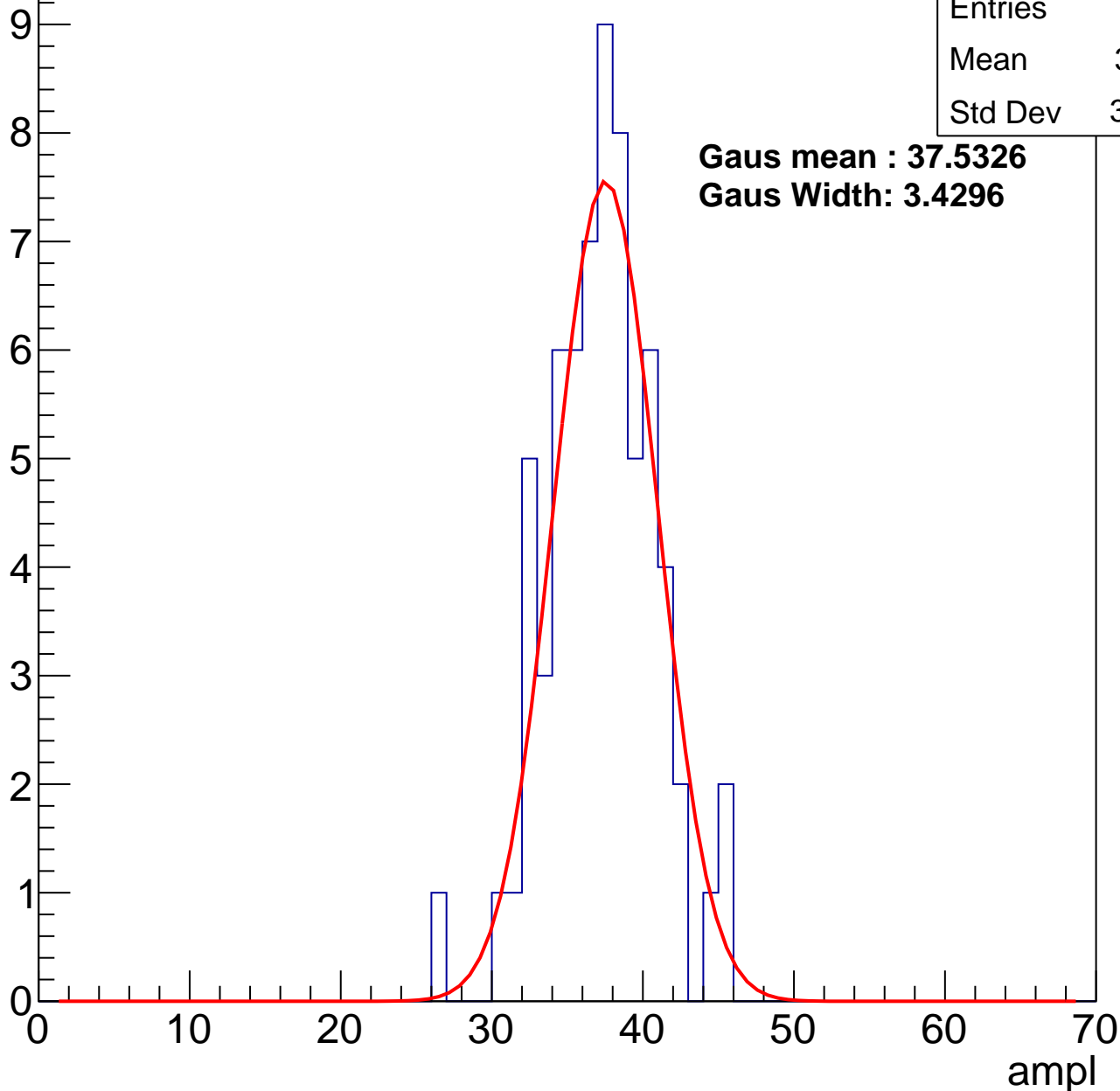
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.81
Std Dev	3.529

Gaus mean : 37.5326

Gaus Width: 3.4296



B0L102S, U5-ch29, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	69
Mean	42.78
Std Dev	3.663

Gaus mean : 43.4573

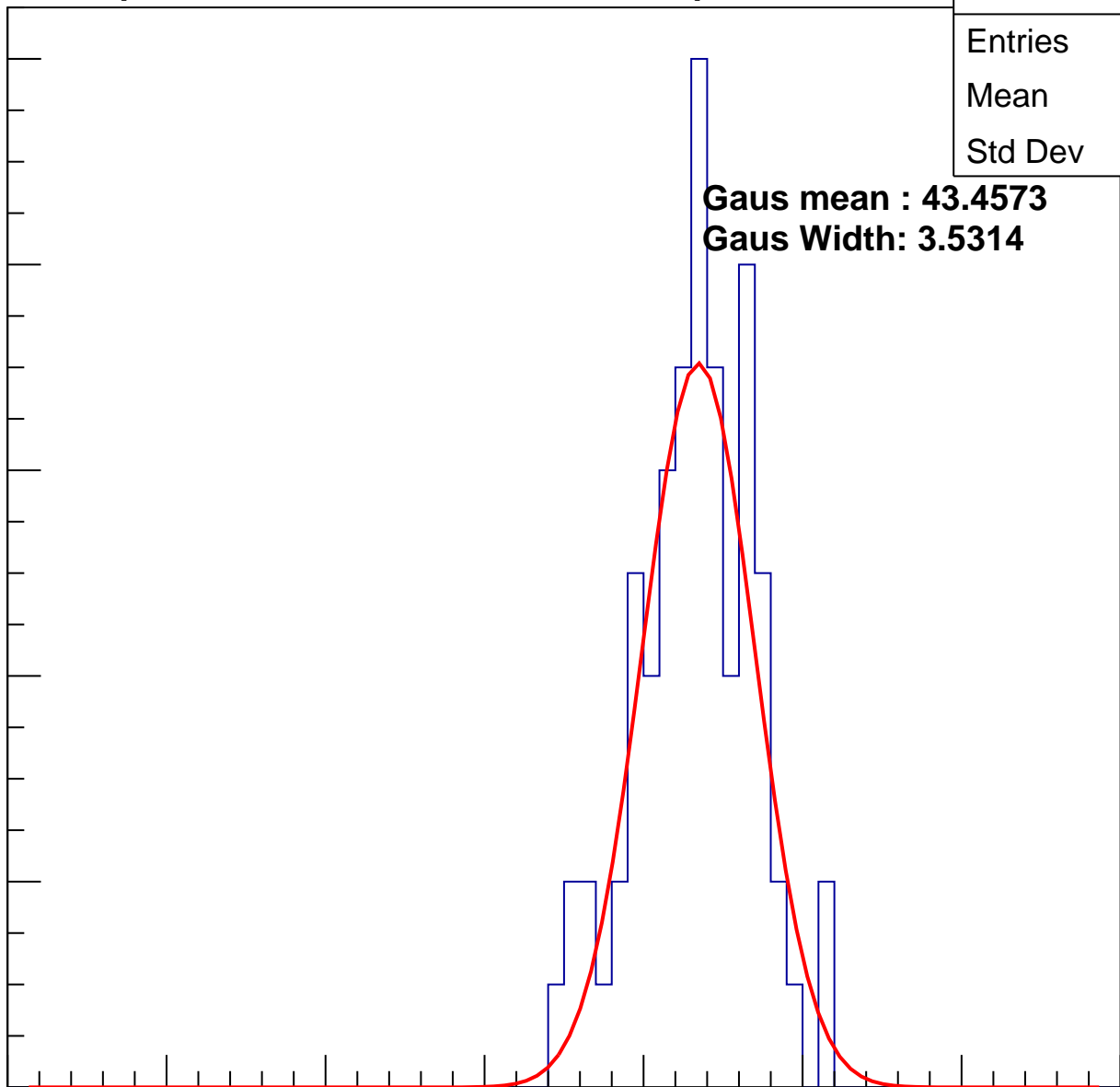
Gaus Width: 3.5314

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

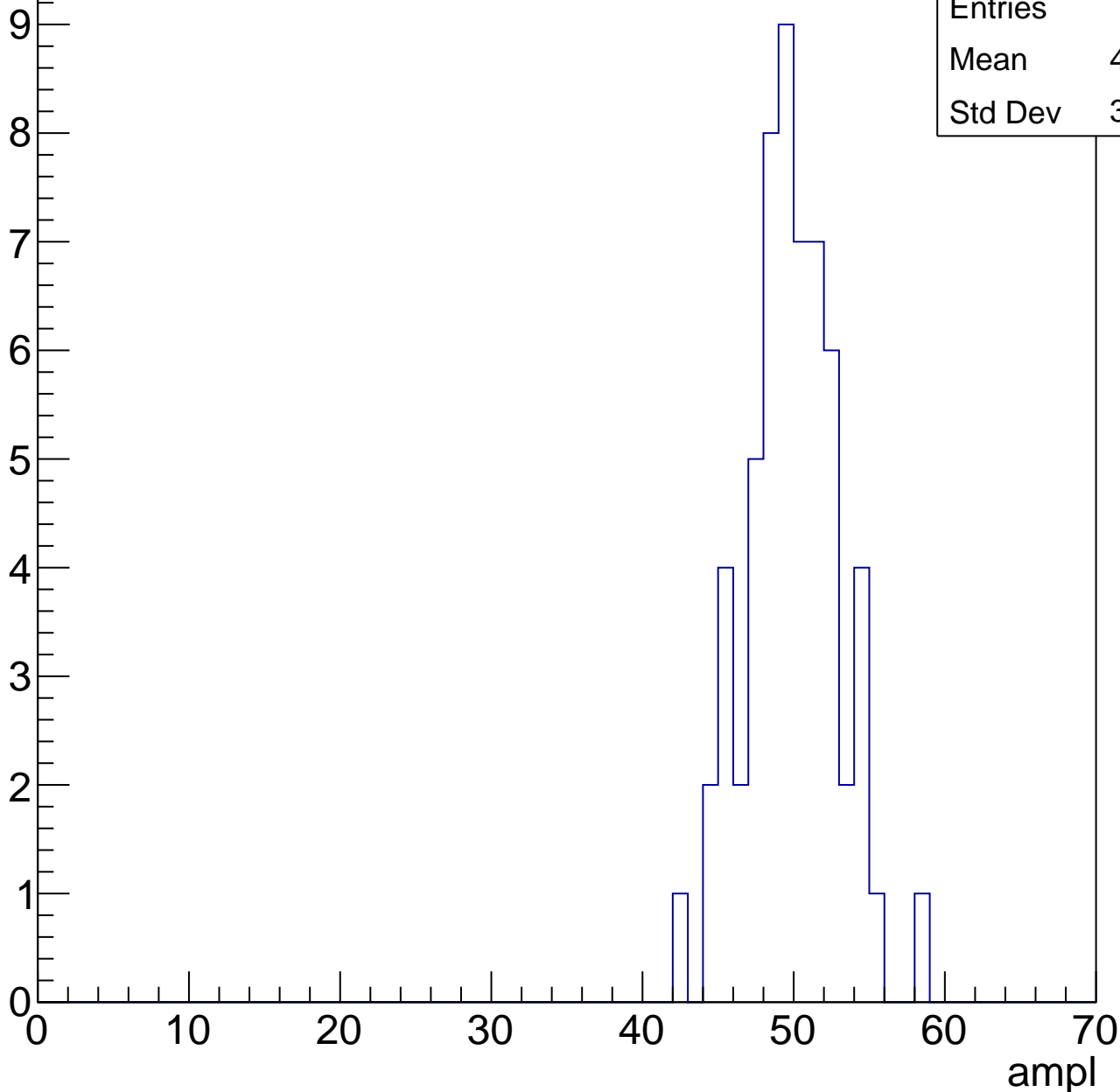


B0L102S, U5-ch29, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

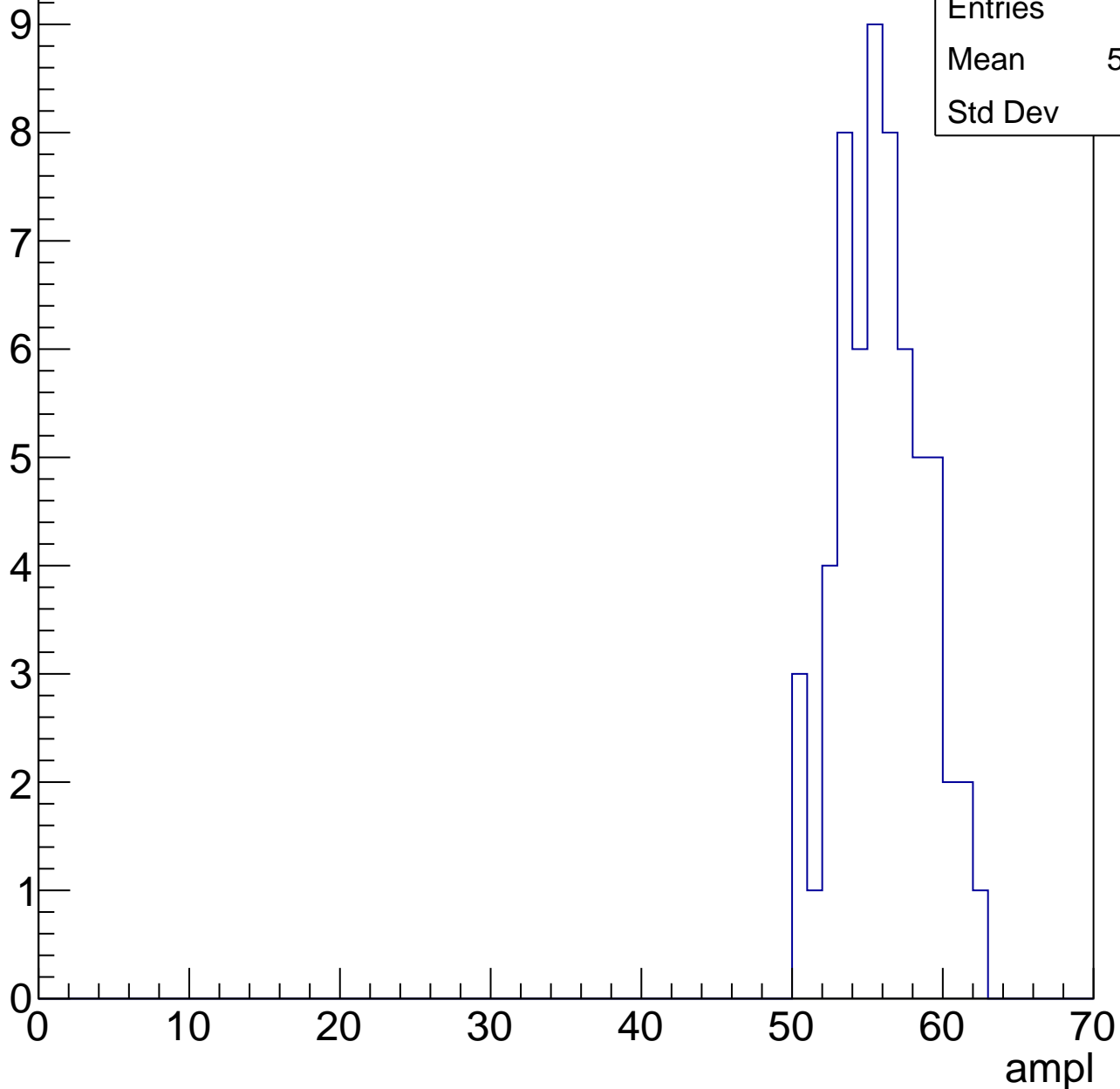
Entries	59
Mean	49.42
Std Dev	3.015



B0L102S, U5-ch29, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



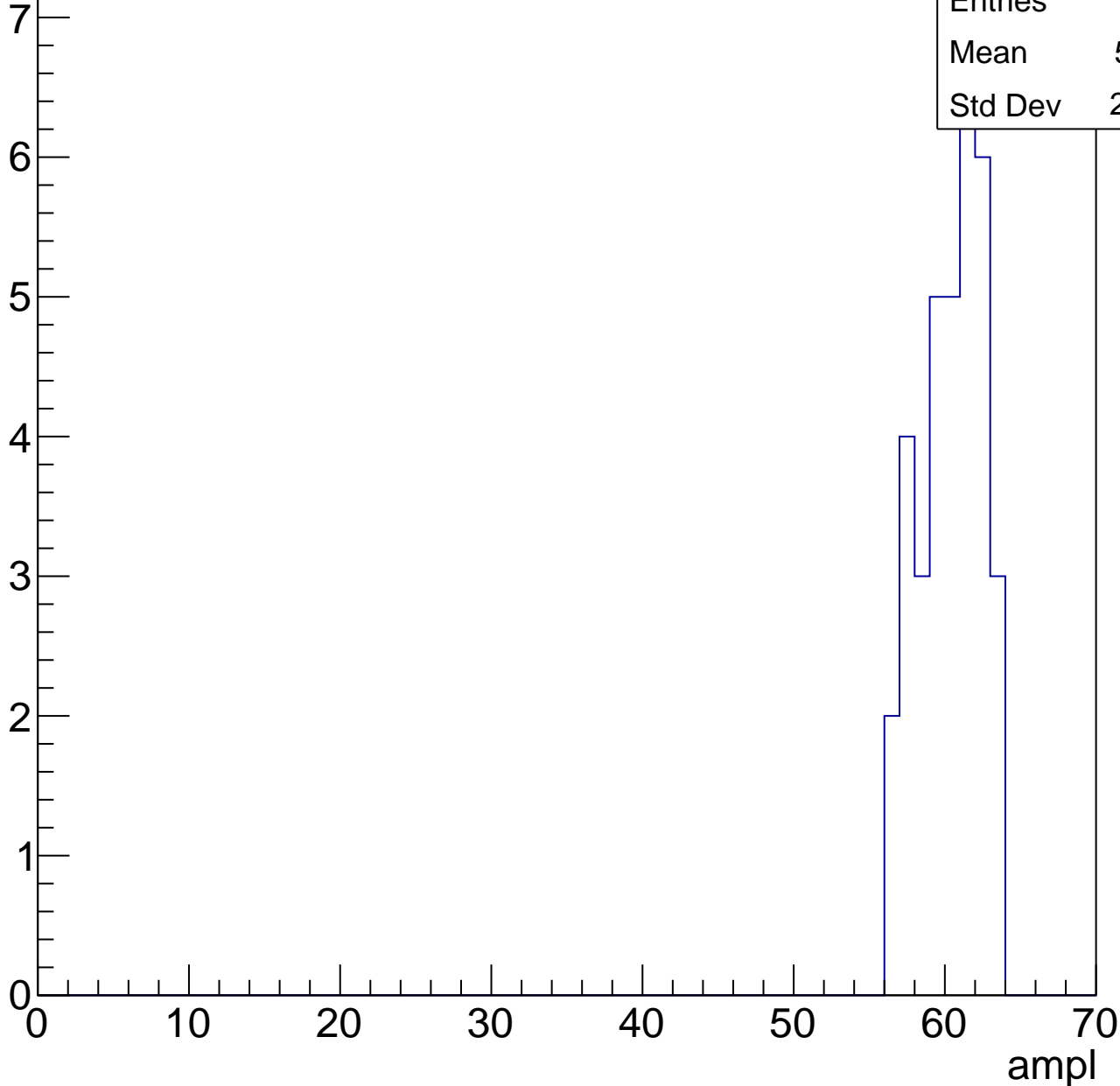
Entries	60
Mean	55.52
Std Dev	2.82

B0L102S, U5-ch29, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	35
Mean	59.91
Std Dev	2.019

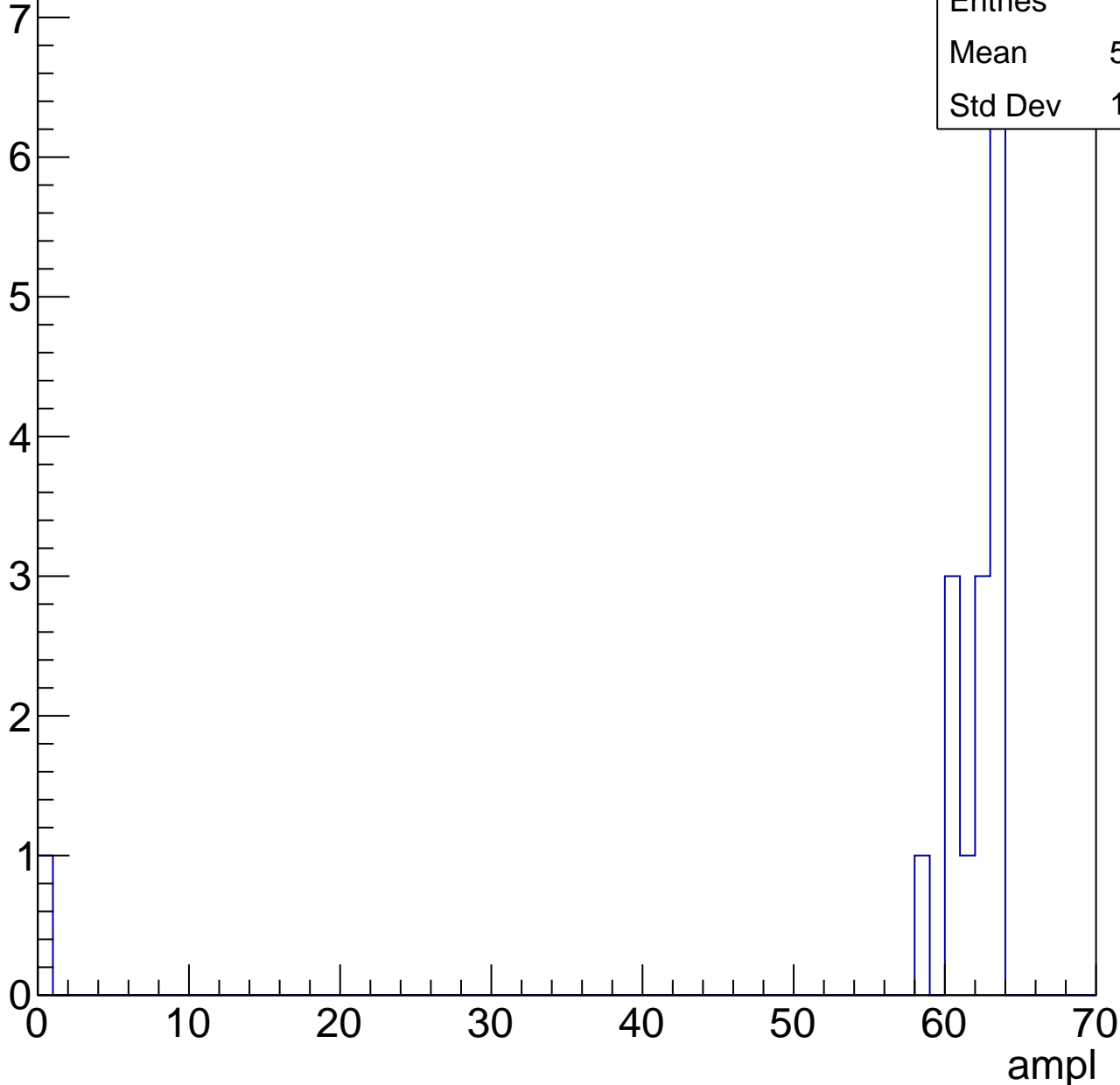


B0L102S, U5-ch29, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	16
Mean	57.88
Std Dev	15.02



B0L102S, U5-ch29, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch30, adc0

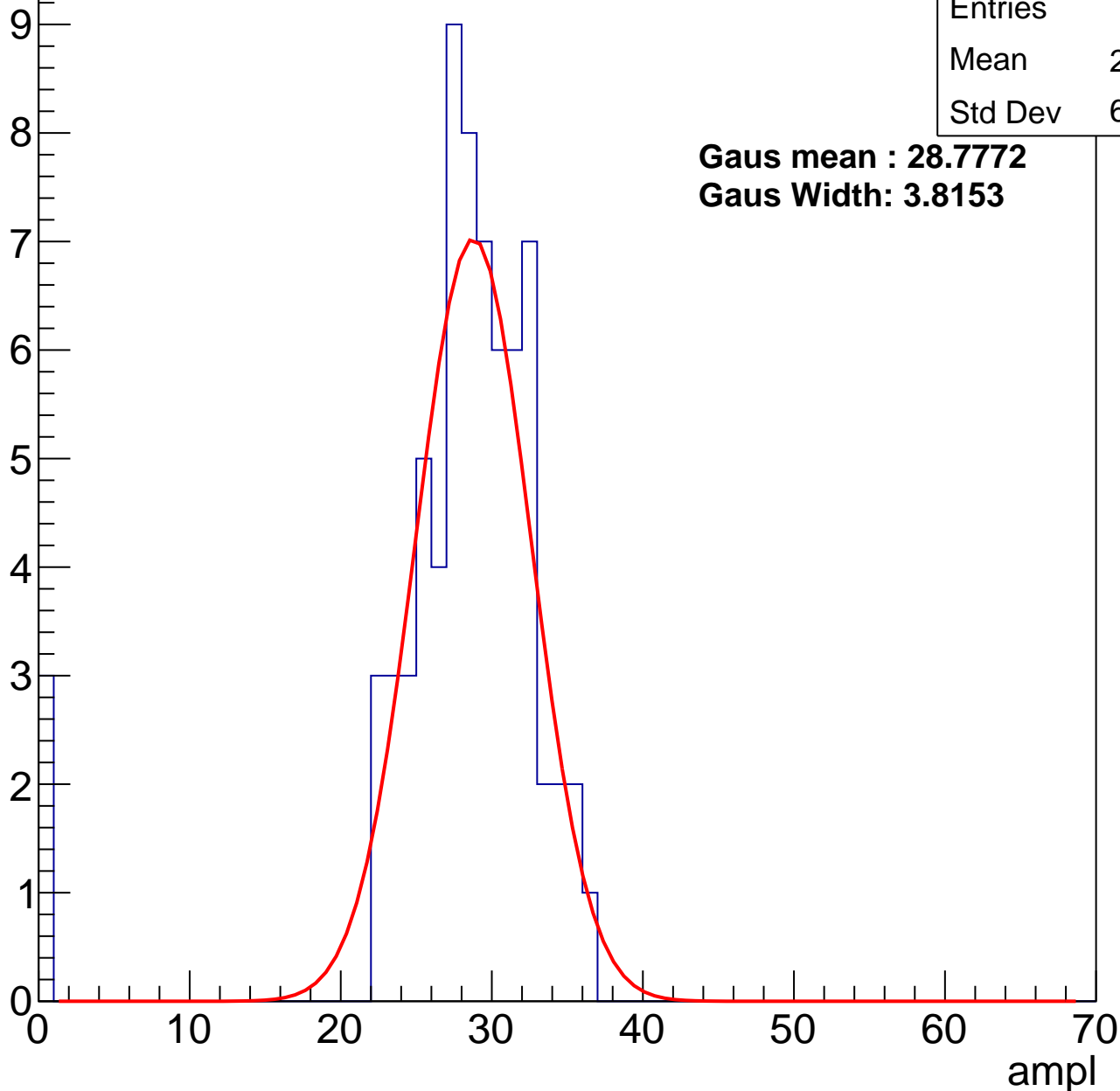
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	27.27
Std Dev	6.603

Gaus mean : 28.7772

Gaus Width: 3.8153



B0L102S, U5-ch30, adc1

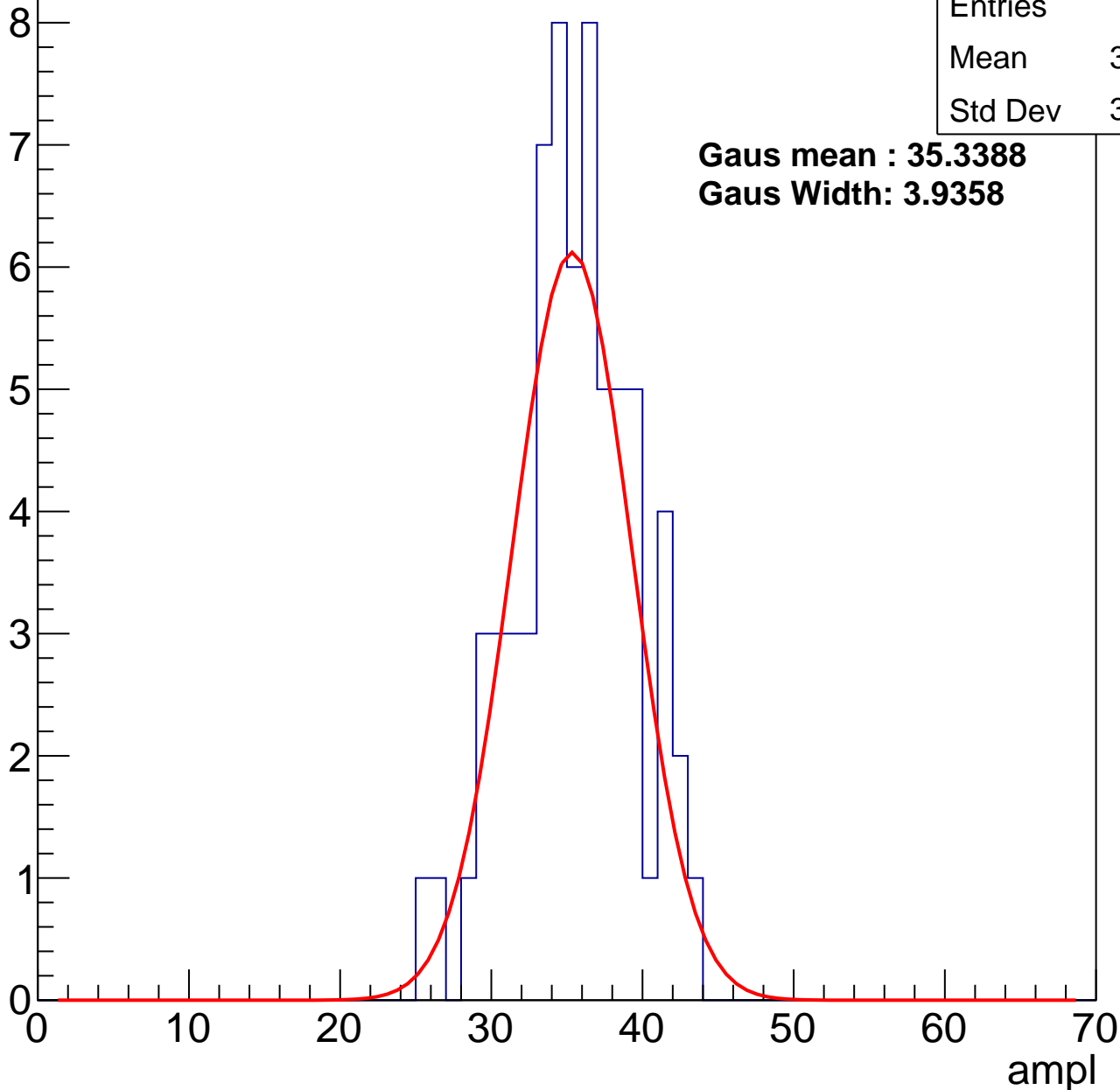
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	35.03
Std Dev	3.879

Gaus mean : 35.3388

Gaus Width: 3.9358



B0L102S, U5-ch30, adc2

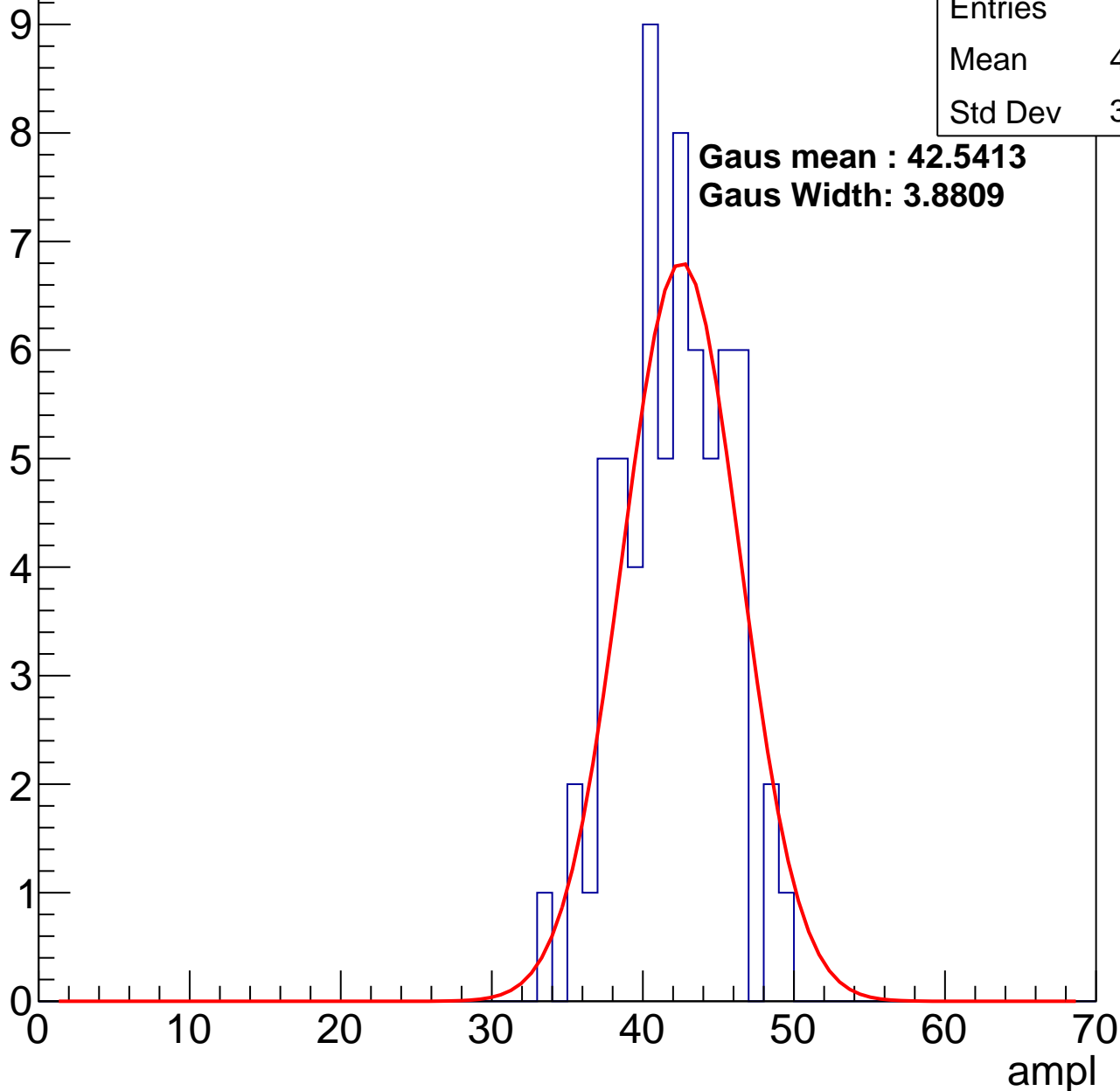
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.52
Std Dev	3.434

Gaus mean : 42.5413

Gaus Width: 3.8809

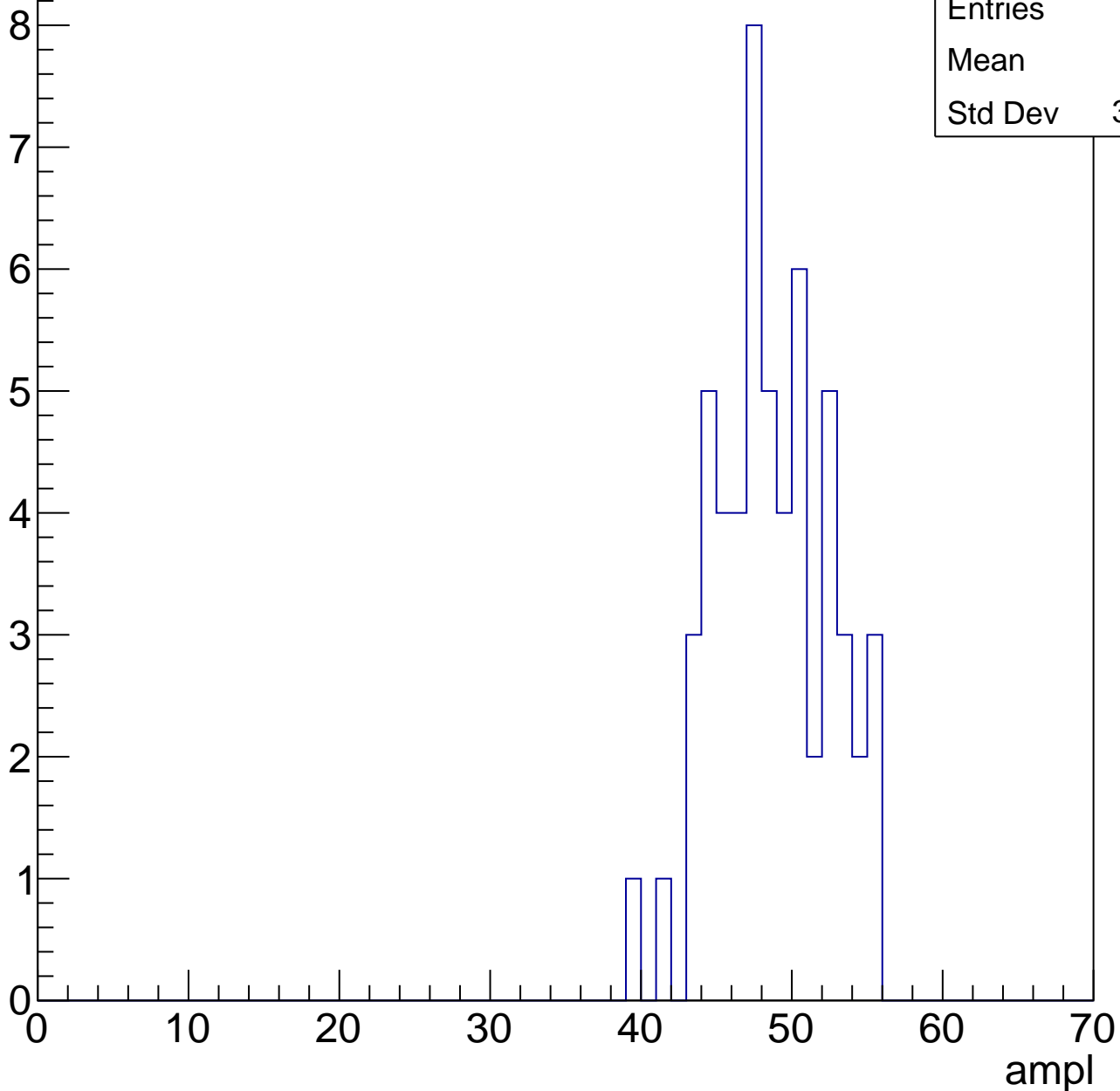


B0L102S, U5-ch30, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	48.2
Std Dev	3.691

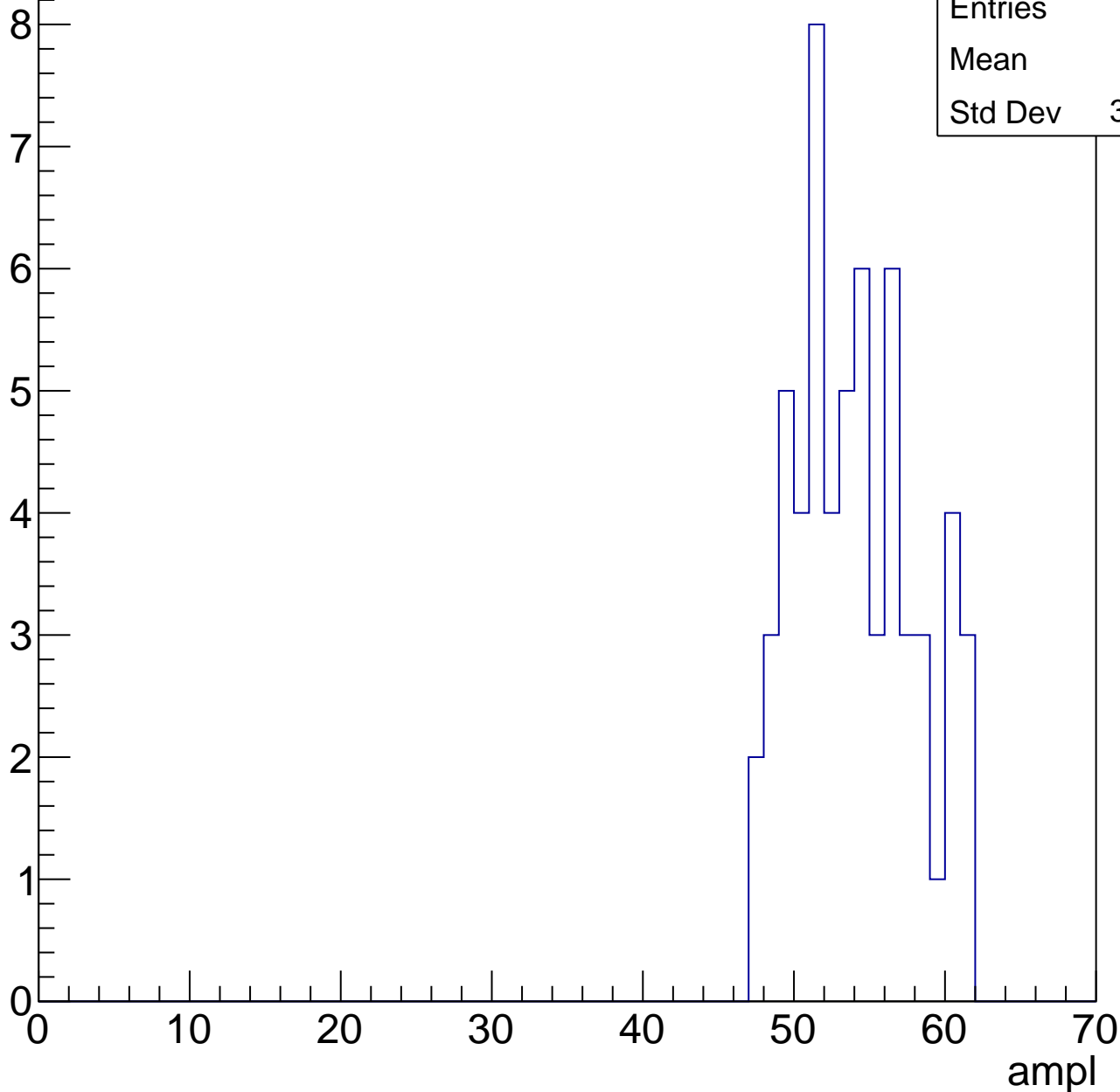


B0L102S, U5-ch30, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	53.6
Std Dev	3.865



B0L102S, U5-ch30, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 58

Mean 58.88

Std Dev 2.925

ampl

0

10

20

30

40

50

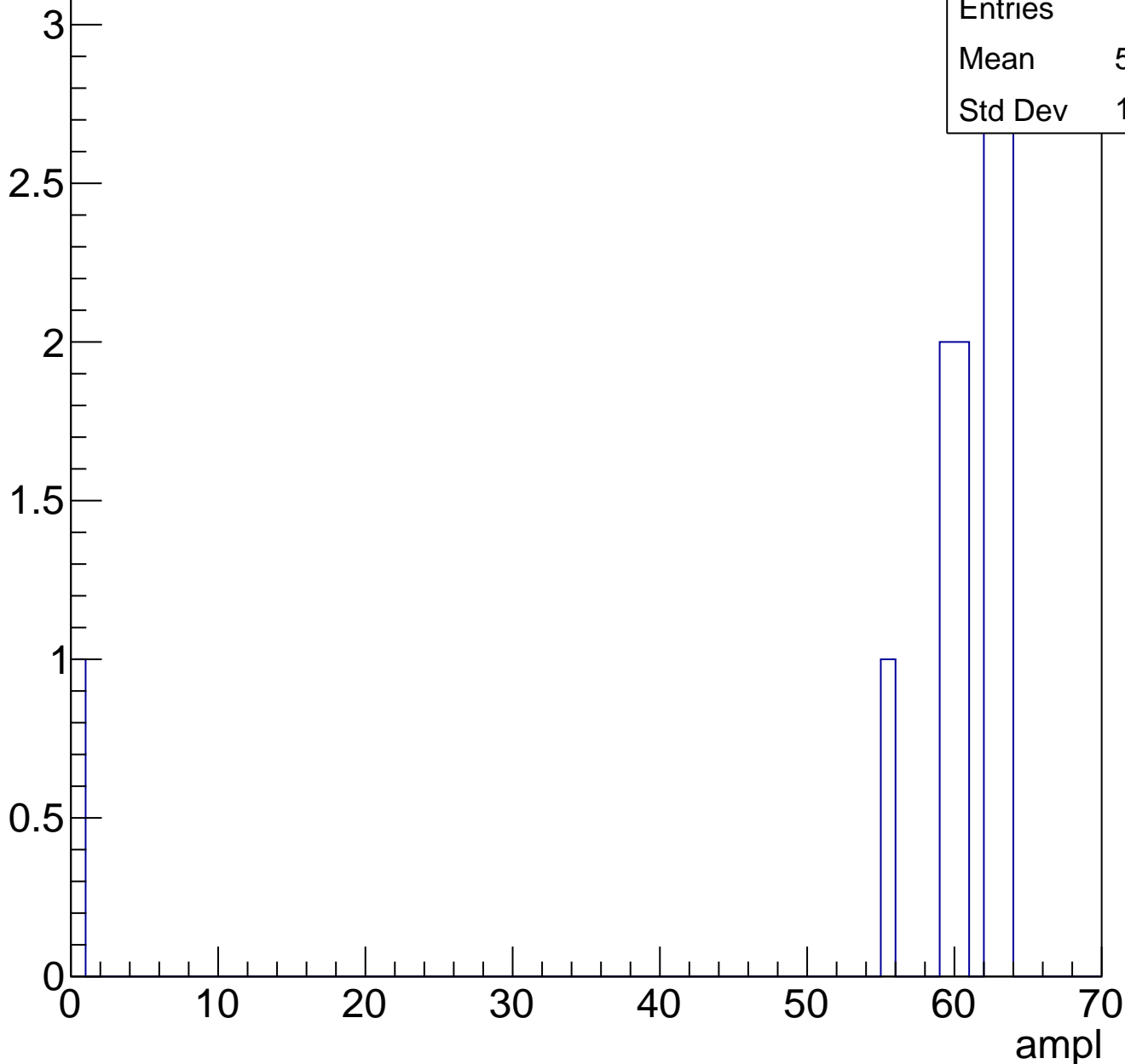
60

70

B0L102S, U5-ch30, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch30, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch31, adc0

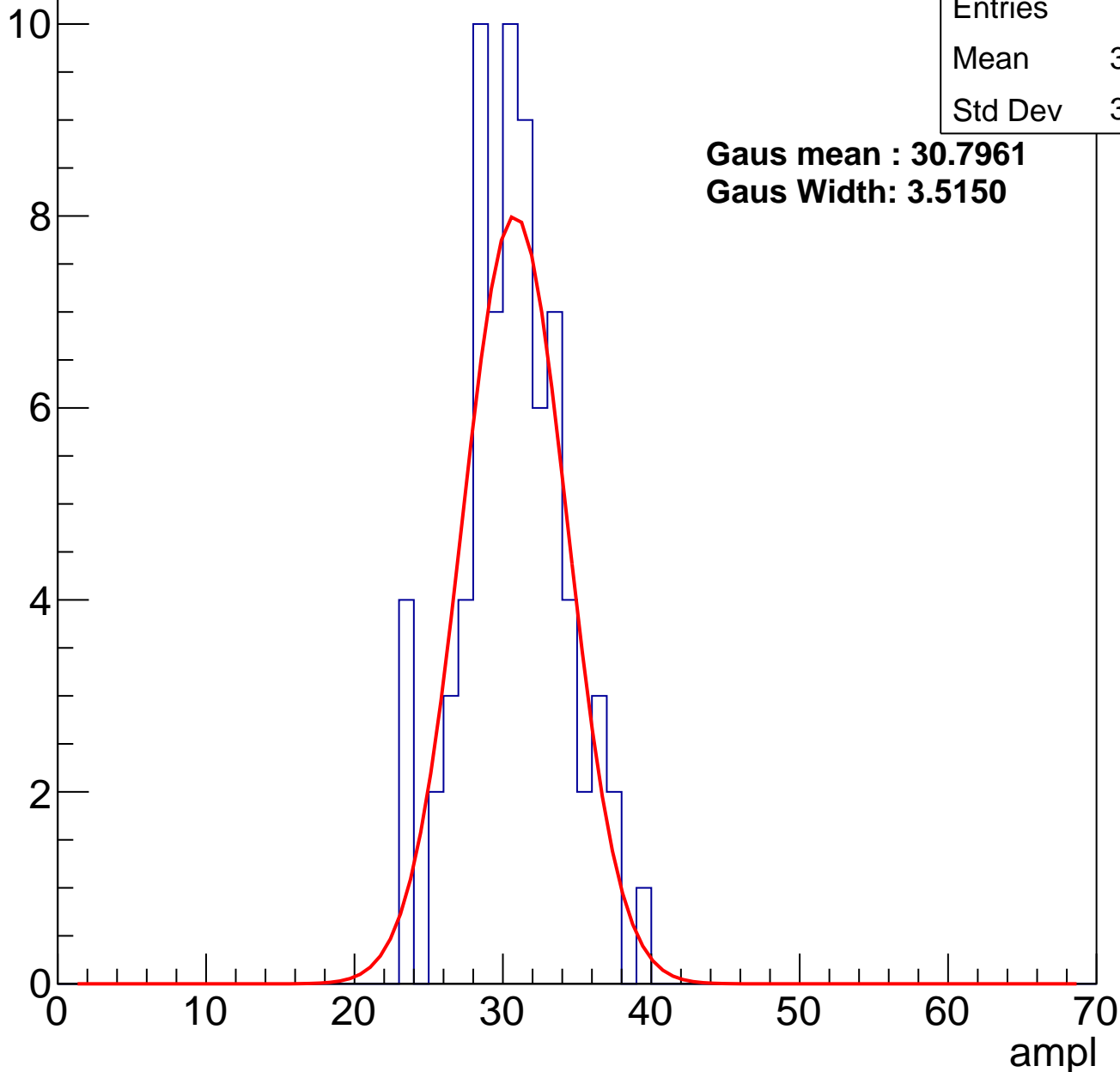
calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	30.27
Std Dev	3.426

Gaus mean : 30.7961

Gaus Width: 3.5150

Entry



B0L102S, U5-ch31, adc1

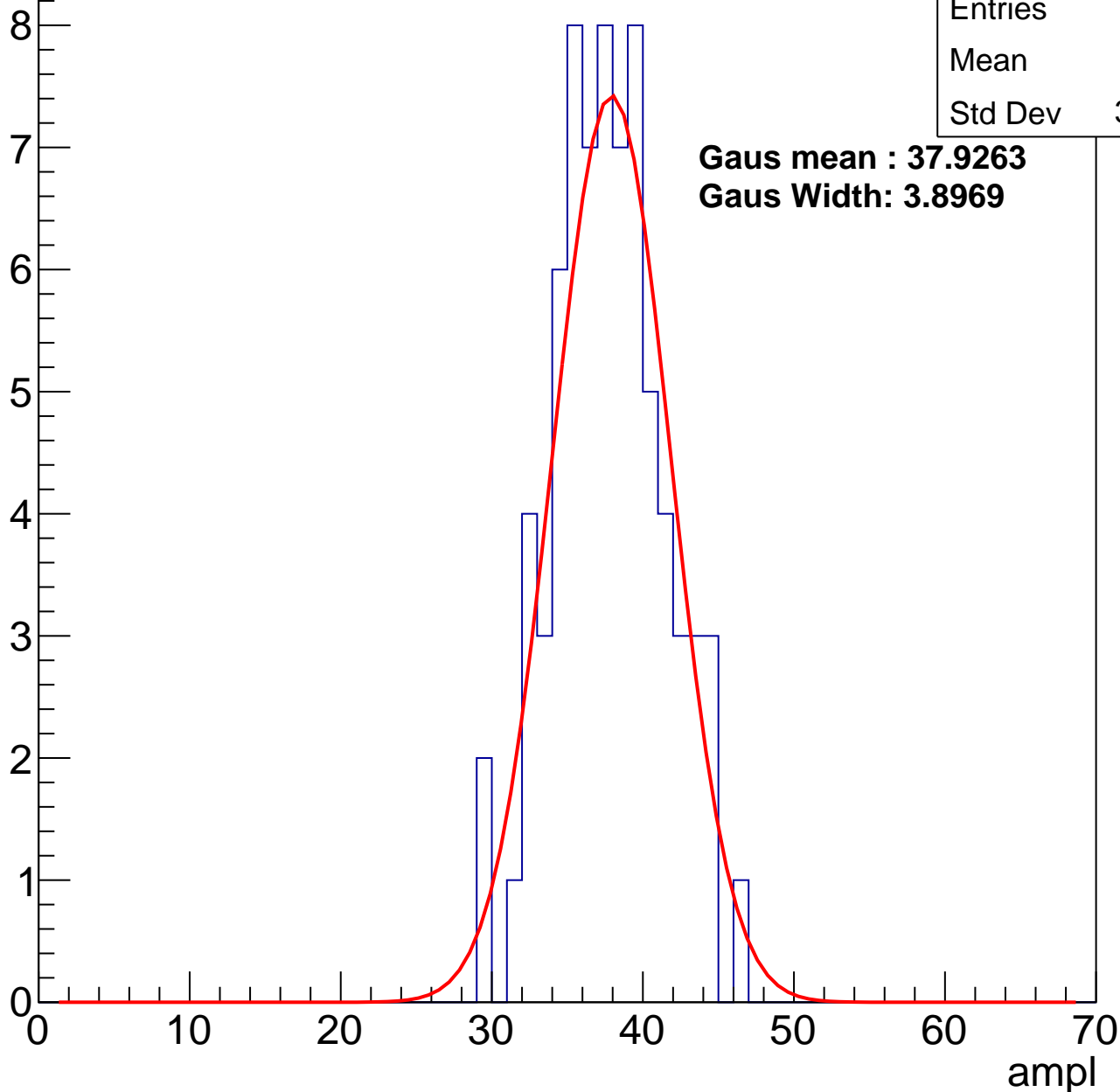
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	37.3
Std Dev	3.621

Gaus mean : 37.9263

Gaus Width: 3.8969



B0L102S, U5-ch31, adc2

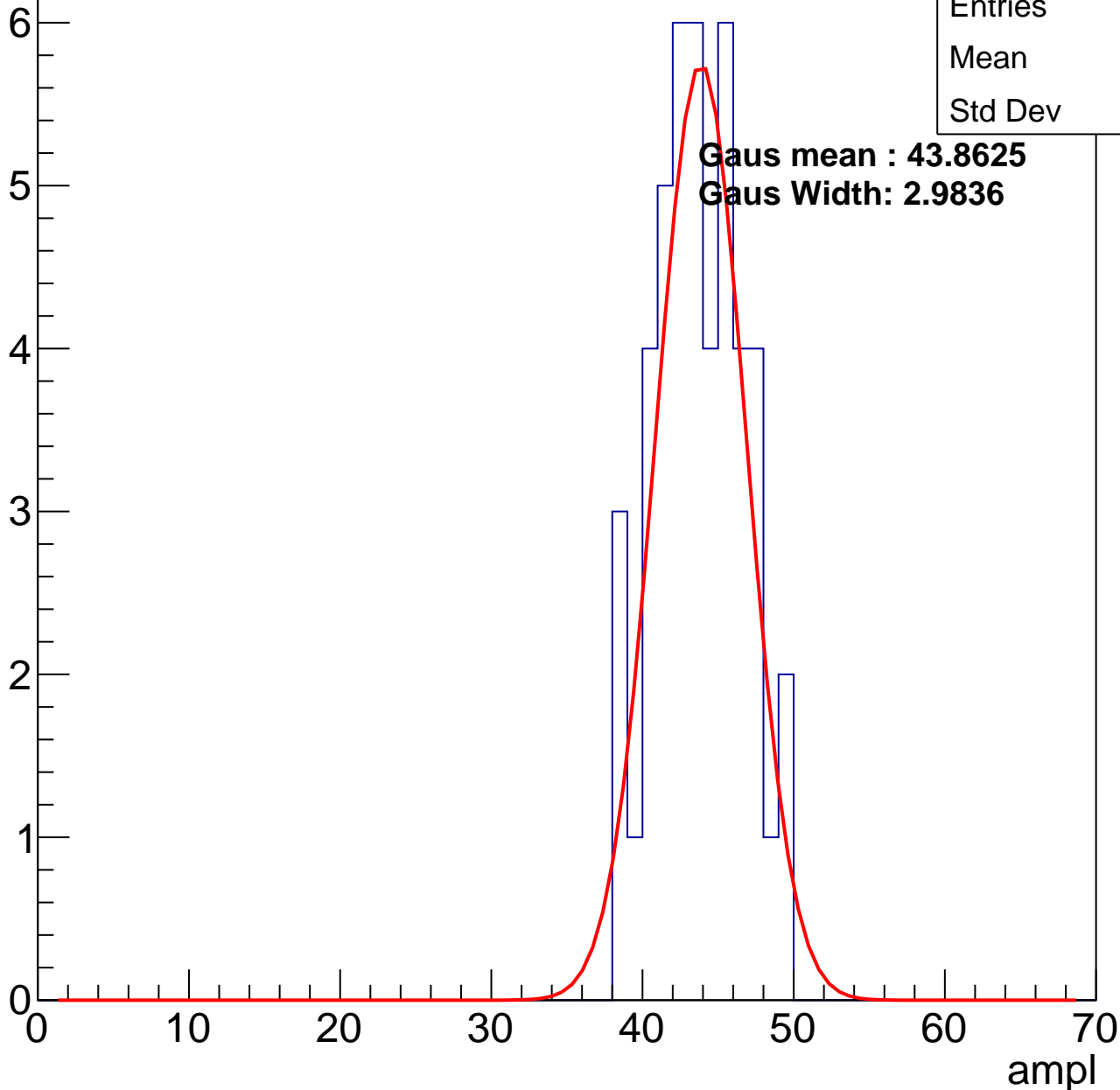
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	43.3
Std Dev	2.85

Gaus mean : 43.8625

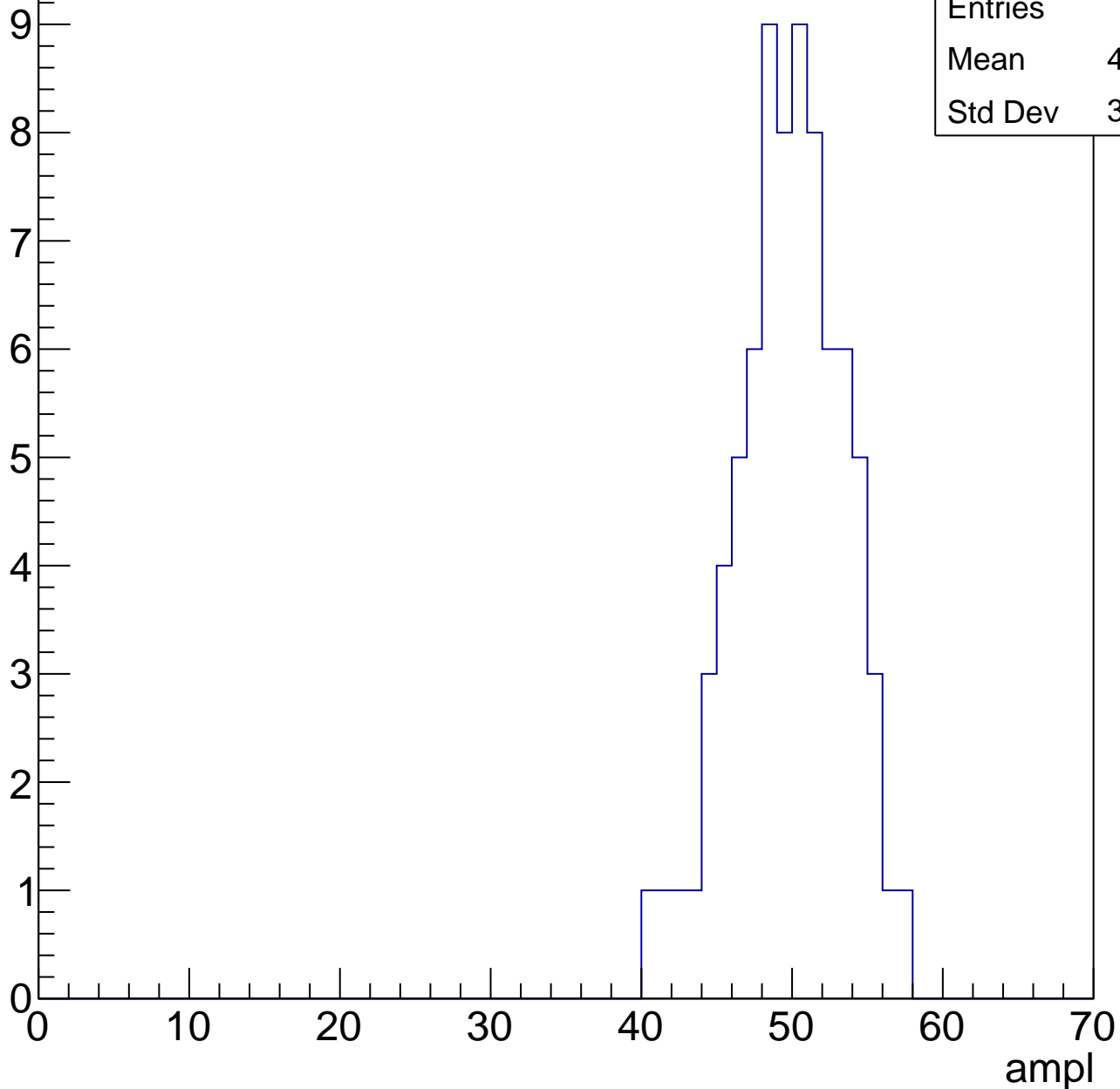
Gaus Width: 2.9836



B0L102S, U5-ch31, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



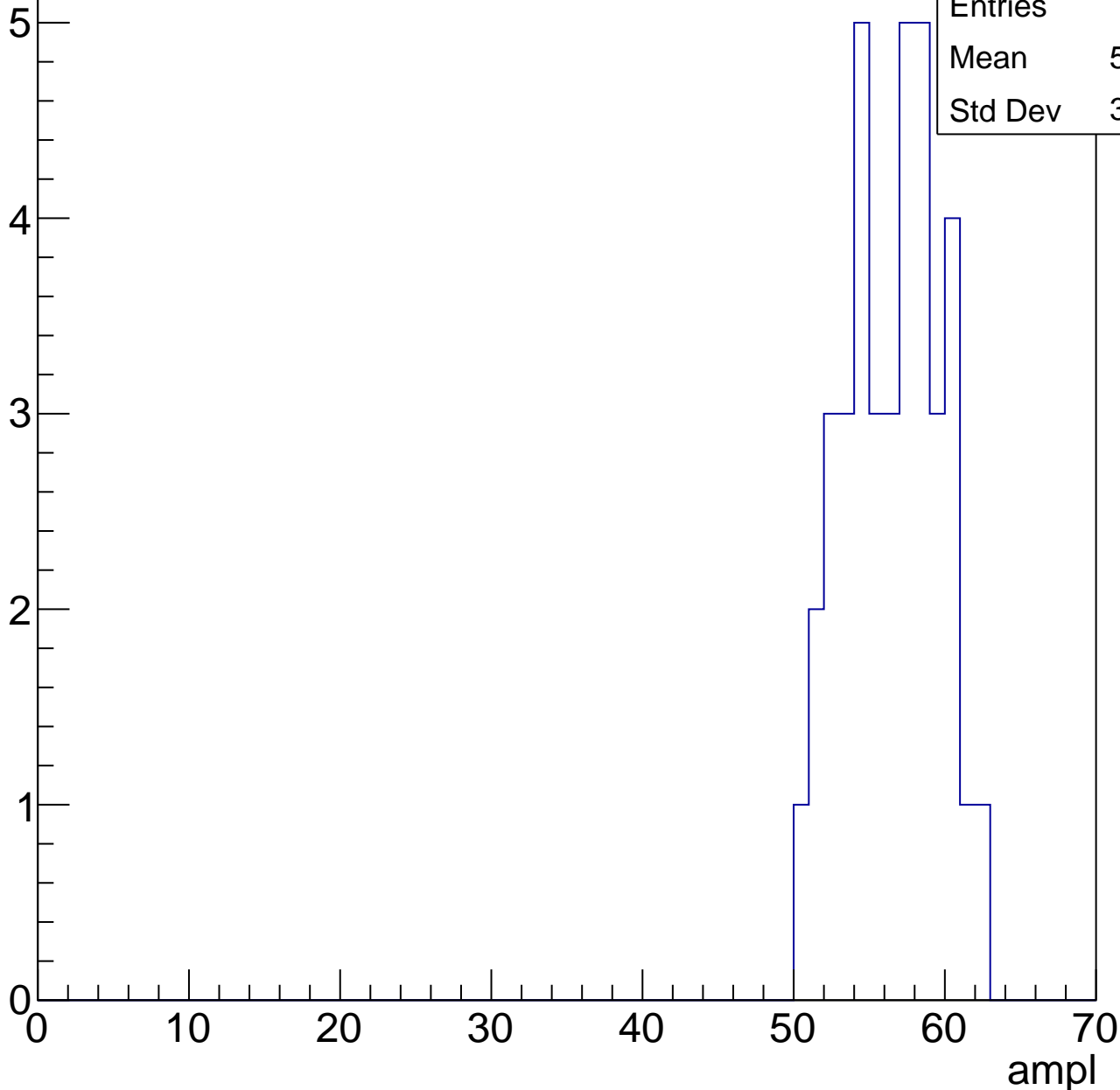
Entries	78
Mean	49.36
Std Dev	3.534

B0L102S, U5-ch31, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	56.03
Std Dev	3.042

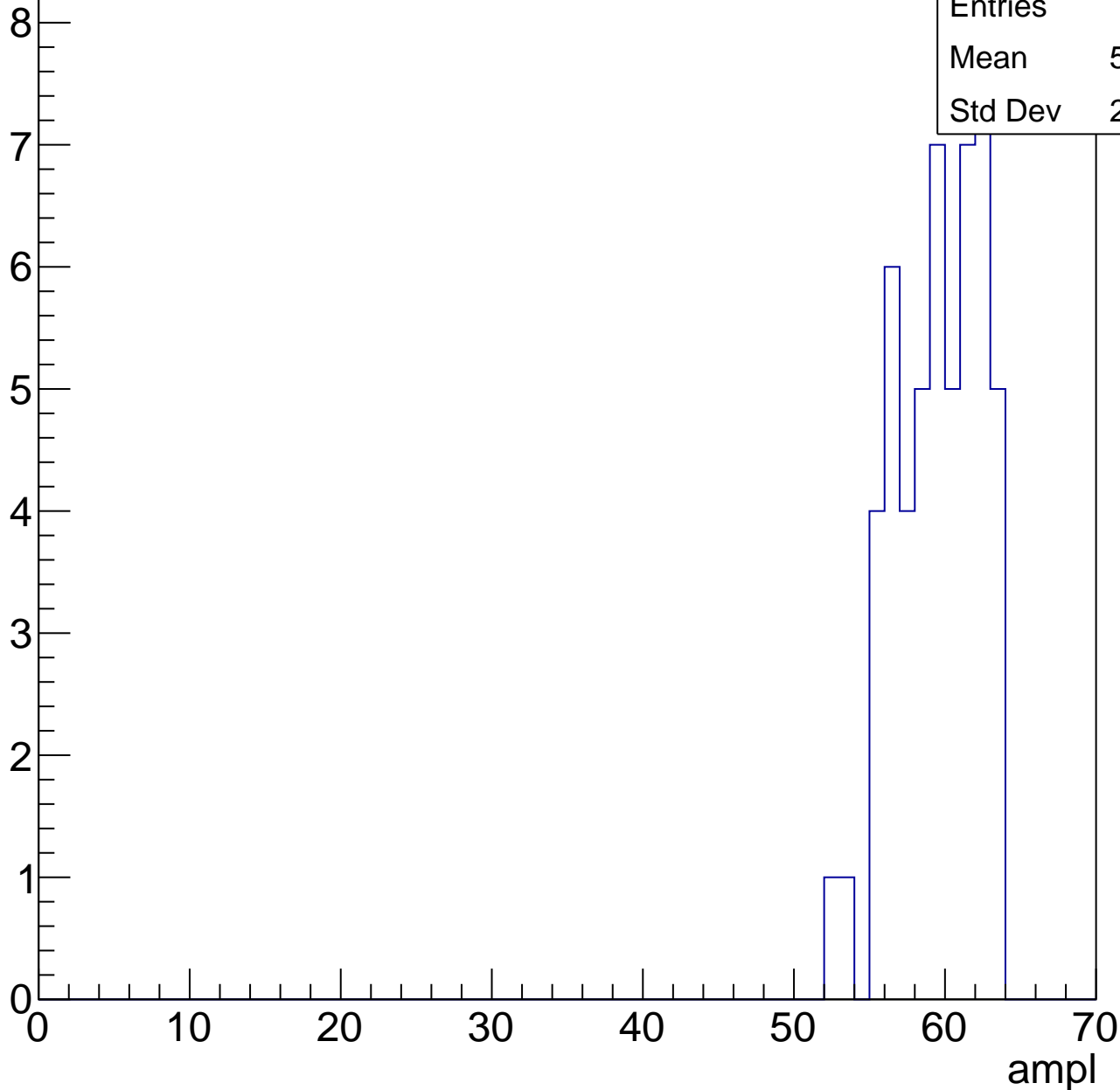


B0L102S, U5-ch31, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

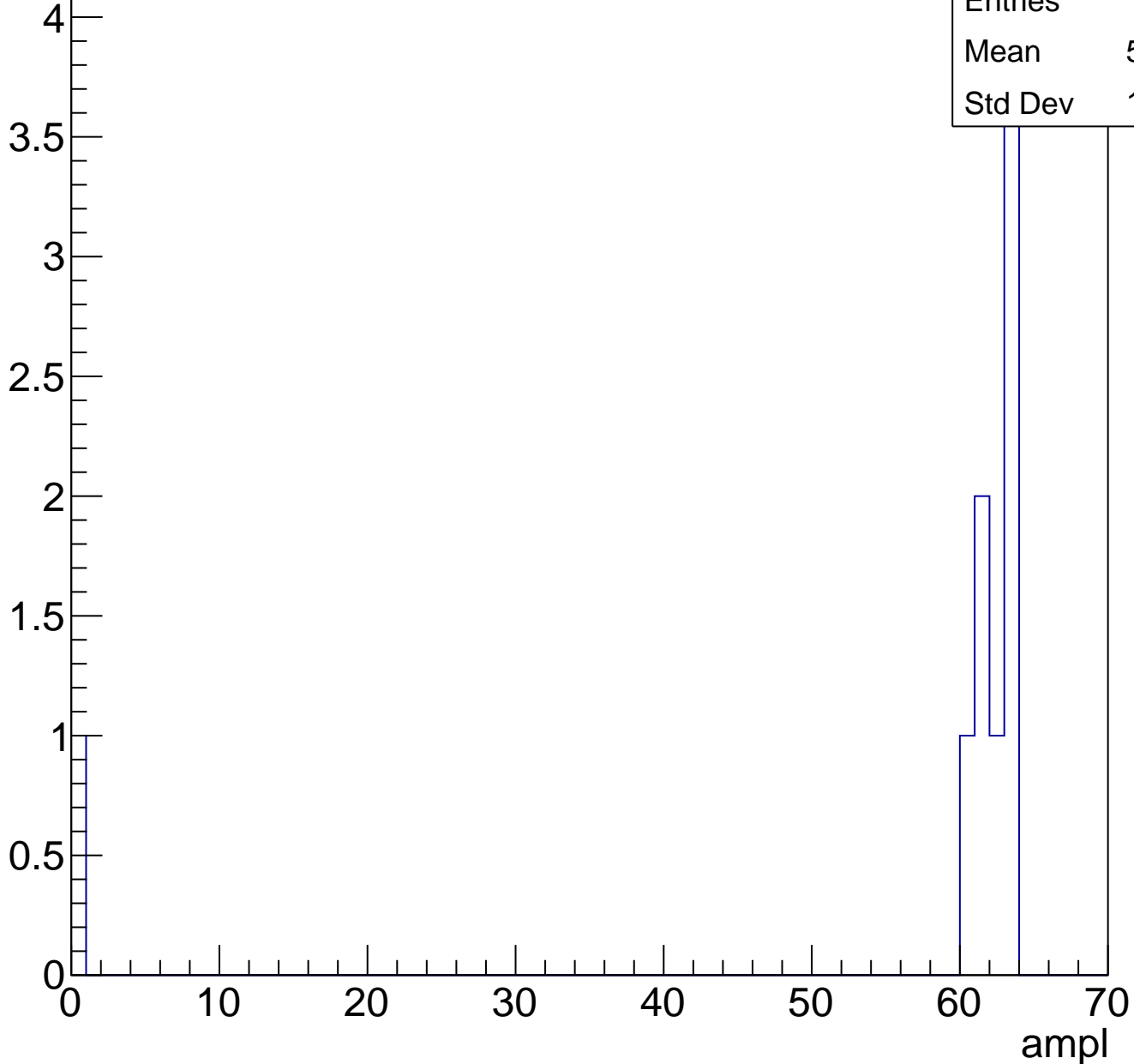
Entries	53
Mean	59.06
Std Dev	2.777



B0L102S, U5-ch31, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch31, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B0L102S, U5-ch32, adc0

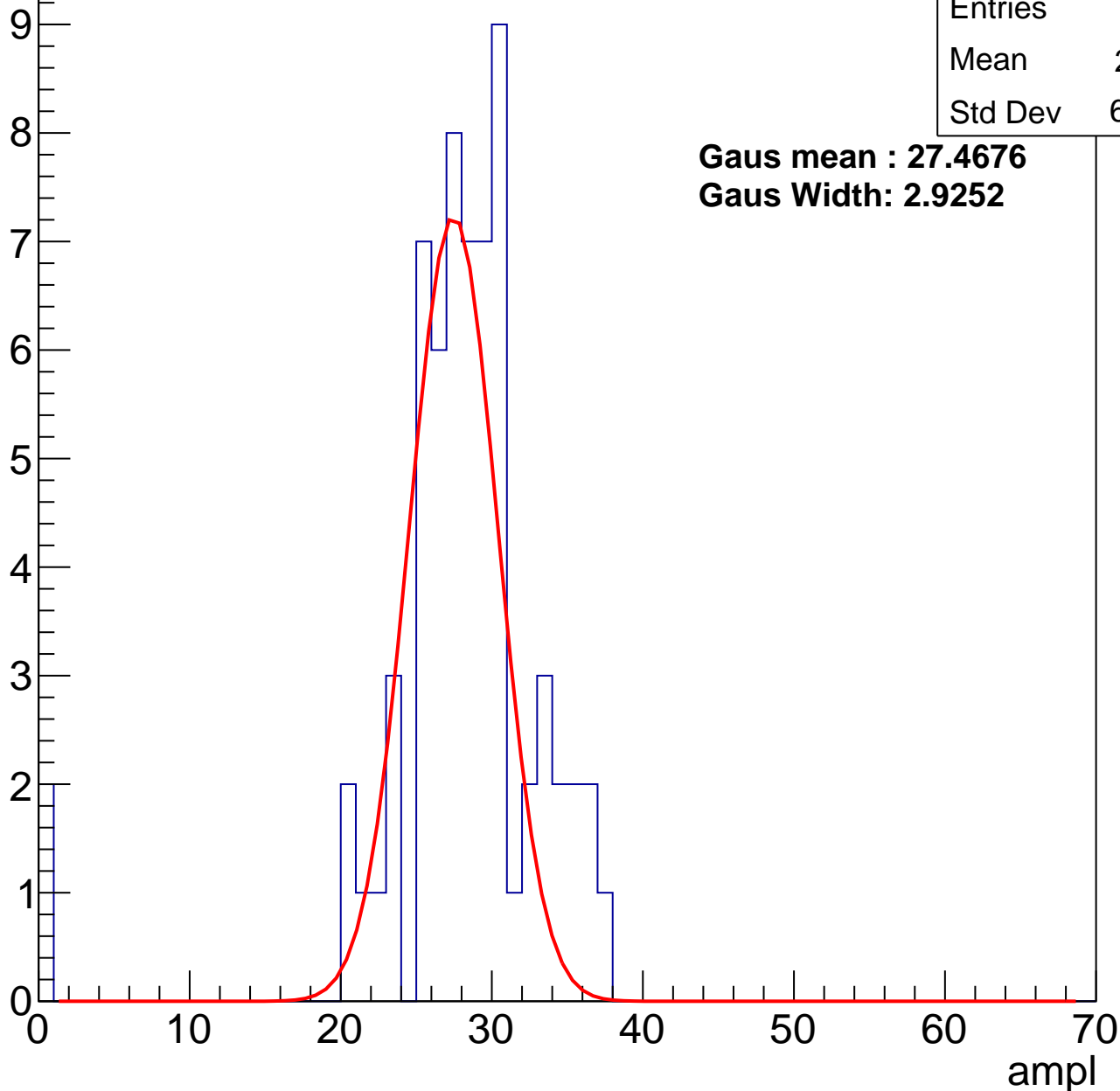
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.41
Std Dev	6.113

Gaus mean : 27.4676

Gaus Width: 2.9252



B0L102S, U5-ch32, adc1

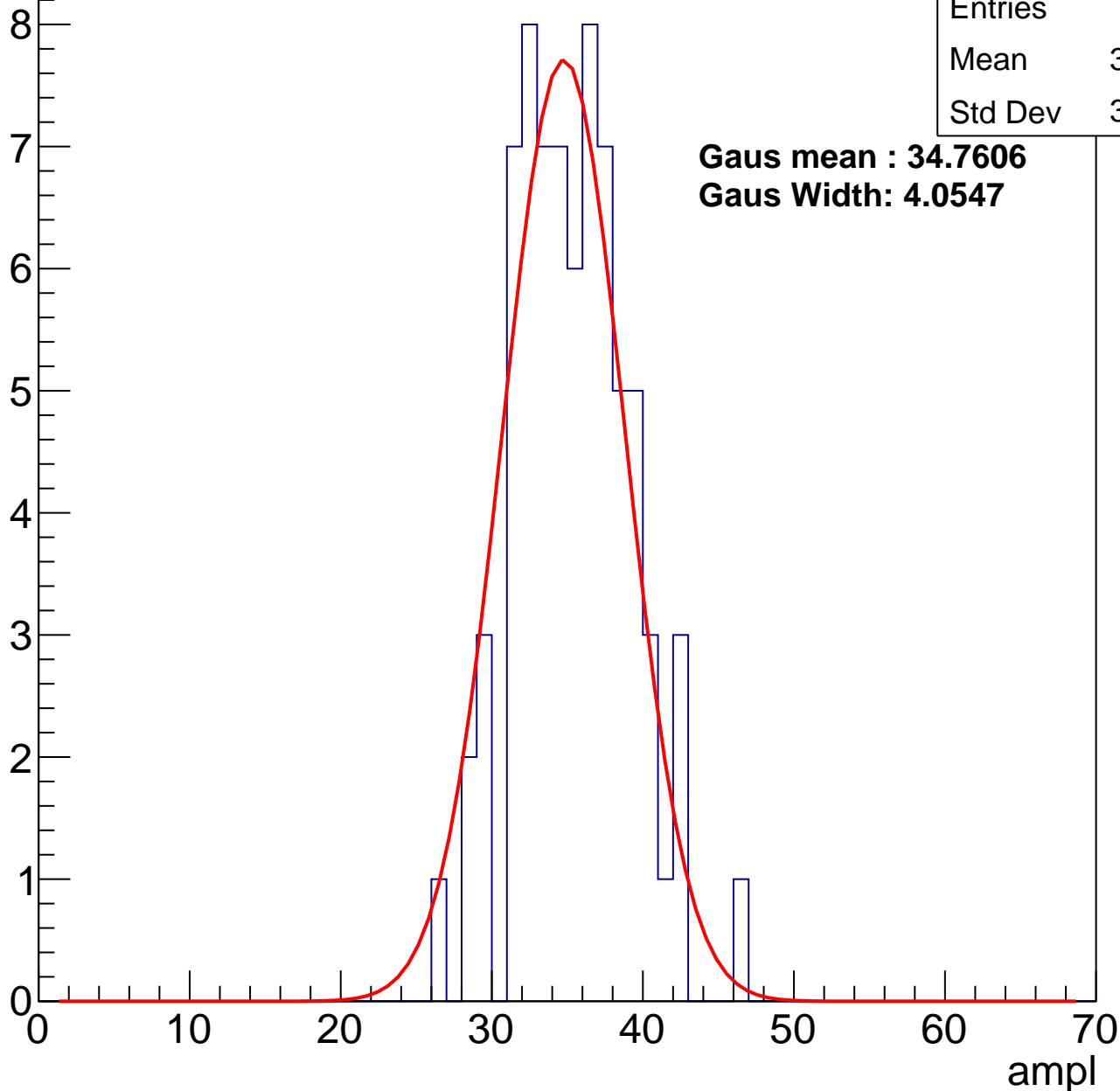
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	34.95
Std Dev	3.759

Gaus mean : 34.7606

Gaus Width: 4.0547



B0L102S, U5-ch32, adc2

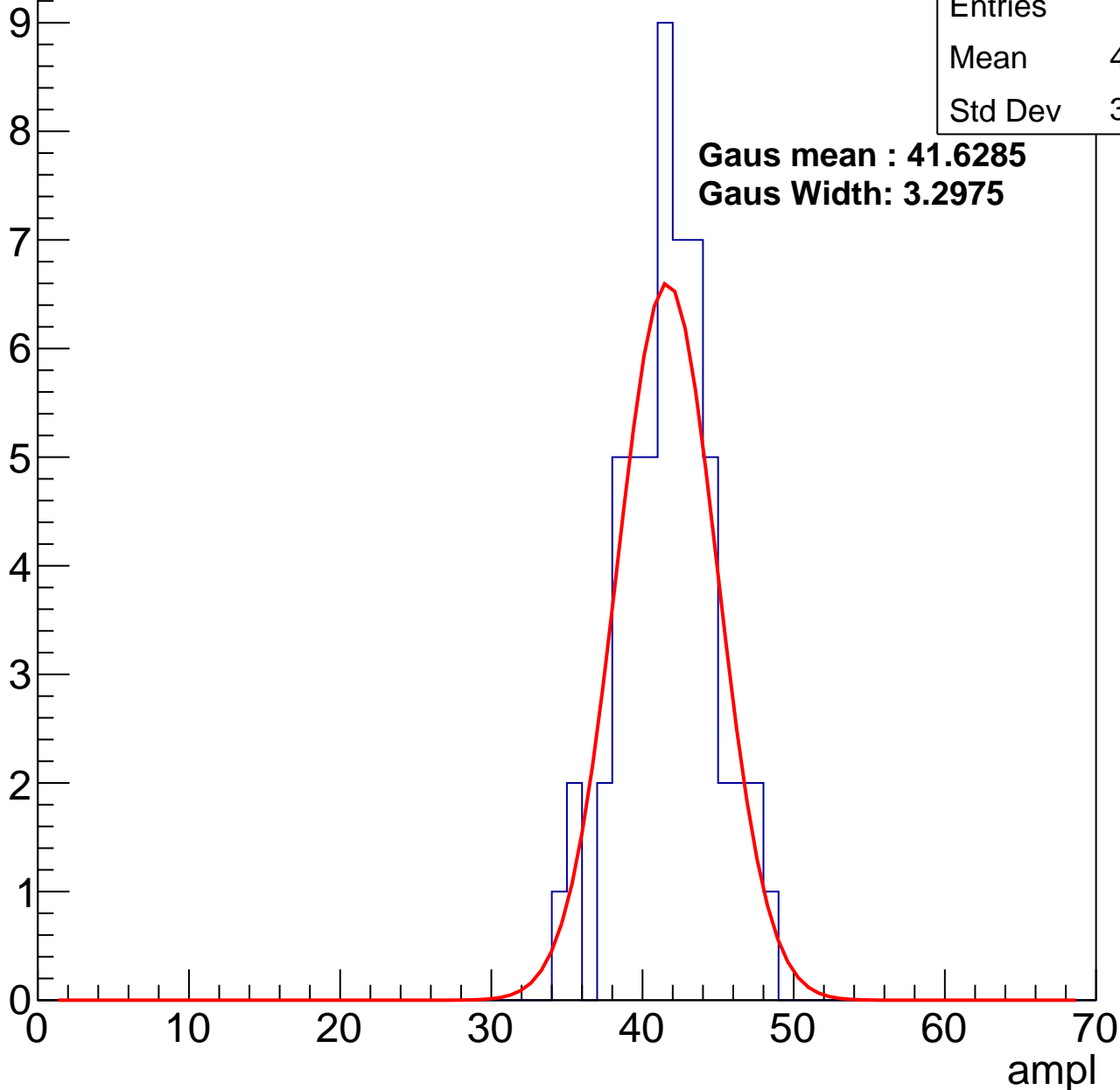
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	41.29
Std Dev	3.019

Gaus mean : 41.6285

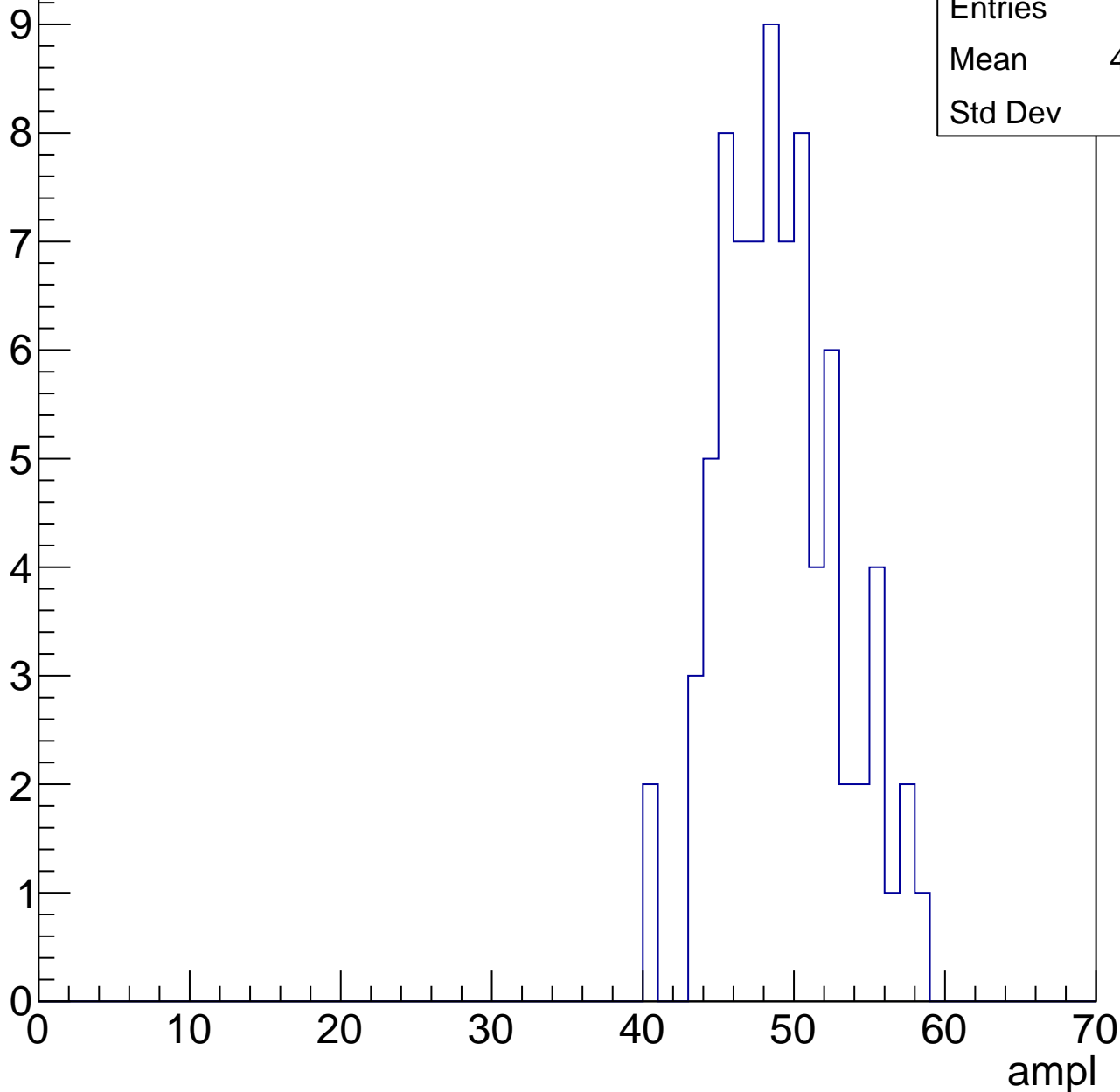
Gaus Width: 3.2975



B0L102S, U5-ch32, adc3

calib_packv5_042523_0143.root, FC#12, port B1

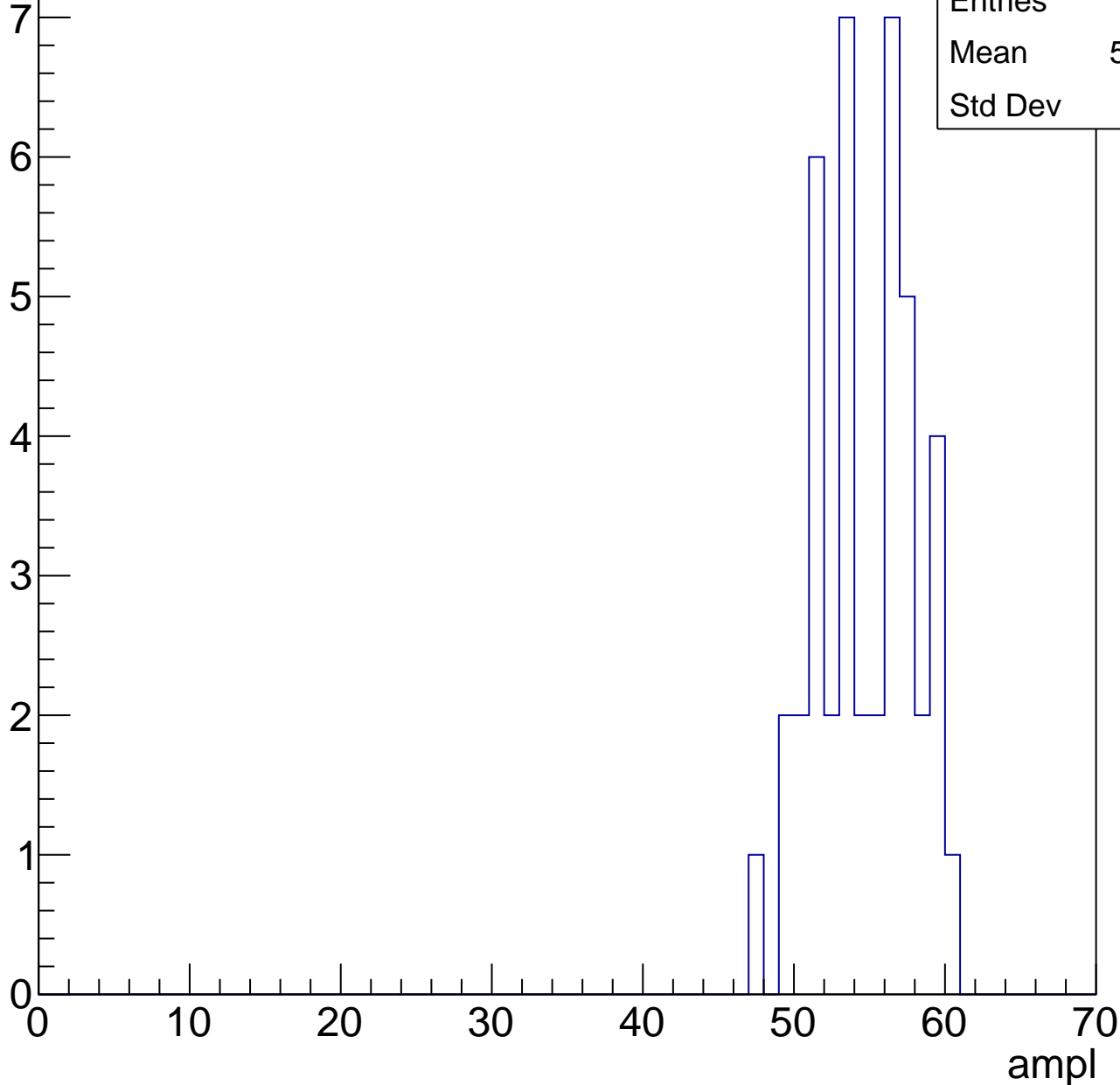
Entry



B0L102S, U5-ch32, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

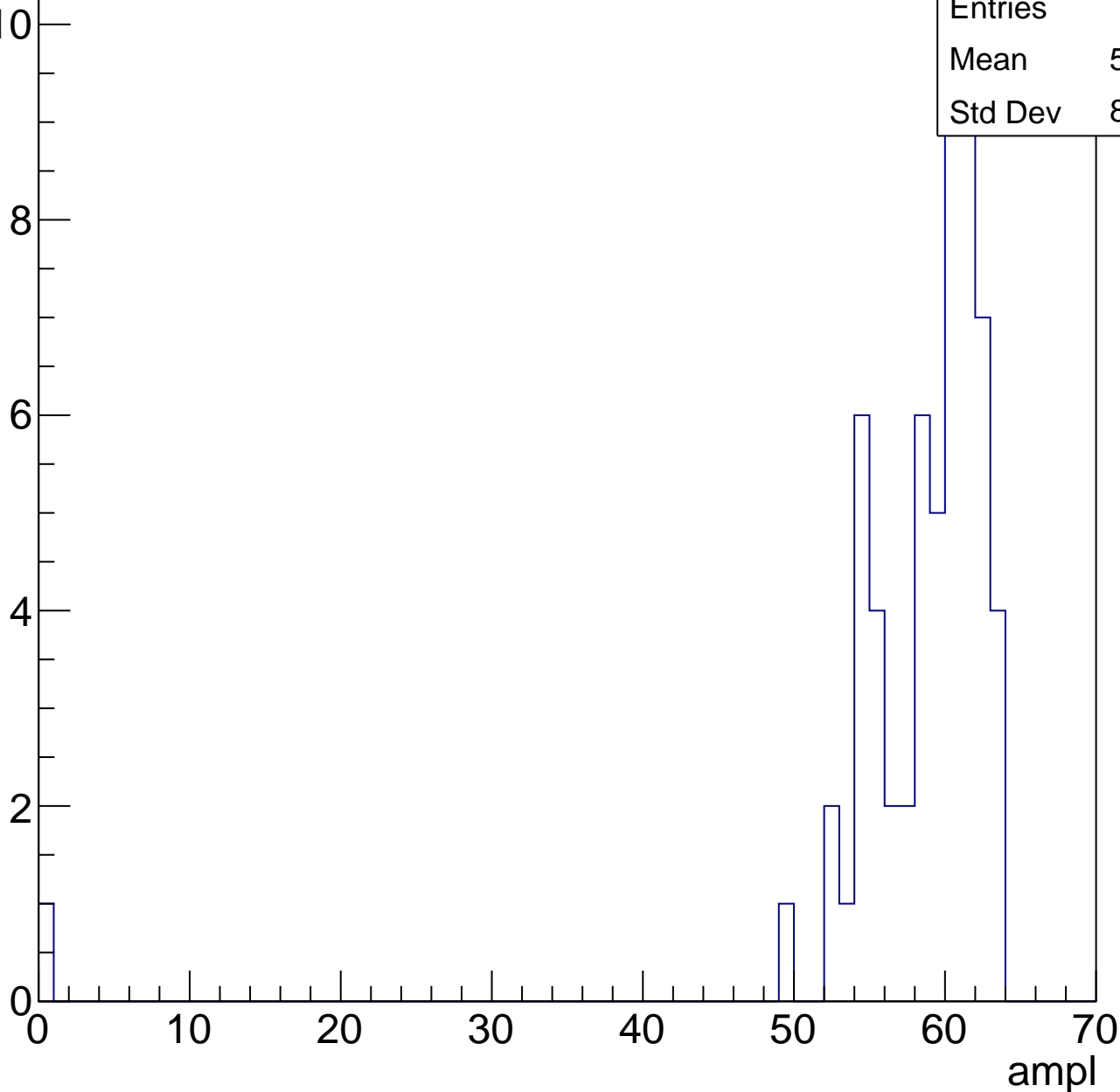


B0L102S, U5-ch32, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

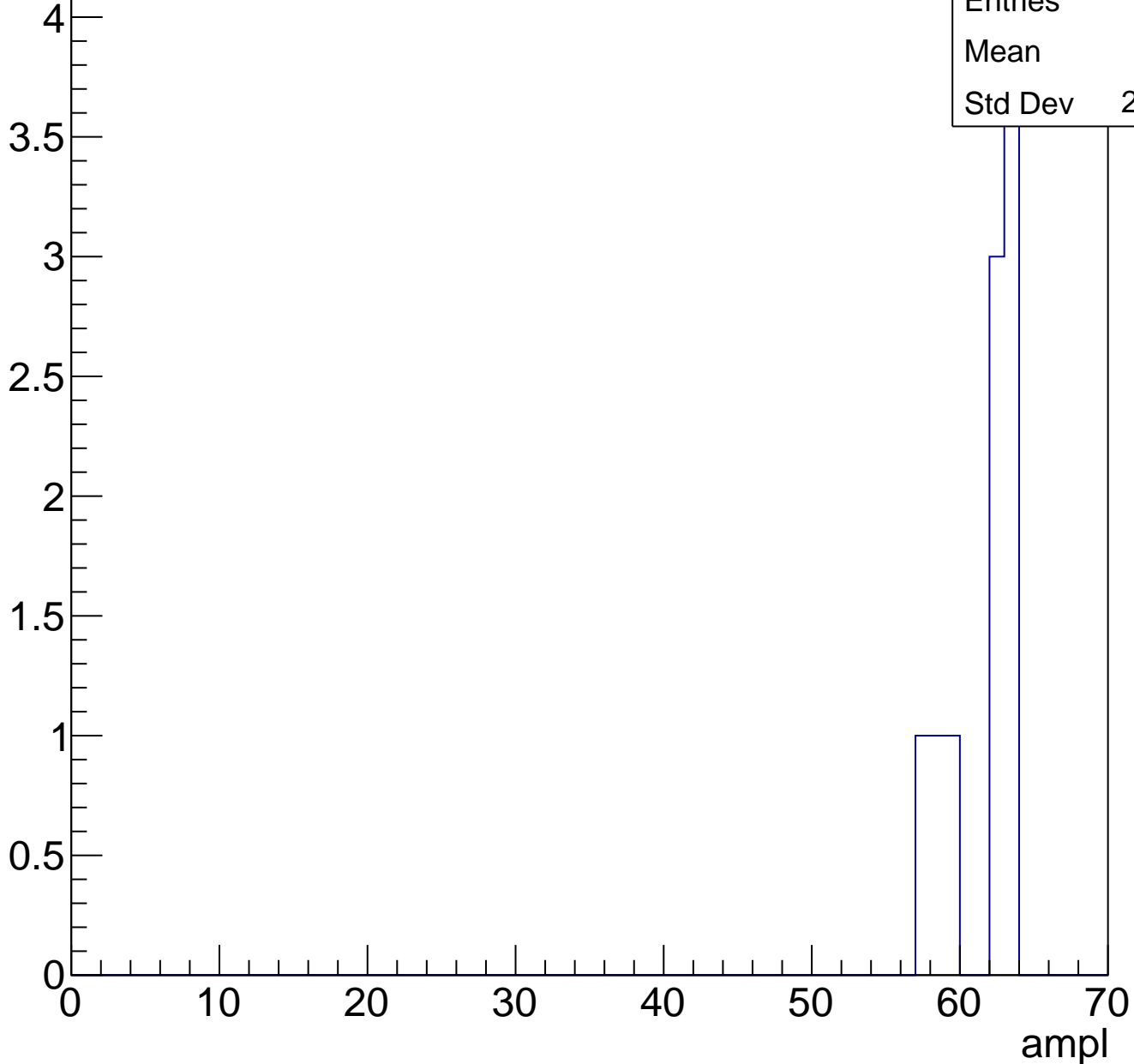
Entries	60
Mean	57.58
Std Dev	8.182



B0L102S, U5-ch32, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch32, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch33, adc0

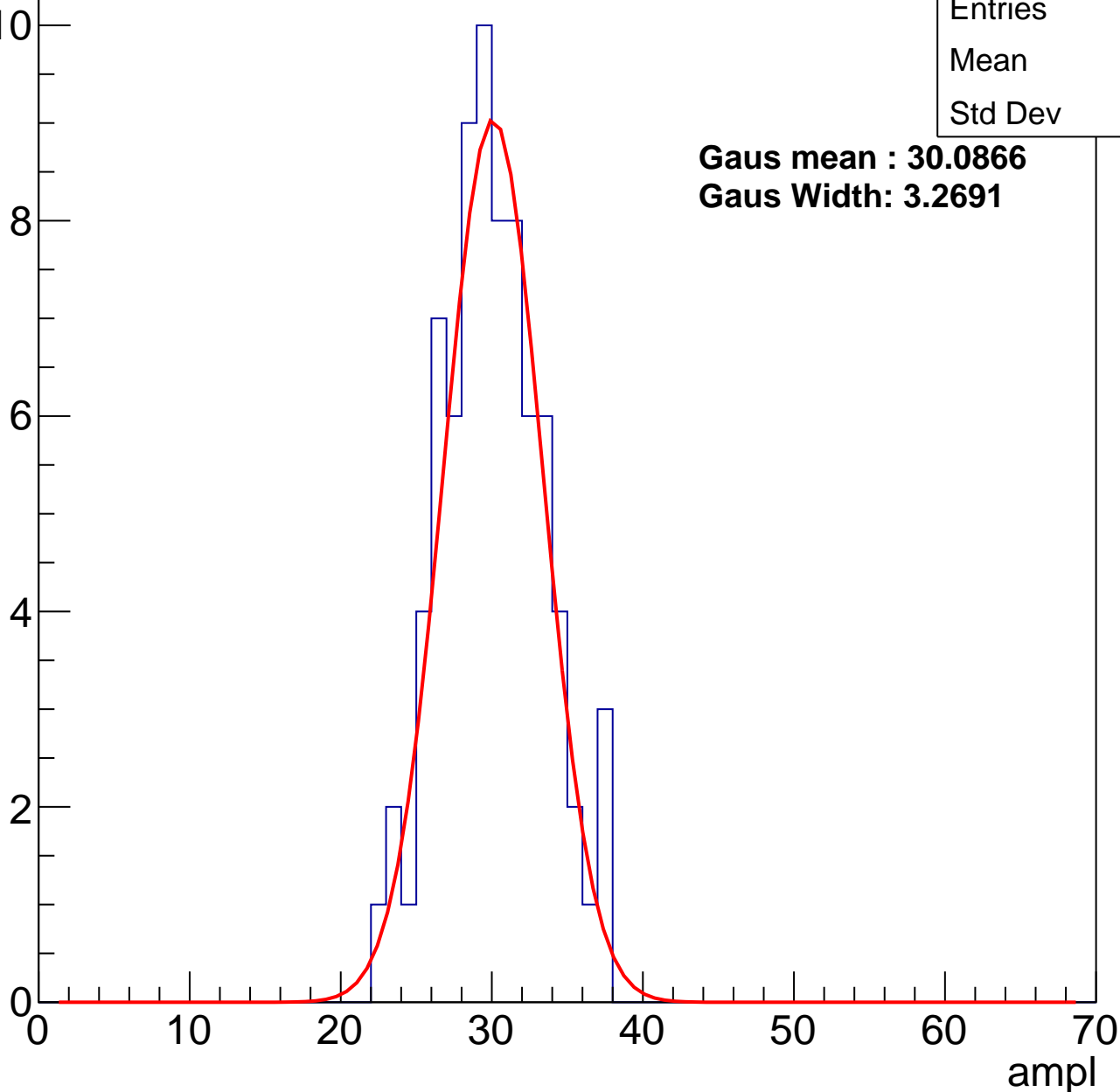
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	29.6
Std Dev	3.36

Gaus mean : 30.0866

Gaus Width: 3.2691



B0L102S, U5-ch33, adc1

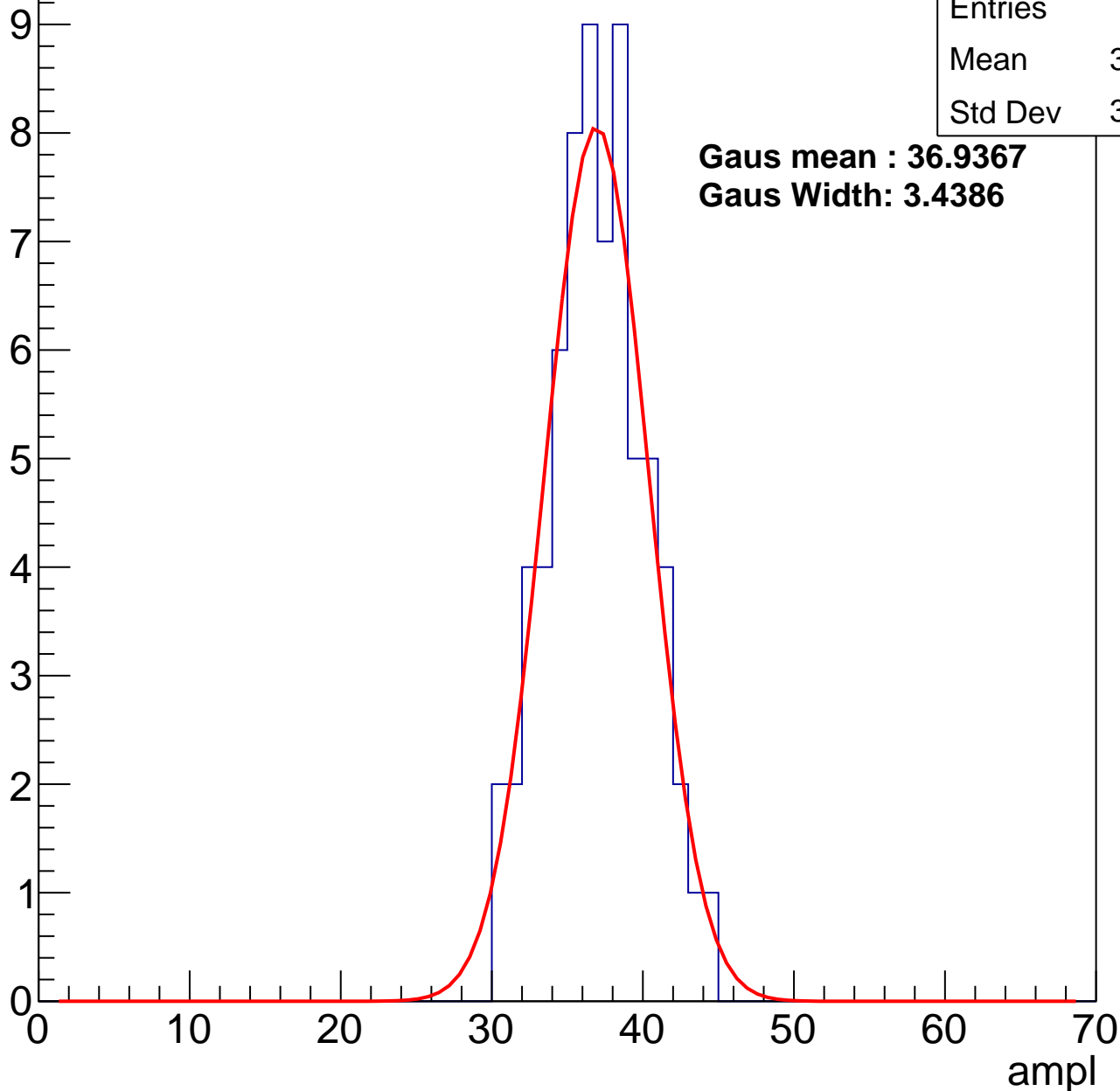
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	36.54
Std Dev	3.156

Gaus mean : 36.9367

Gaus Width: 3.4386



B0L102S, U5-ch33, adc2

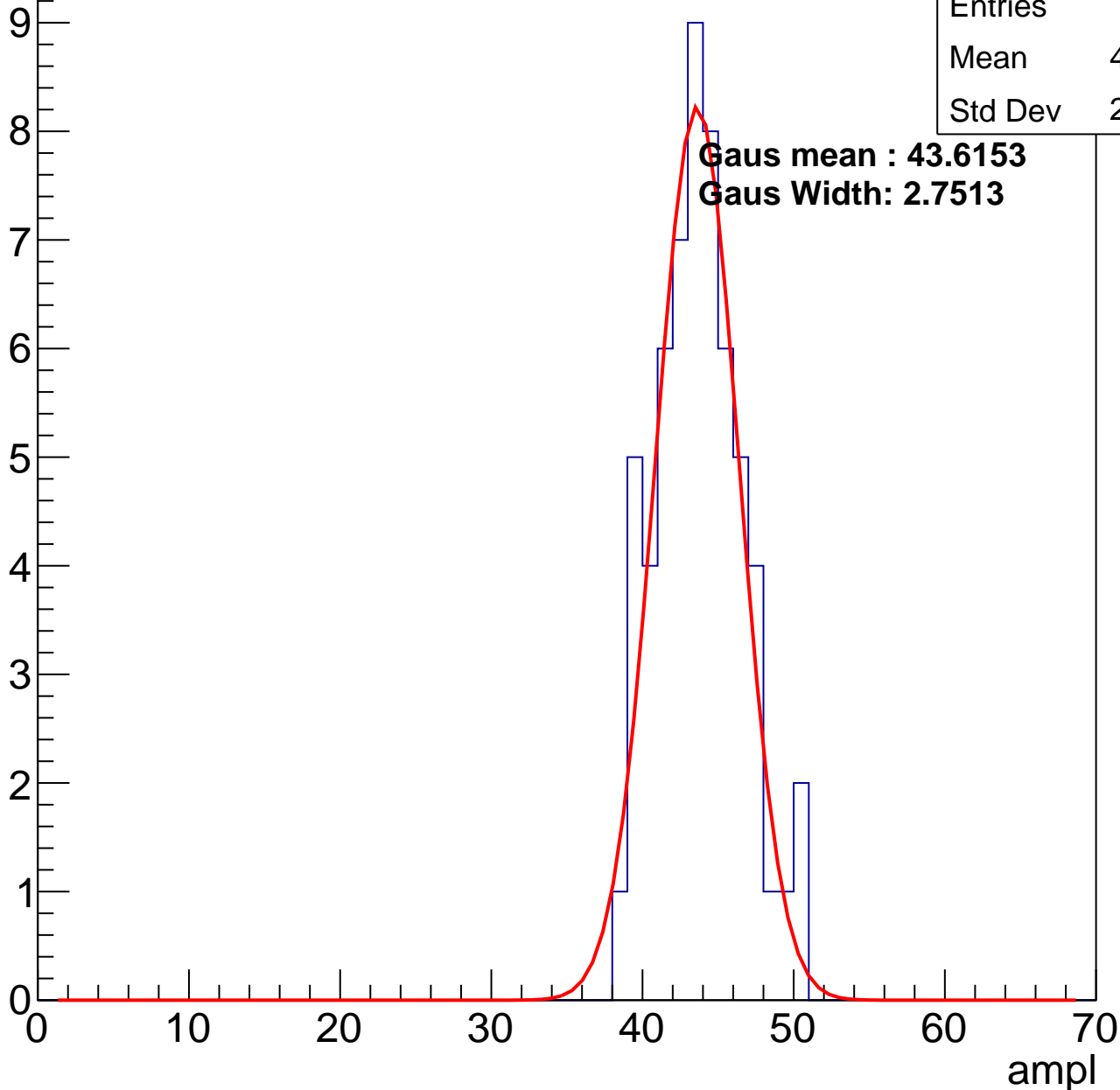
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	43.34
Std Dev	2.808

Gaus mean : 43.6153

Gaus Width: 2.7513



B0L102S, U5-ch33, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	49.44
Std Dev	3.201

Entry

10

8

6

4

2

0

0

10

20

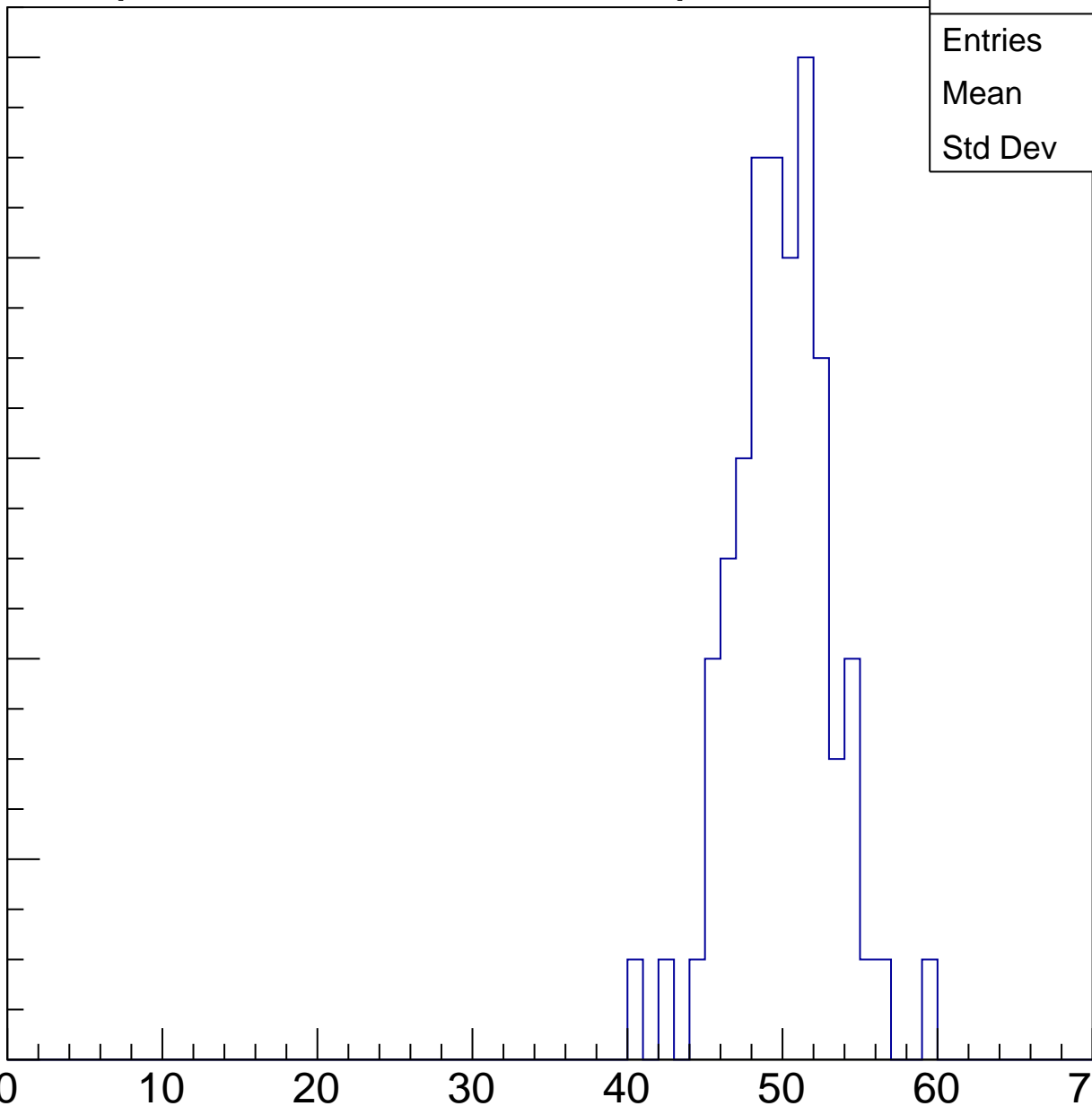
30

40

50

60

ampl

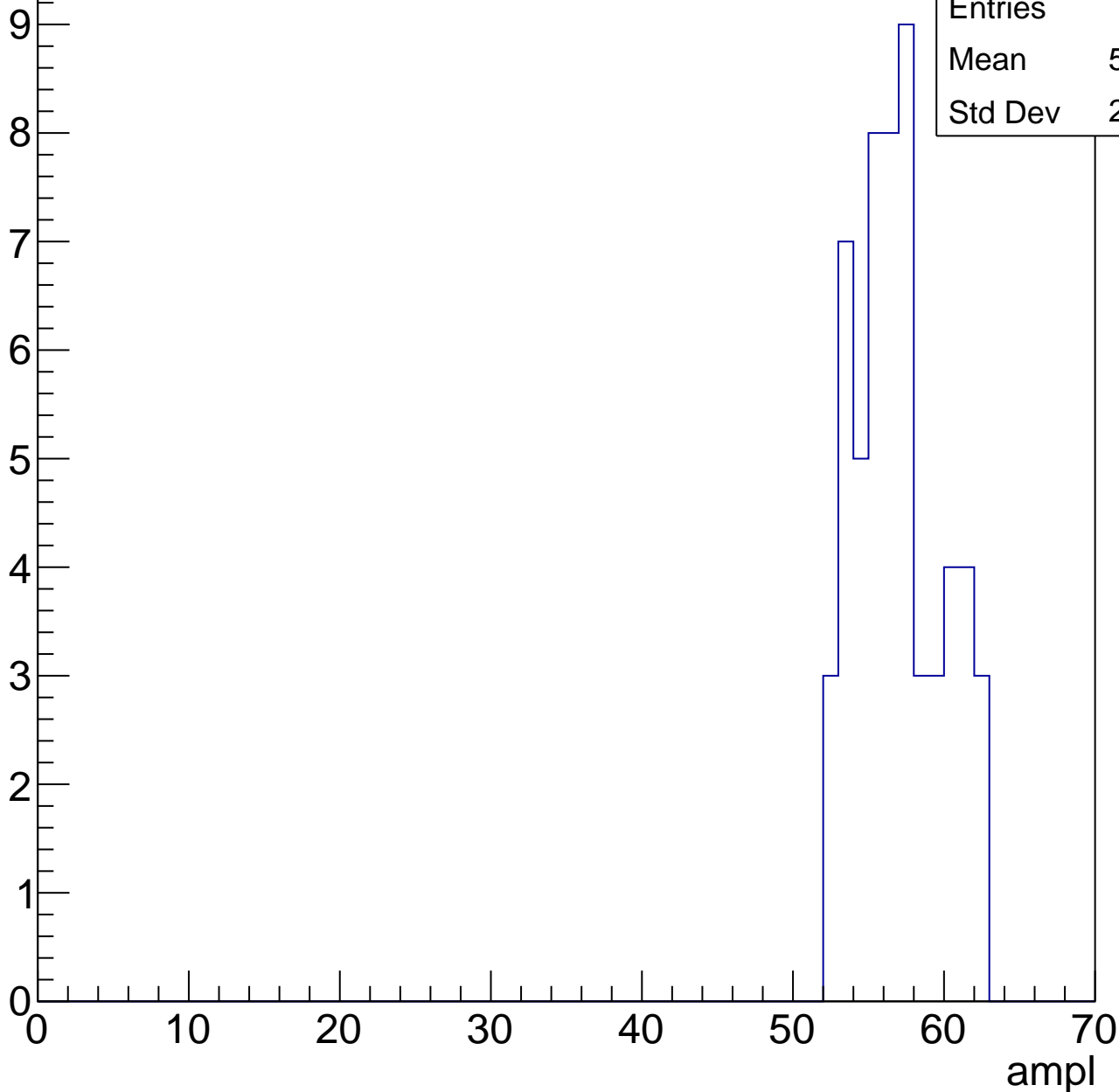


B0L102S, U5-ch33, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	56.47
Std Dev	2.798



B0L102S, U5-ch33, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	35
---------	----

Mean	59.66
------	-------

Std Dev	2.242
---------	-------

ampl

0

10

20

30

40

50

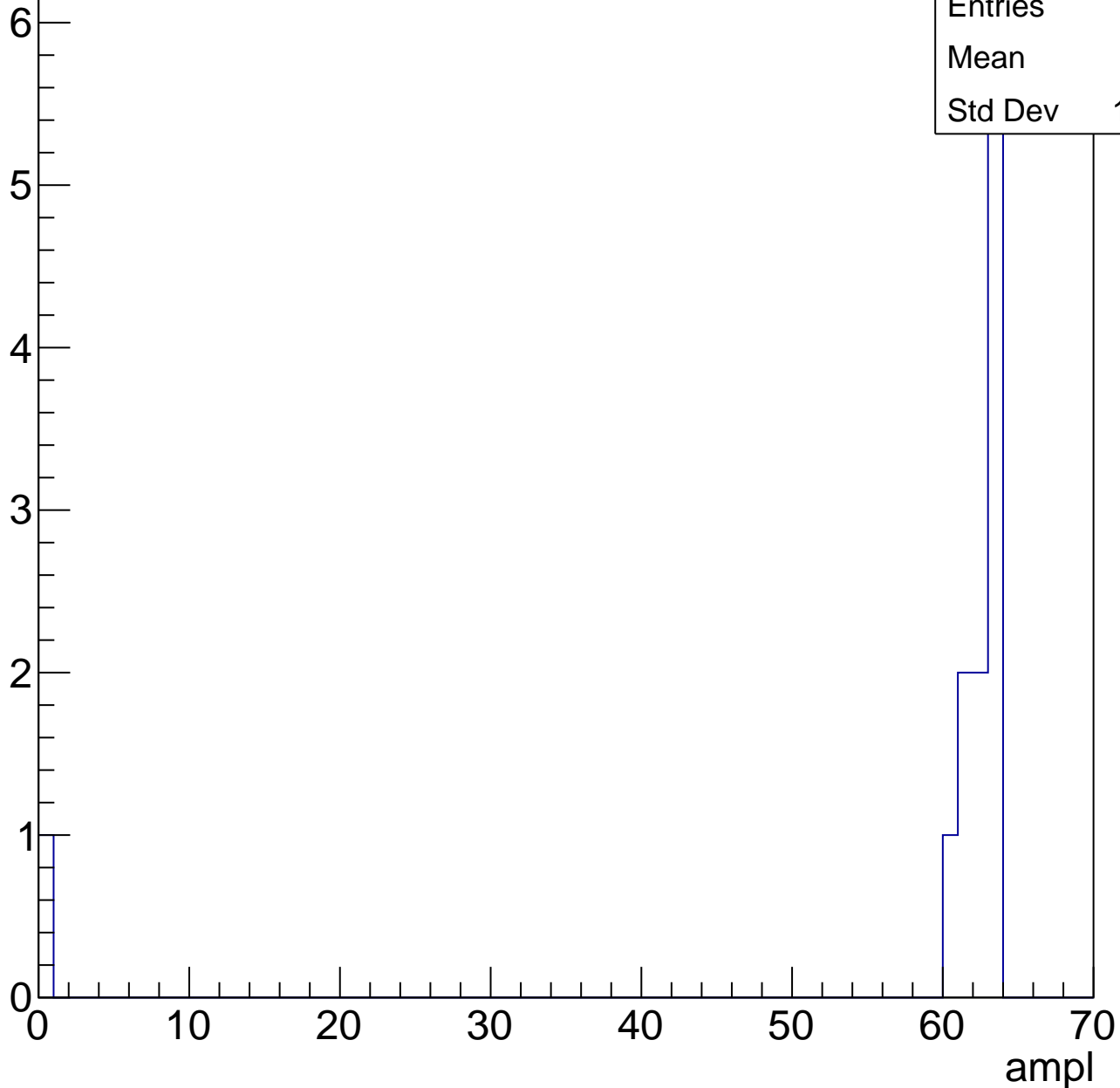
60

70

B0L102S, U5-ch33, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch33, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch34, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	79
Mean	28.35
Std Dev	3.92

Gaus mean : 29.5072

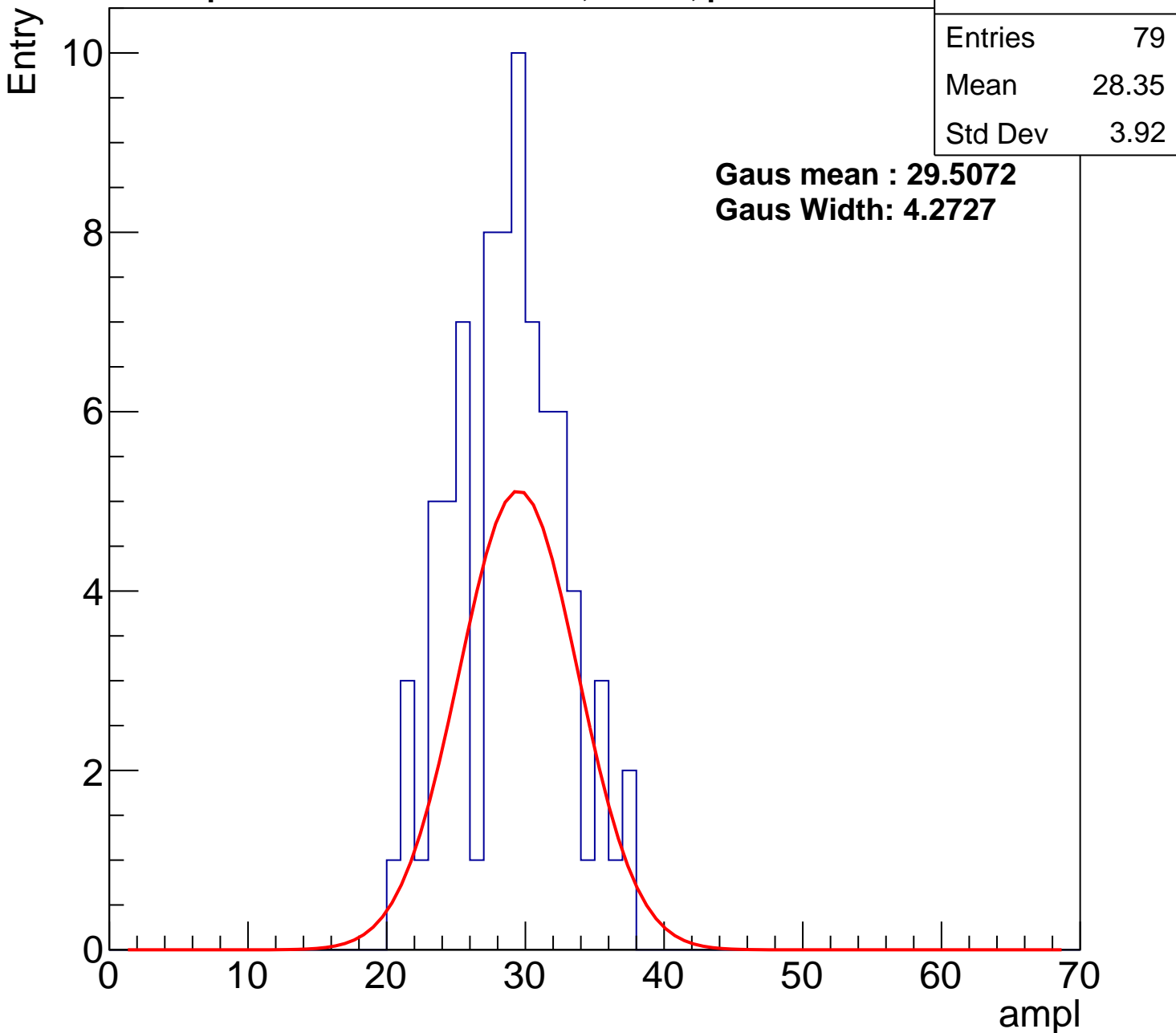
Gaus Width: 4.2727

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch34, adc1

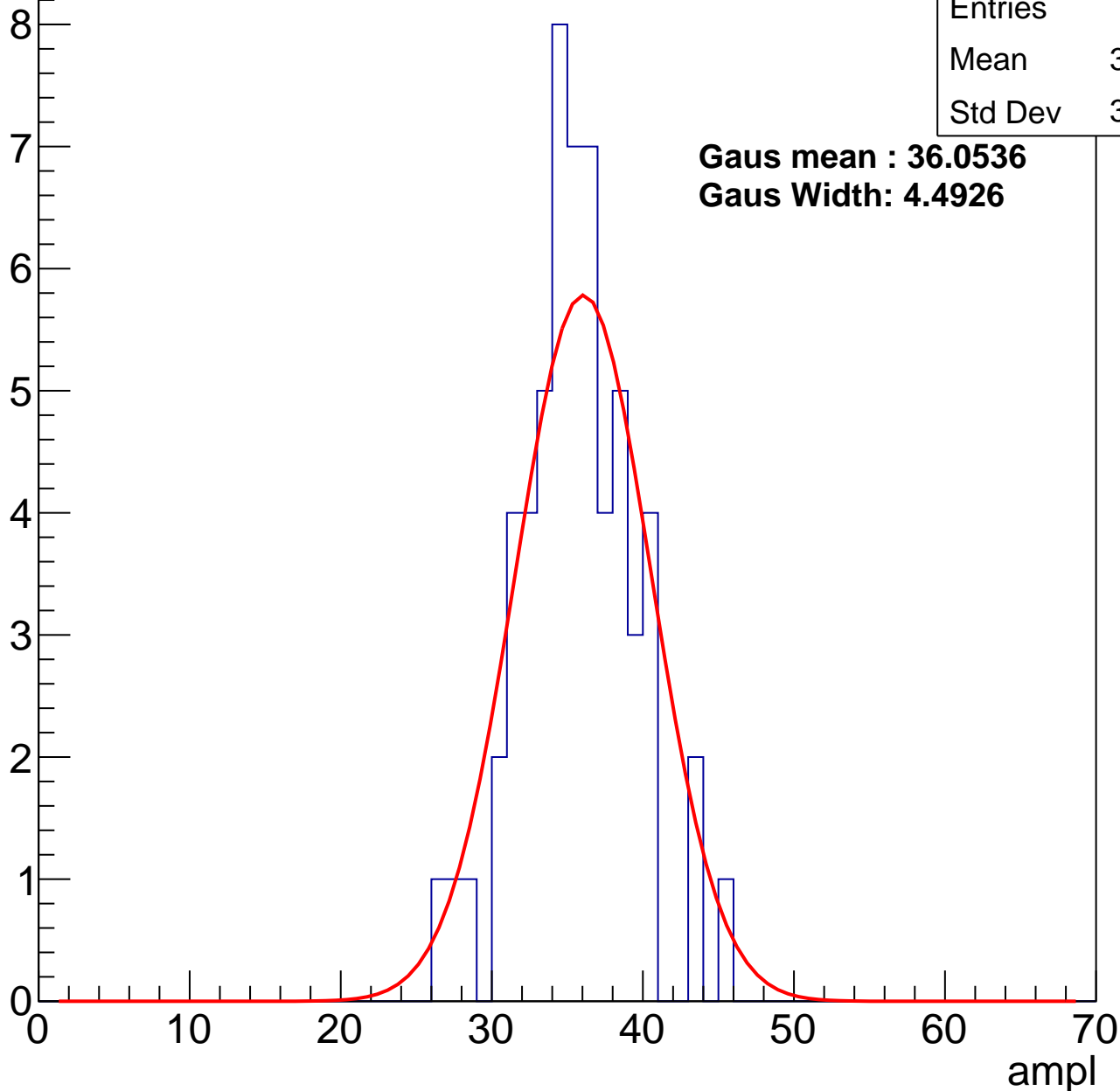
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	35.14
Std Dev	3.716

Gaus mean : 36.0536

Gaus Width: 4.4926



B0L102S, U5-ch34, adc2

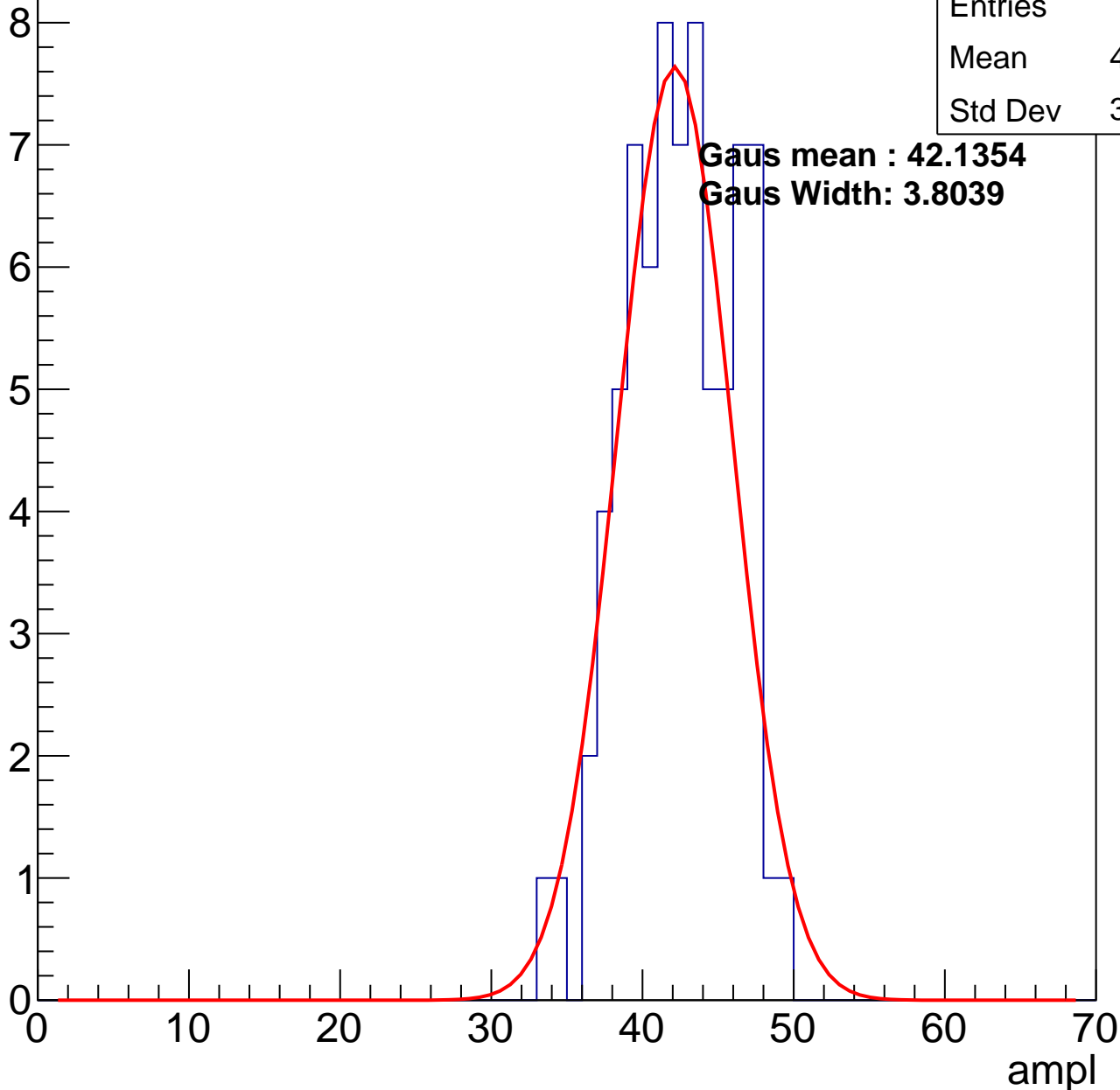
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	41.99
Std Dev	3.535

Gaus mean : 42.1354

Gaus Width: 3.8039

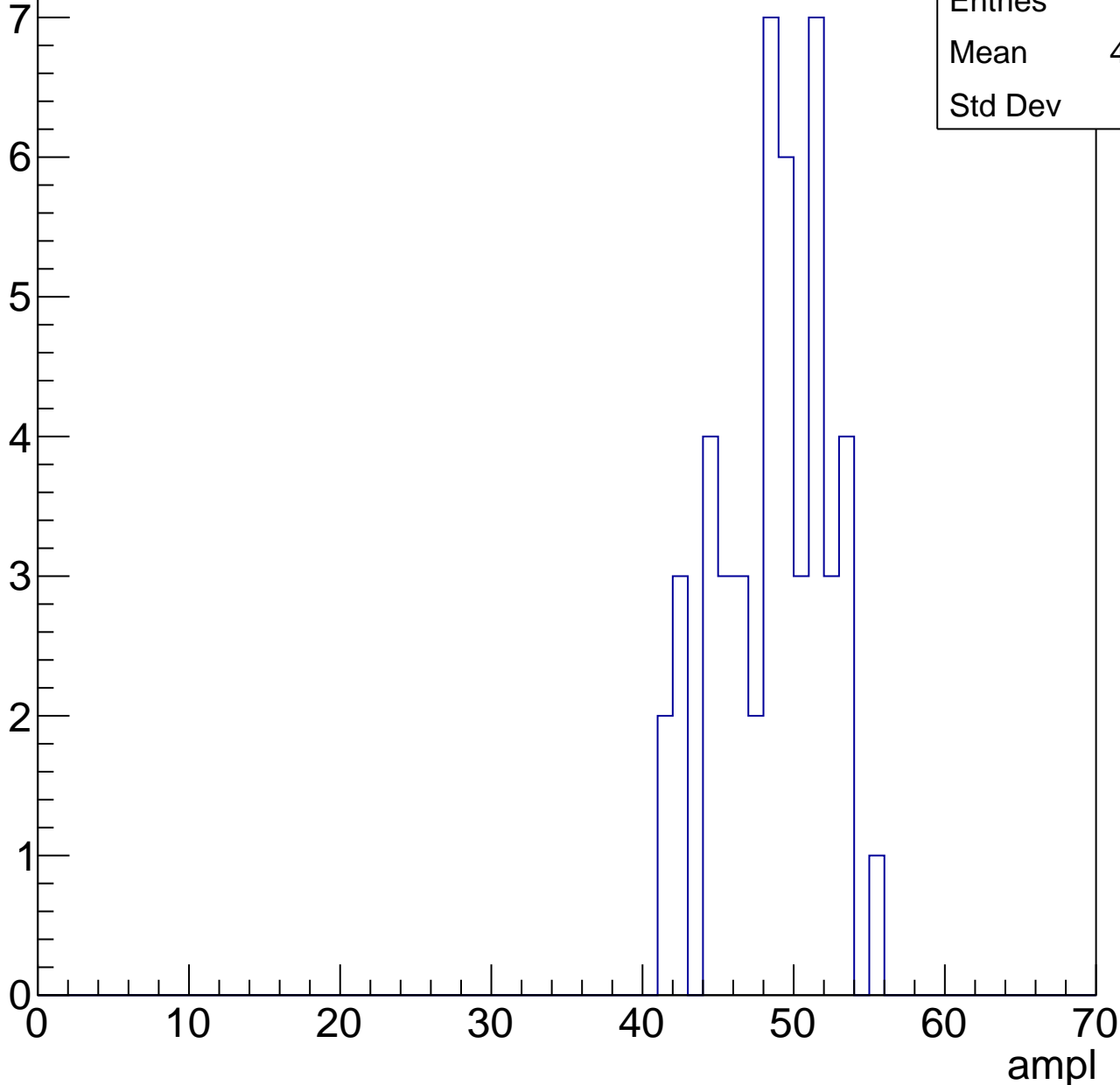


B0L102S, U5-ch34, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	48.15
Std Dev	3.5

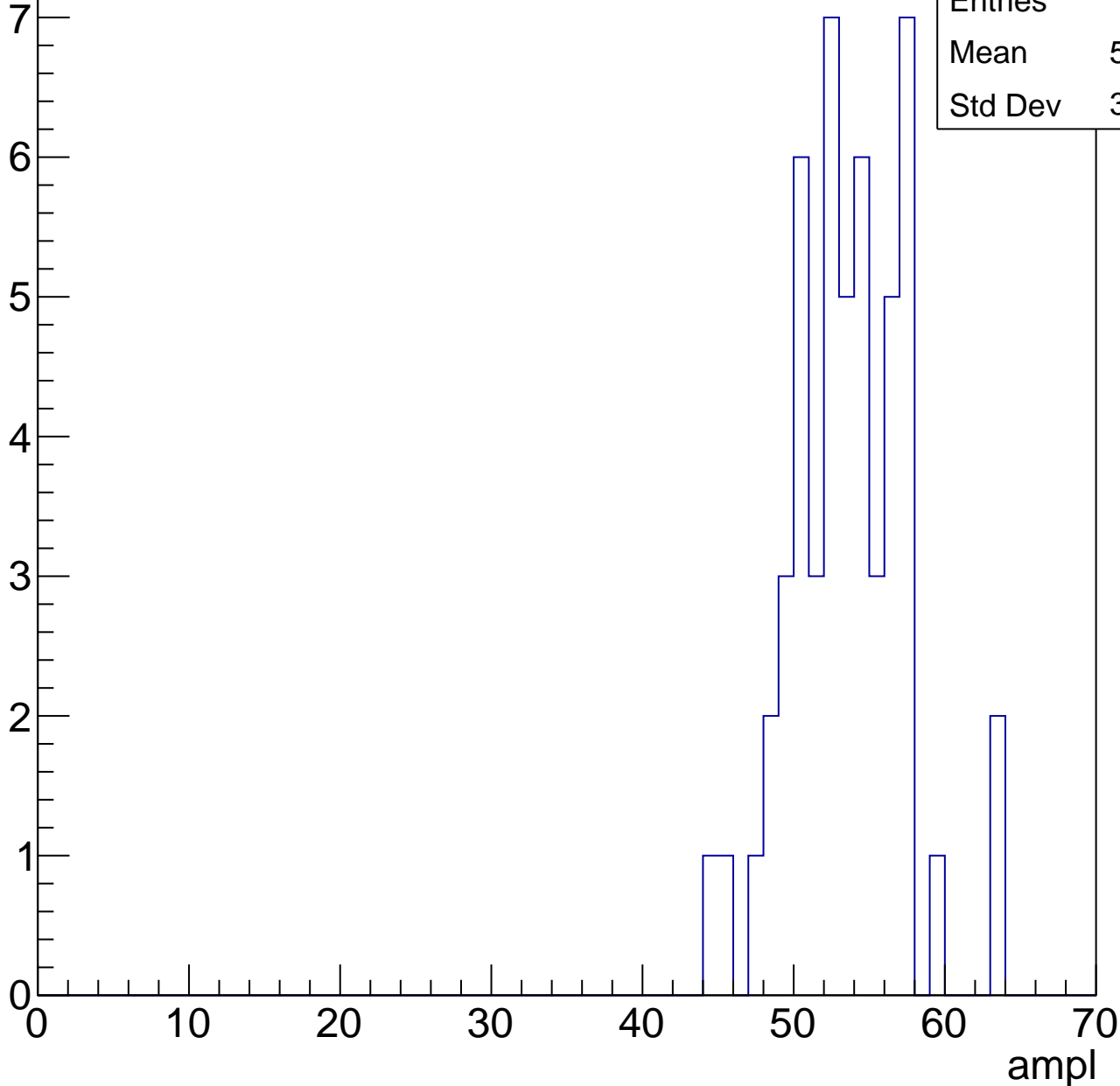


B0L102S, U5-ch34, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	53.09
Std Dev	3.793

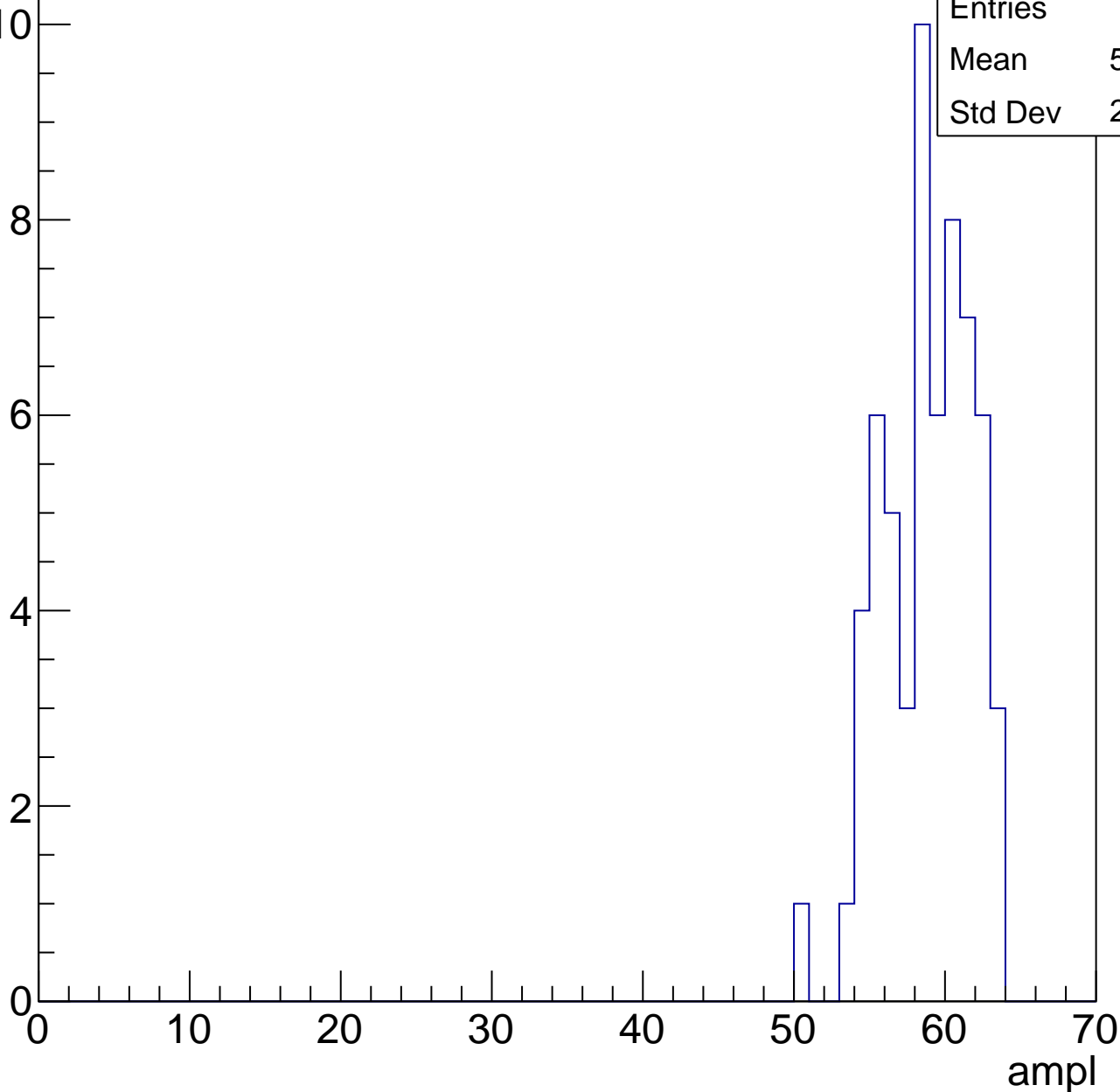


B0L102S, U5-ch34, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	58.37
Std Dev	2.869

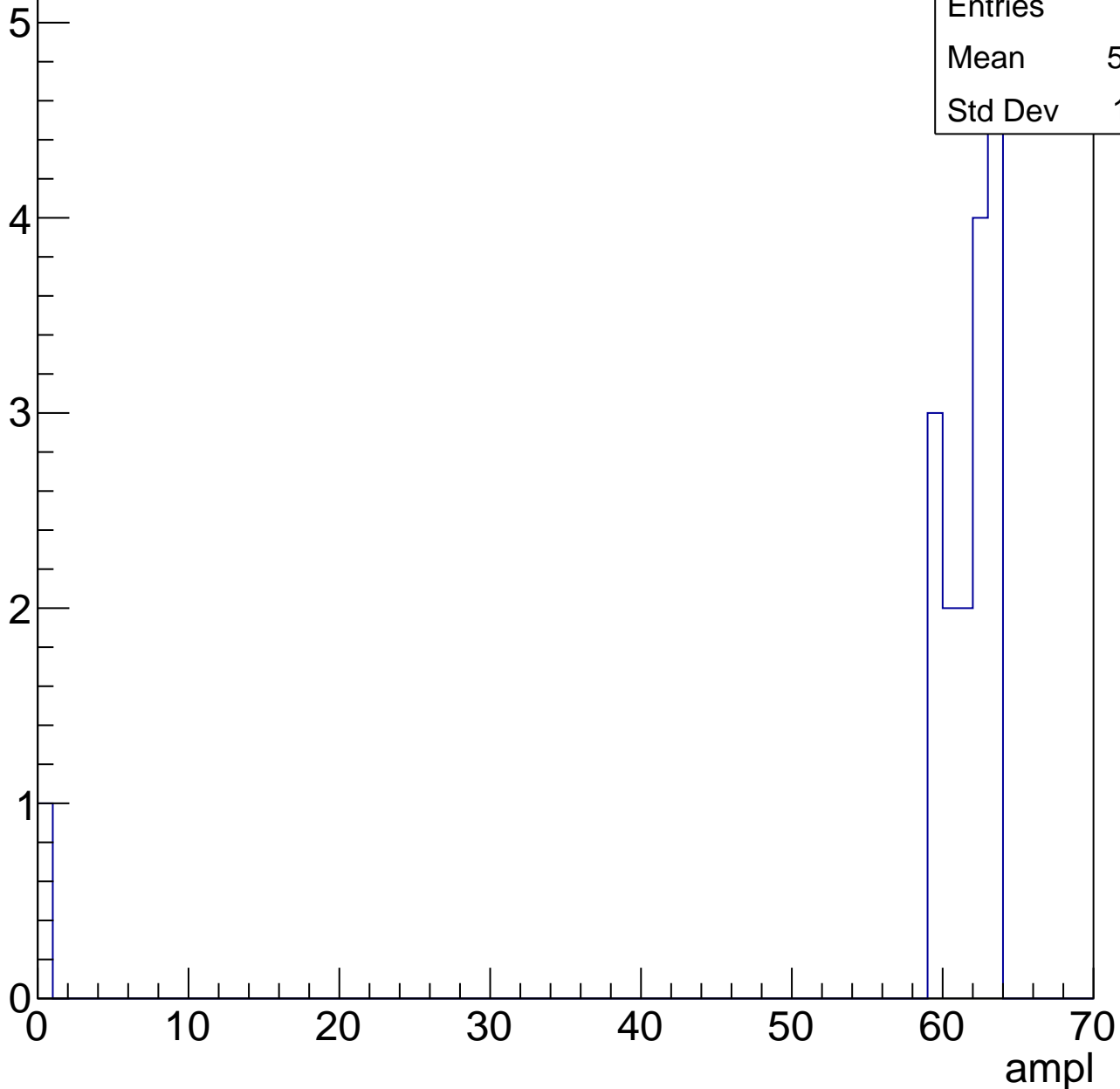


B0L102S, U5-ch34, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	17
Mean	57.76
Std Dev	14.51



B0L102S, U5-ch34, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch35, adc0

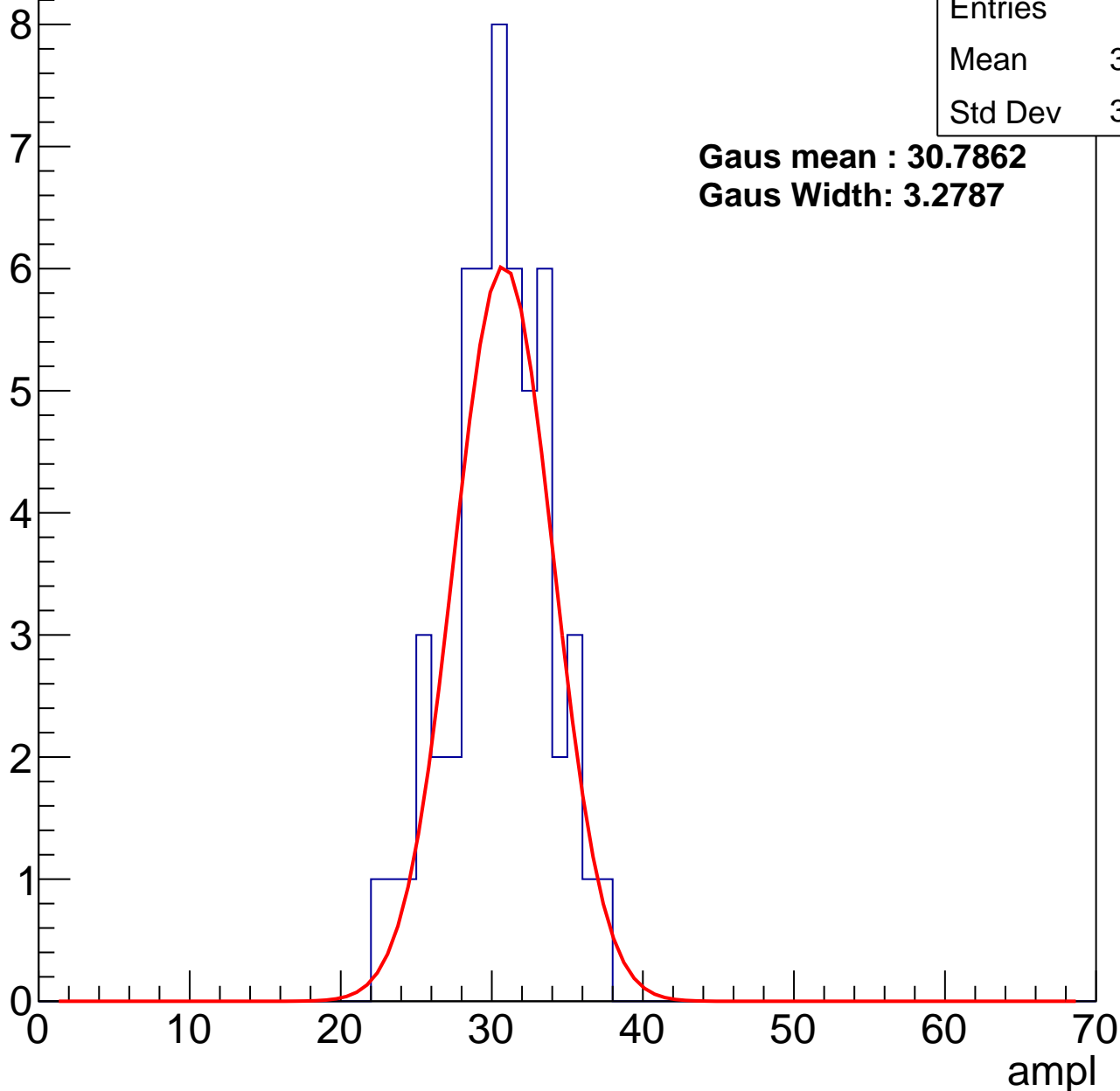
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	30.04
Std Dev	3.266

Gaus mean : 30.7862

Gaus Width: 3.2787



B0L102S, U5-ch35, adc1

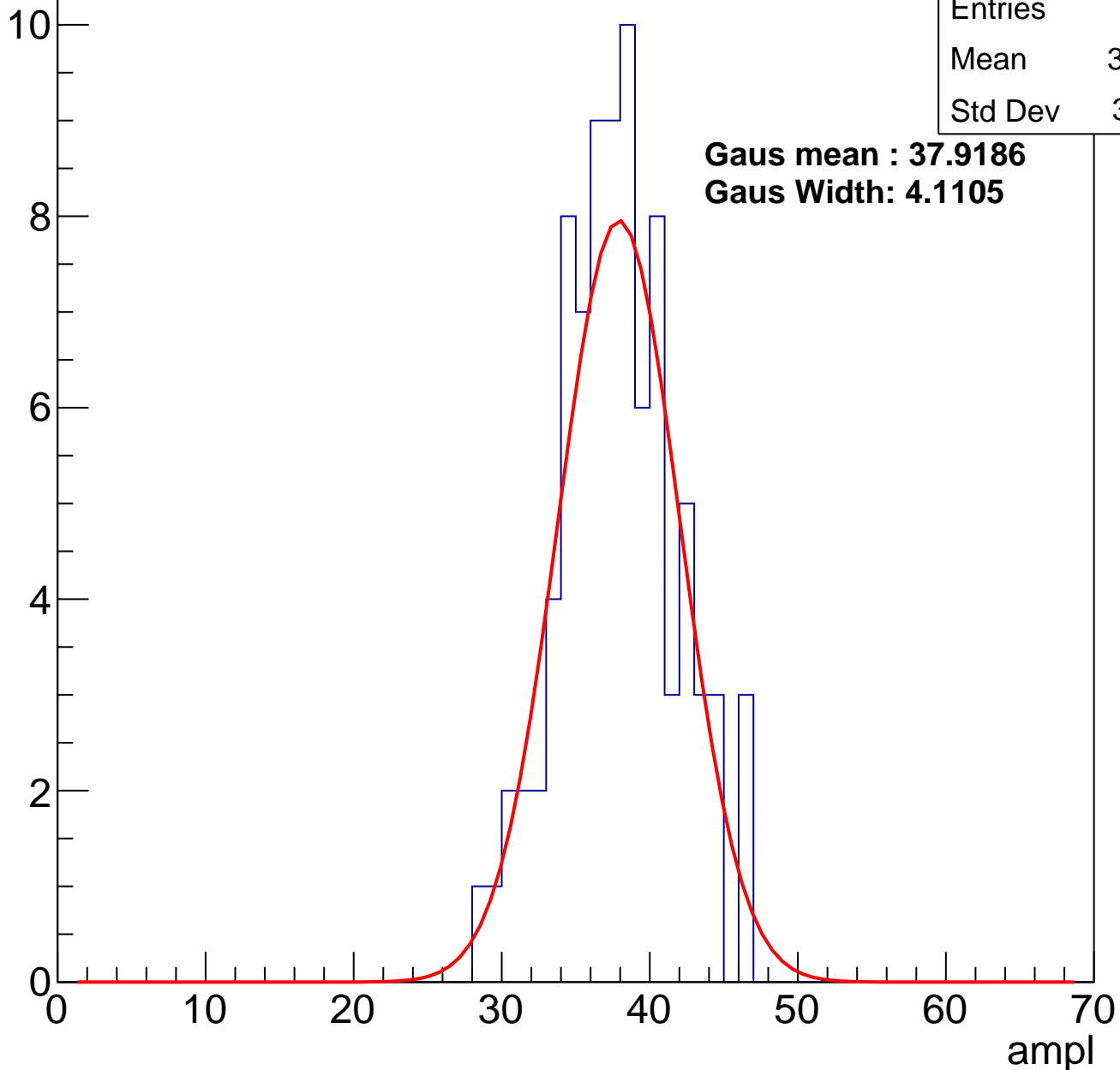
calib_packv5_042523_0143.root, FC#12, port B1

Entries	86
Mean	37.38
Std Dev	3.891

Gaus mean : 37.9186

Gaus Width: 4.1105

Entry



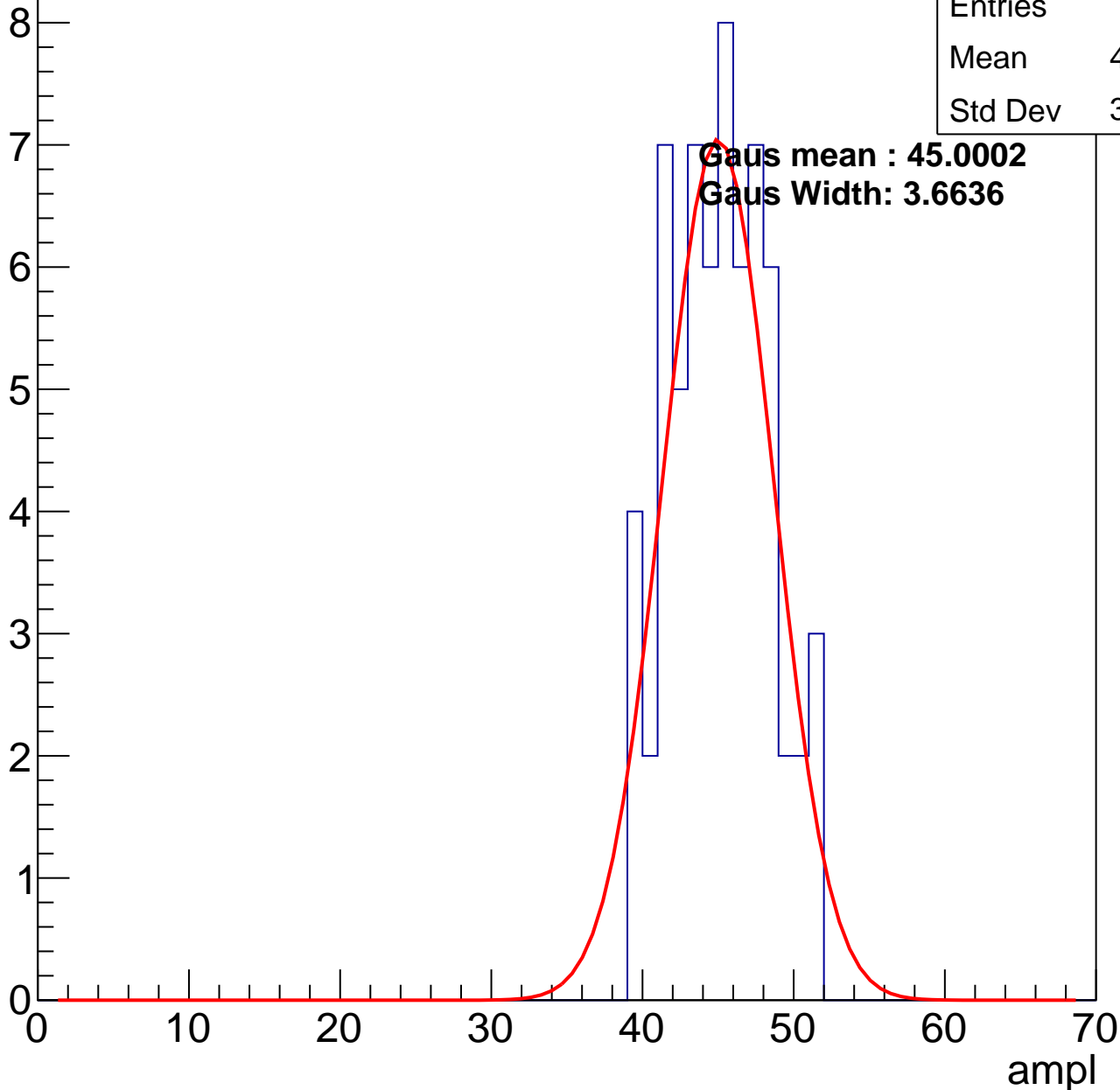
B0L102S, U5-ch35, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	44.65
Std Dev	3.174

Gaus mean : 45.0002
Gaus Width: 3.6636

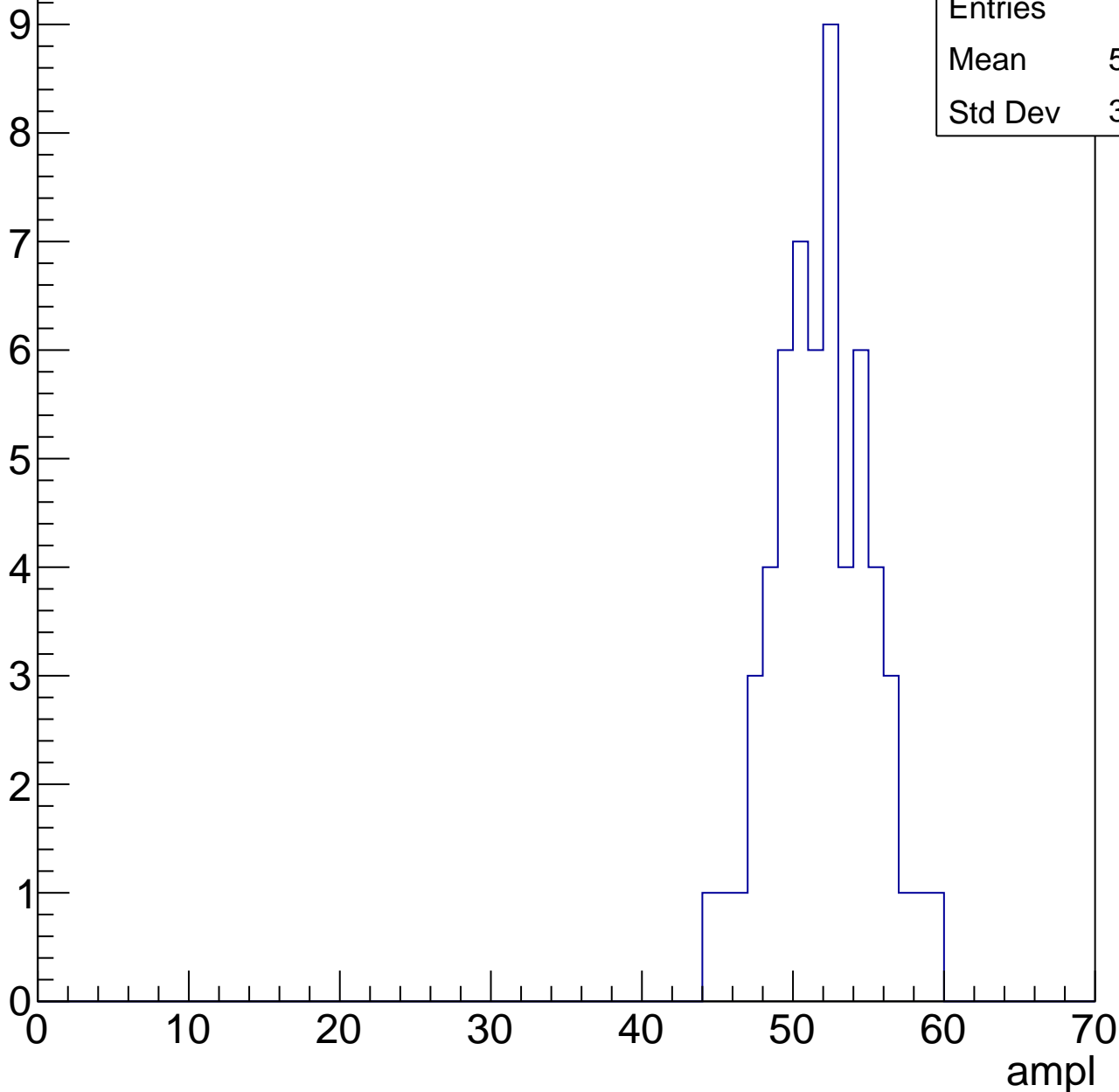


B0L102S, U5-ch35, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	51.45
Std Dev	3.163

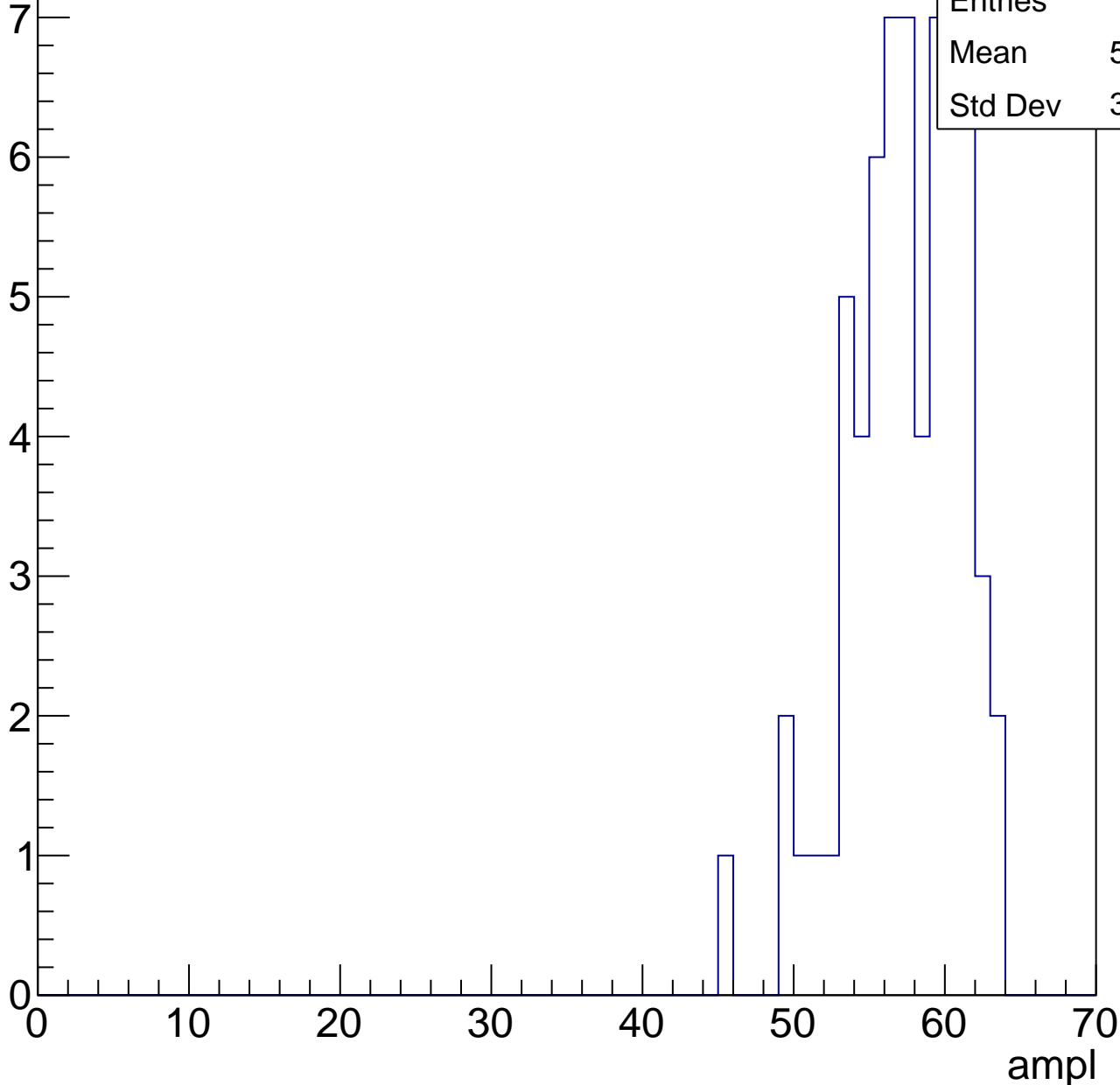


B0L102S, U5-ch35, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	56.95
Std Dev	3.685

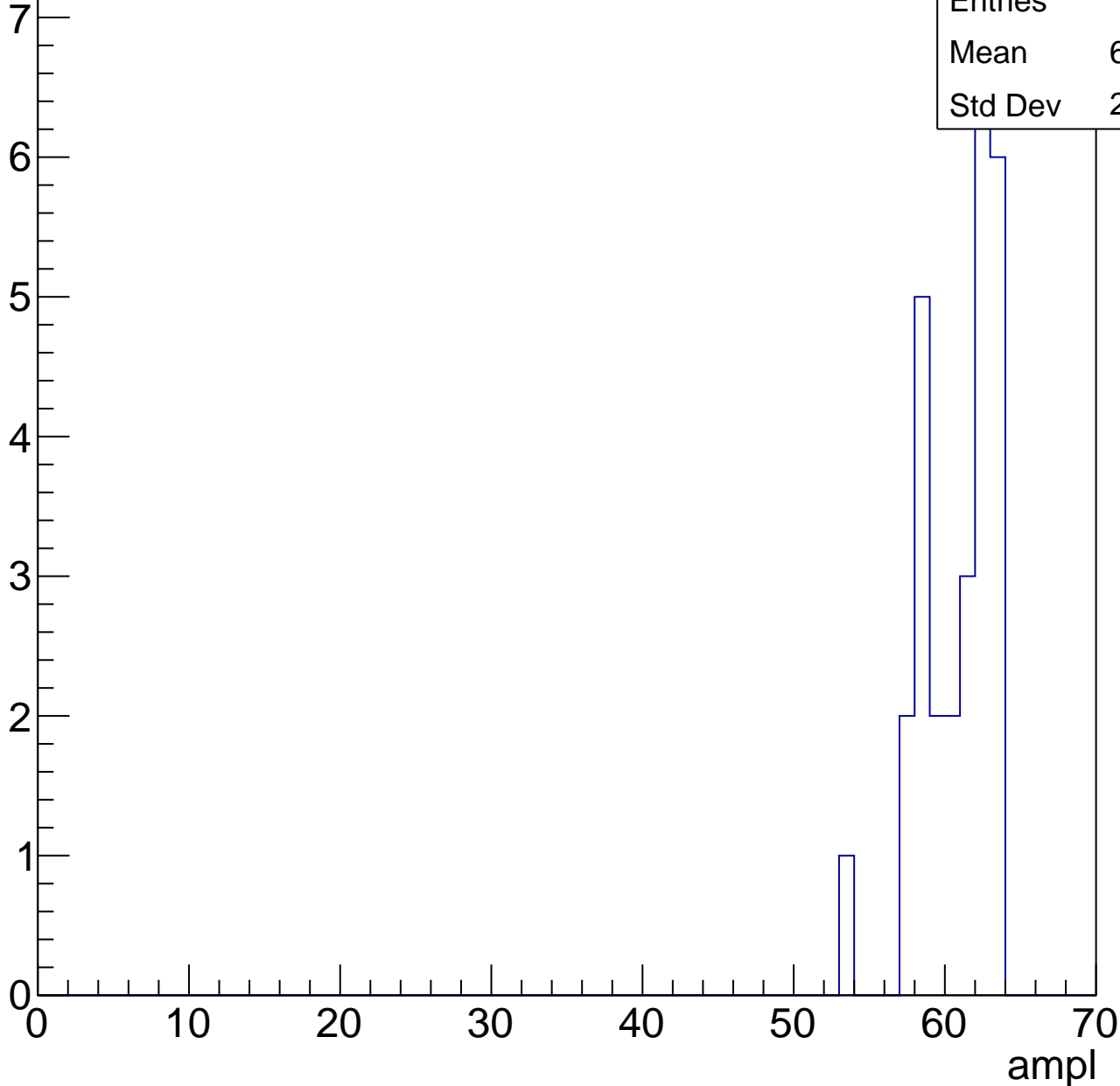


B0L102S, U5-ch35, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

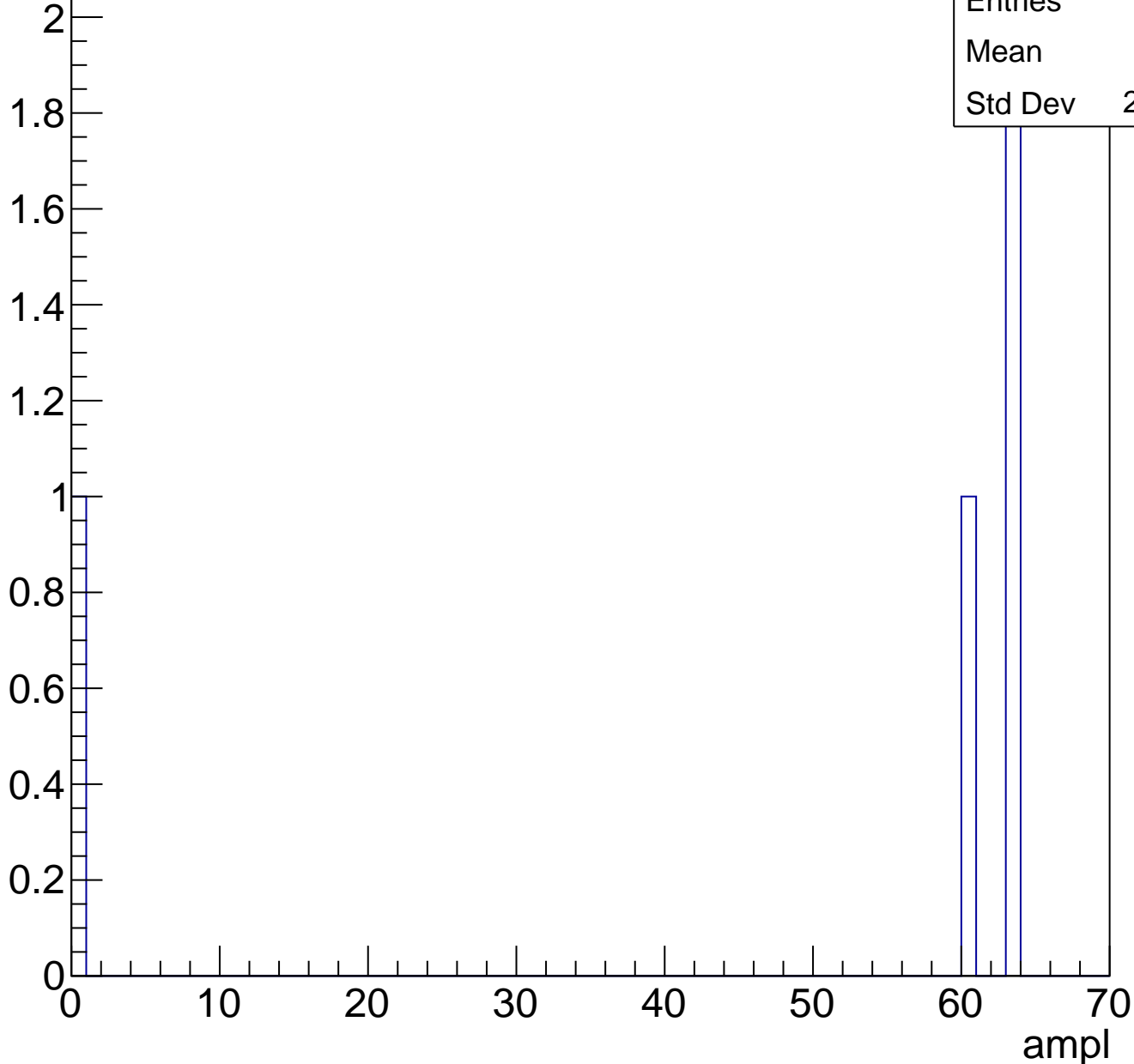
Entries	28
Mean	60.36
Std Dev	2.467



B0L102S, U5-ch35, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch35, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch36, adc0

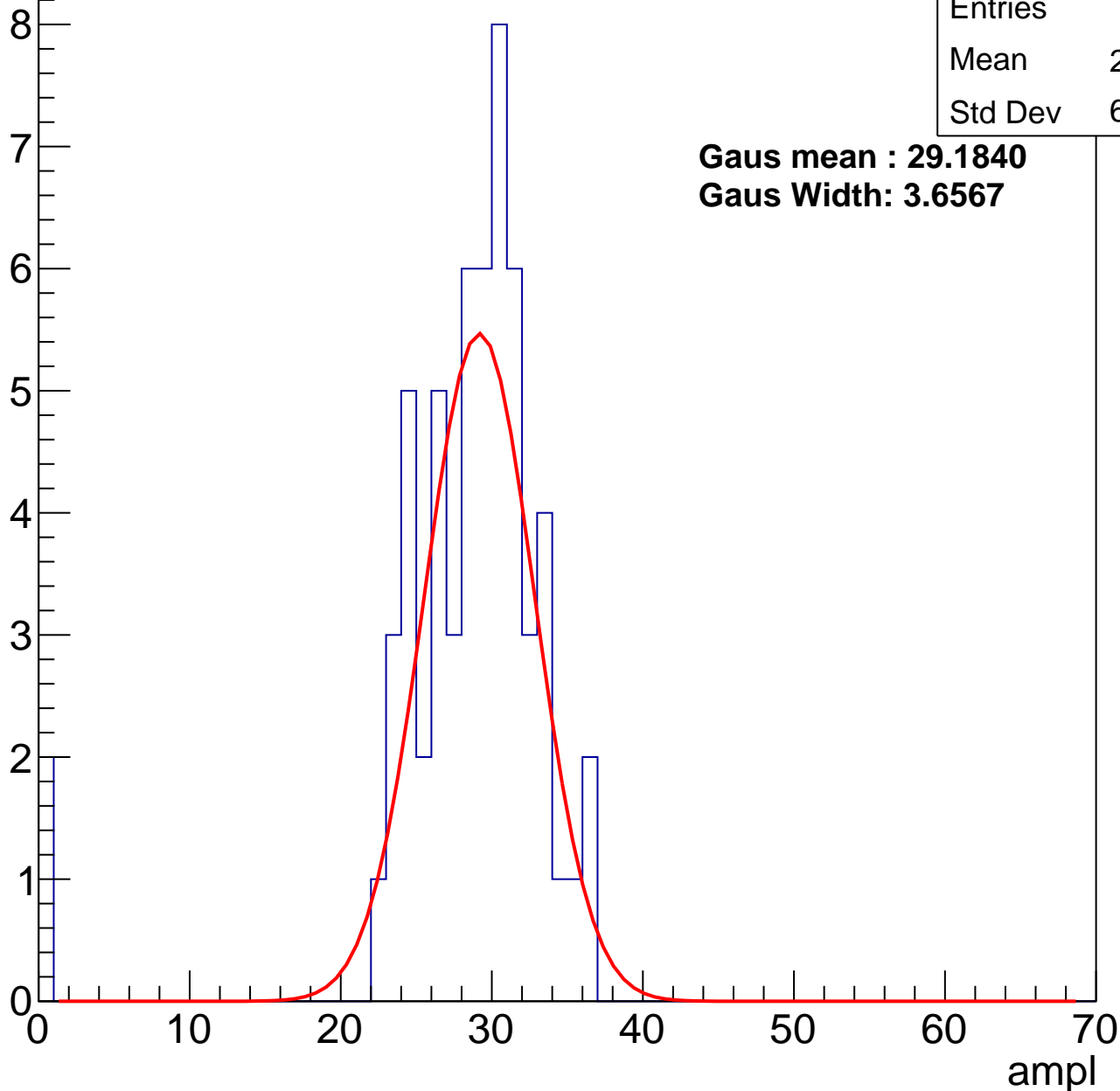
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	27.74
Std Dev	6.227

Gaus mean : 29.1840

Gaus Width: 3.6567



B0L102S, U5-ch36, adc1

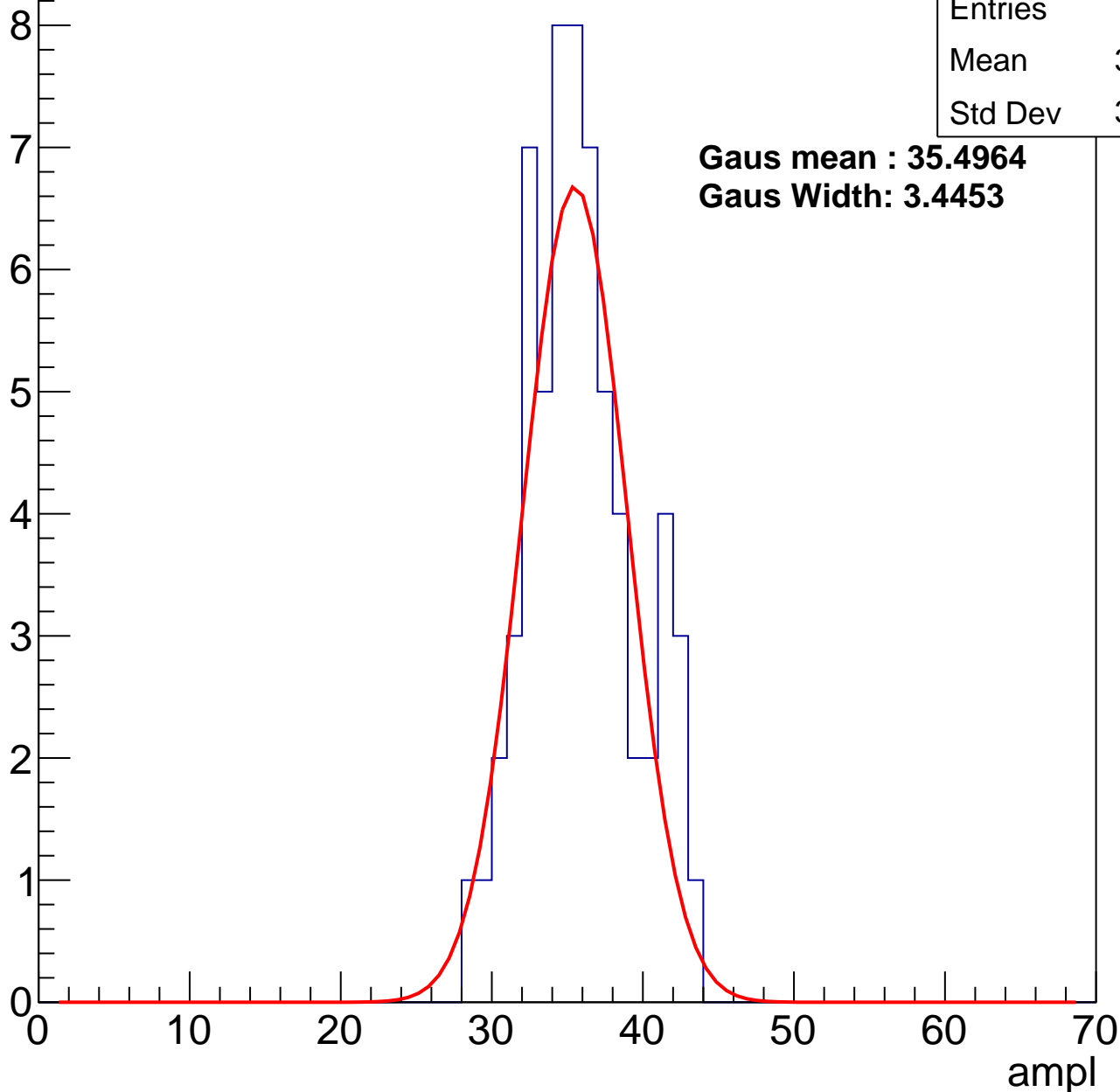
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	35.41
Std Dev	3.481

Gaus mean : 35.4964

Gaus Width: 3.4453



B0L102S, U5-ch36, adc2

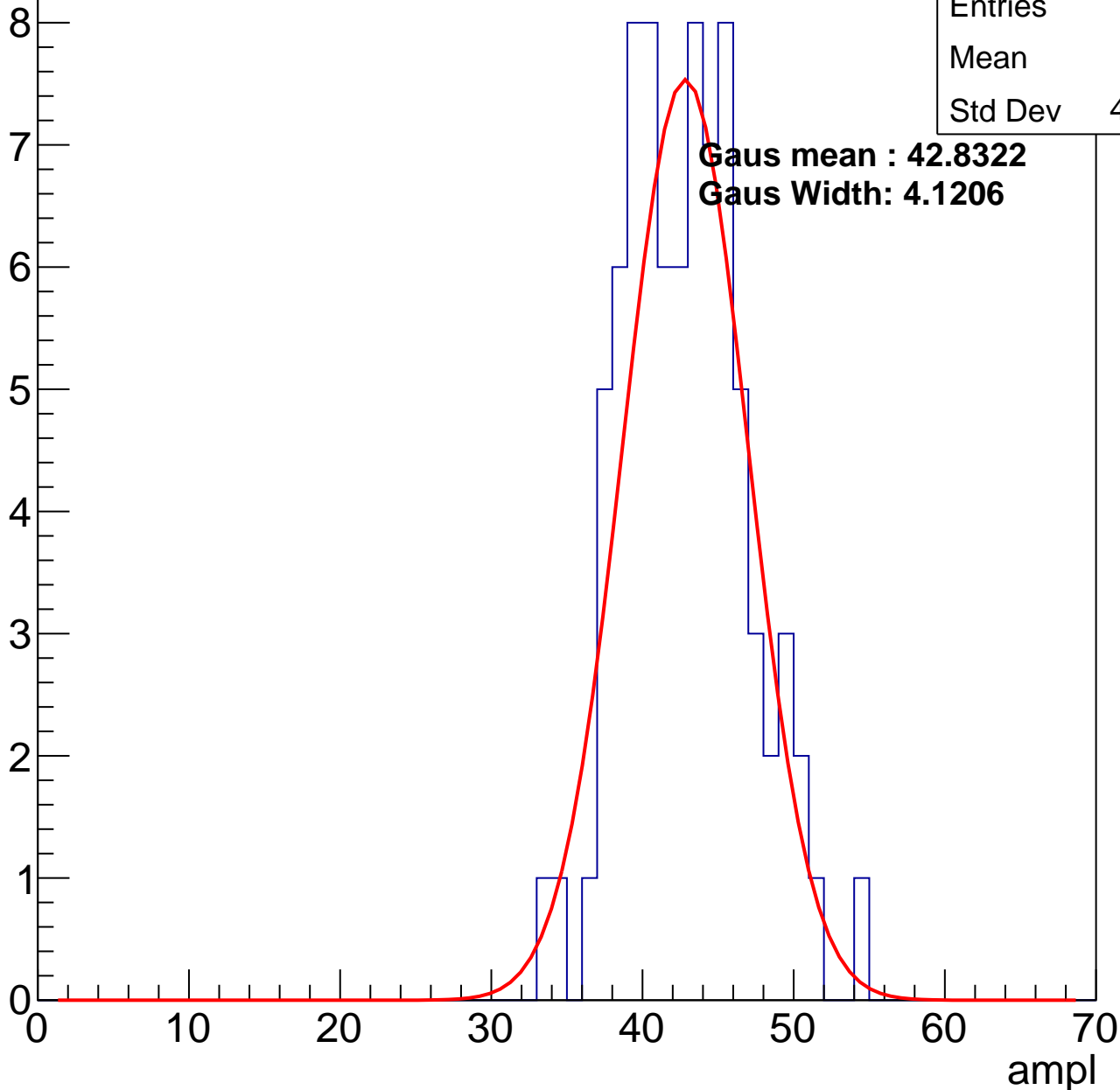
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	42.4
Std Dev	4.033

Gaus mean : 42.8322

Gaus Width: 4.1206

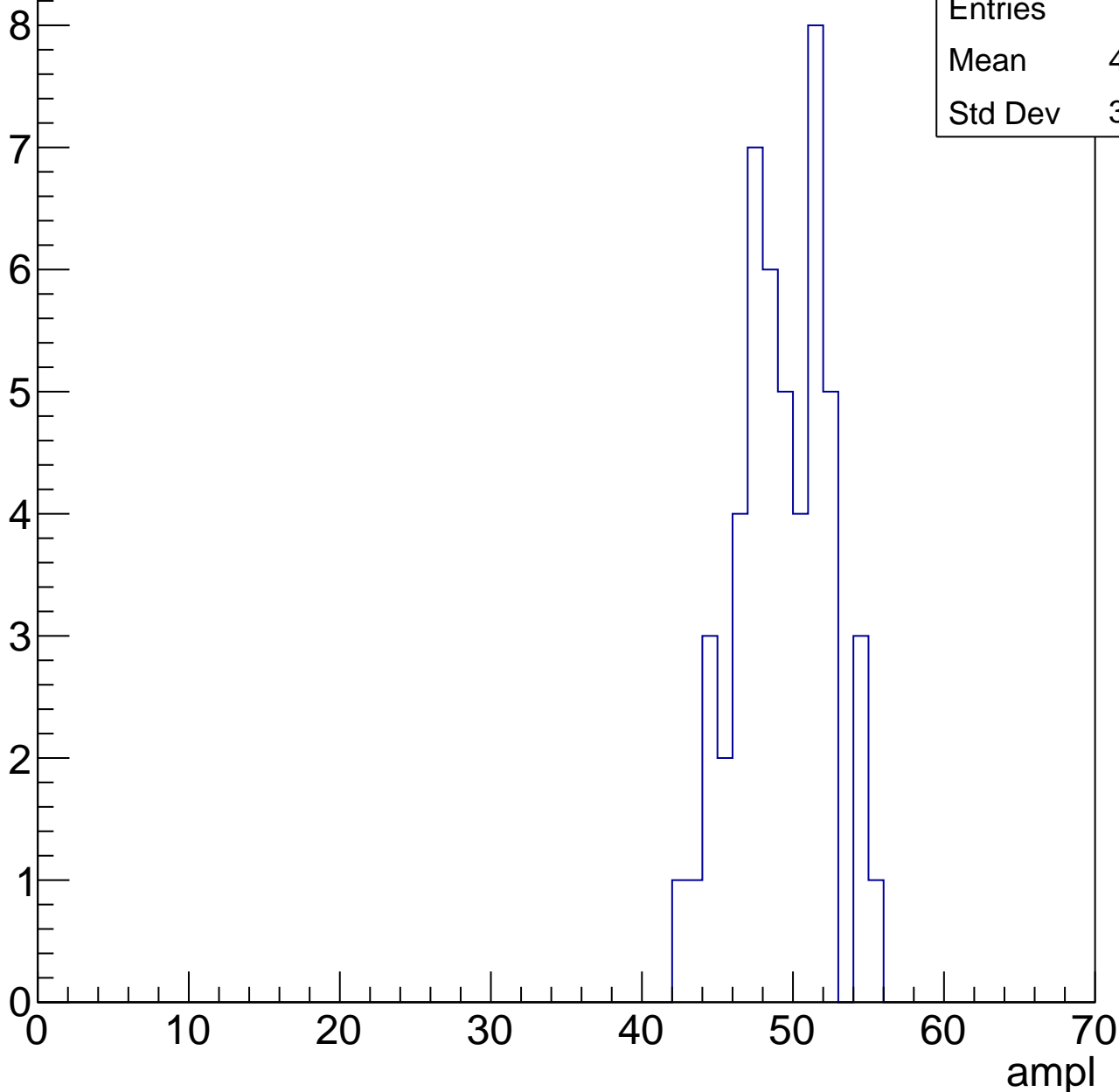


B0L102S, U5-ch36, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	48.76
Std Dev	3.004



B0L102S, U5-ch36, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	70
Mean	55.14
Std Dev	3.622

Entry

10

8

6

4

2

0

0

10

20

30

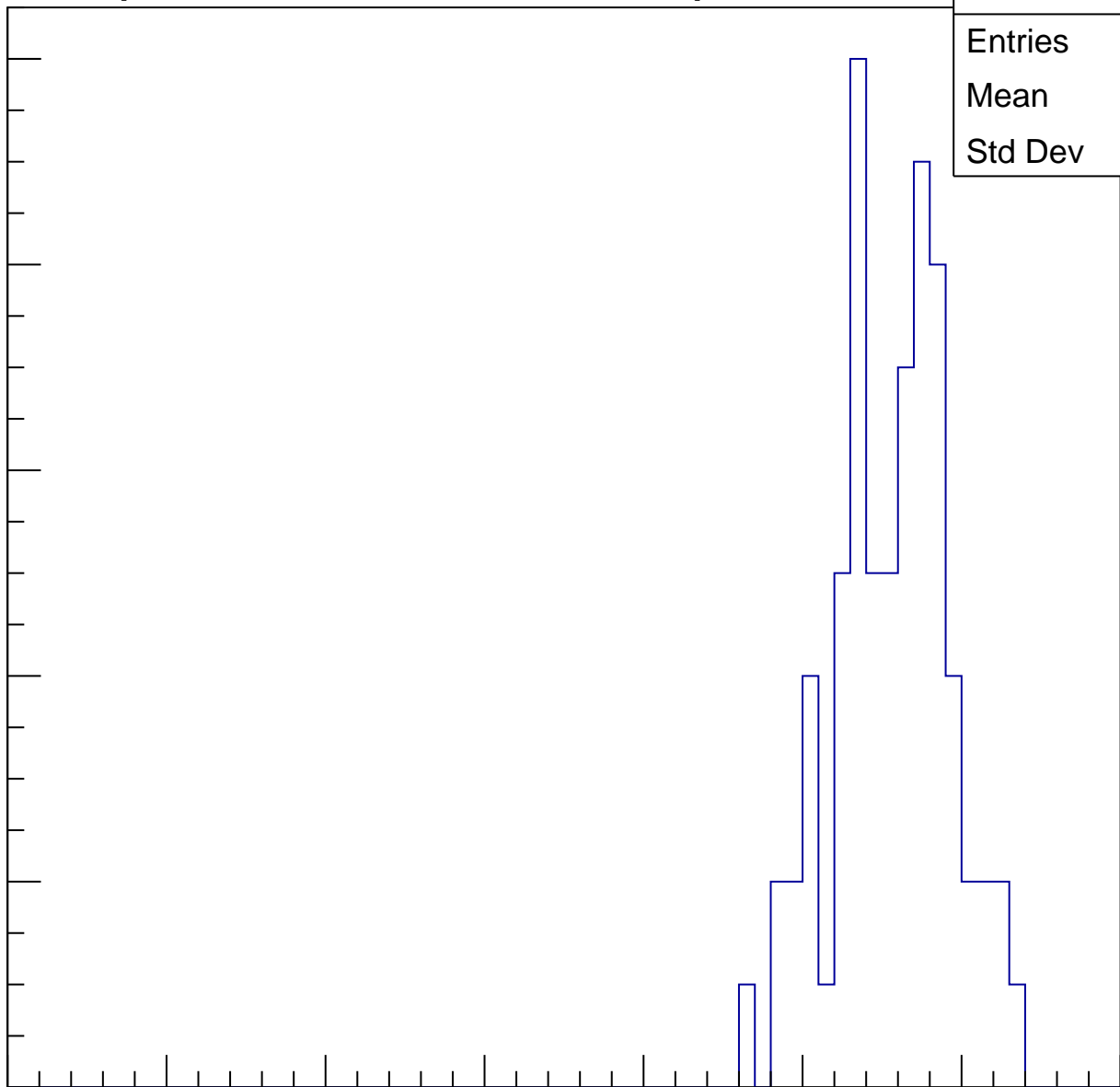
40

50

60

70

ampl

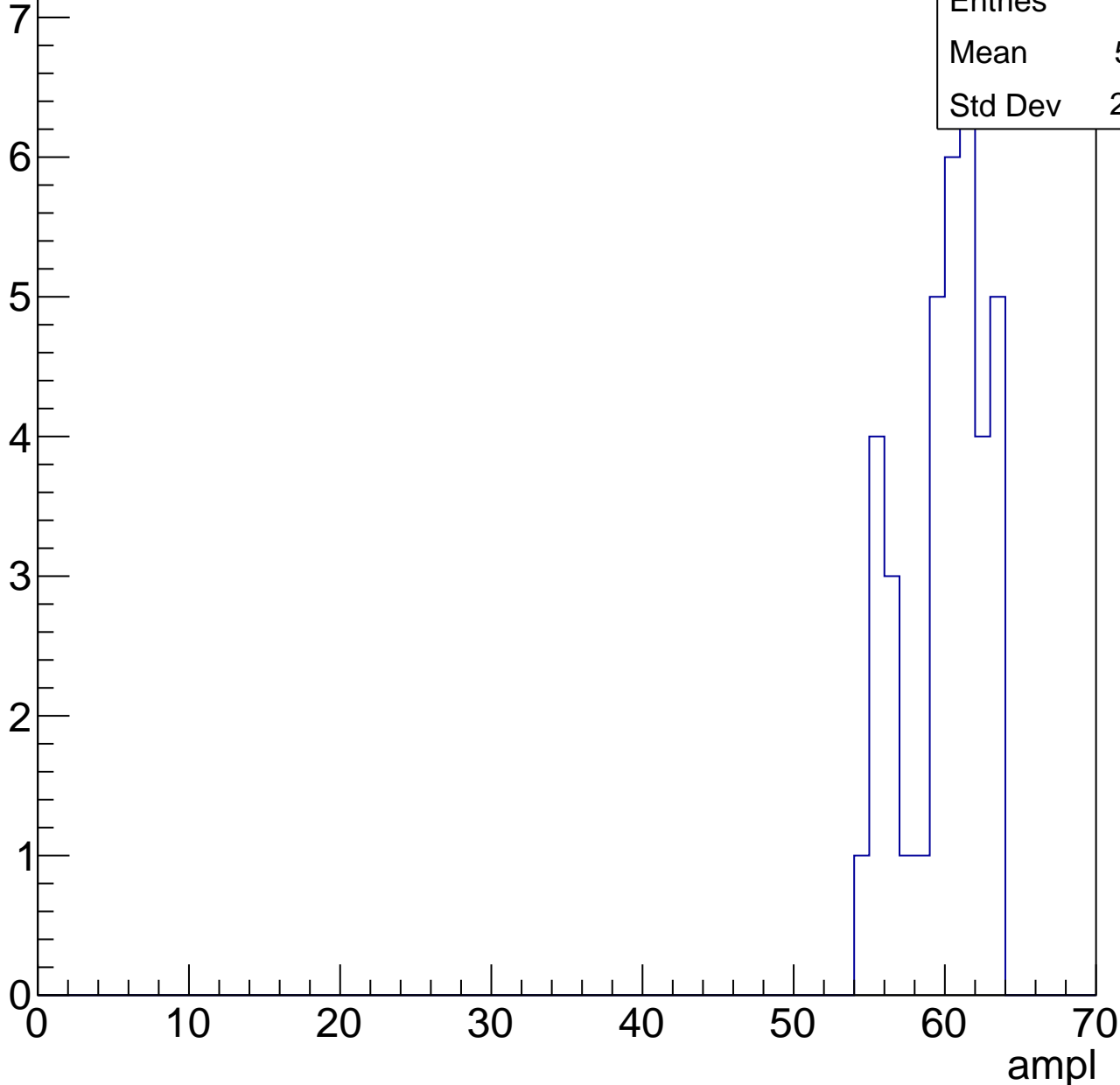


B0L102S, U5-ch36, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

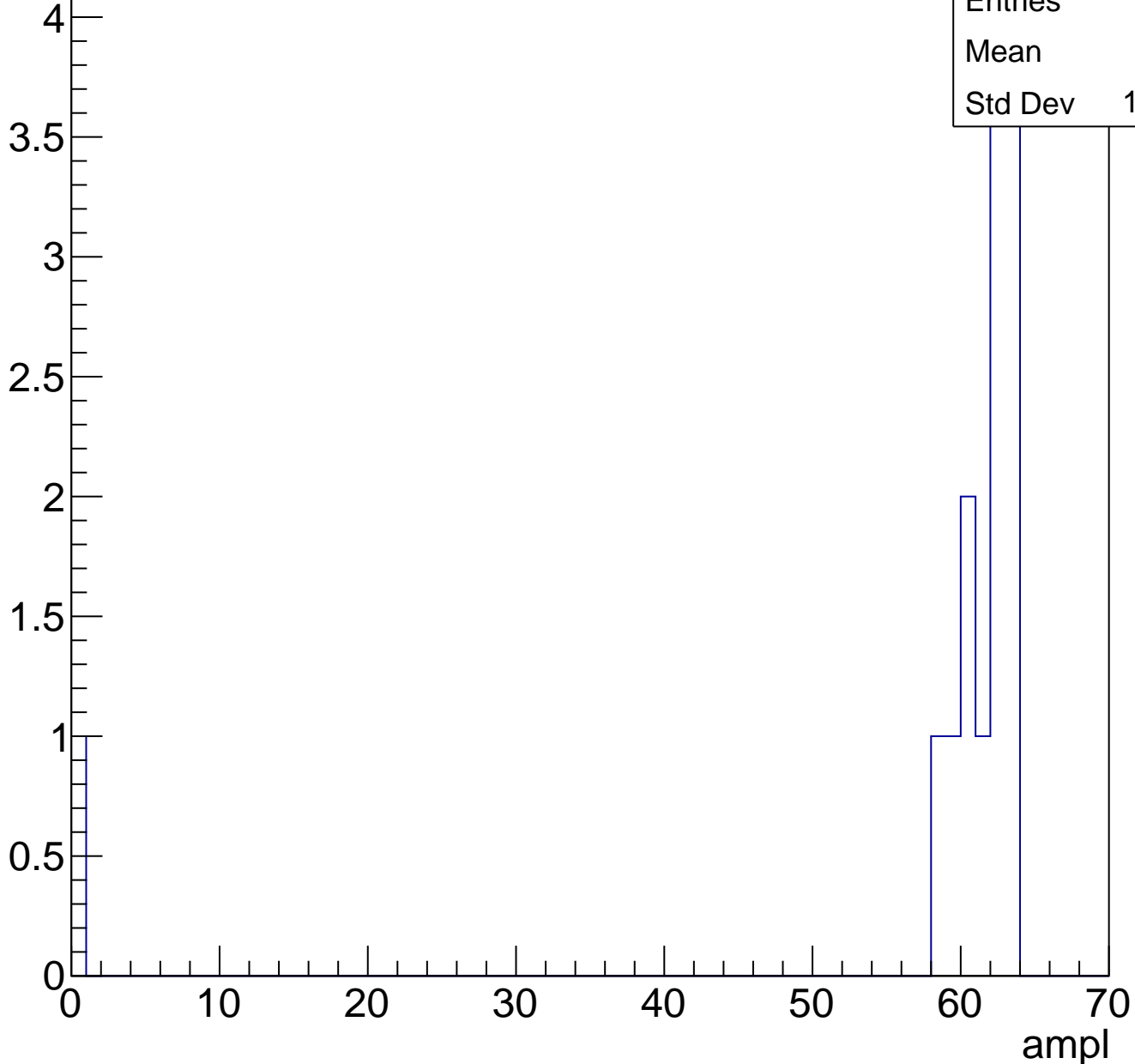
Entries	37
Mean	59.51
Std Dev	2.657



B0L102S, U5-ch36, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch36, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch37, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	28.08
Std Dev	4.76

Gaus mean : 28.3217

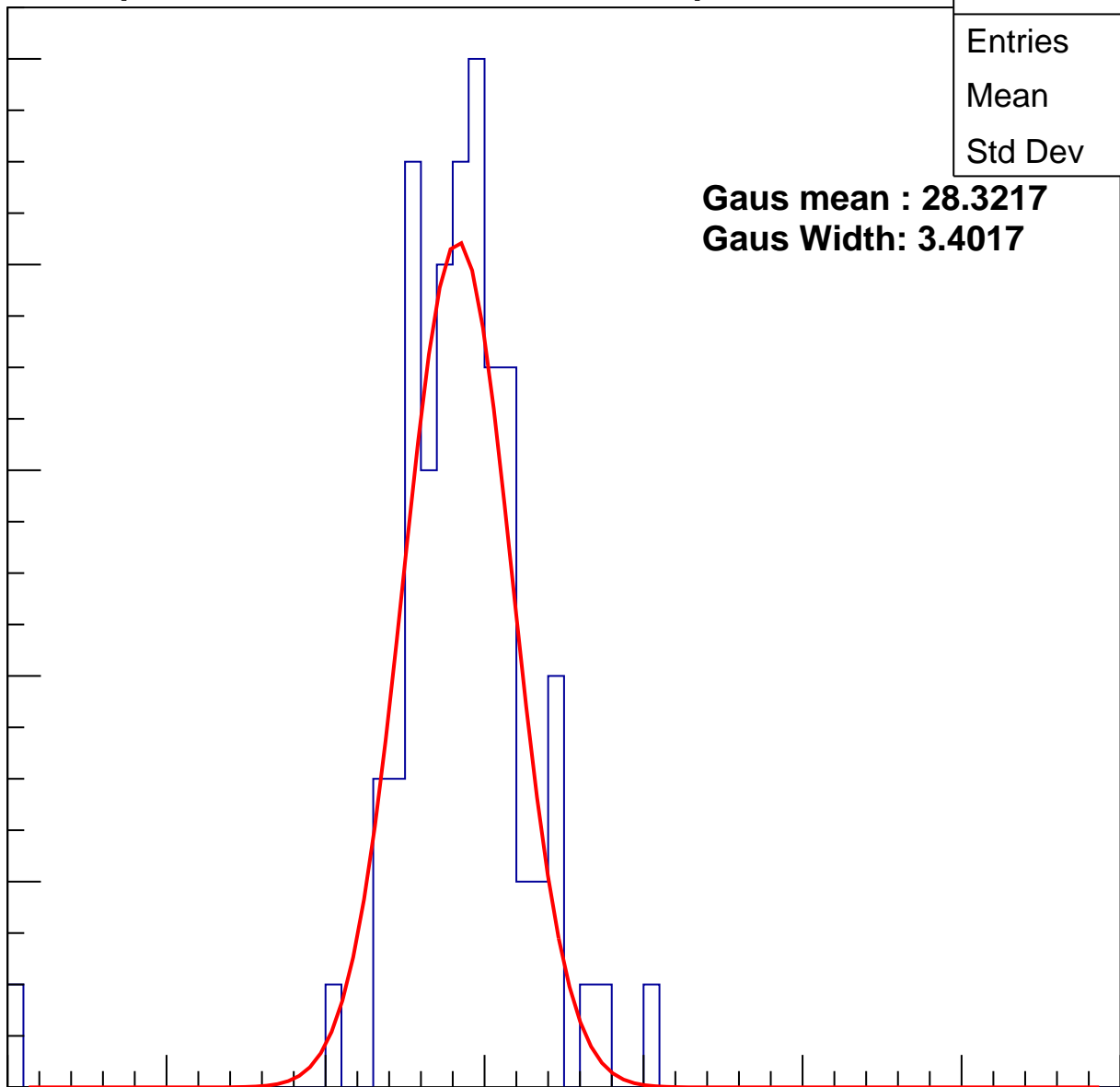
Gaus Width: 3.4017

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch37, adc1

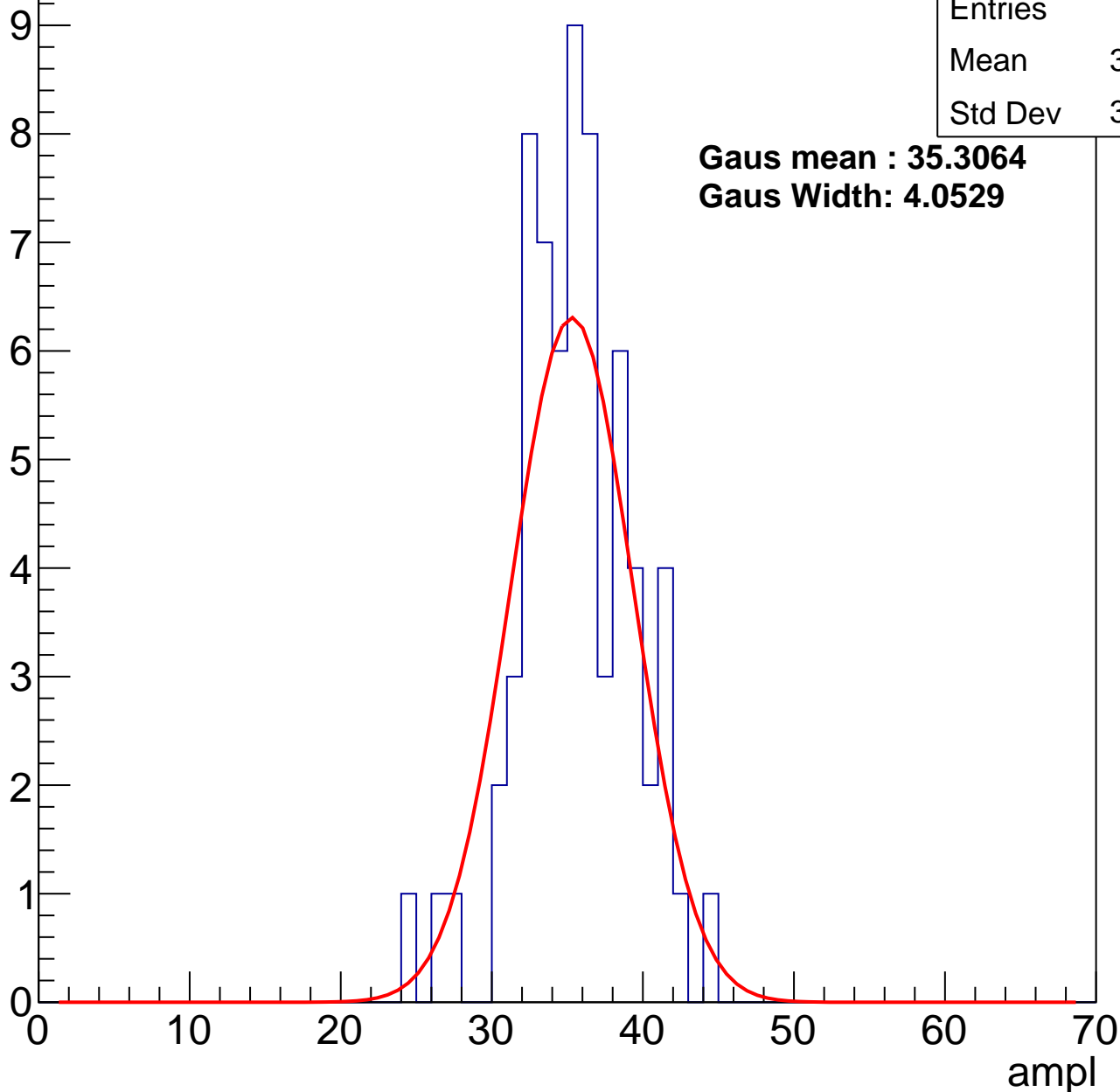
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	35.06
Std Dev	3.729

Gaus mean : 35.3064

Gaus Width: 4.0529



B0L102S, U5-ch37, adc2

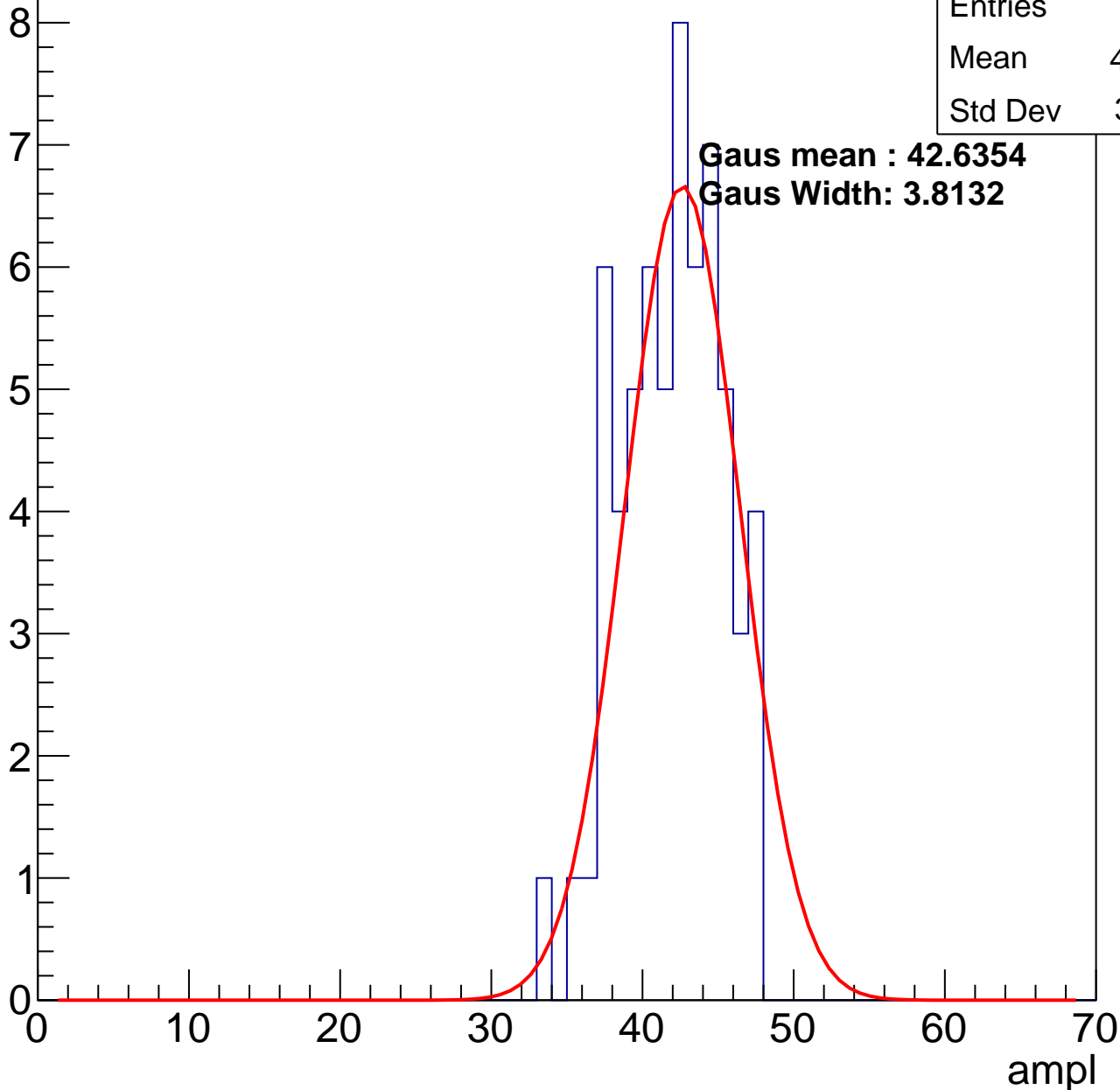
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	41.47
Std Dev	3.271

Gaus mean : 42.6354

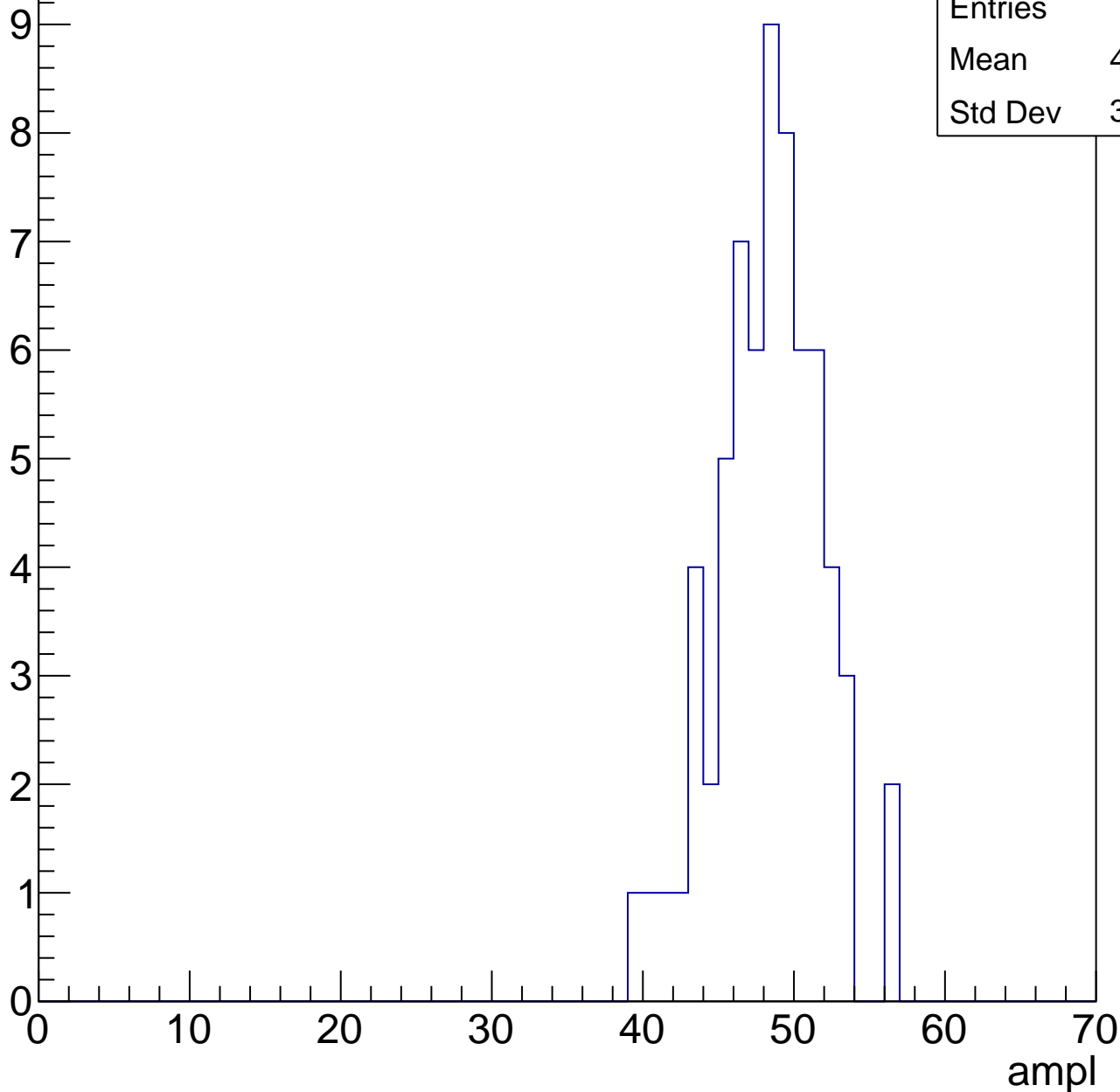
Gaus Width: 3.8132



B0L102S, U5-ch37, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

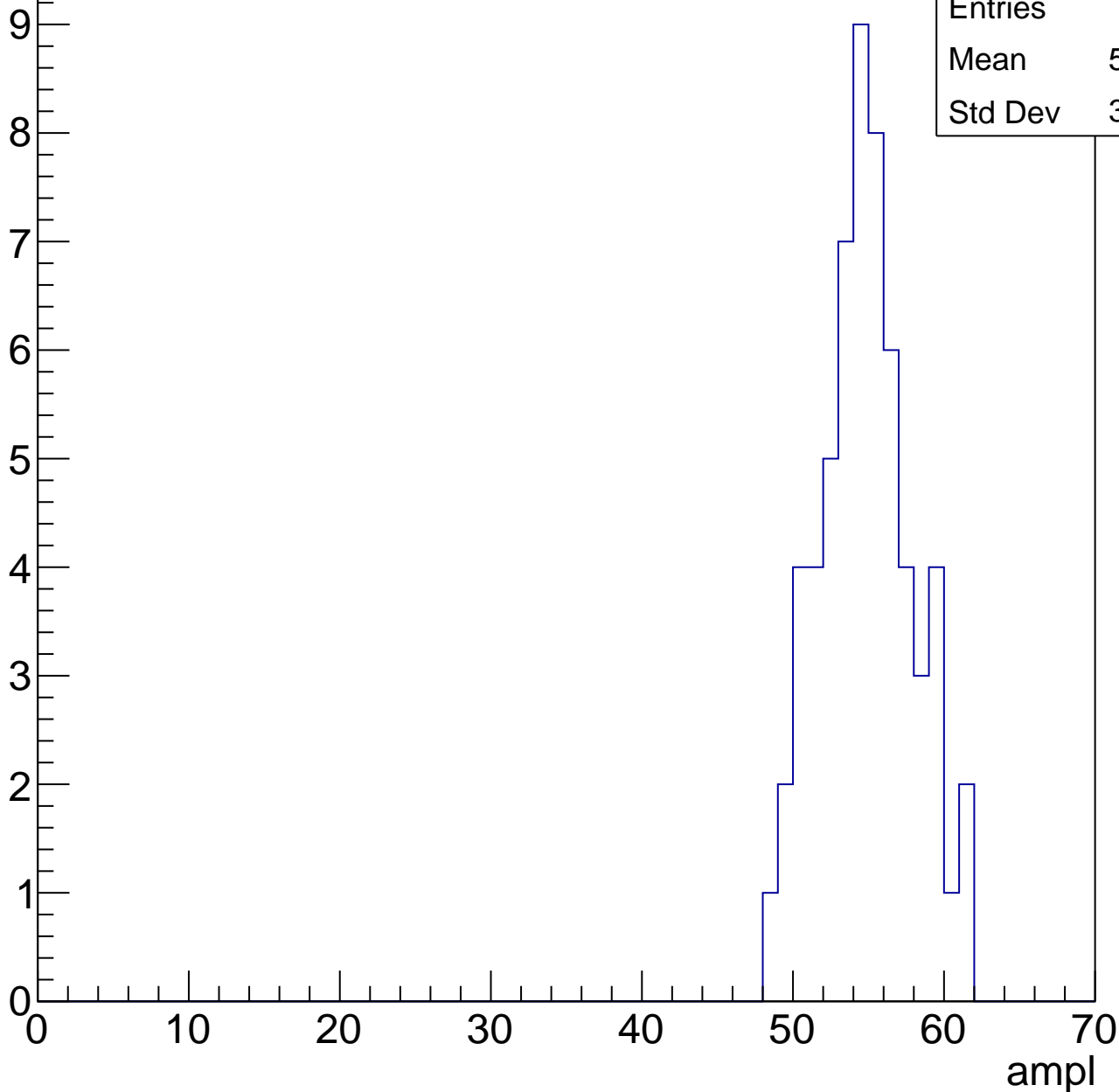


B0L102S, U5-ch37, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.38
Std Dev	3.039

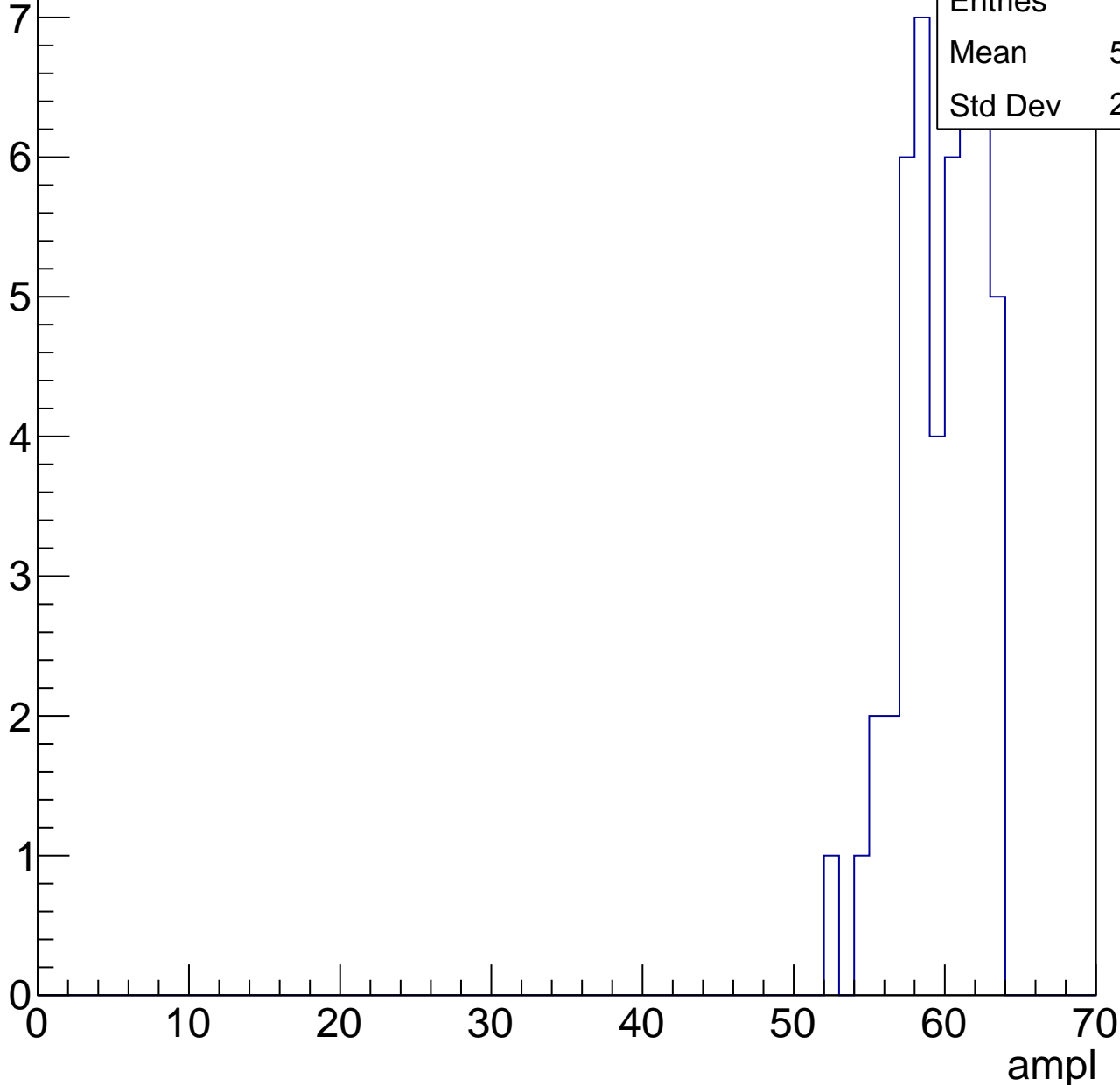


B0L102S, U5-ch37, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

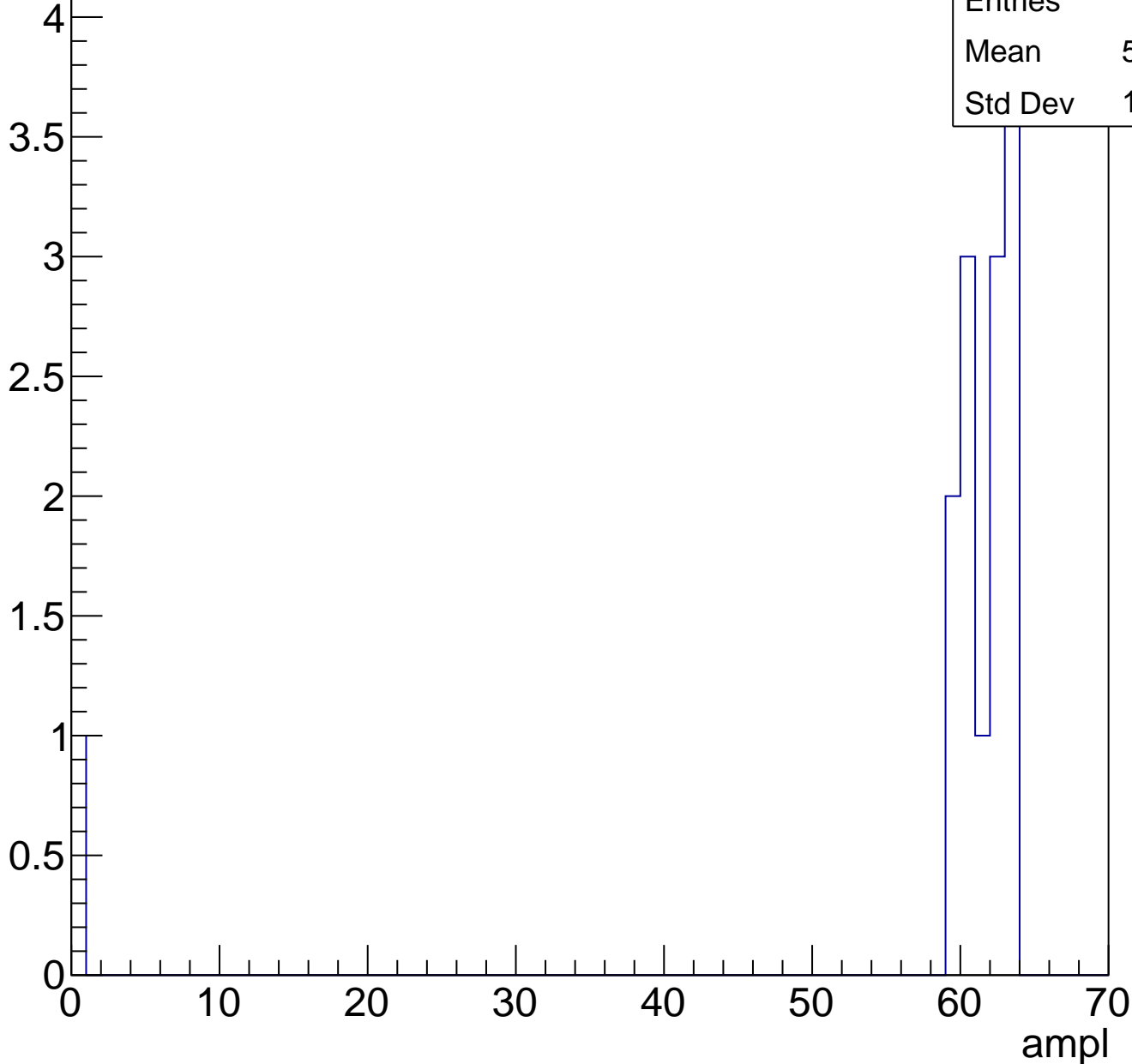
Entries	48
Mean	59.33
Std Dev	2.609



B0L102S, U5-ch37, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch37, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch38, adc0

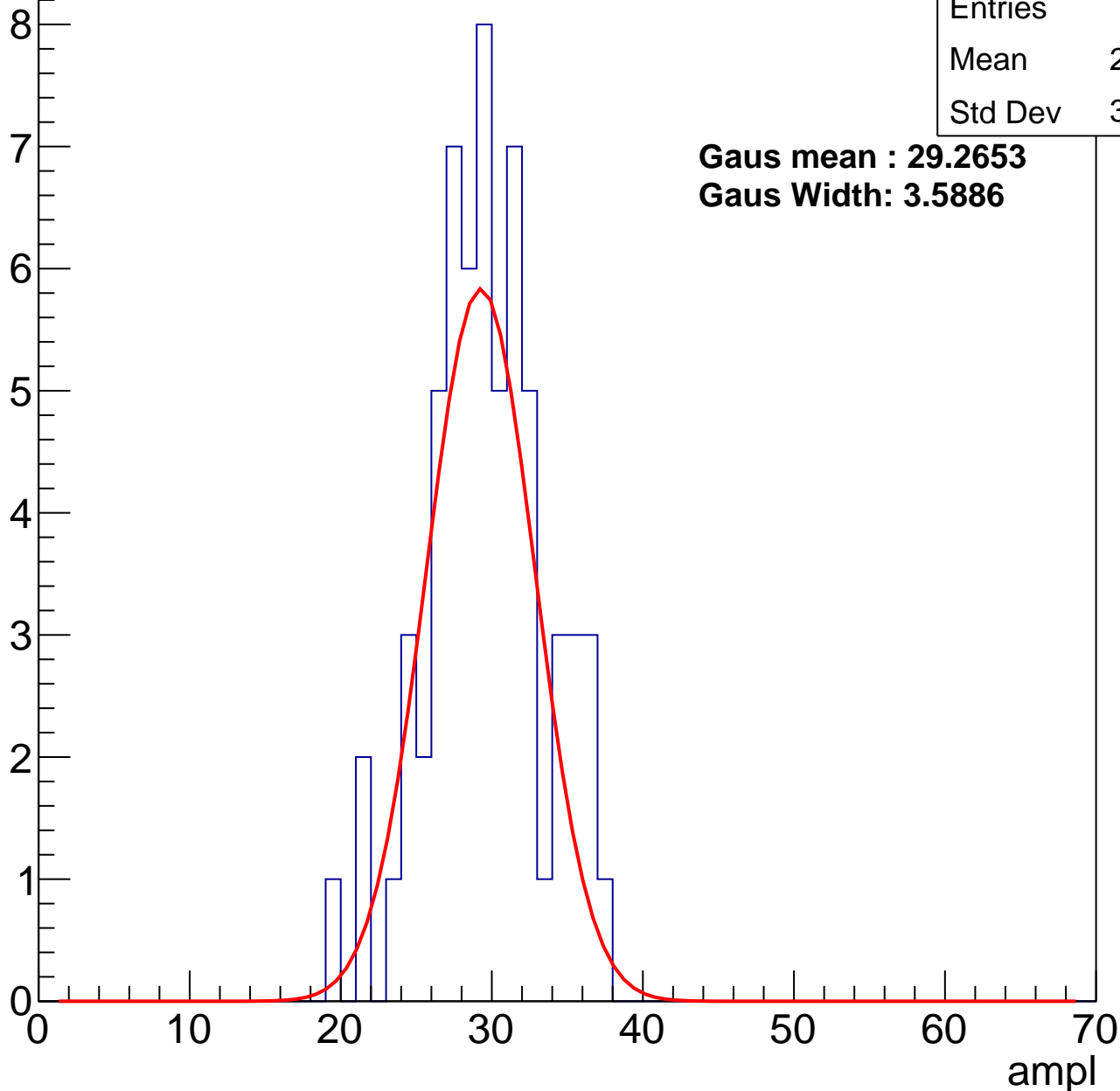
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	29.16
Std Dev	3.855

Gaus mean : 29.2653

Gaus Width: 3.5886



B0L102S, U5-ch38, adc1

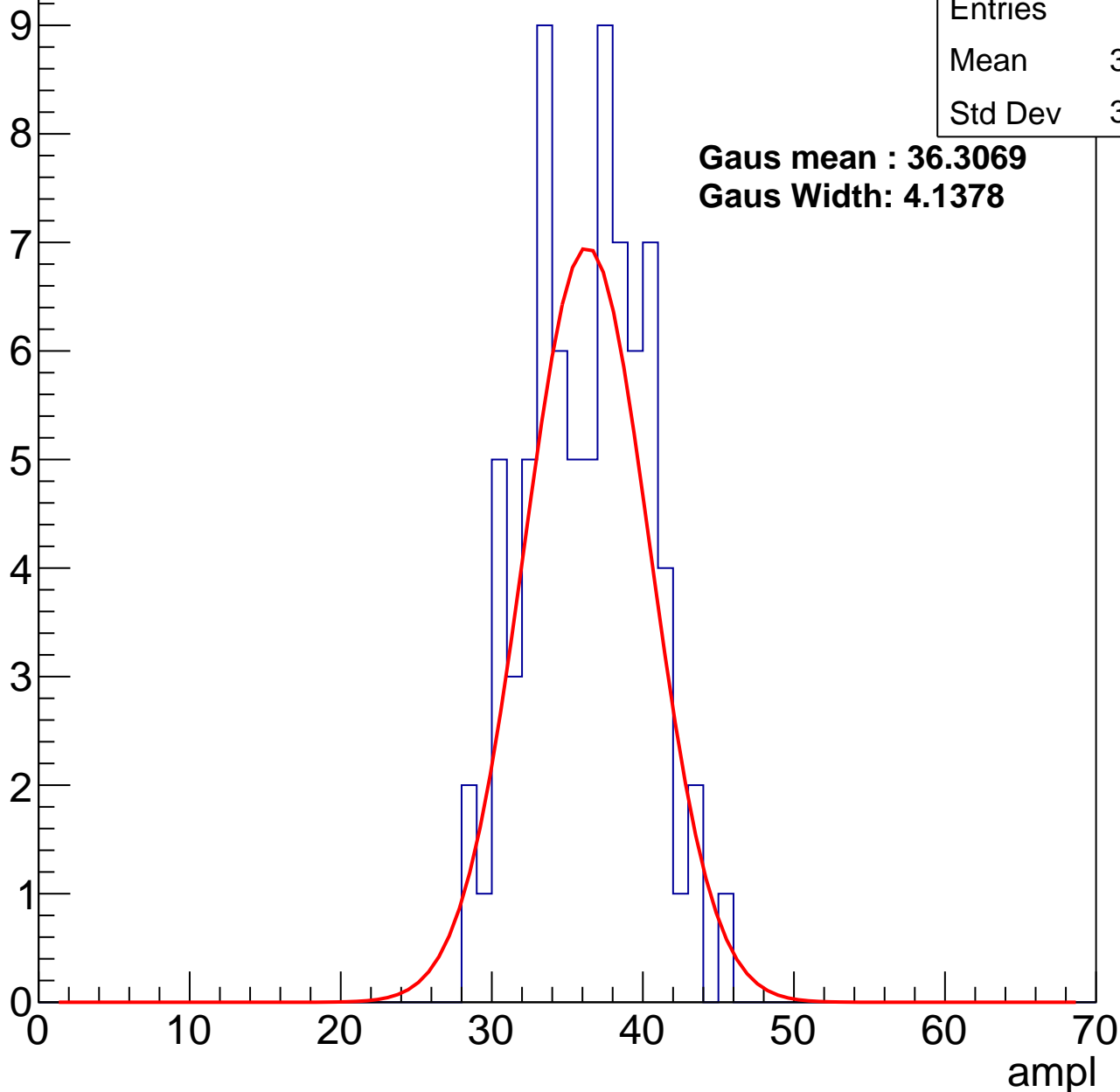
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	35.82
Std Dev	3.819

Gaus mean : 36.3069

Gaus Width: 4.1378



B0L102S, U5-ch38, adc2

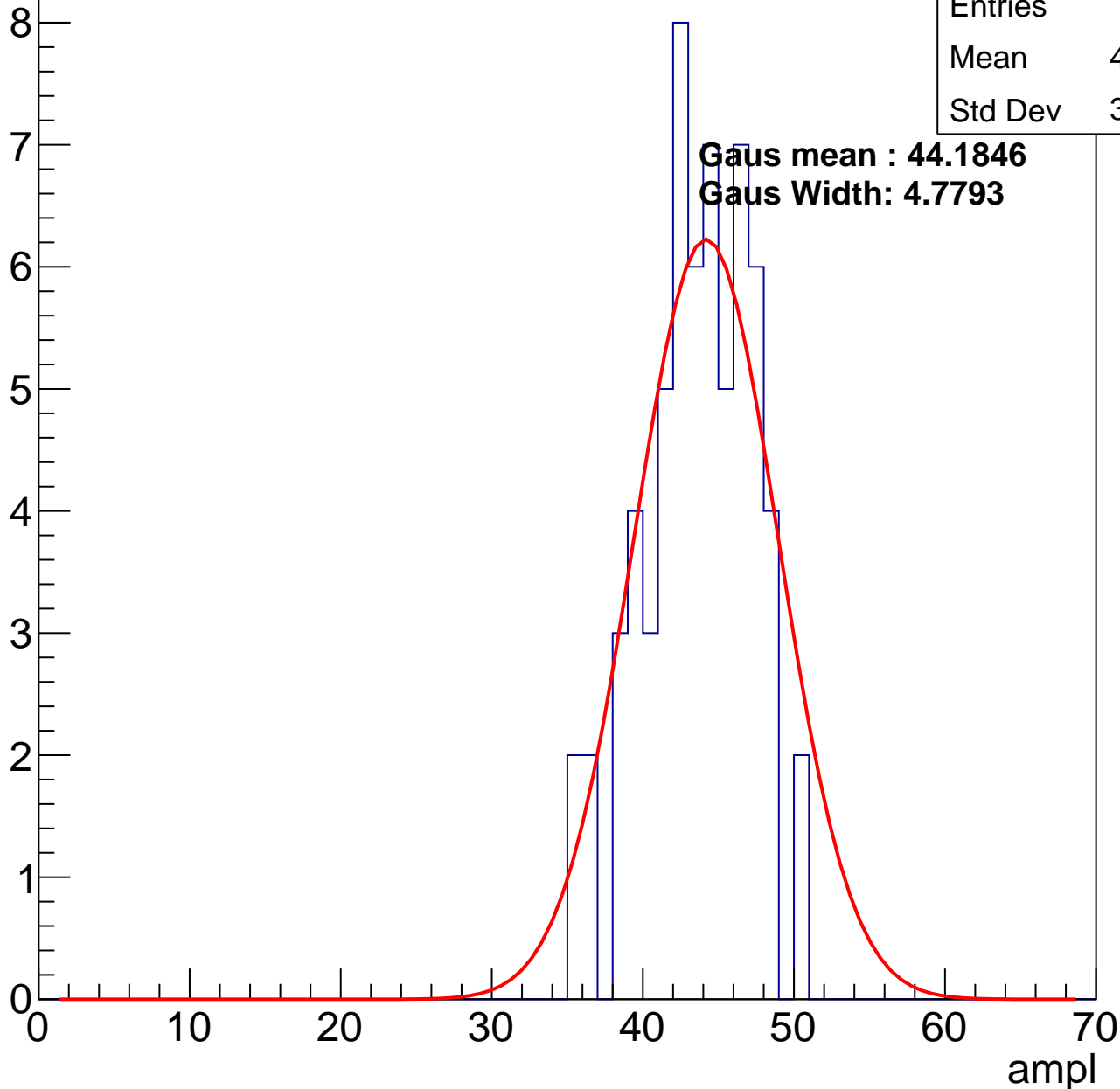
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	43.12
Std Dev	3.542

Gaus mean : 44.1846

Gaus Width: 4.7793

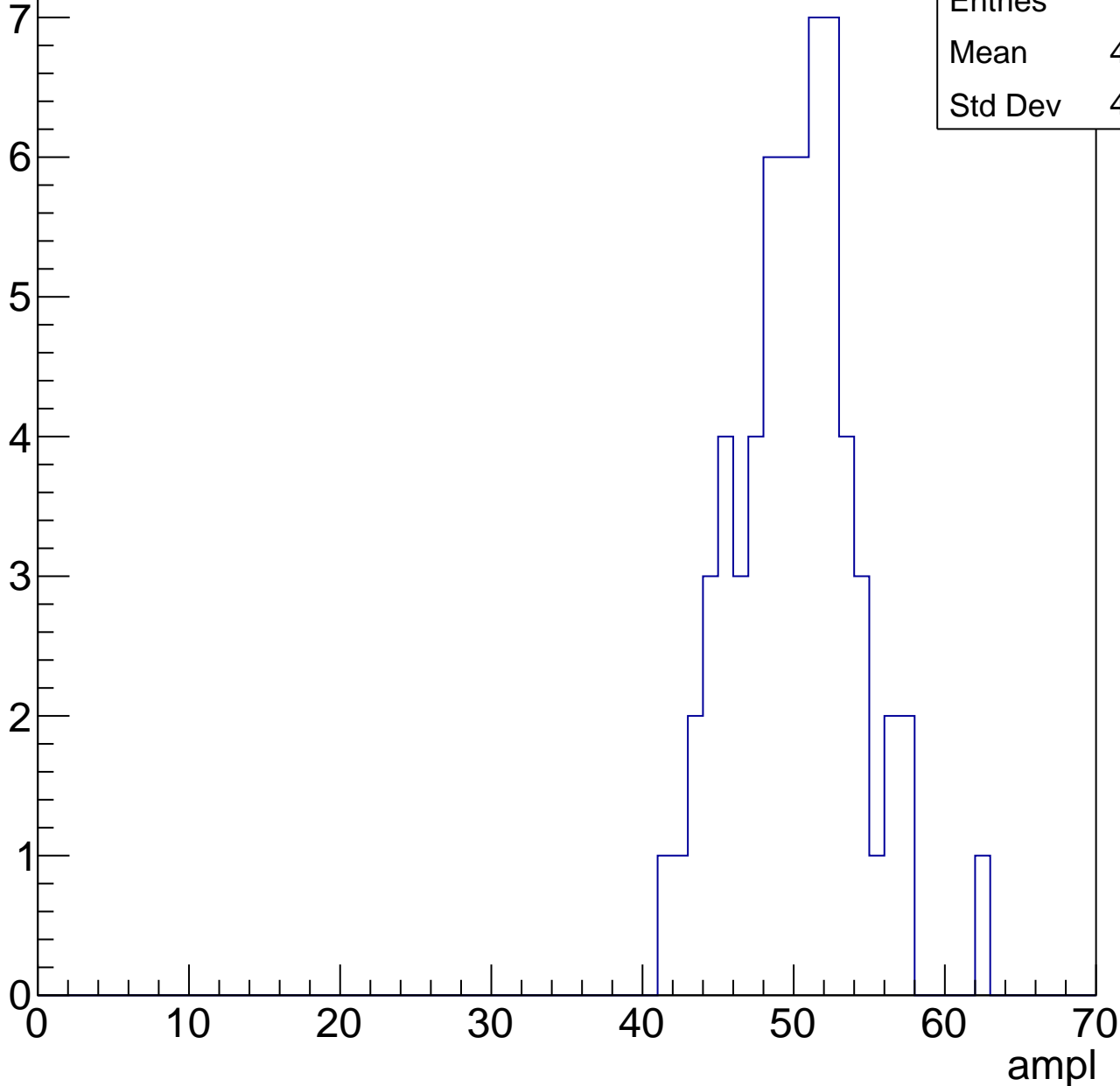


B0L102S, U5-ch38, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.63
Std Dev	4.013



B0L102S, U5-ch38, adc4

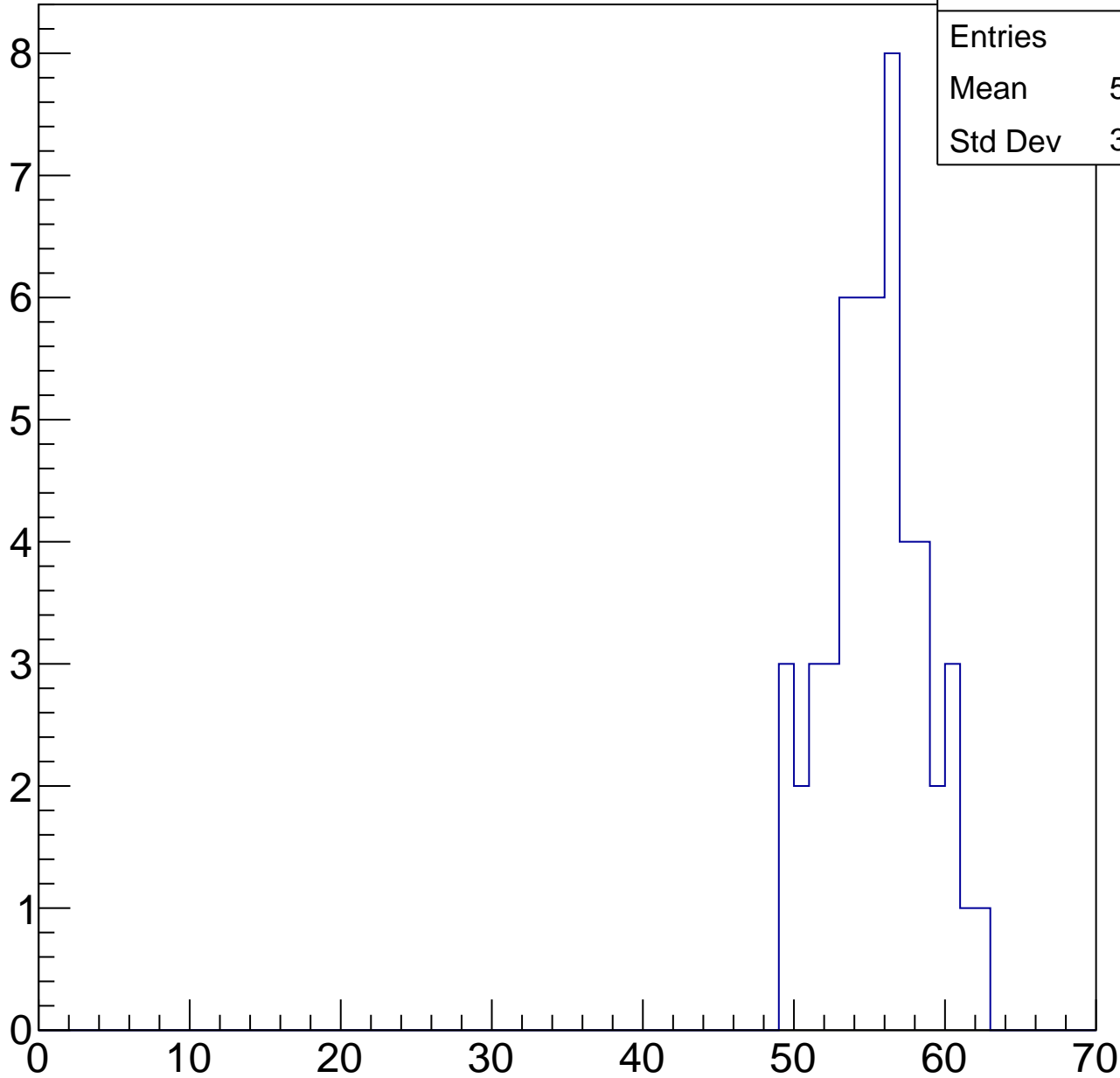
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	54.94
Std Dev	3.147

ampl



B0L102S, U5-ch38, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 39

Mean 59.03

Std Dev 2.796

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

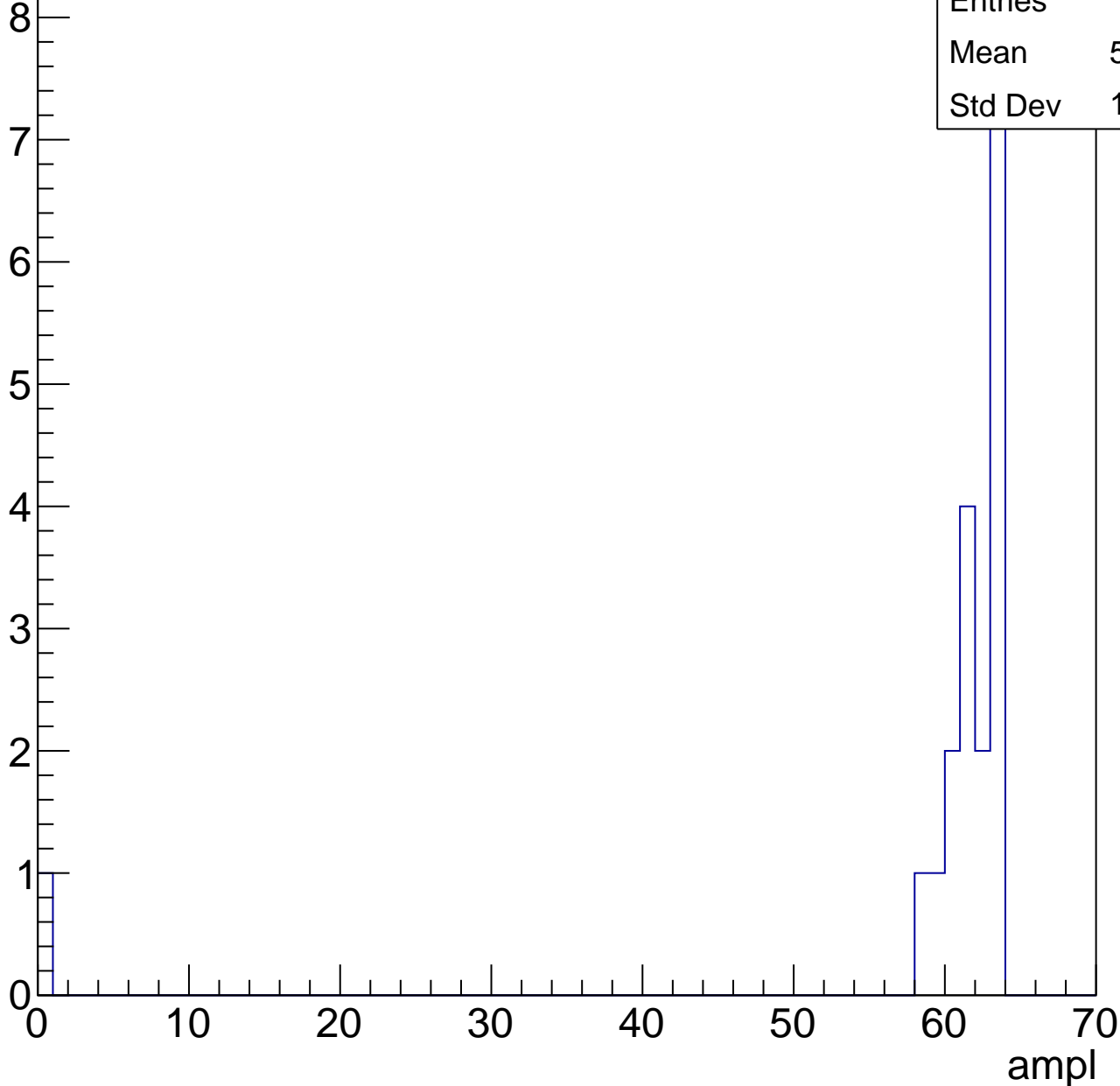
7

B0L102S, U5-ch38, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	19
Mean	58.37
Std Dev	13.84



B0L102S, U5-ch38, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch39, adc0

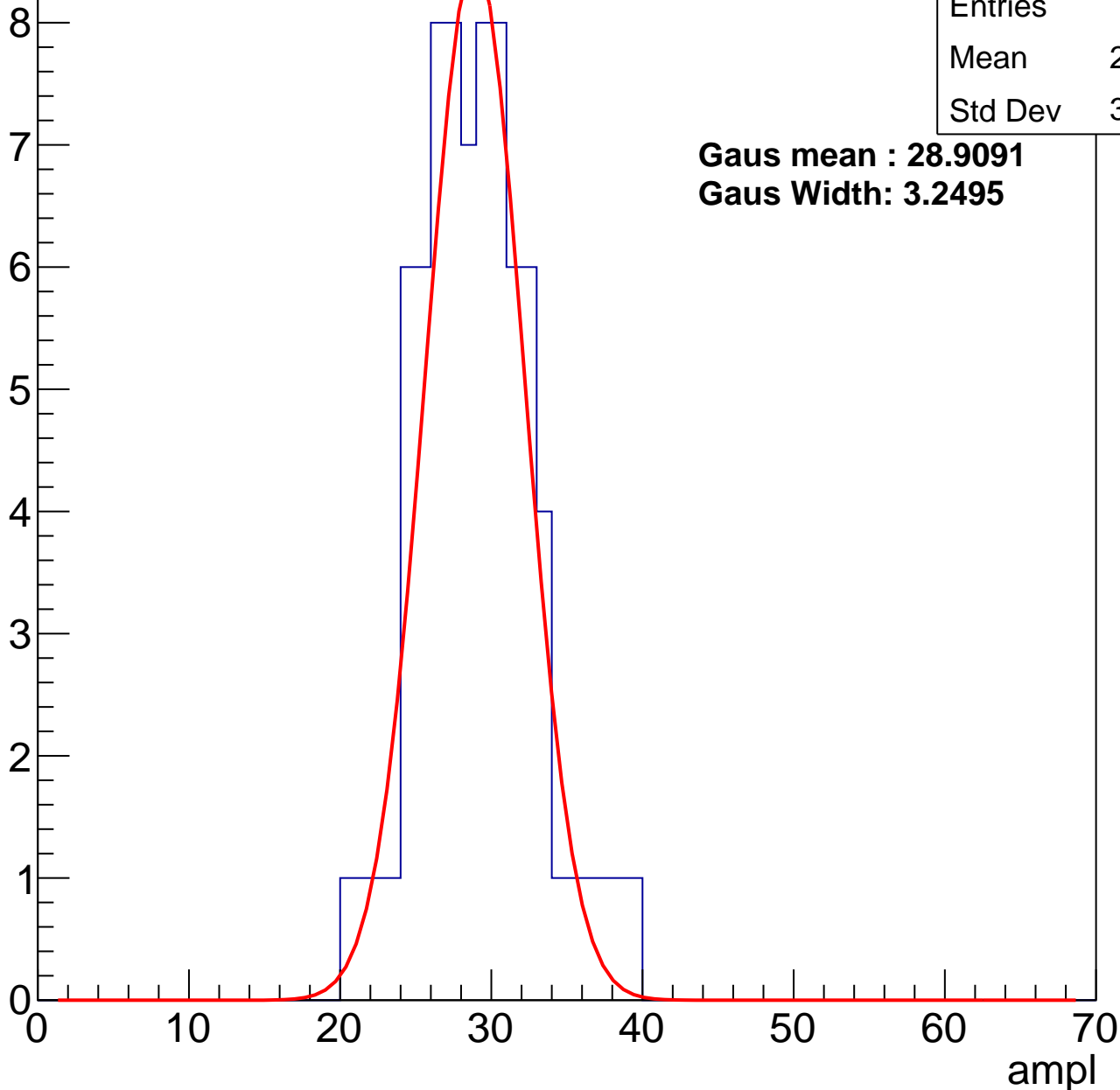
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.58
Std Dev	3.743

Gaus mean : 28.9091

Gaus Width: 3.2495



B0L102S, U5-ch39, adc1

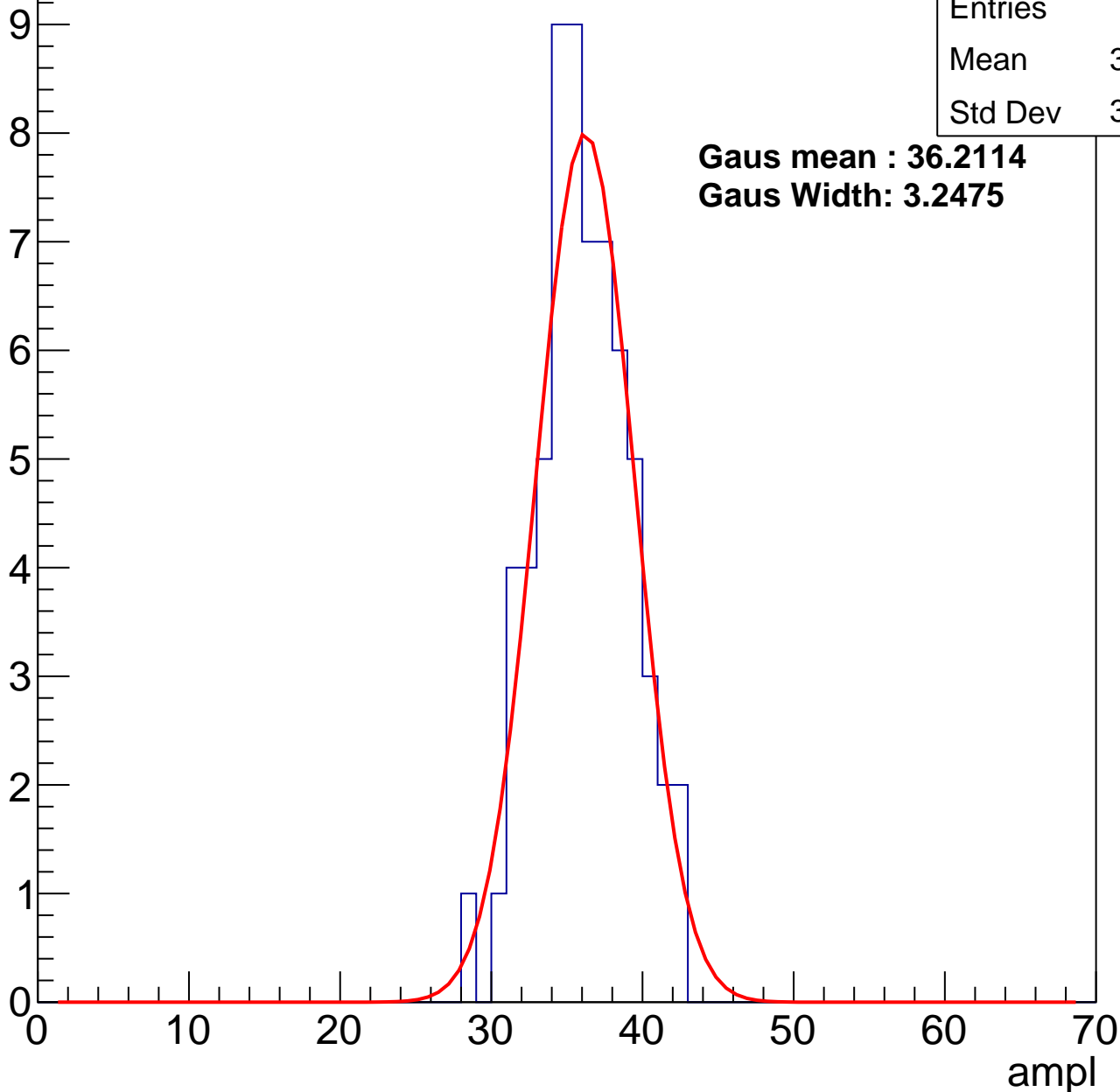
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	35.63
Std Dev	3.016

Gaus mean : 36.2114

Gaus Width: 3.2475



B0L102S, U5-ch39, adc2

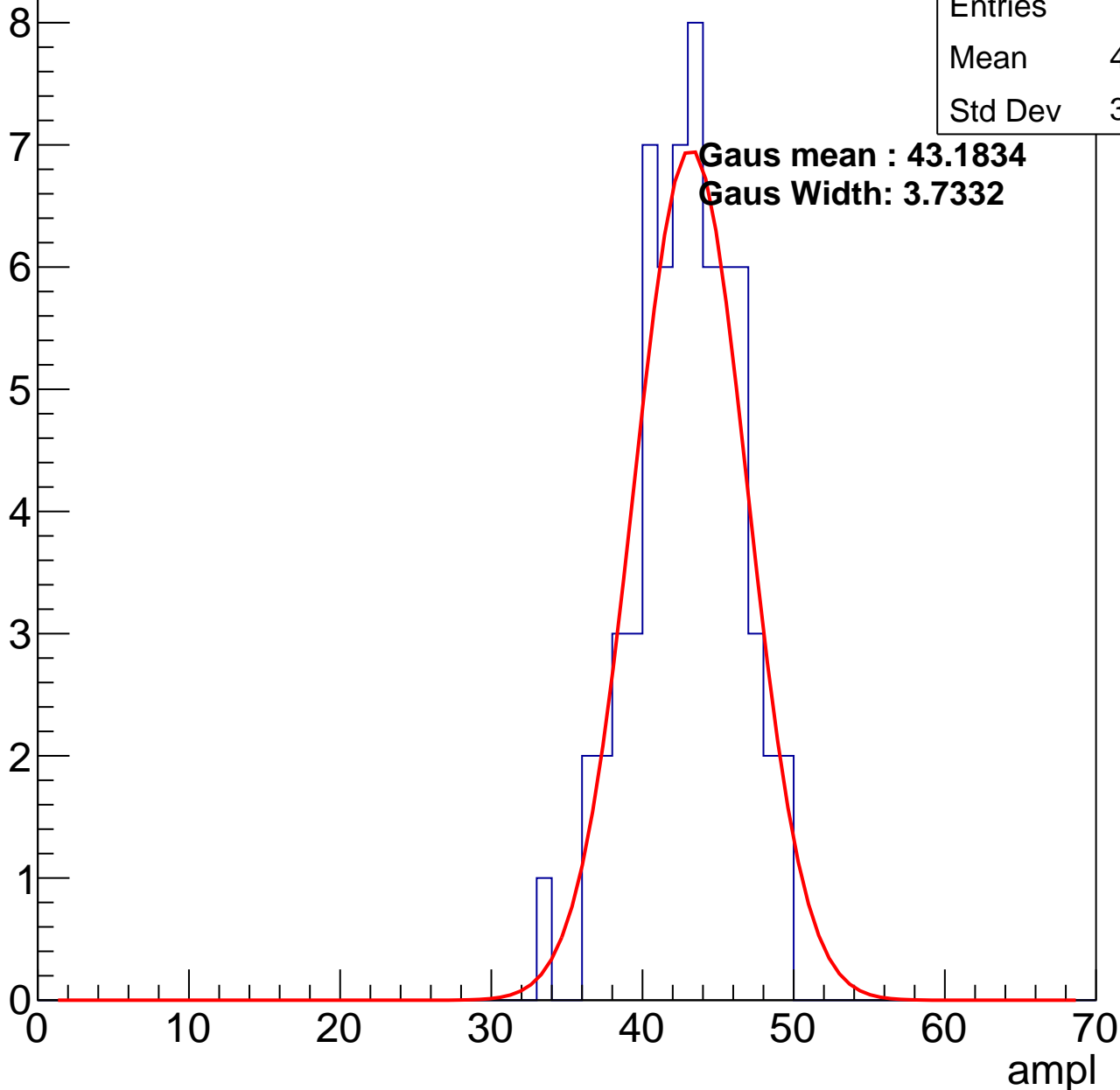
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	42.48
Std Dev	3.363

Gaus mean : 43.1834

Gaus Width: 3.7332

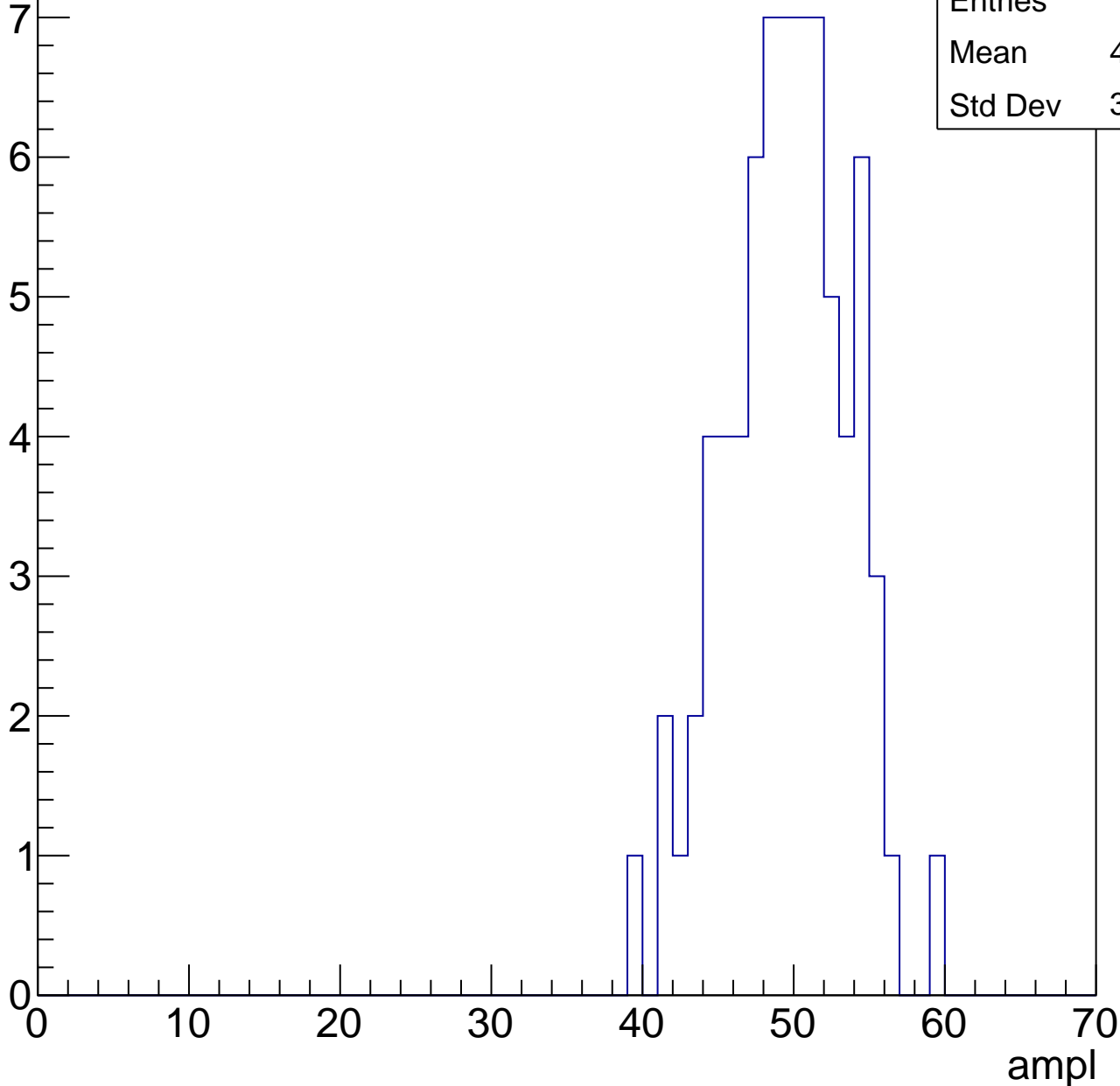


B0L102S, U5-ch39, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	49.07
Std Dev	3.963

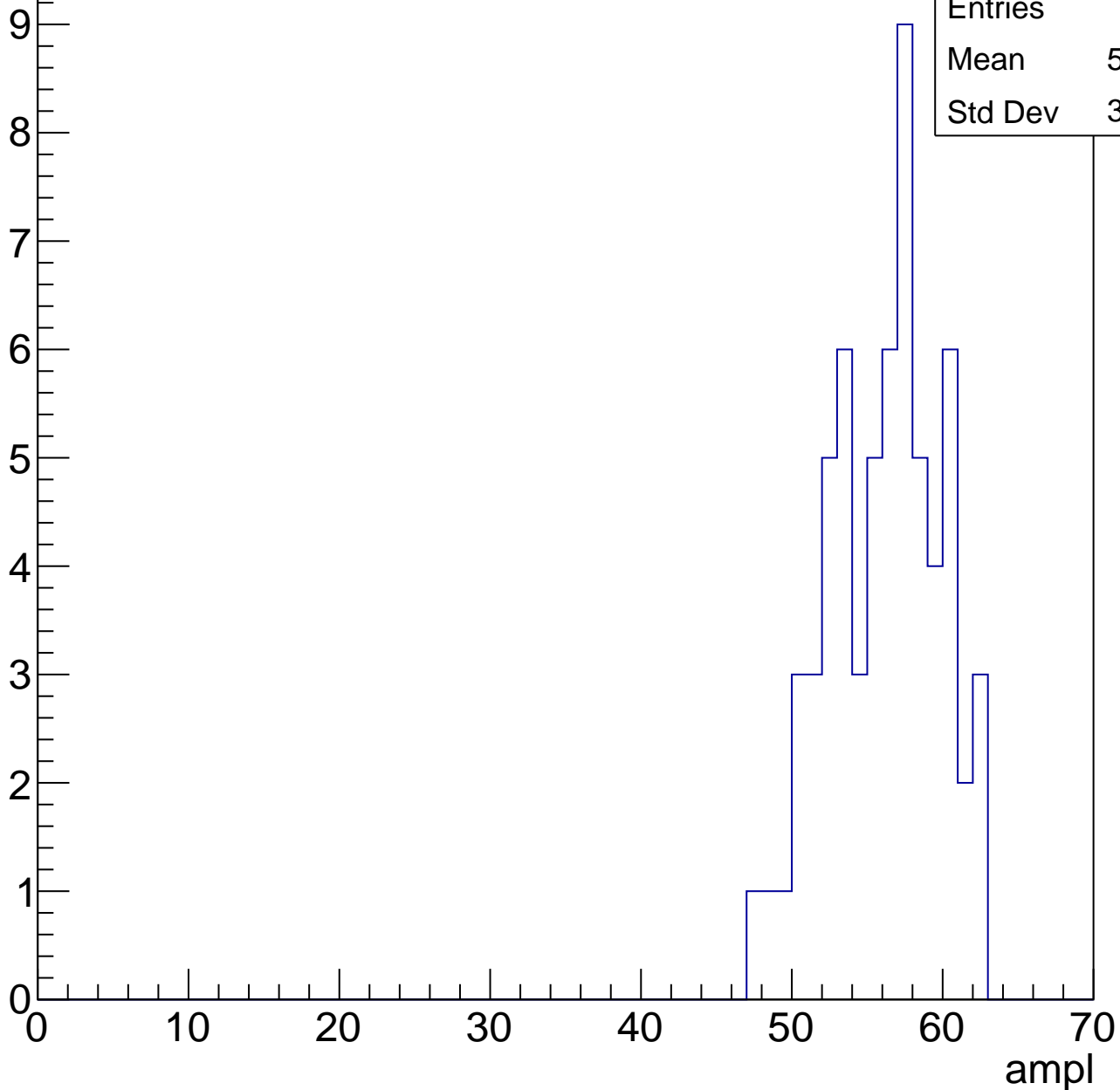


B0L102S, U5-ch39, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.63
Std Dev	3.648

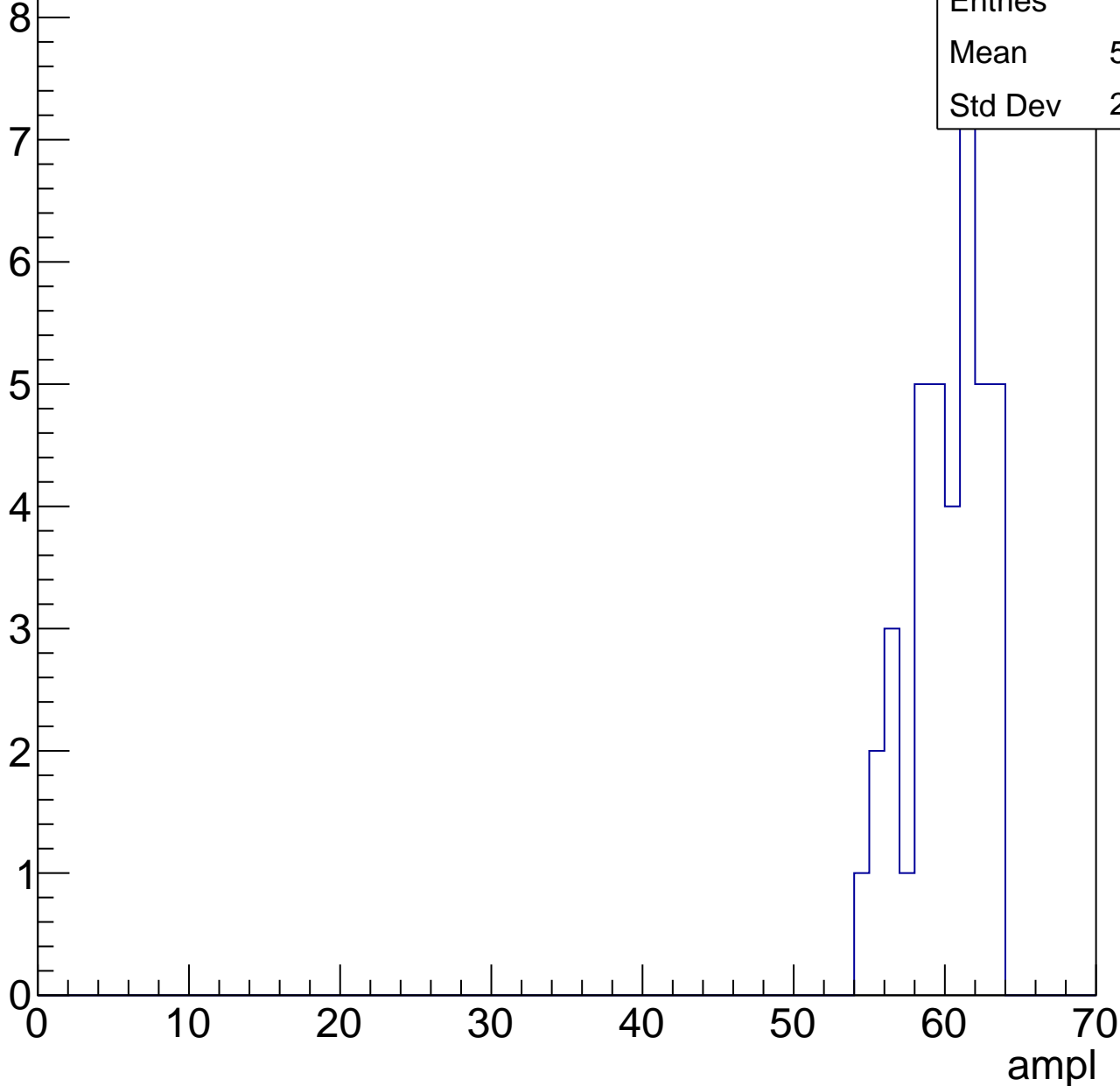


B0L102S, U5-ch39, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	59.67
Std Dev	2.463

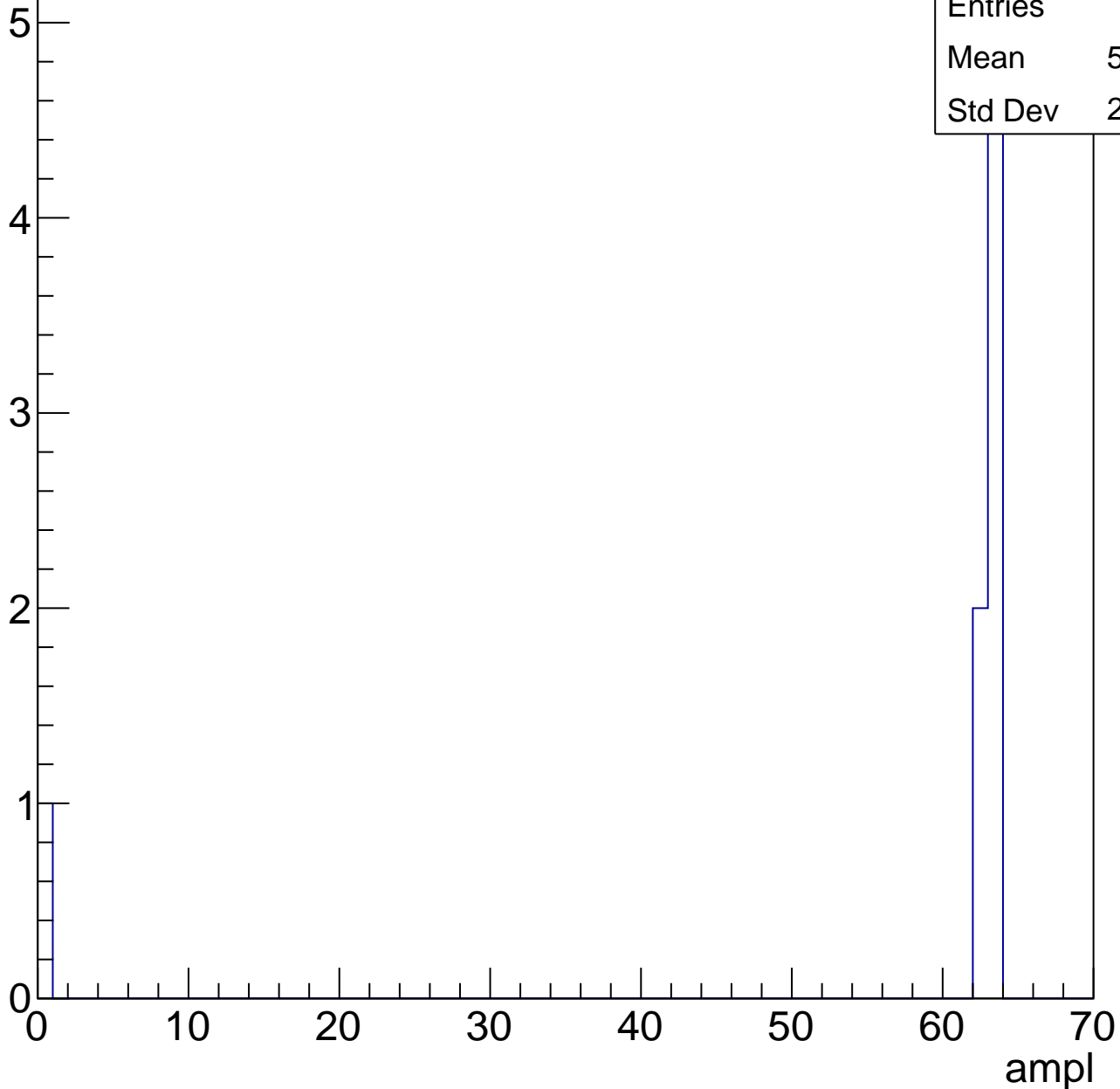


B0L102S, U5-ch39, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B0L102S, U5-ch39, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch40, adc0

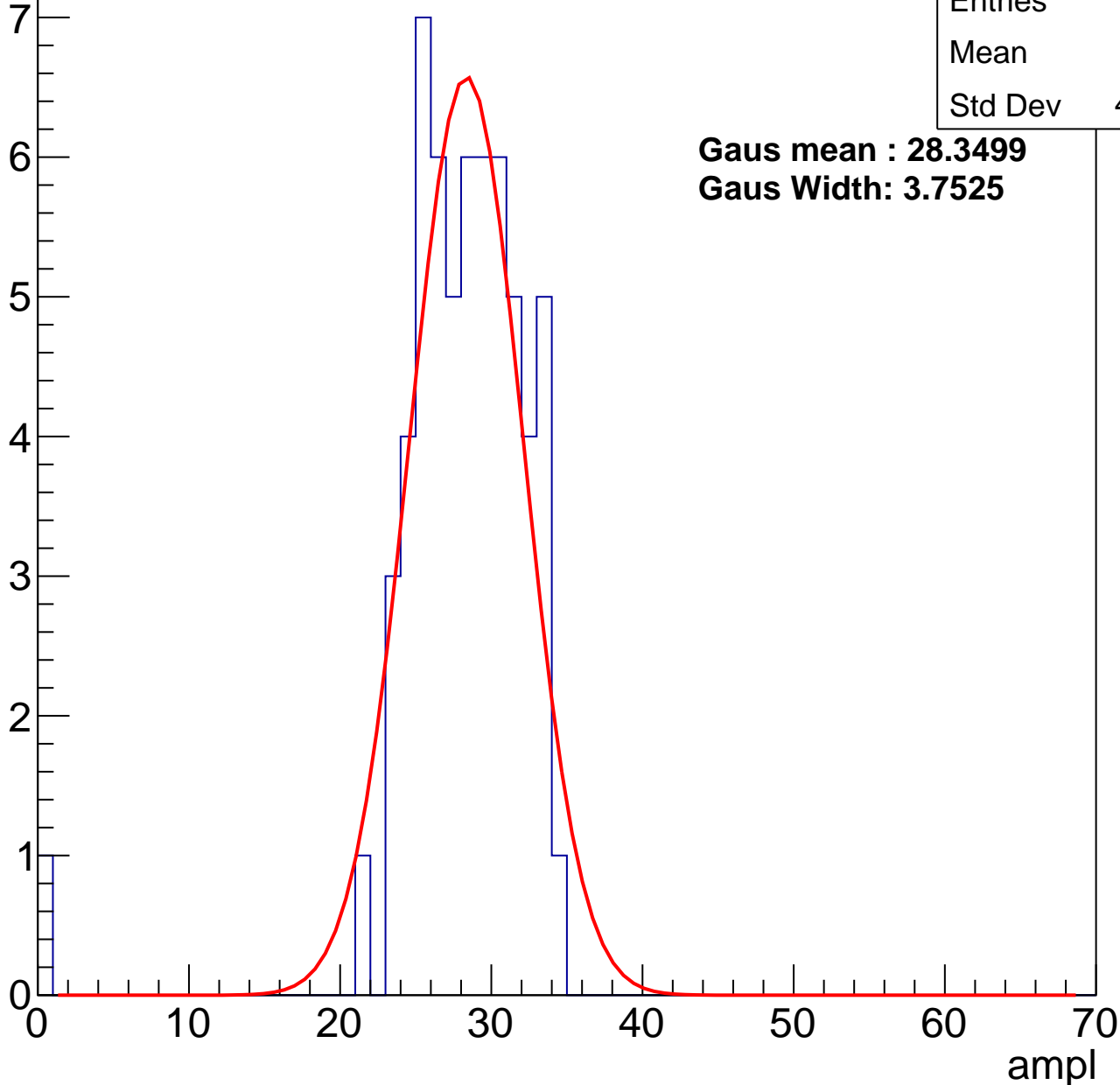
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	27.6
Std Dev	4.751

Gaus mean : 28.3499

Gaus Width: 3.7525



B0L102S, U5-ch40, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	78
Mean	34.26
Std Dev	6.634

Gaus mean : 35.3387

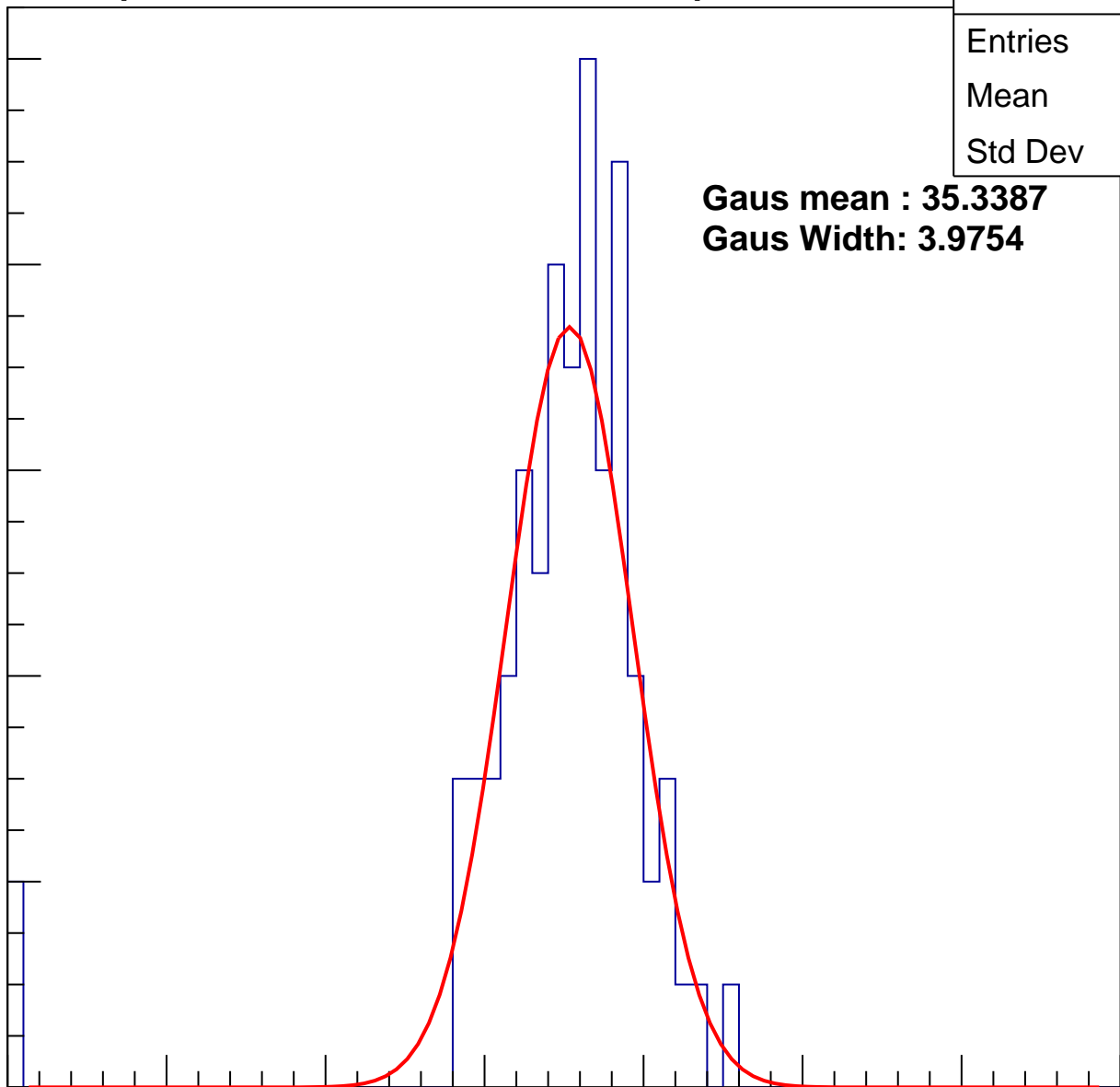
Gaus Width: 3.9754

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch40, adc2

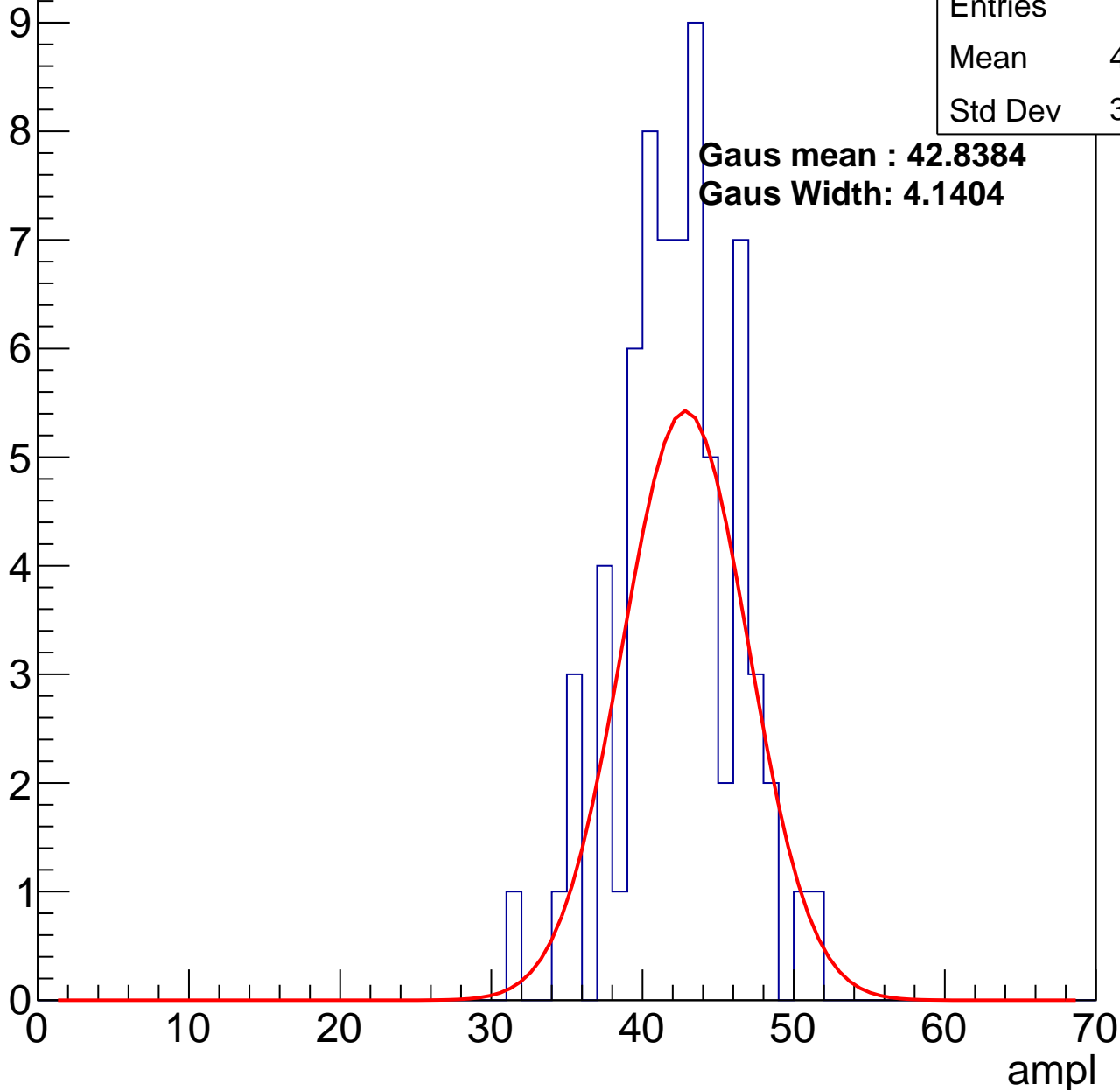
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.88
Std Dev	3.818

Gaus mean : 42.8384

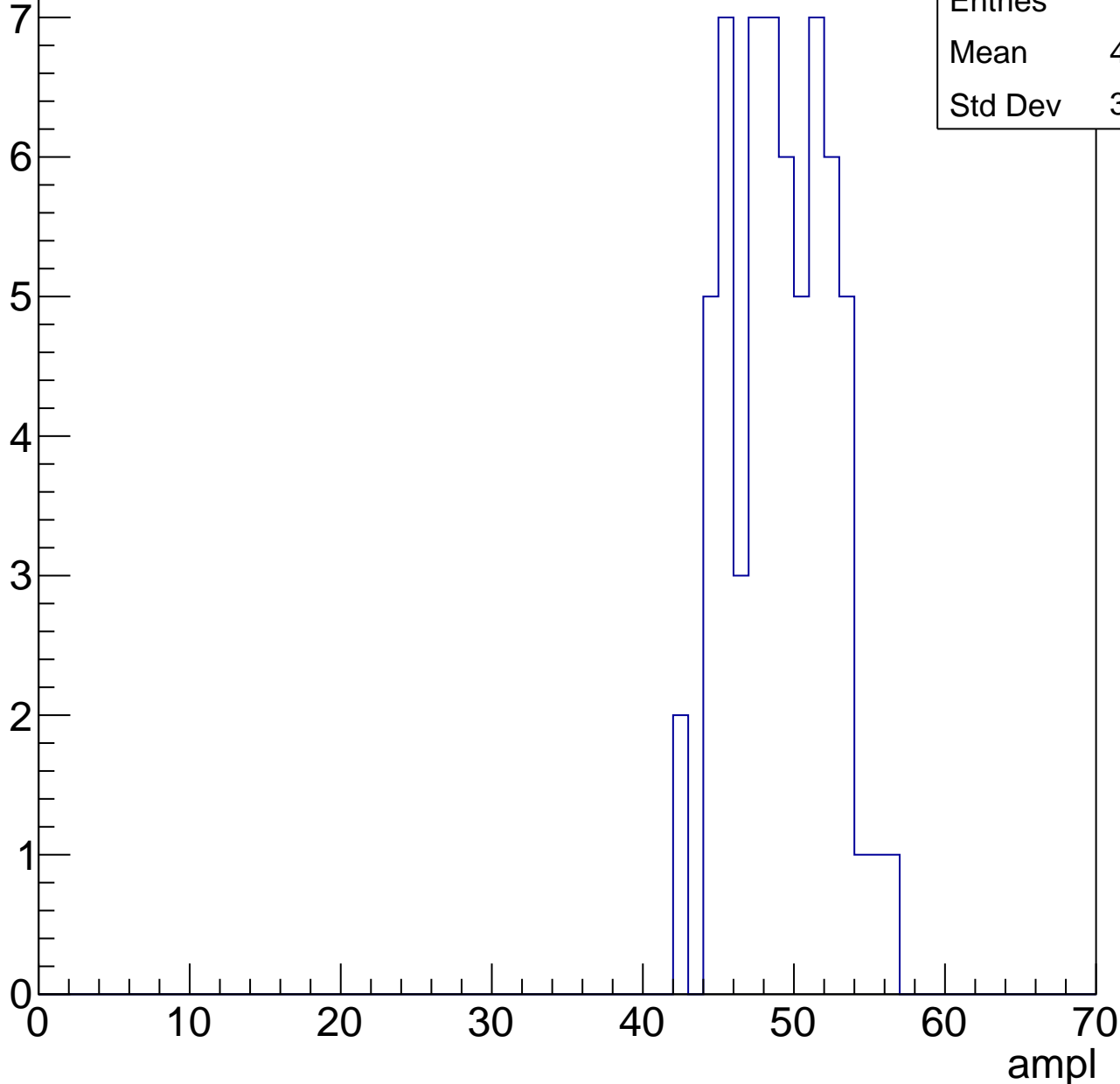
Gaus Width: 4.1404



B0L102S, U5-ch40, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



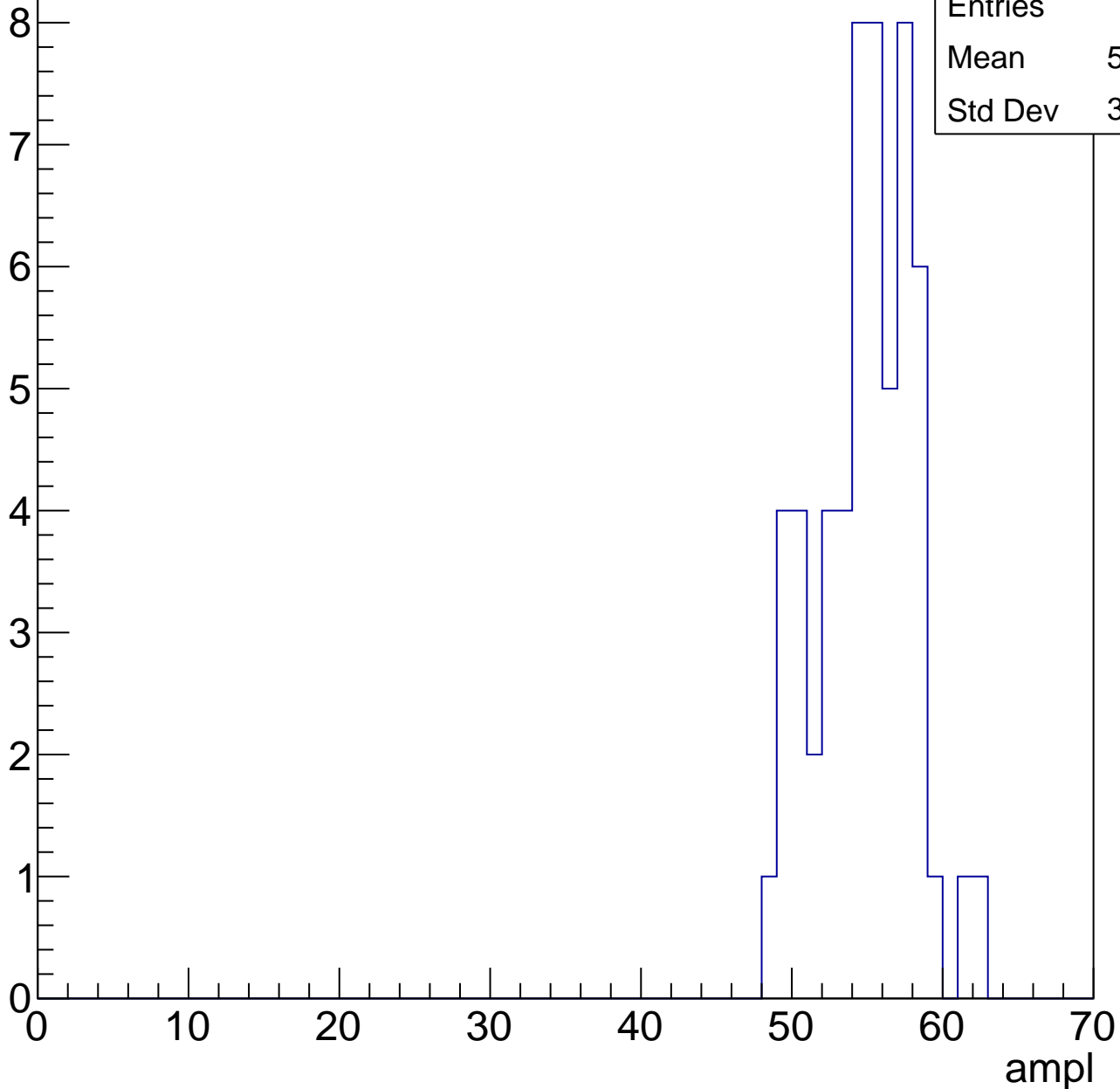
Entries	63
Mean	48.65
Std Dev	3.252

B0L102S, U5-ch40, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	54.46
Std Dev	3.135



B0L102S, U5-ch40, adc5

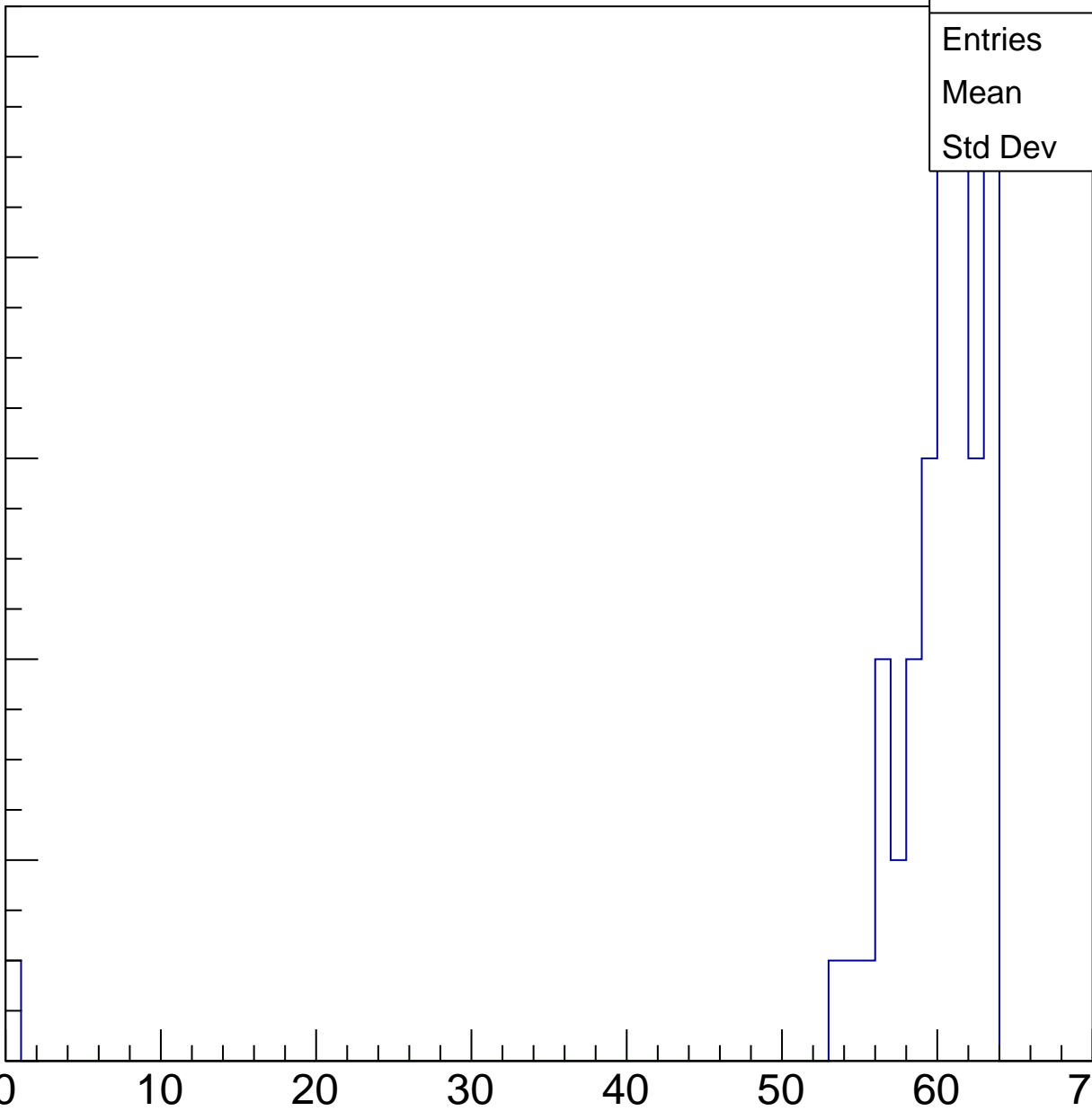
calib_packv5_042523_0143.root, FC#12, port B1

Entry

10
8
6
4
2
0

Entries	54
Mean	58.78
Std Dev	8.441

ampl



B0L102S, U5-ch40, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

7

Mean

60.86

Std Dev

1.641

B0L102S, U5-ch40, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch41, adc0

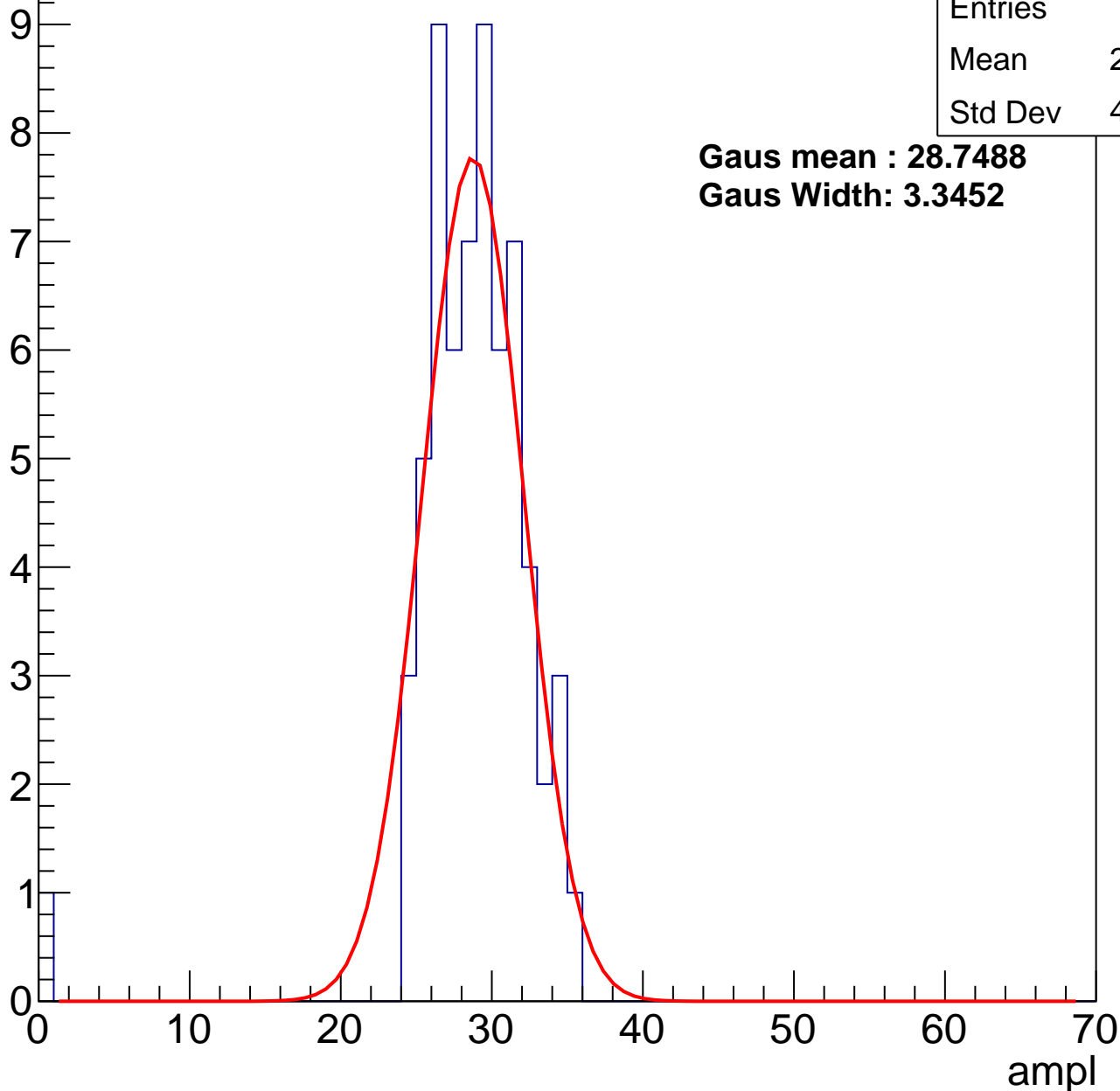
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.22
Std Dev	4.513

Gaus mean : 28.7488

Gaus Width: 3.3452



B0L102S, U5-ch41, adc1

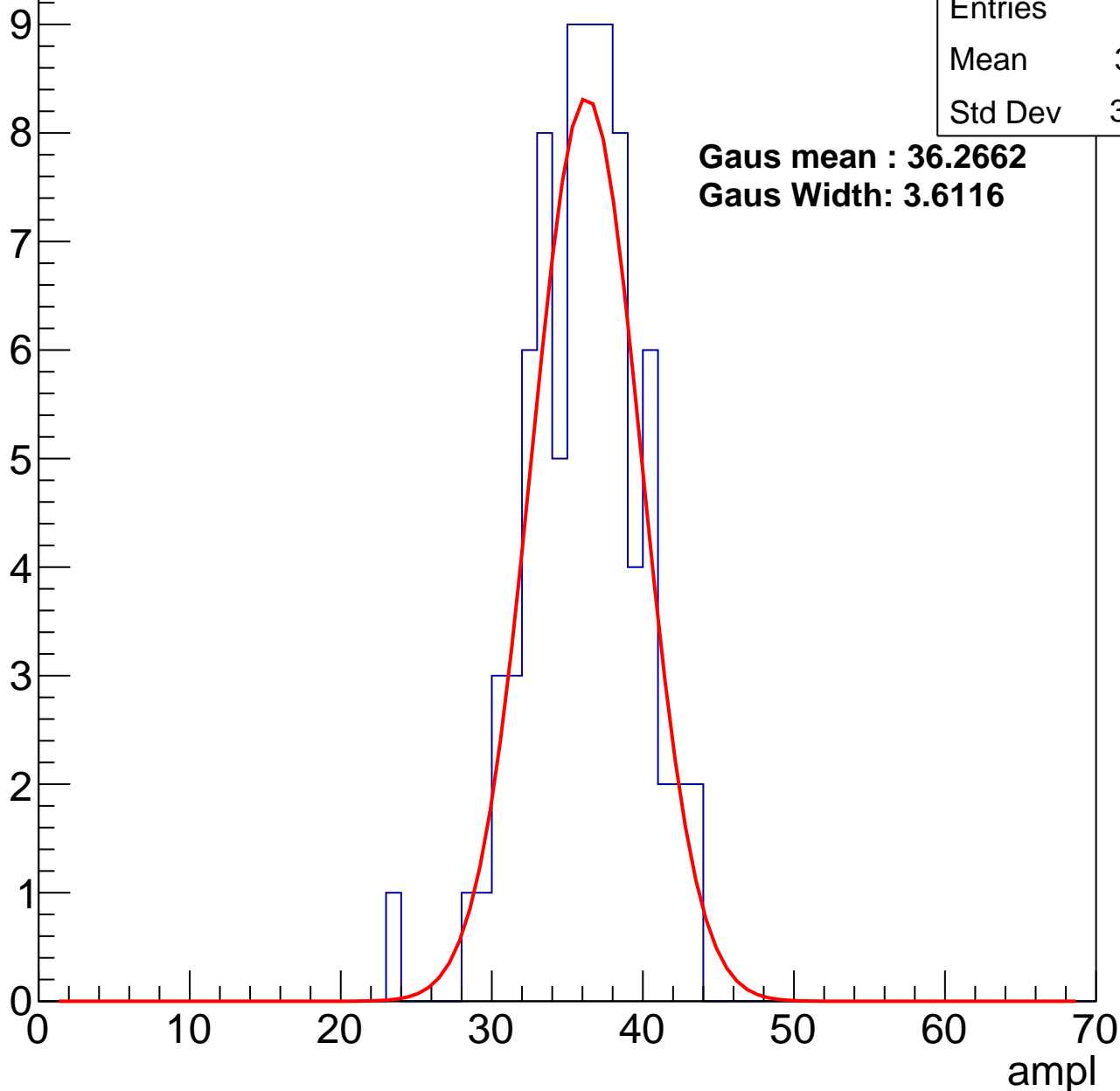
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.61
Std Dev	3.633

Gaus mean : 36.2662

Gaus Width: 3.6116



B0L102S, U5-ch41, adc2

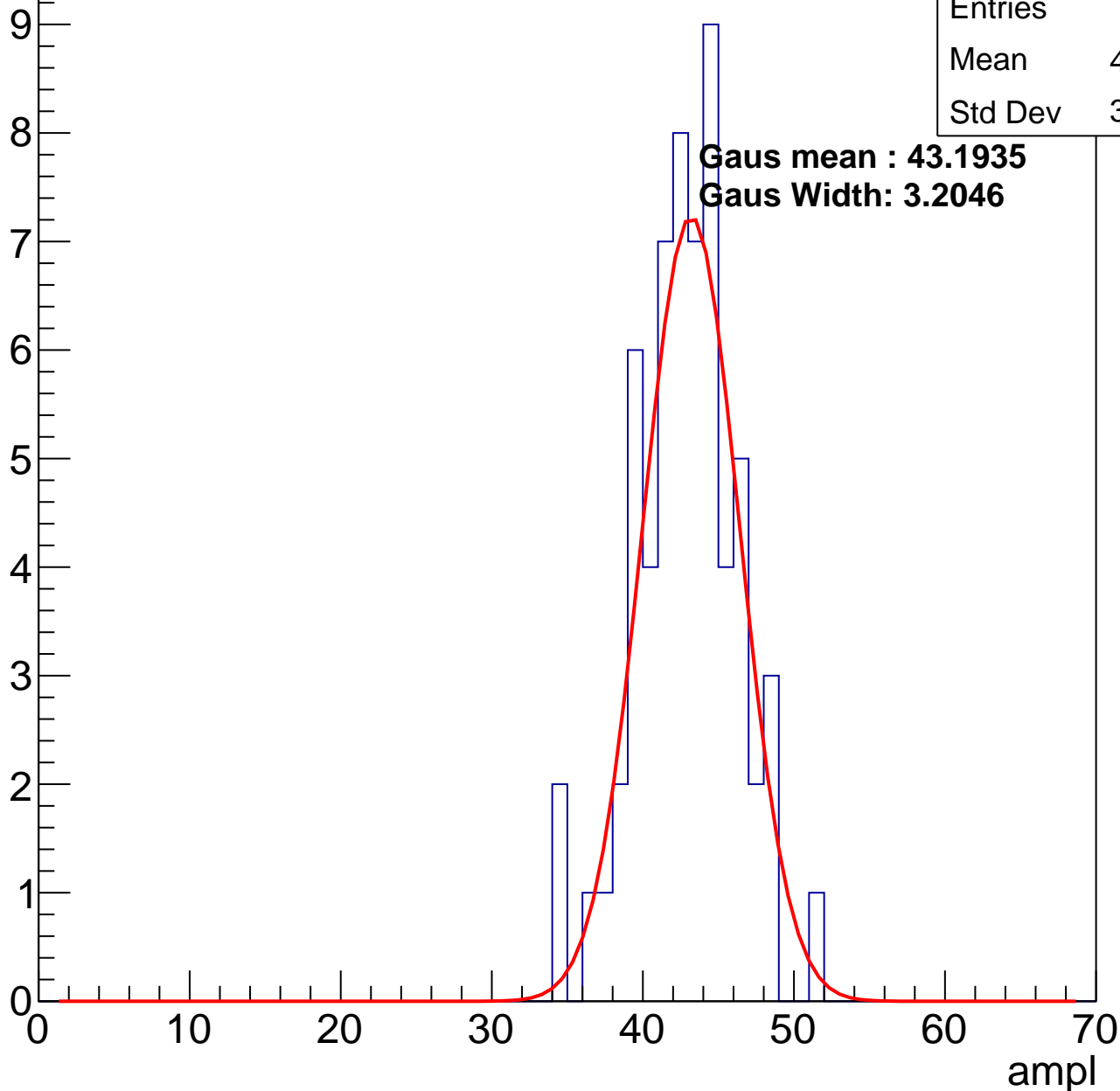
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.42
Std Dev	3.324

Gaus mean : 43.1935

Gaus Width: 3.2046

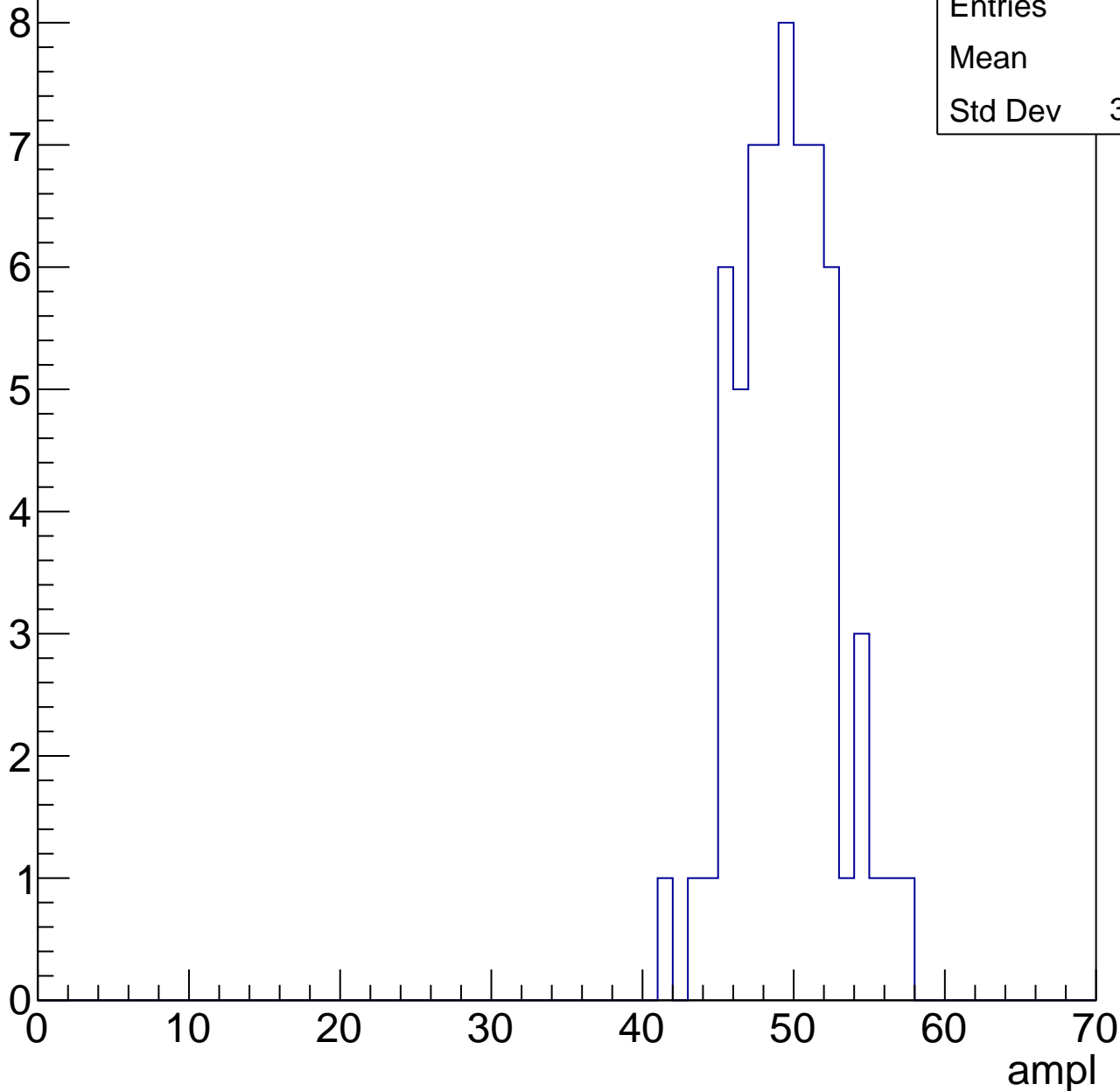


B0L102S, U5-ch41, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49
Std Dev	3.162

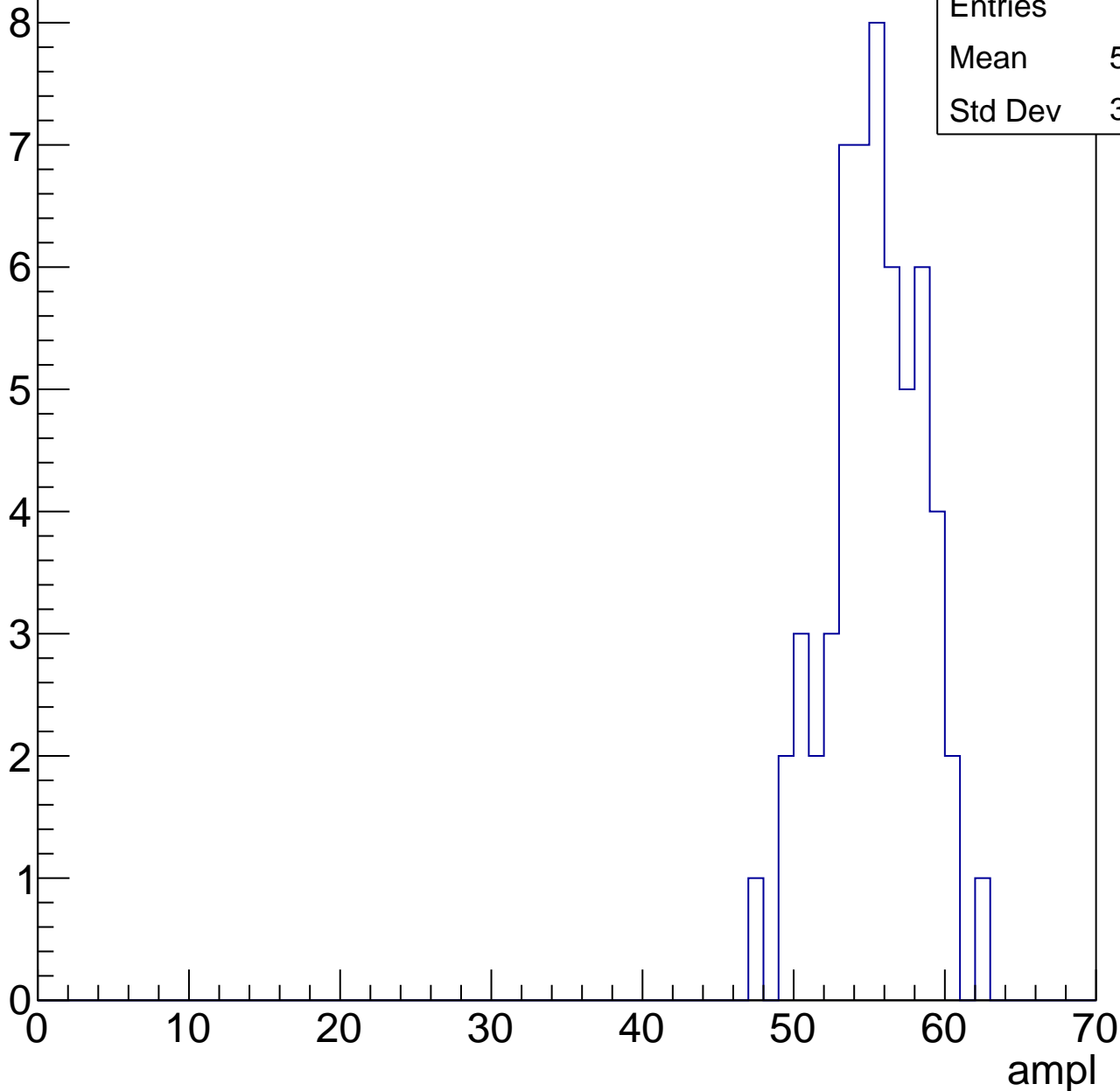


B0L102S, U5-ch41, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	54.89
Std Dev	3.099

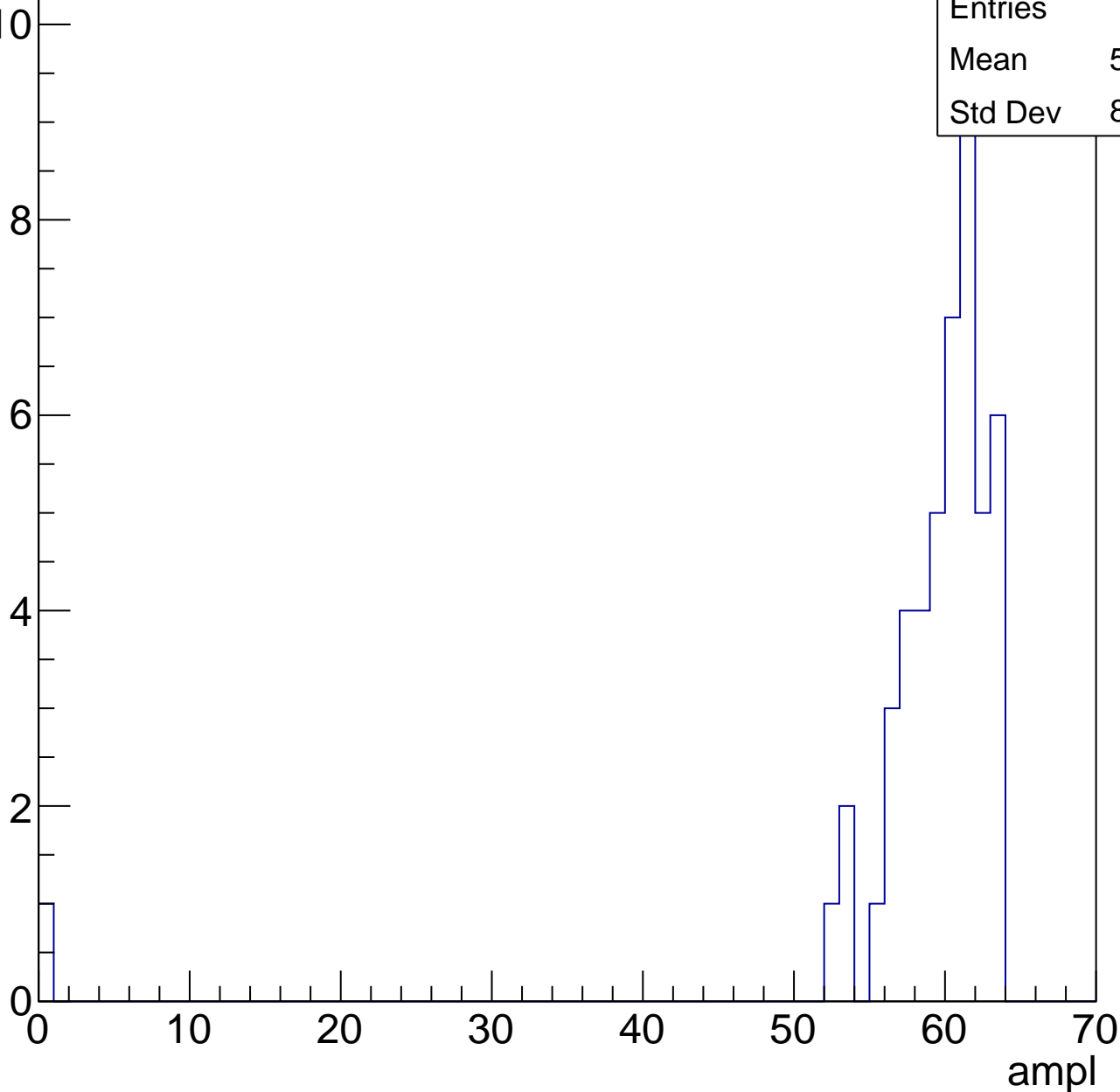


B0L102S, U5-ch41, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

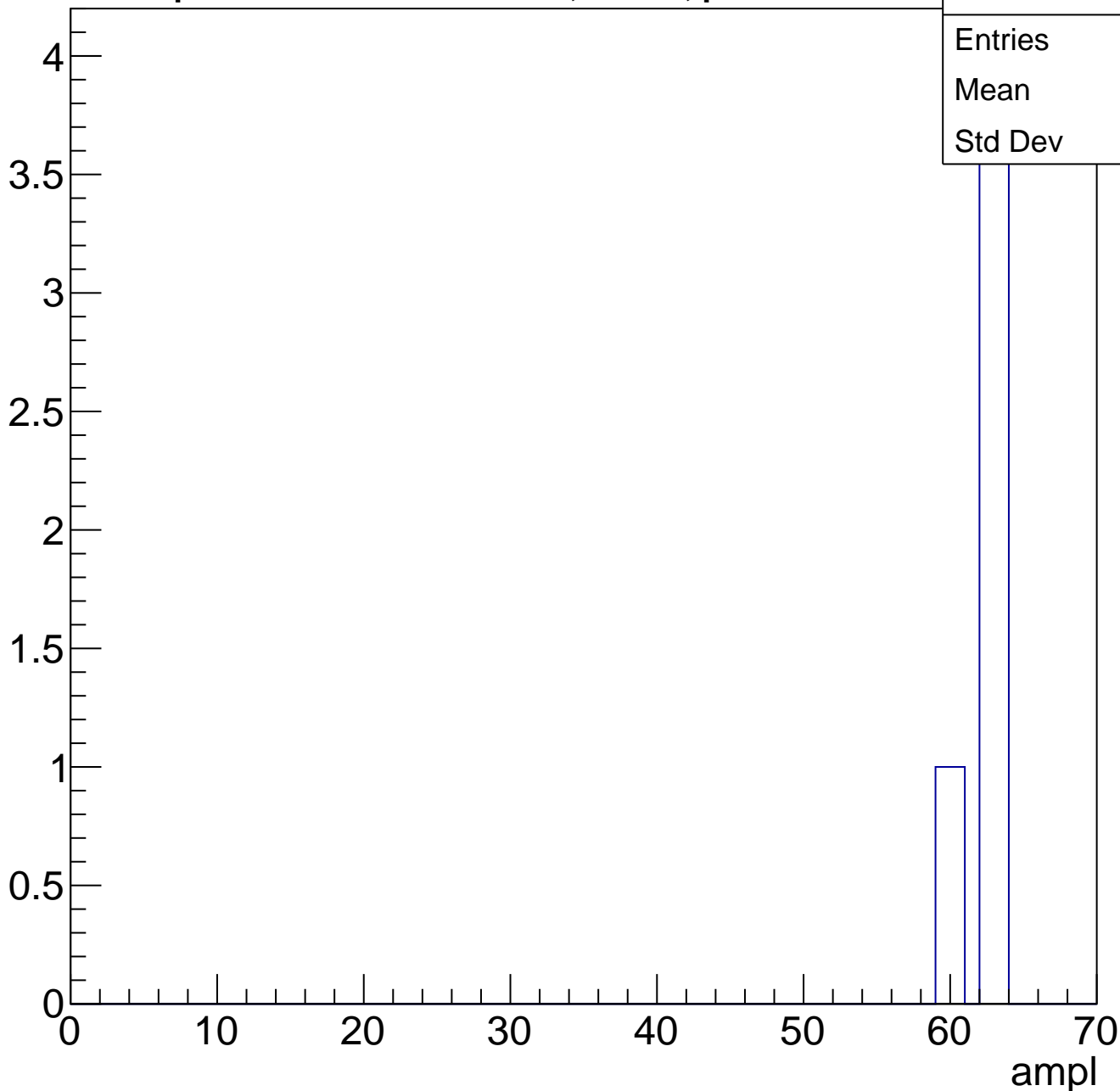
Entries	49
Mean	58.24
Std Dev	8.835



B0L102S, U5-ch41, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch41, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch42, adc0

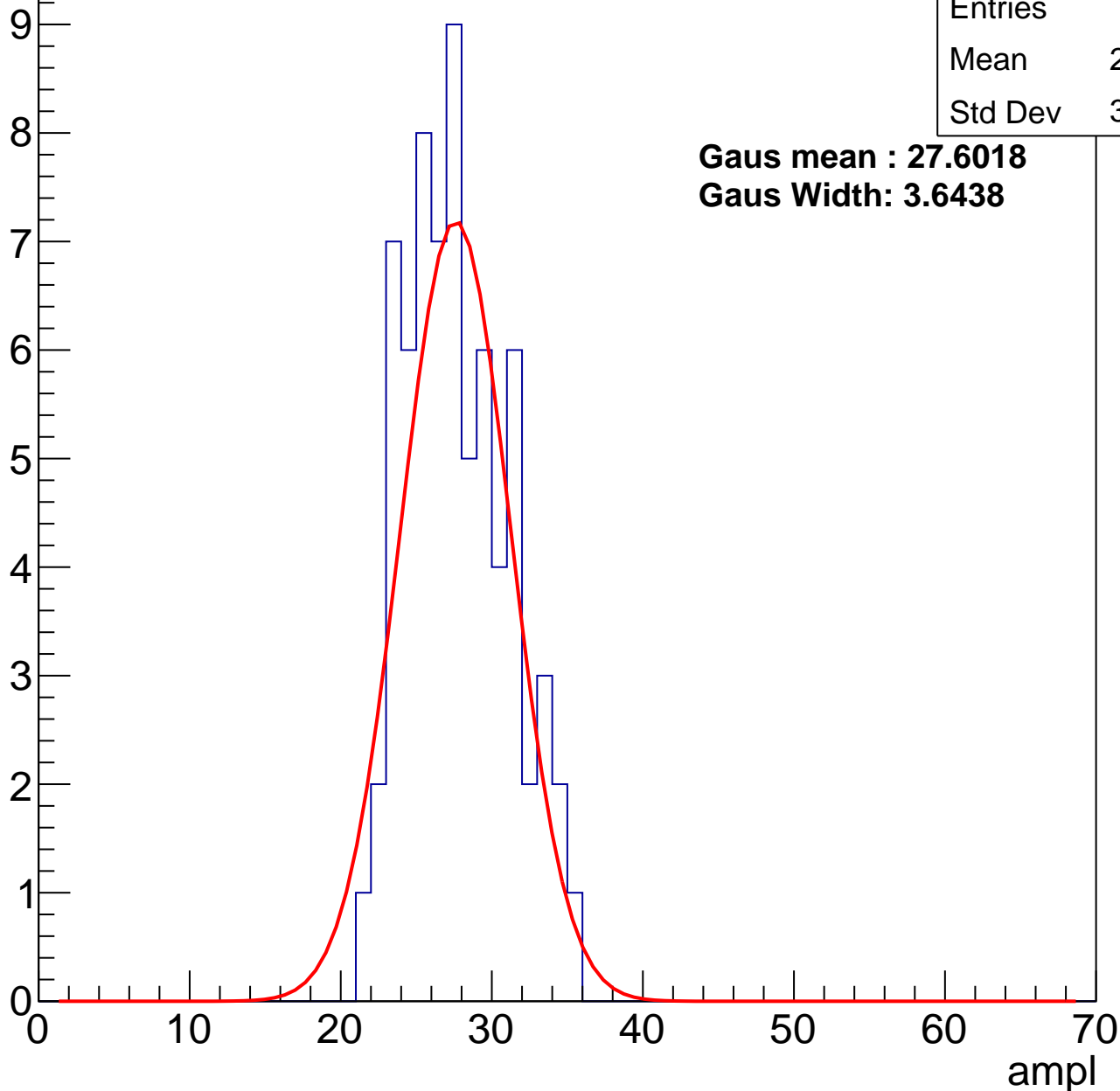
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	27.26
Std Dev	3.335

Gaus mean : 27.6018

Gaus Width: 3.6438



B0L102S, U5-ch42, adc1

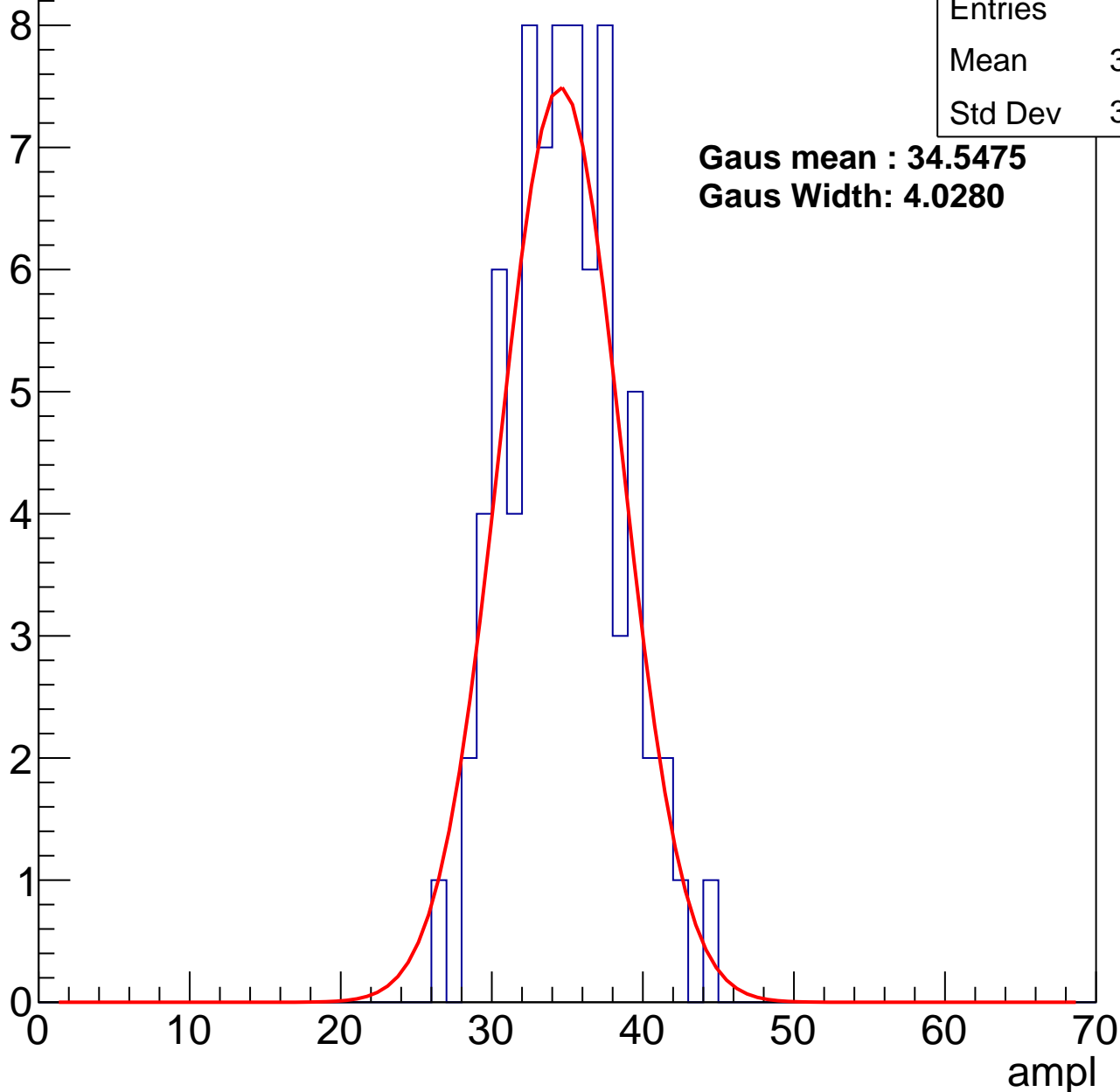
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	34.34
Std Dev	3.644

Gaus mean : 34.5475

Gaus Width: 4.0280



B0L102S, U5-ch42, adc2

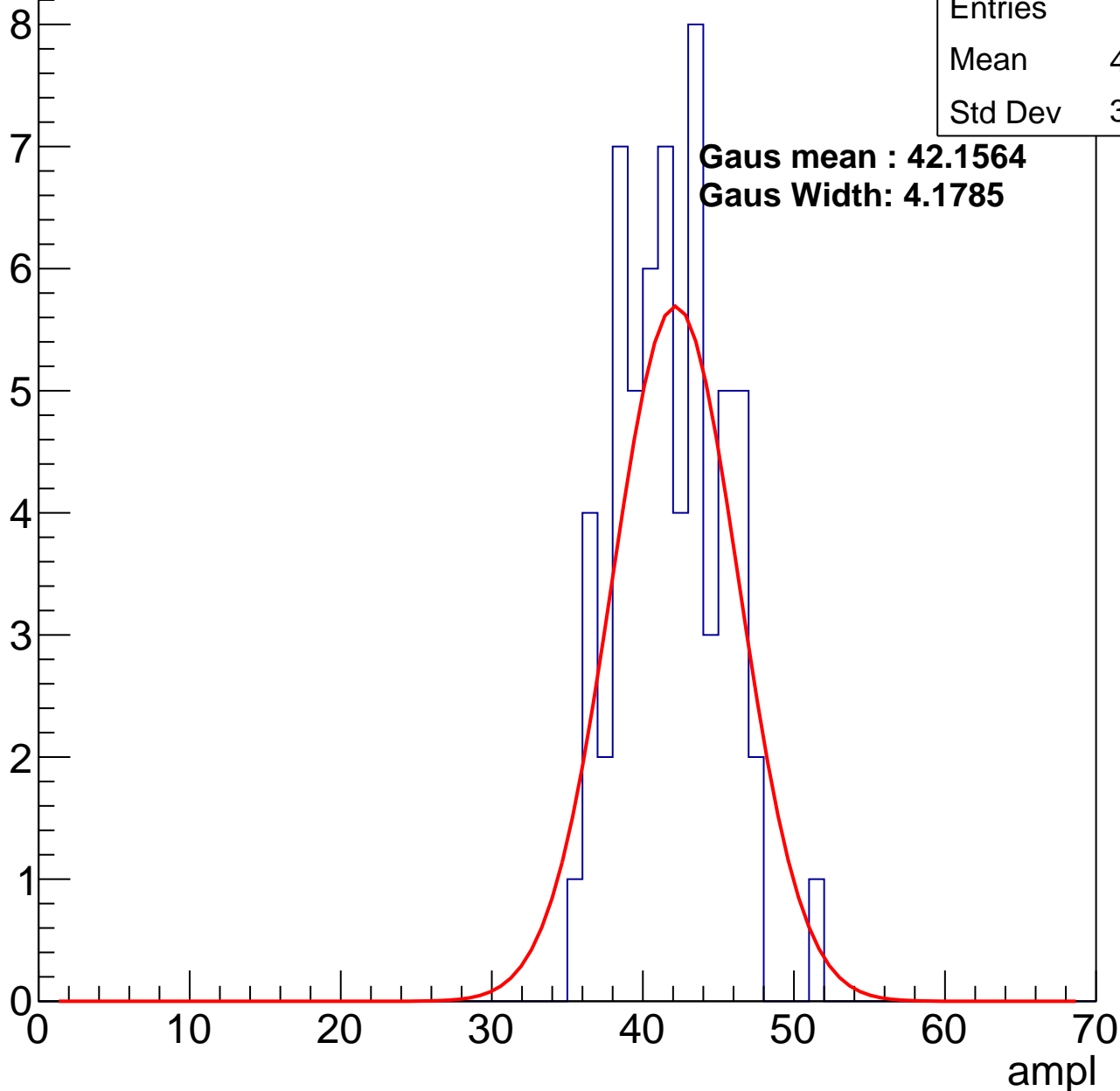
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.42
Std Dev	3.383

Gaus mean : 42.1564

Gaus Width: 4.1785

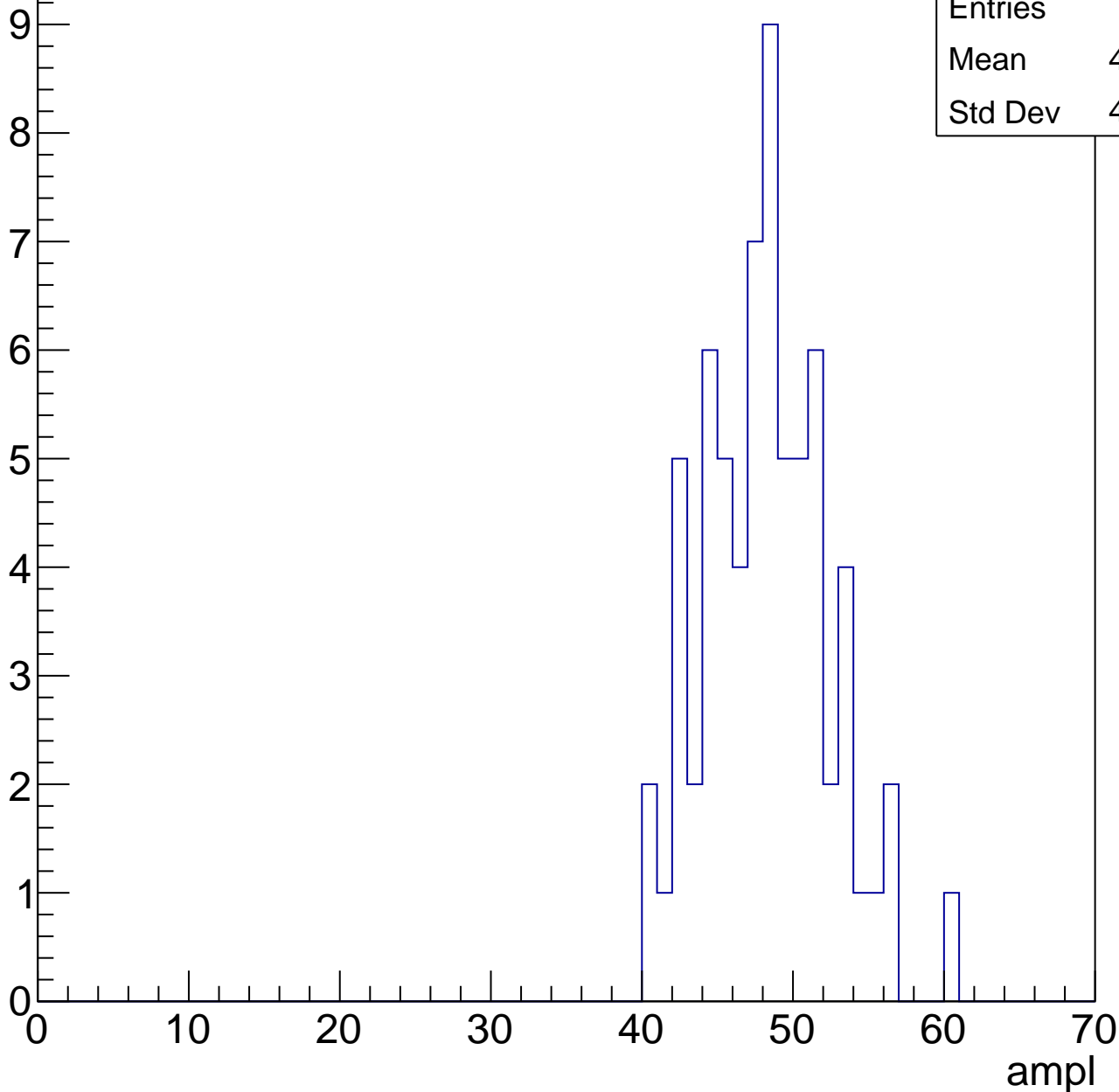


B0L102S, U5-ch42, adc3

calib_packv5_042523_0143.root, FC#12, port B1

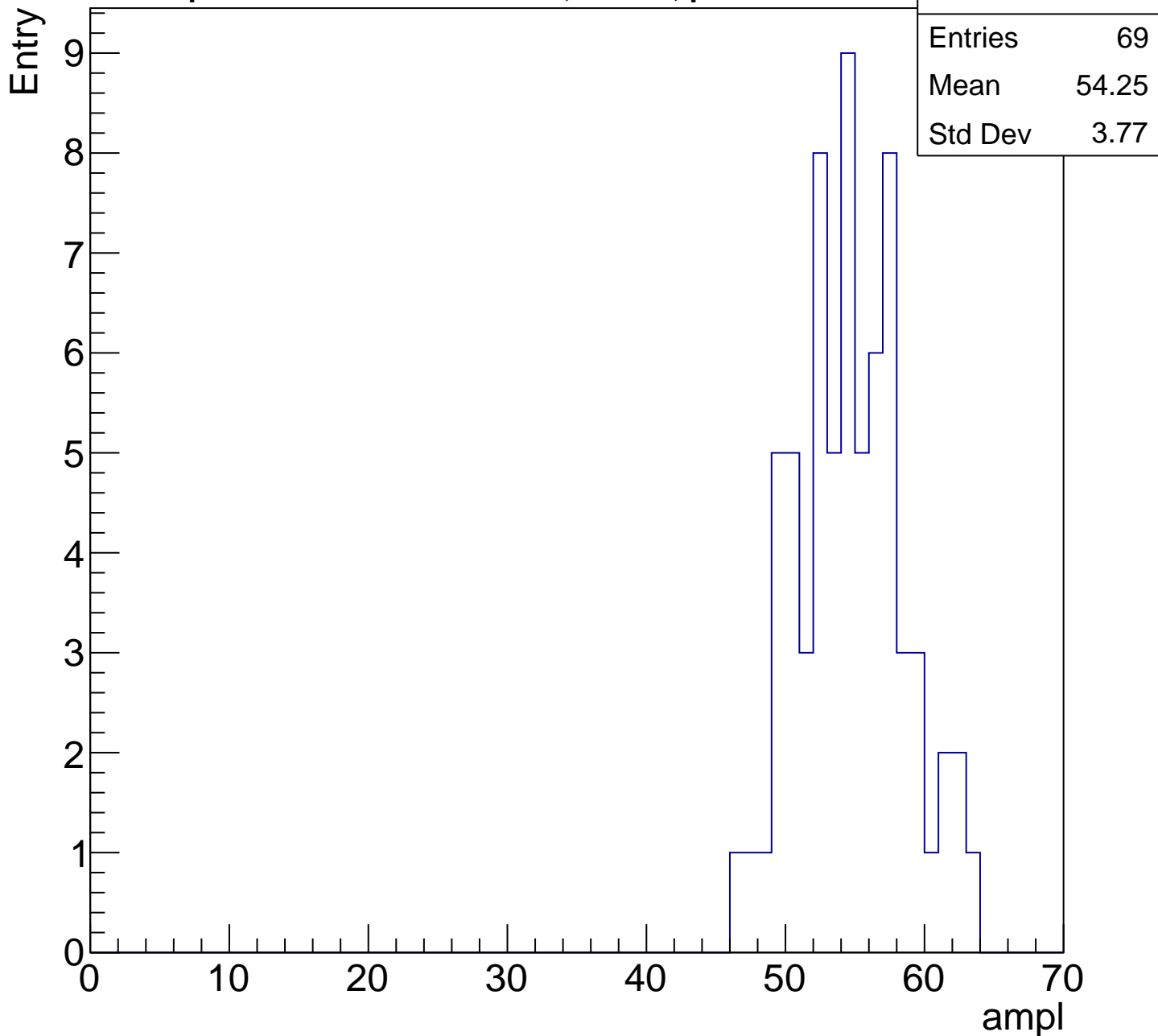
Entry

Entries	68
Mean	47.78
Std Dev	4.087



B0L102S, U5-ch42, adc4

calib_packv5_042523_0143.root, FC#12, port B1

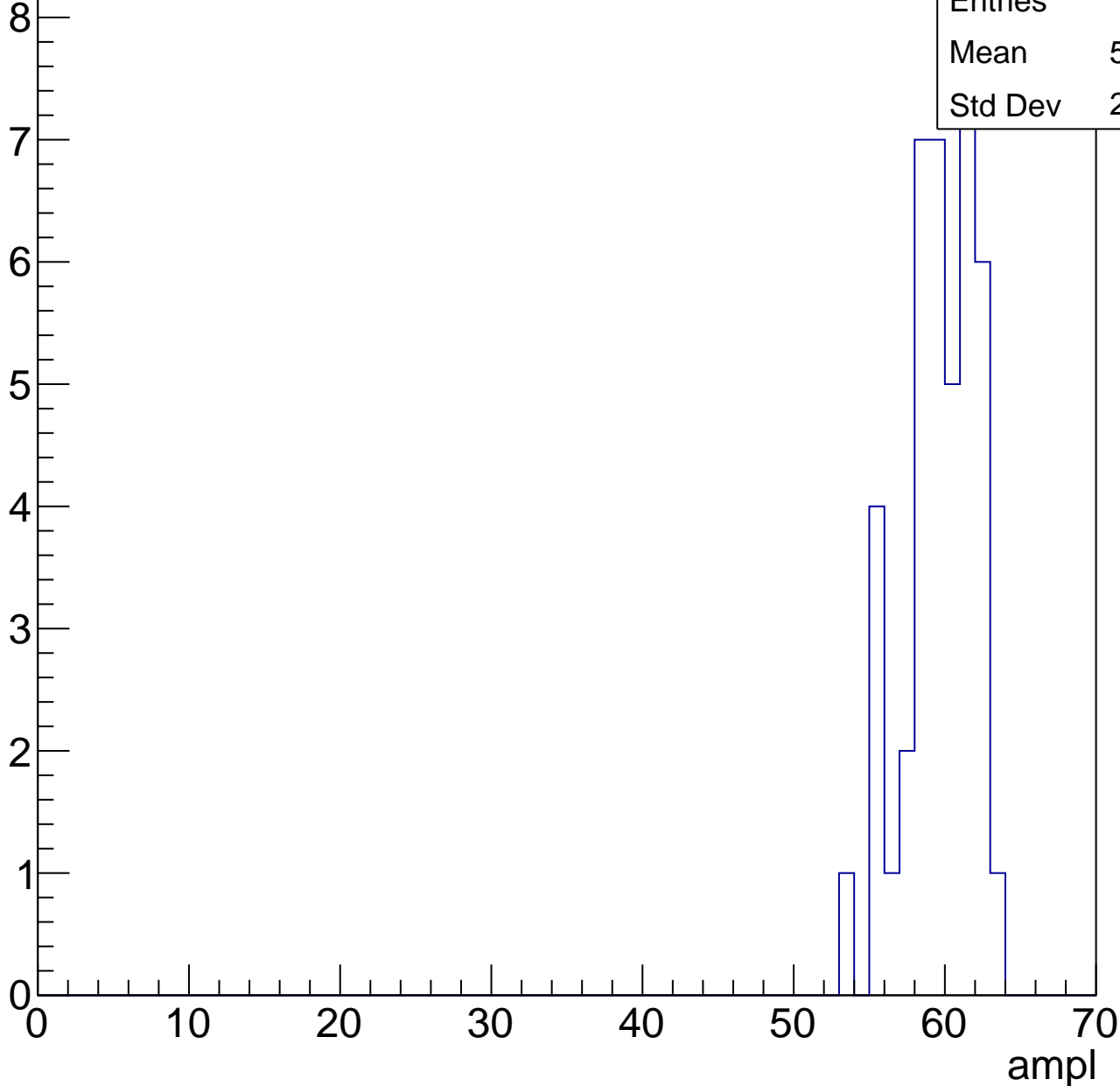


B0L102S, U5-ch42, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	59.17
Std Dev	2.339

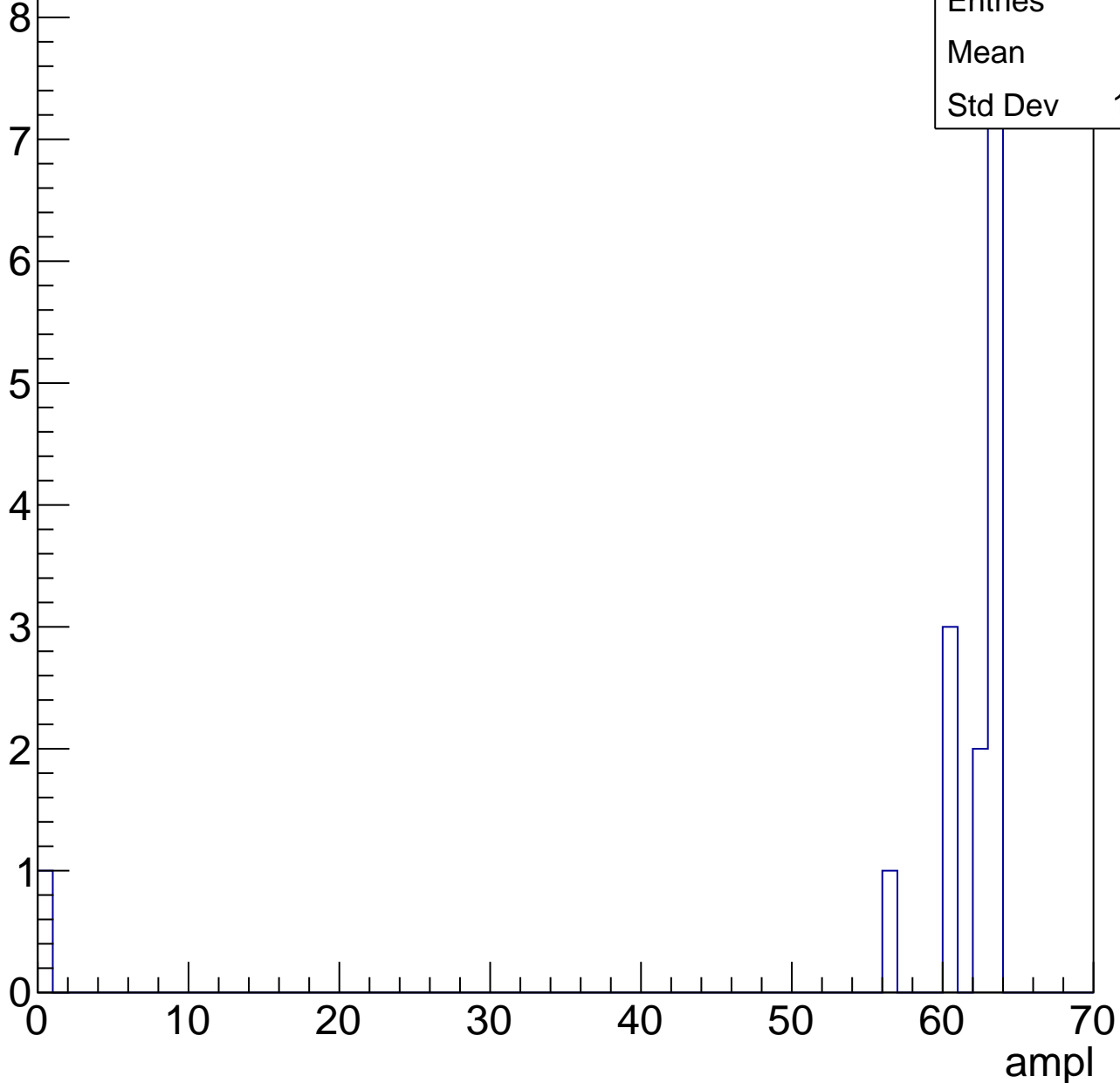


B0L102S, U5-ch42, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

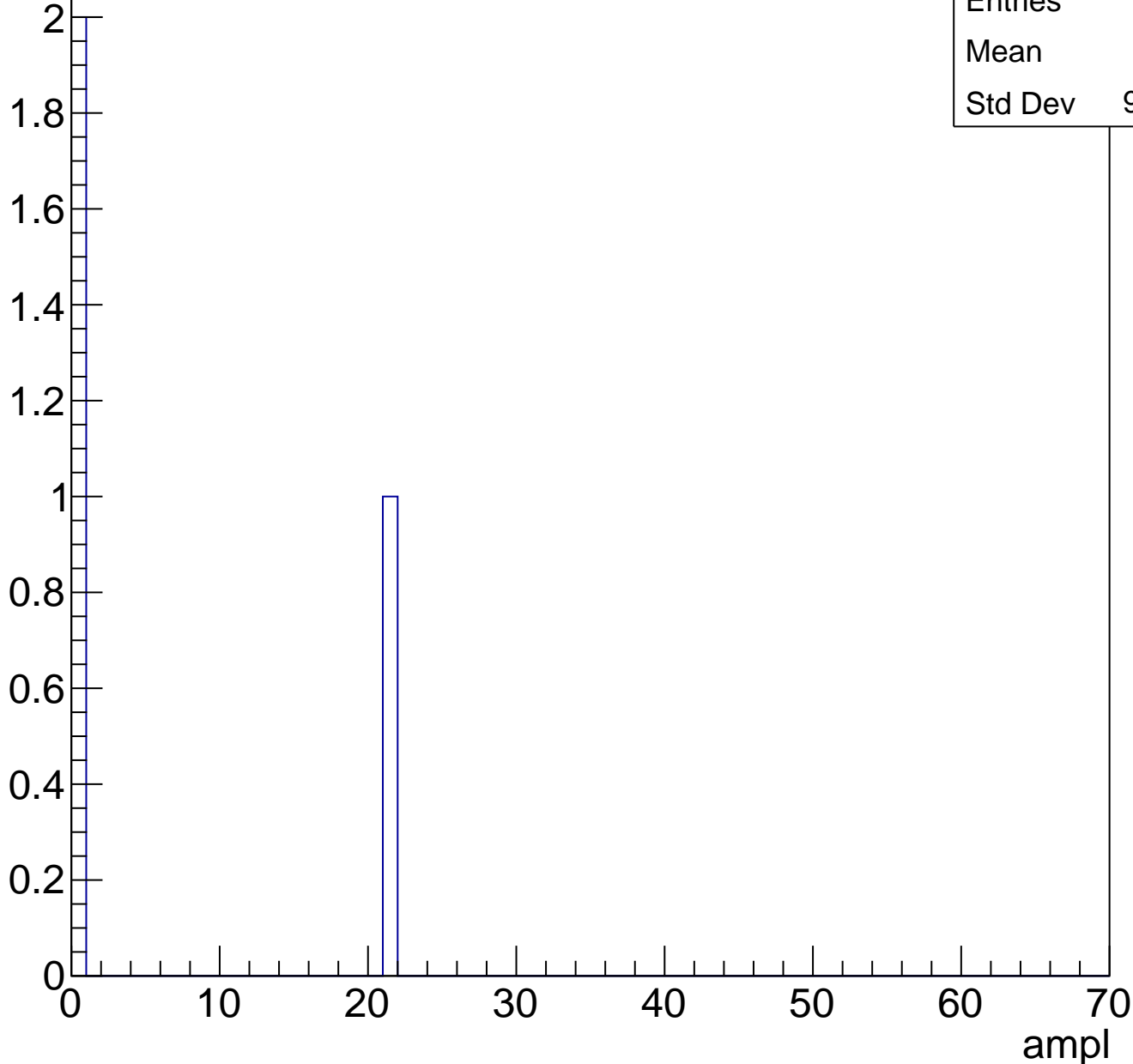
Entries	15
Mean	57.6
Std Dev	15.51



B0L102S, U5-ch42, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch43, adc0

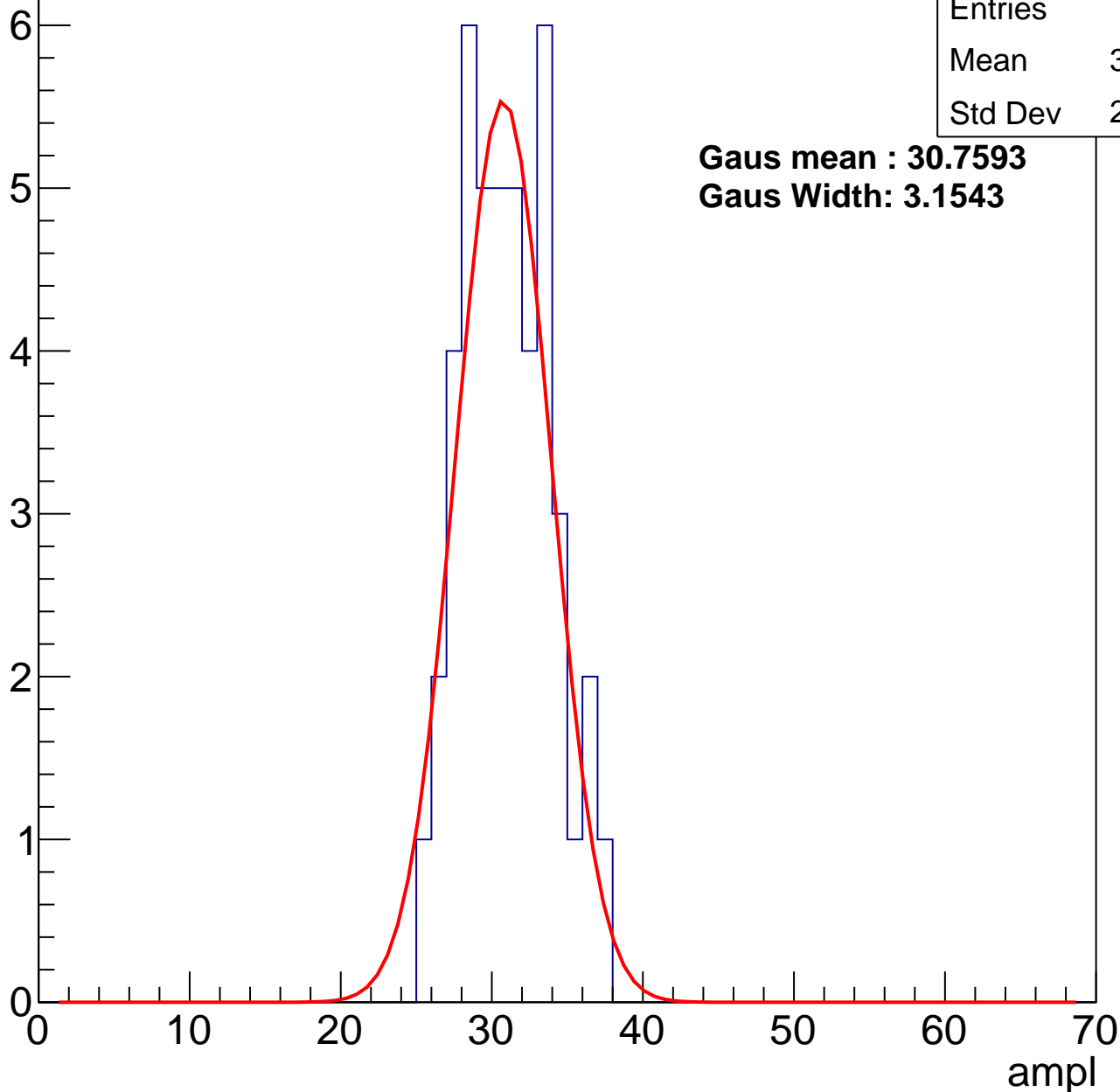
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	45
Mean	30.56
Std Dev	2.895

Gaus mean : 30.7593

Gaus Width: 3.1543



B0L102S, U5-ch43, adc1

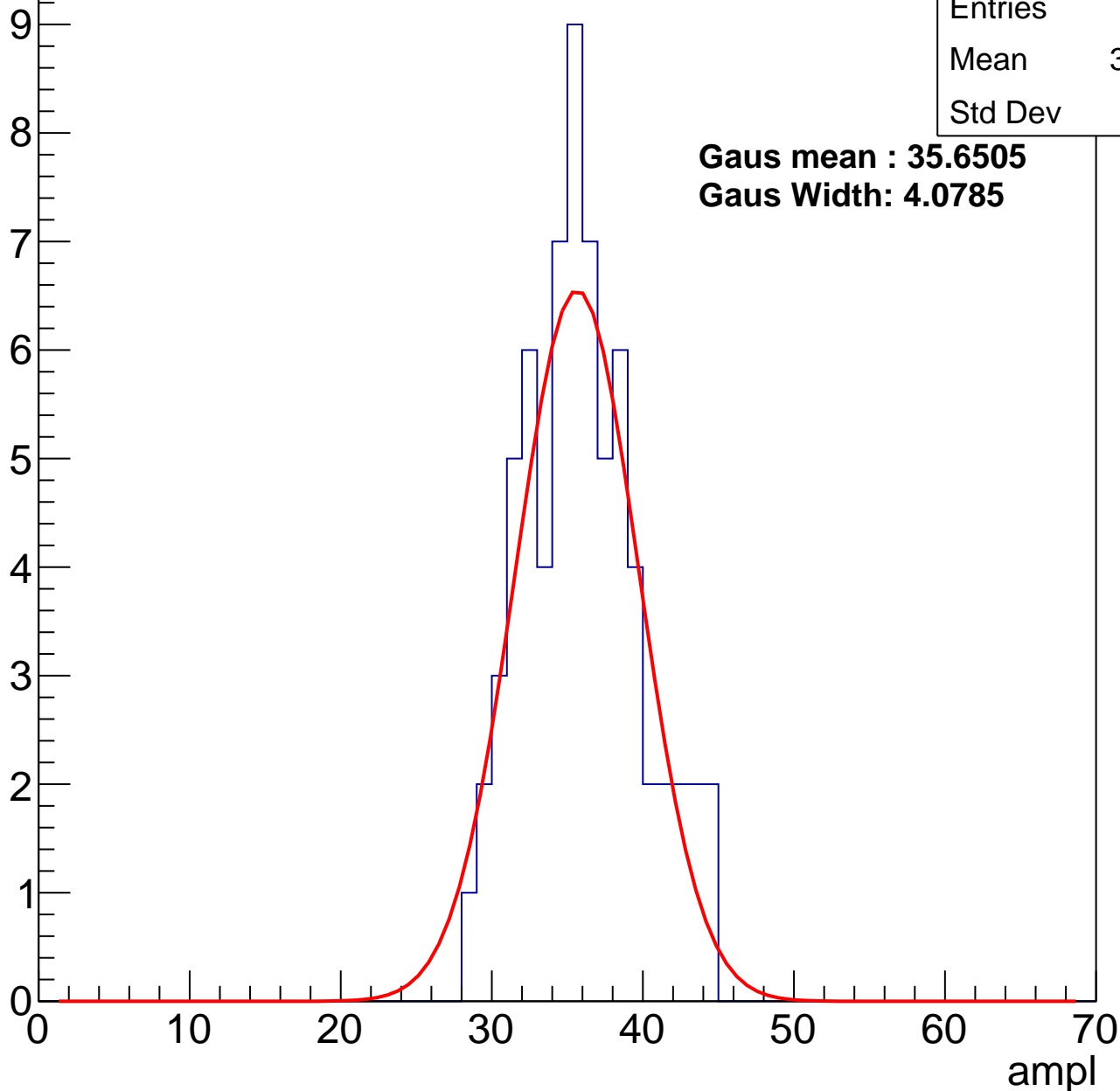
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	35.49
Std Dev	3.79

Gaus mean : 35.6505

Gaus Width: 4.0785



B0L102S, U5-ch43, adc2

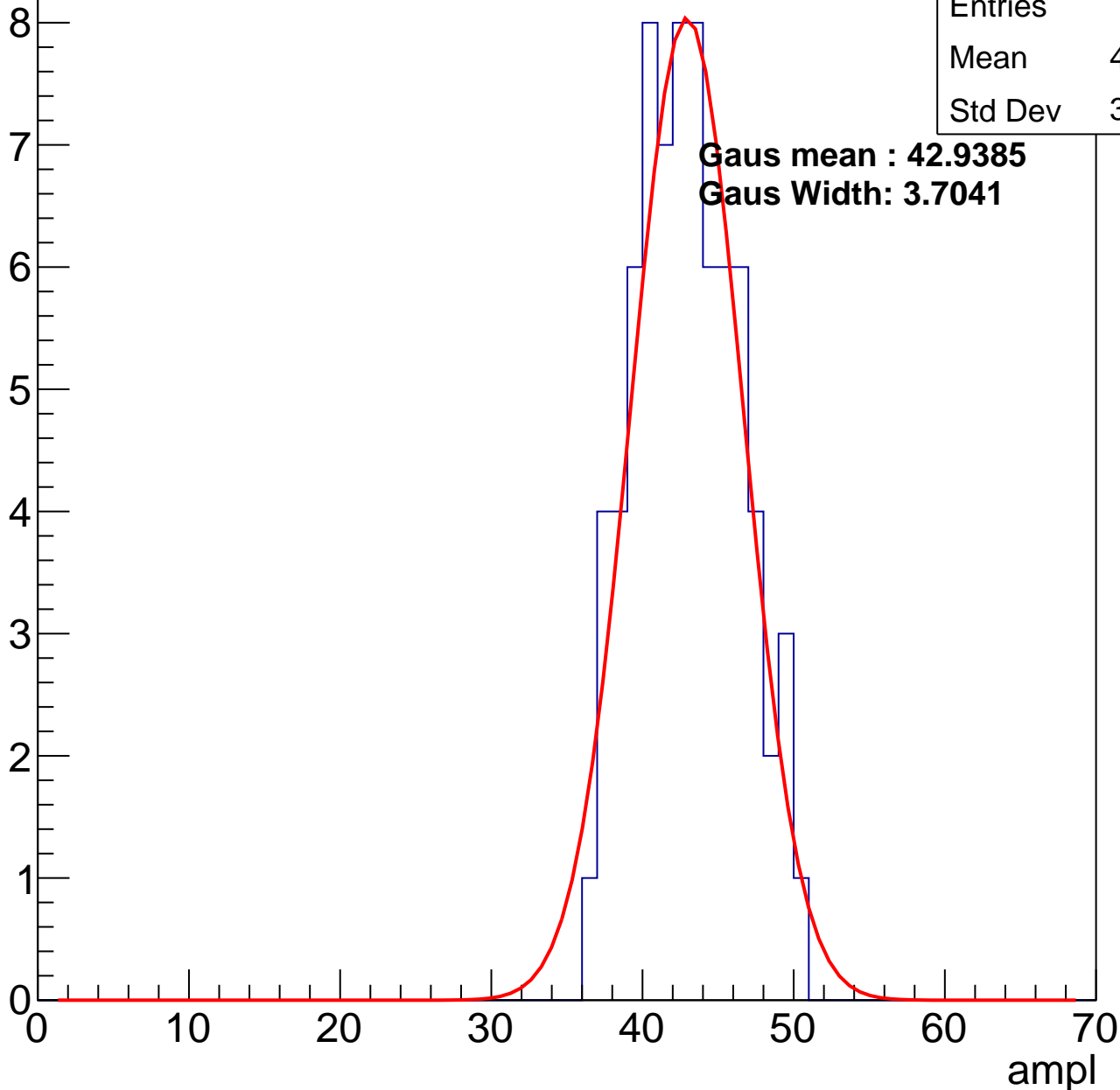
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	42.54
Std Dev	3.362

Gaus mean : 42.9385

Gaus Width: 3.7041

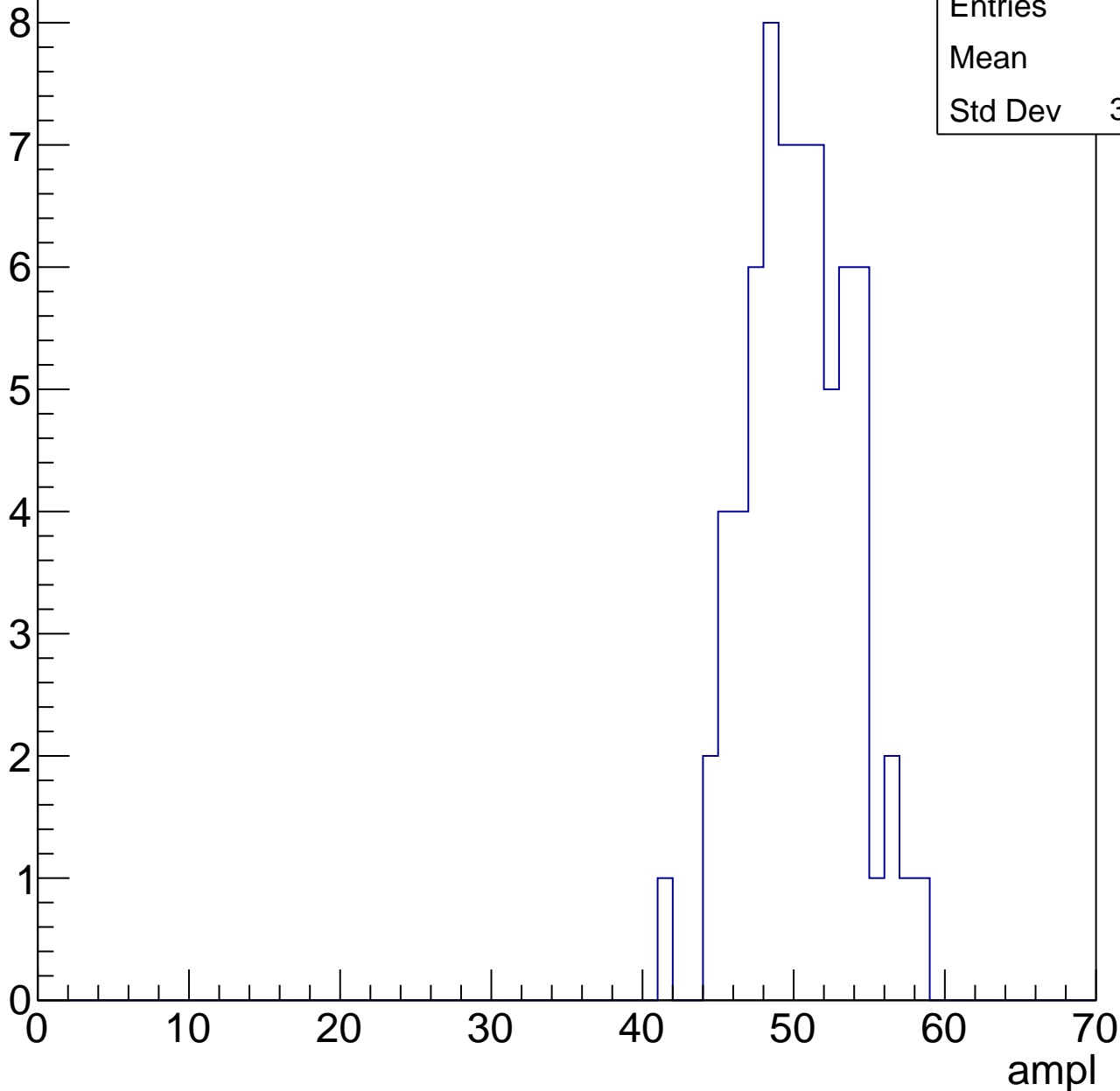


B0L102S, U5-ch43, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.9
Std Dev	3.422

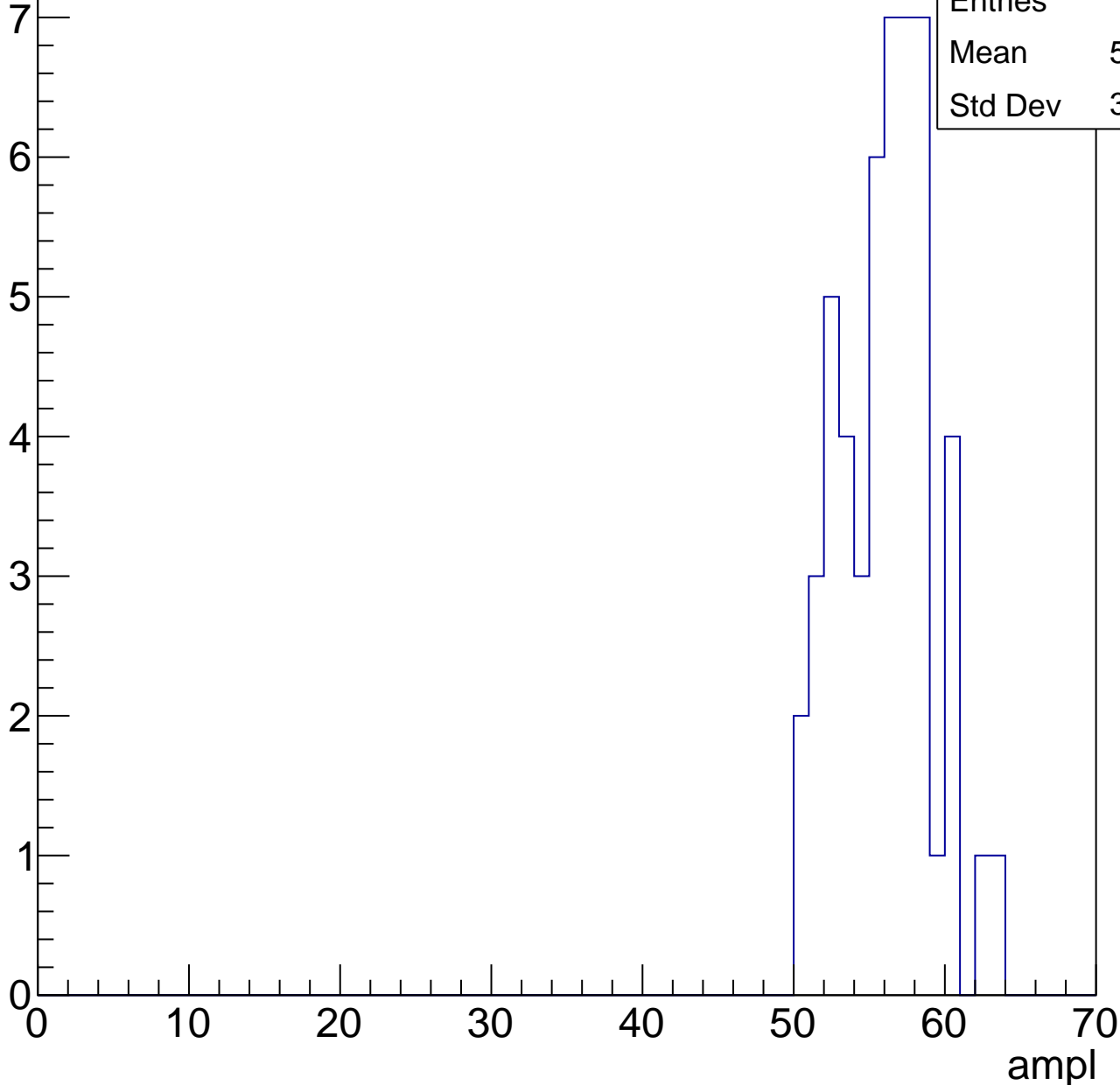


B0L102S, U5-ch43, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	55.65
Std Dev	3.028



B0L102S, U5-ch43, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	51
Mean	58.94
Std Dev	8.653

0

B0L102S, U5-ch43, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch43, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch44, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	27.87
Std Dev	5.983

Gaus mean : 28.9255

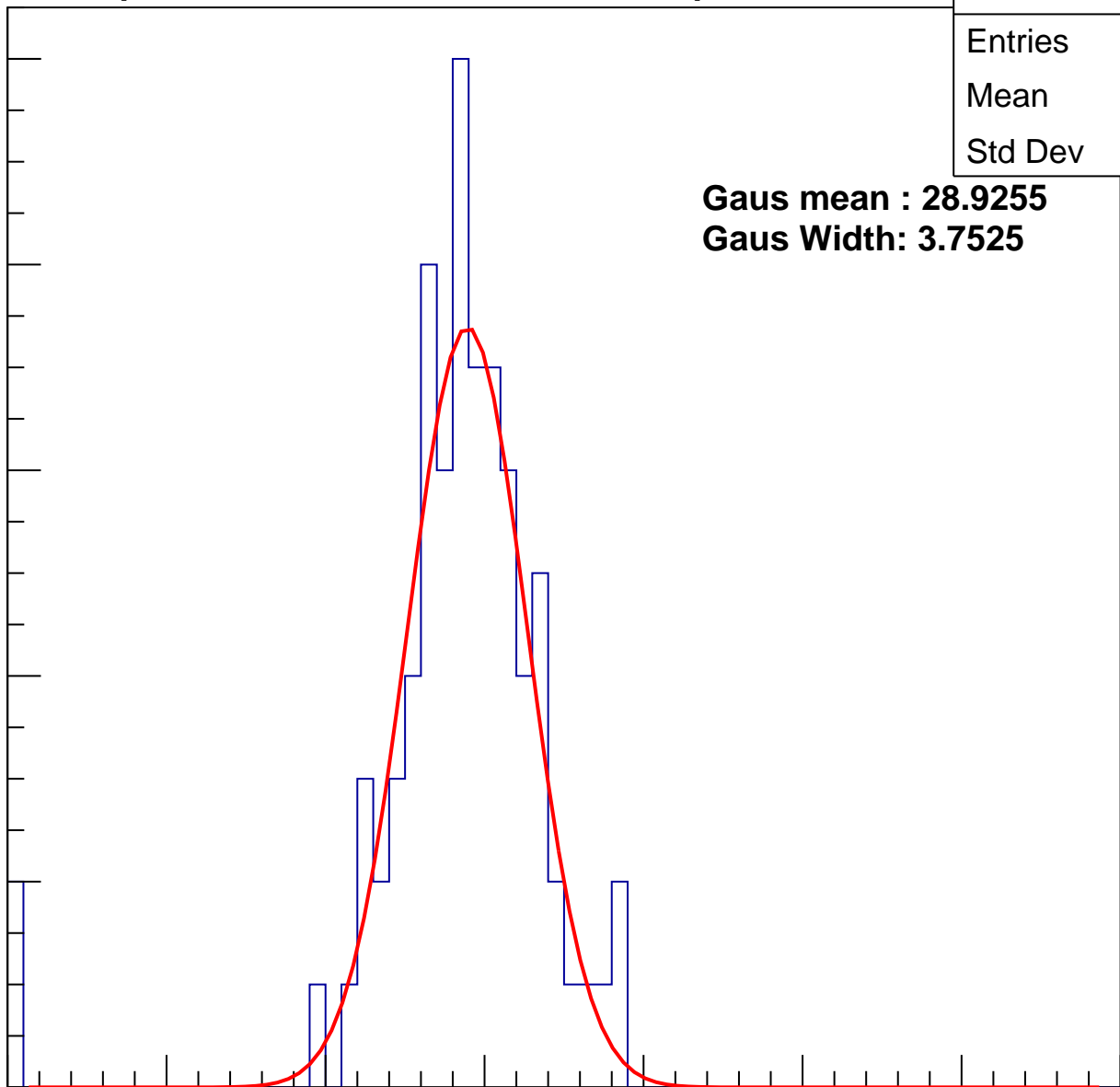
Gaus Width: 3.7525

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch44, adc1

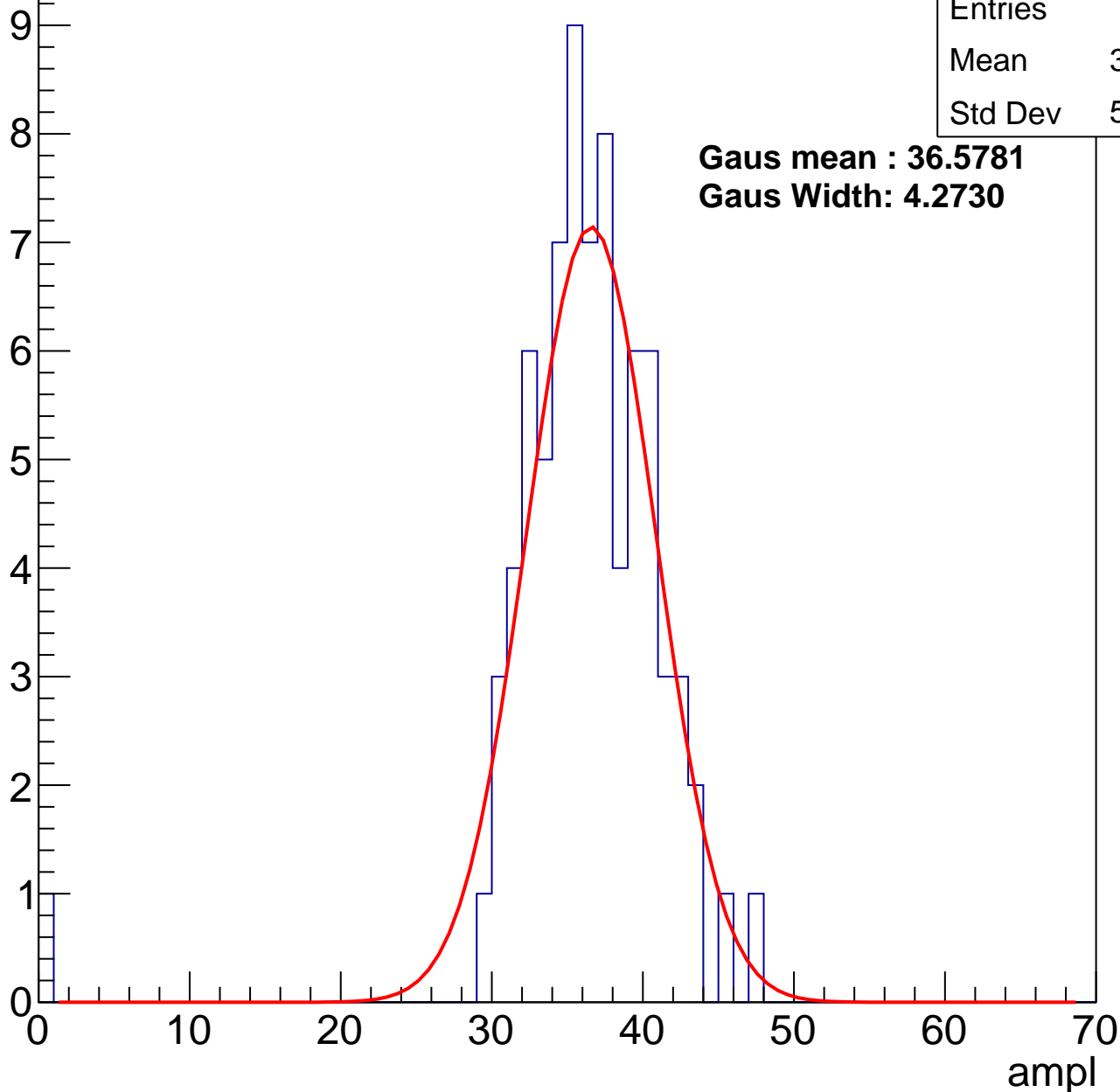
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.77
Std Dev	5.564

Gaus mean : 36.5781

Gaus Width: 4.2730



B0L102S, U5-ch44, adc2

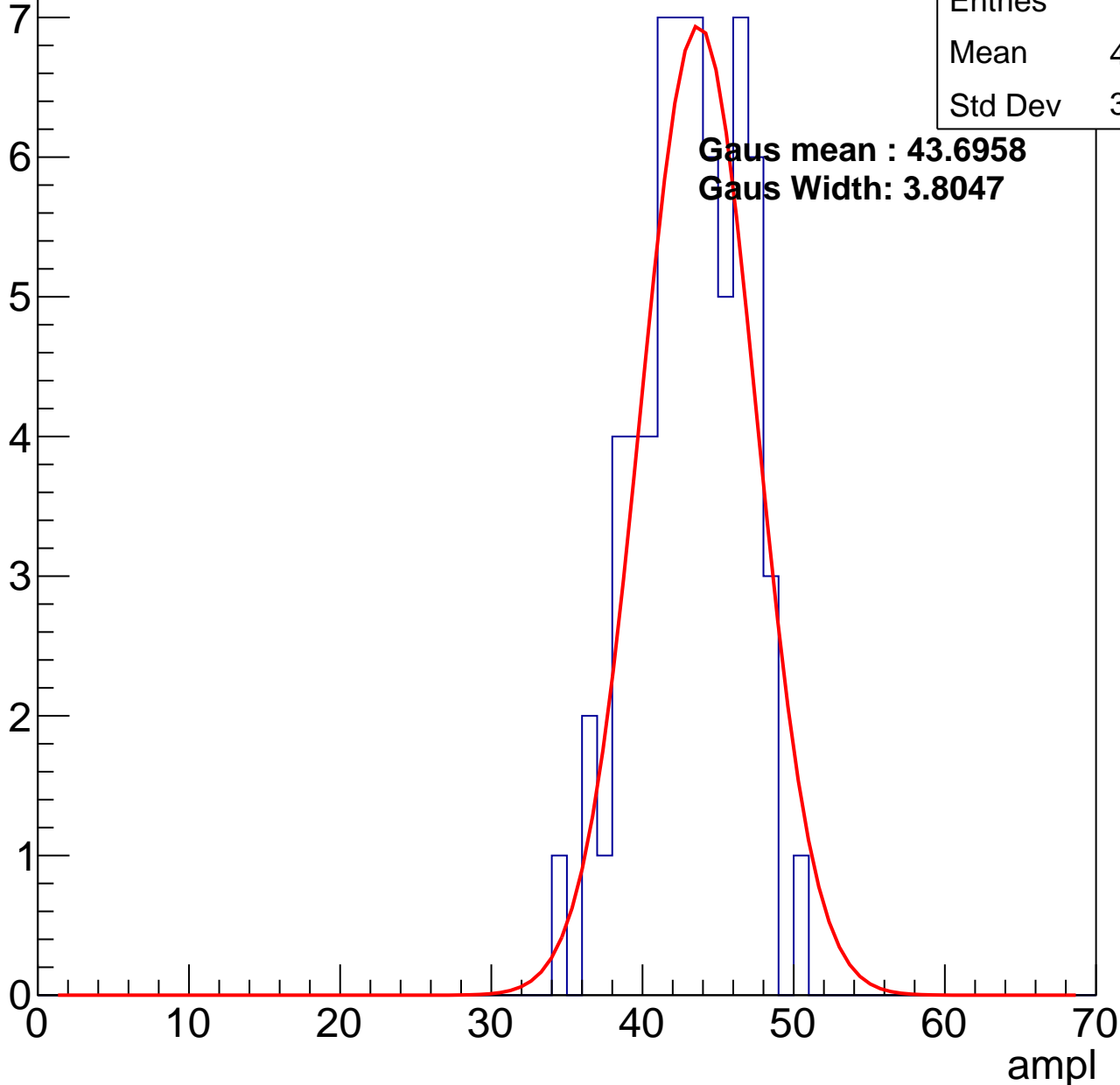
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.77
Std Dev	3.409

Gaus mean : 43.6958

Gaus Width: 3.8047

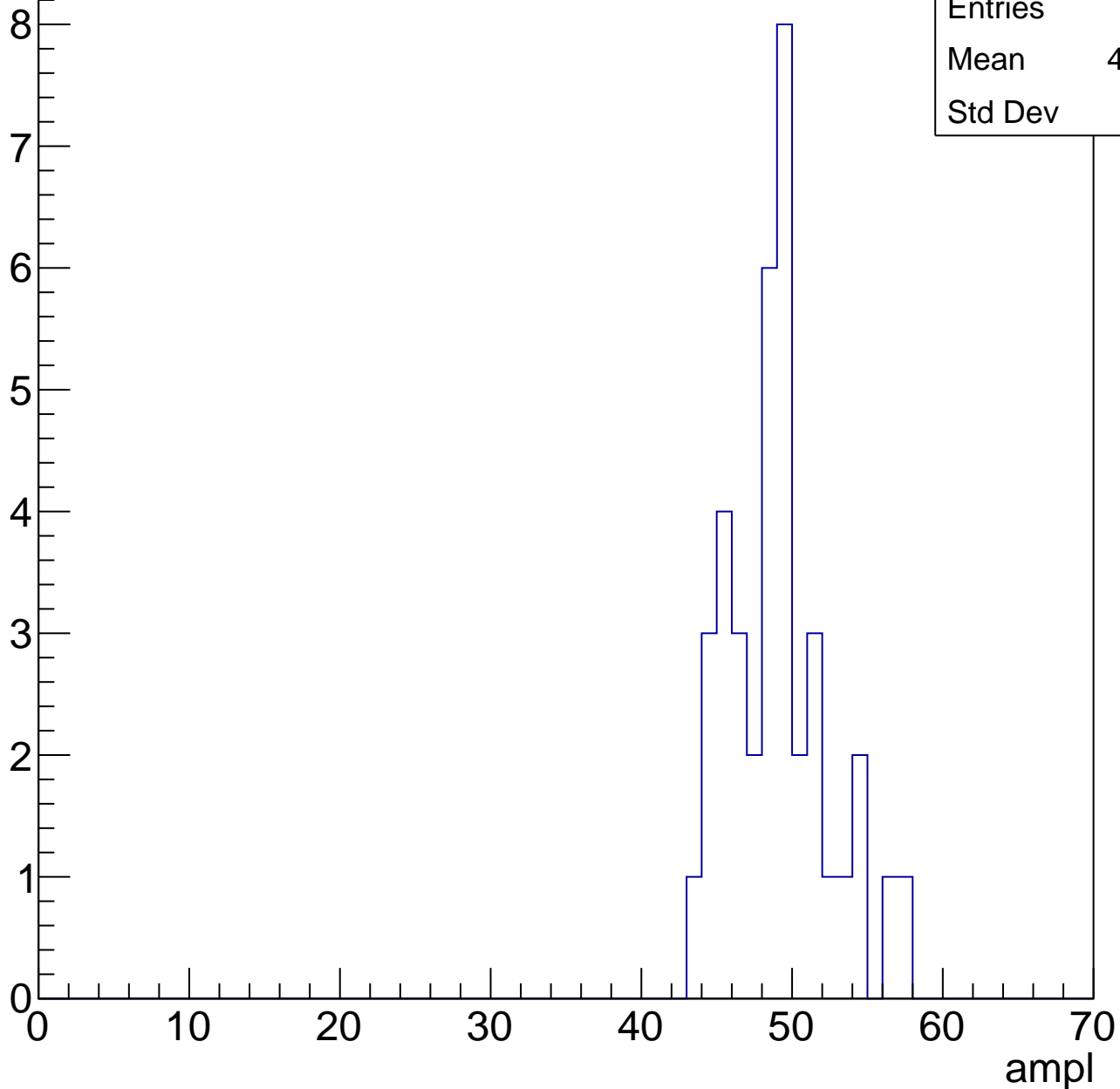


B0L102S, U5-ch44, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	38
Mean	48.58
Std Dev	3.29

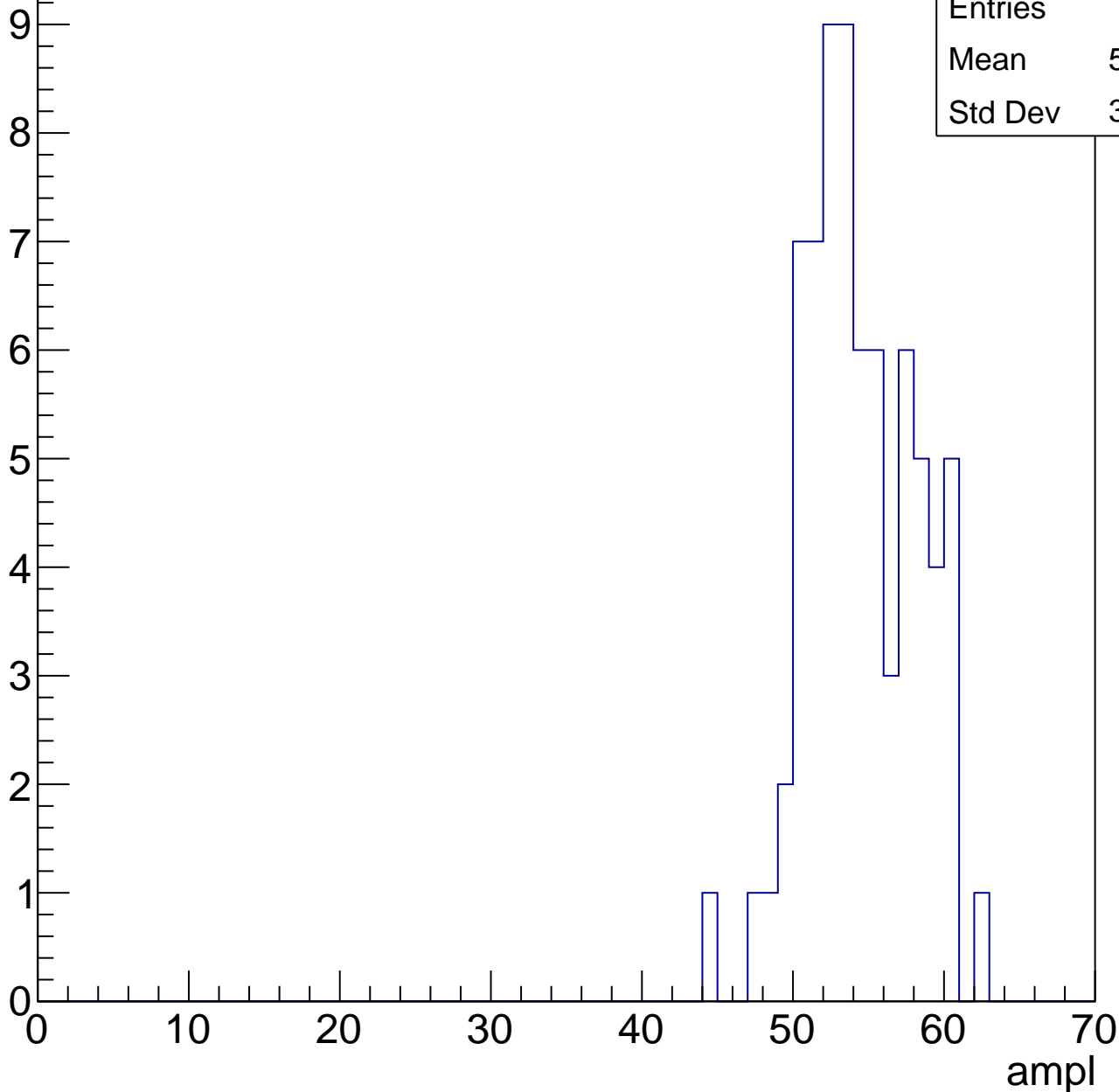


B0L102S, U5-ch44, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	53.99
Std Dev	3.609

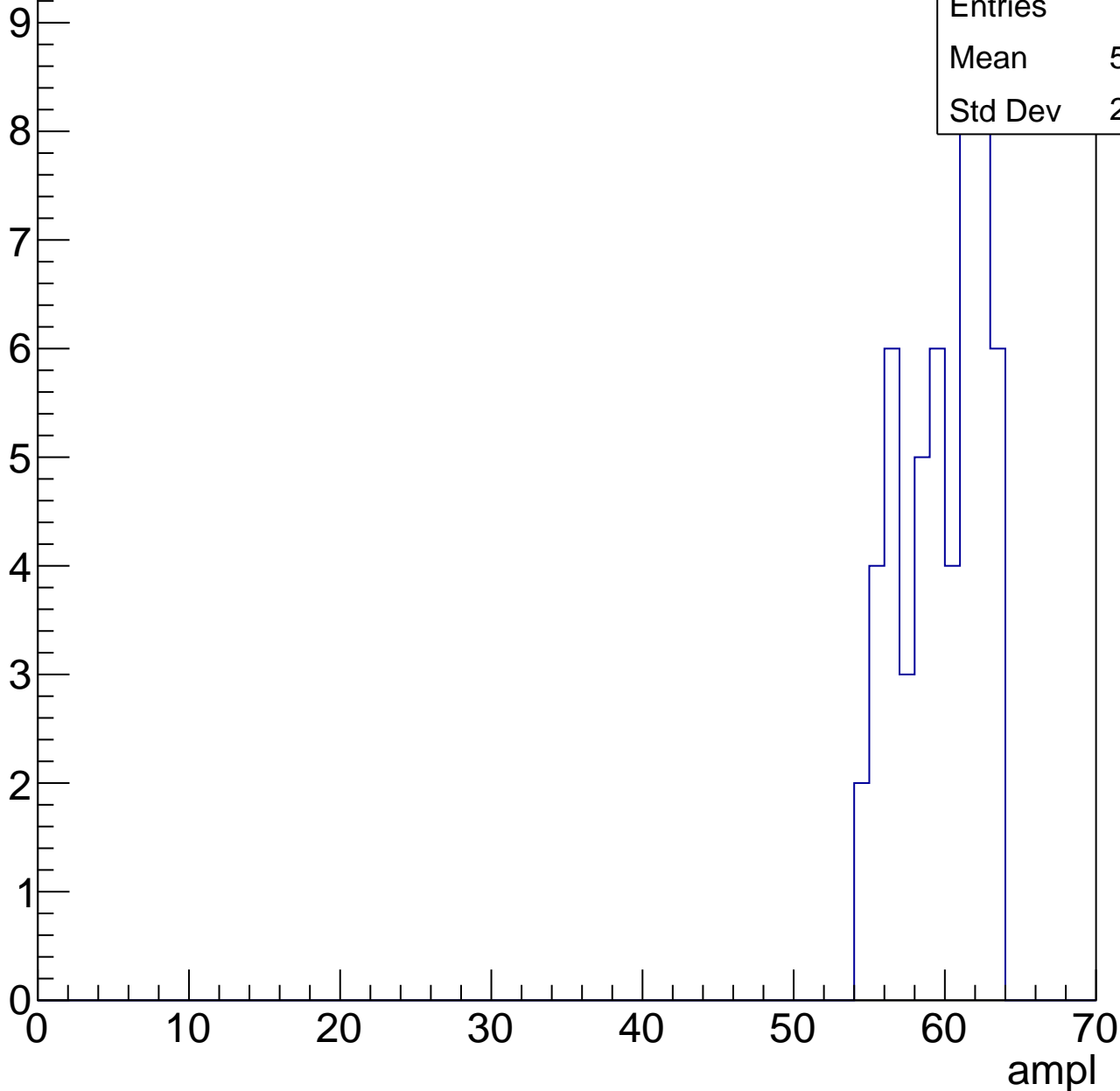


B0L102S, U5-ch44, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	59.28
Std Dev	2.708



B0L102S, U5-ch44, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch44, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch45, adc0

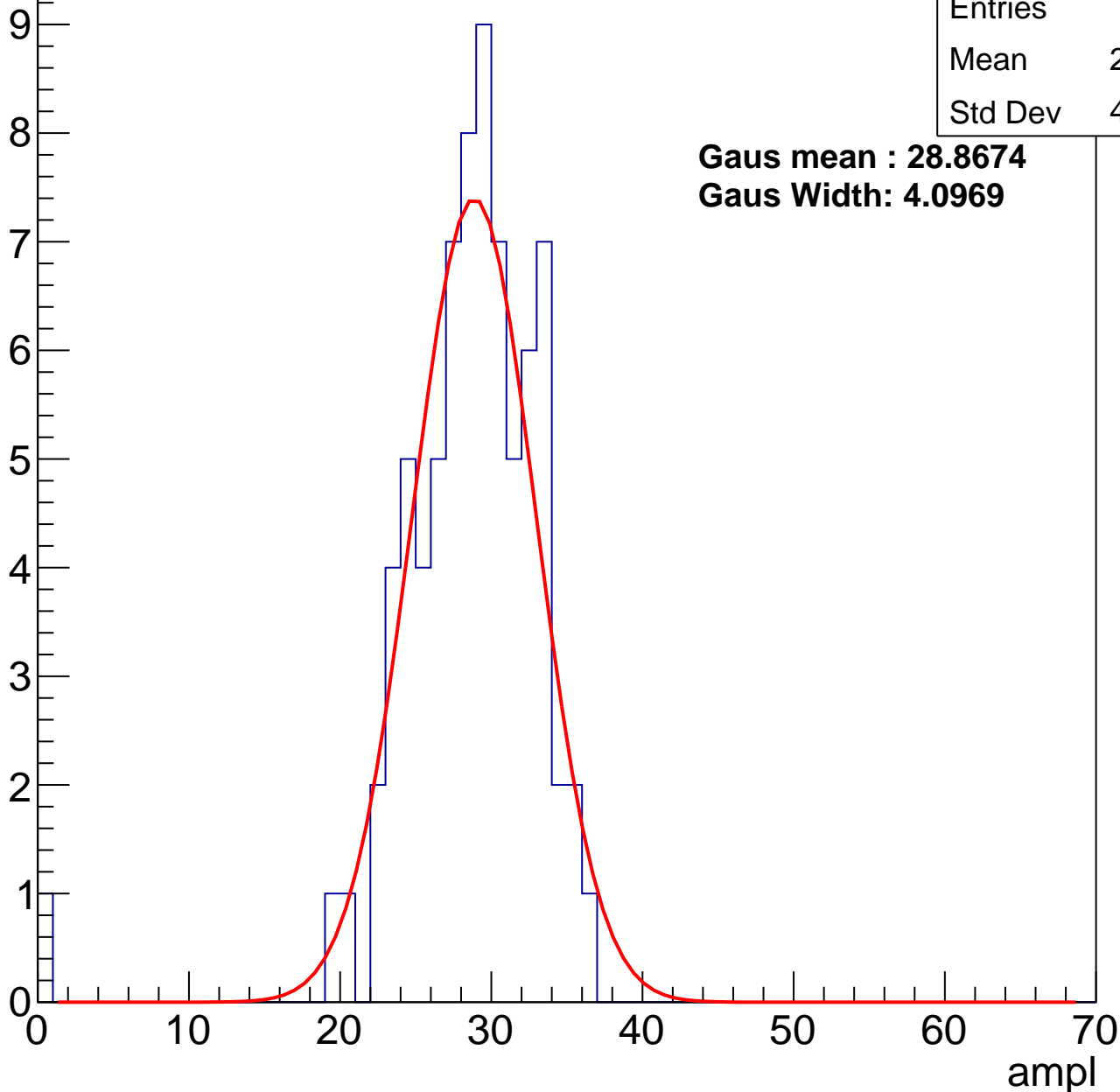
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	28.06
Std Dev	4.876

Gaus mean : 28.8674

Gaus Width: 4.0969



B0L102S, U5-ch45, adc1

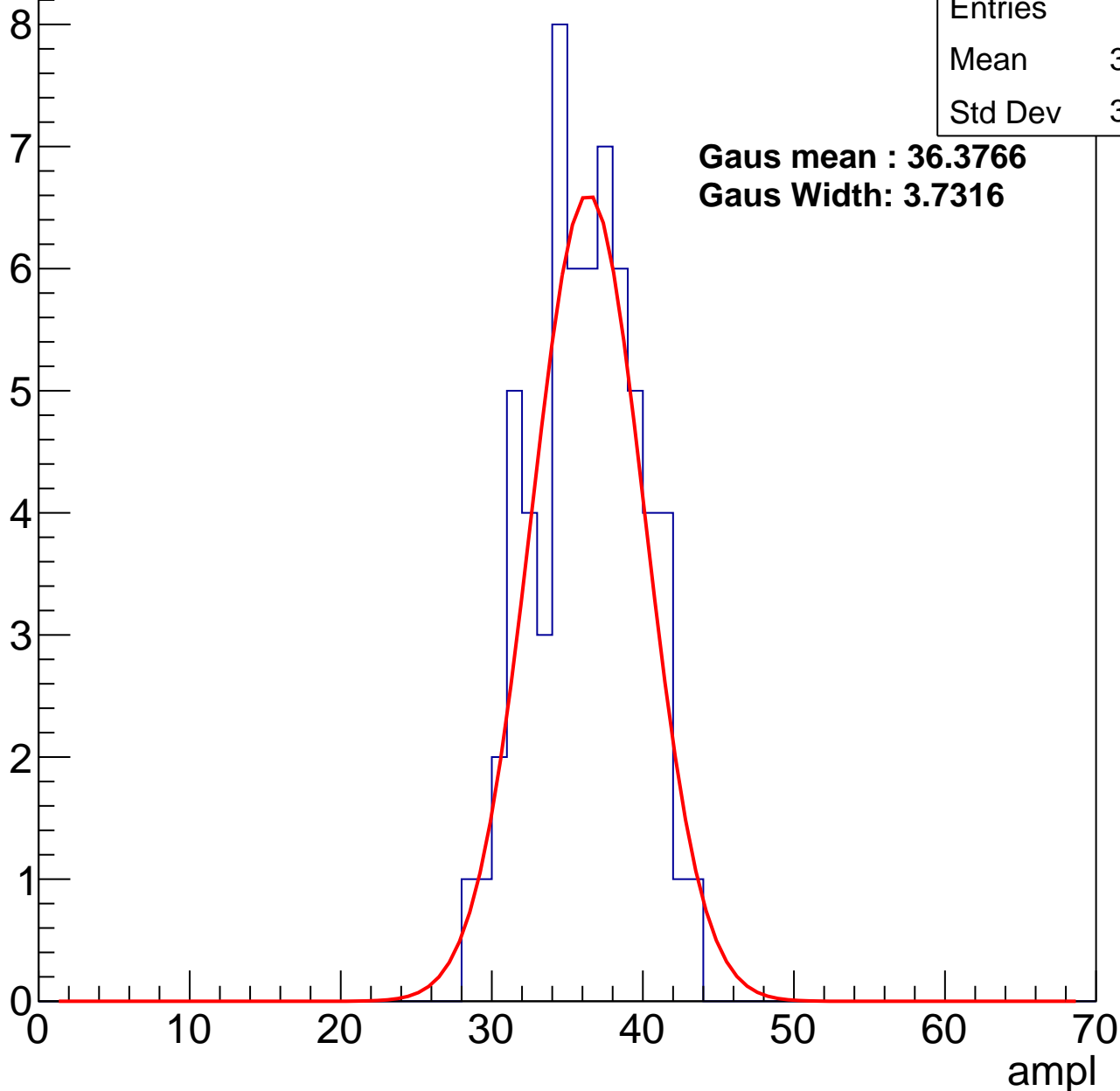
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	35.75
Std Dev	3.446

Gaus mean : 36.3766

Gaus Width: 3.7316



B0L102S, U5-ch45, adc2

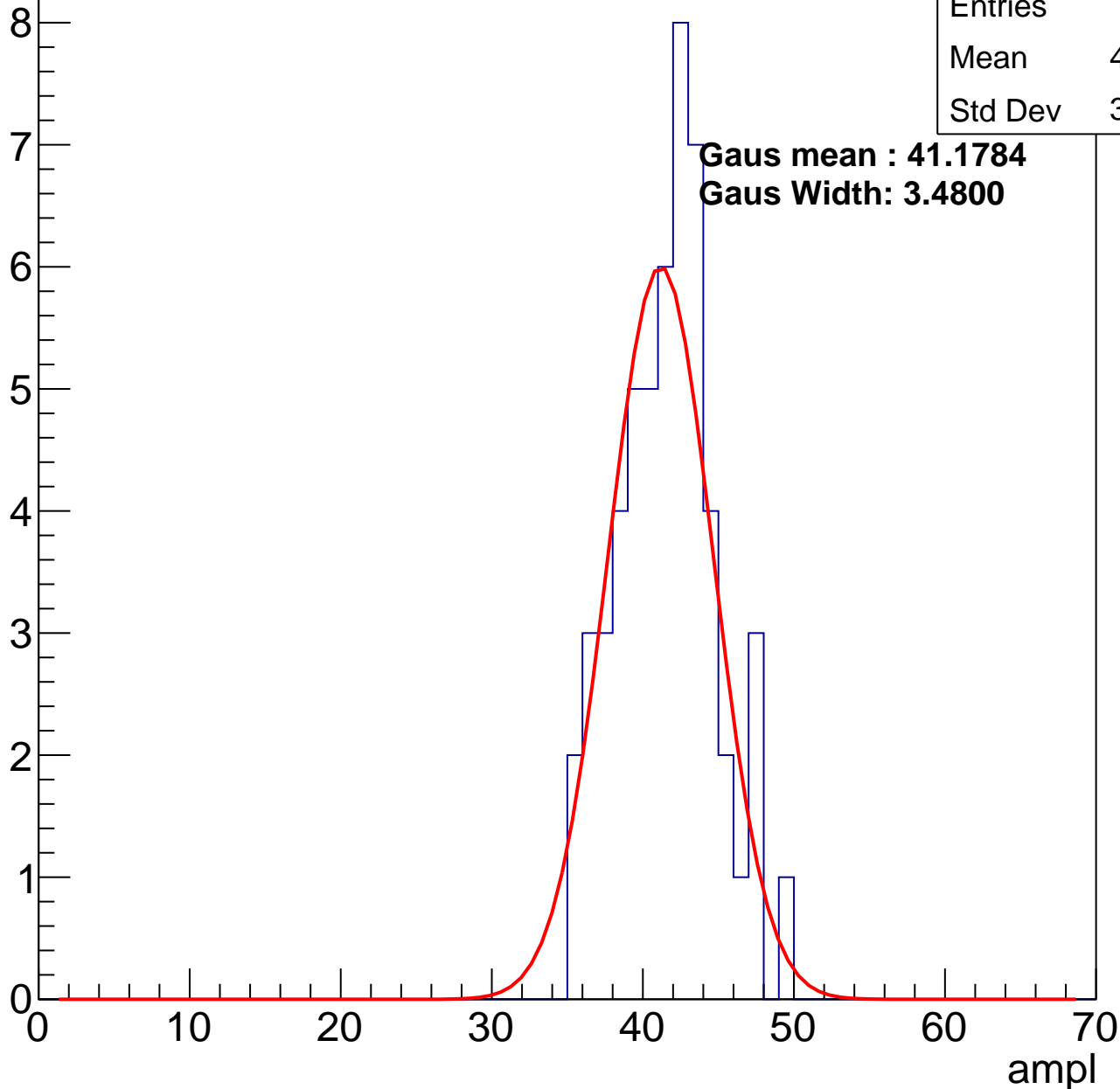
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	41.13
Std Dev	3.209

Gaus mean : 41.1784

Gaus Width: 3.4800



B0L102S, U5-ch45, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	48.4
Std Dev	3.544

Entry

10

8

6

4

2

0

0

10

20

30

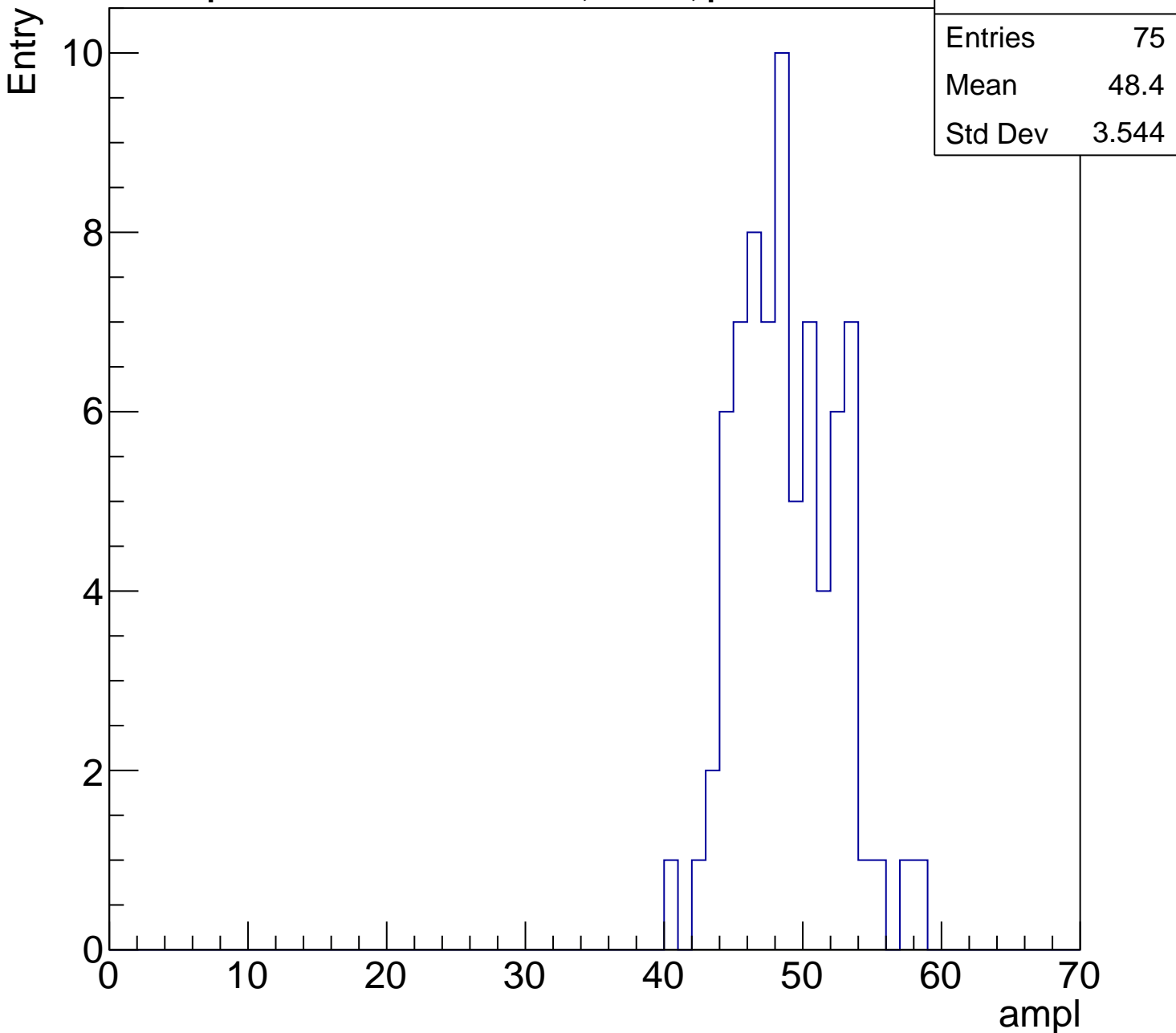
40

50

60

ampl

70

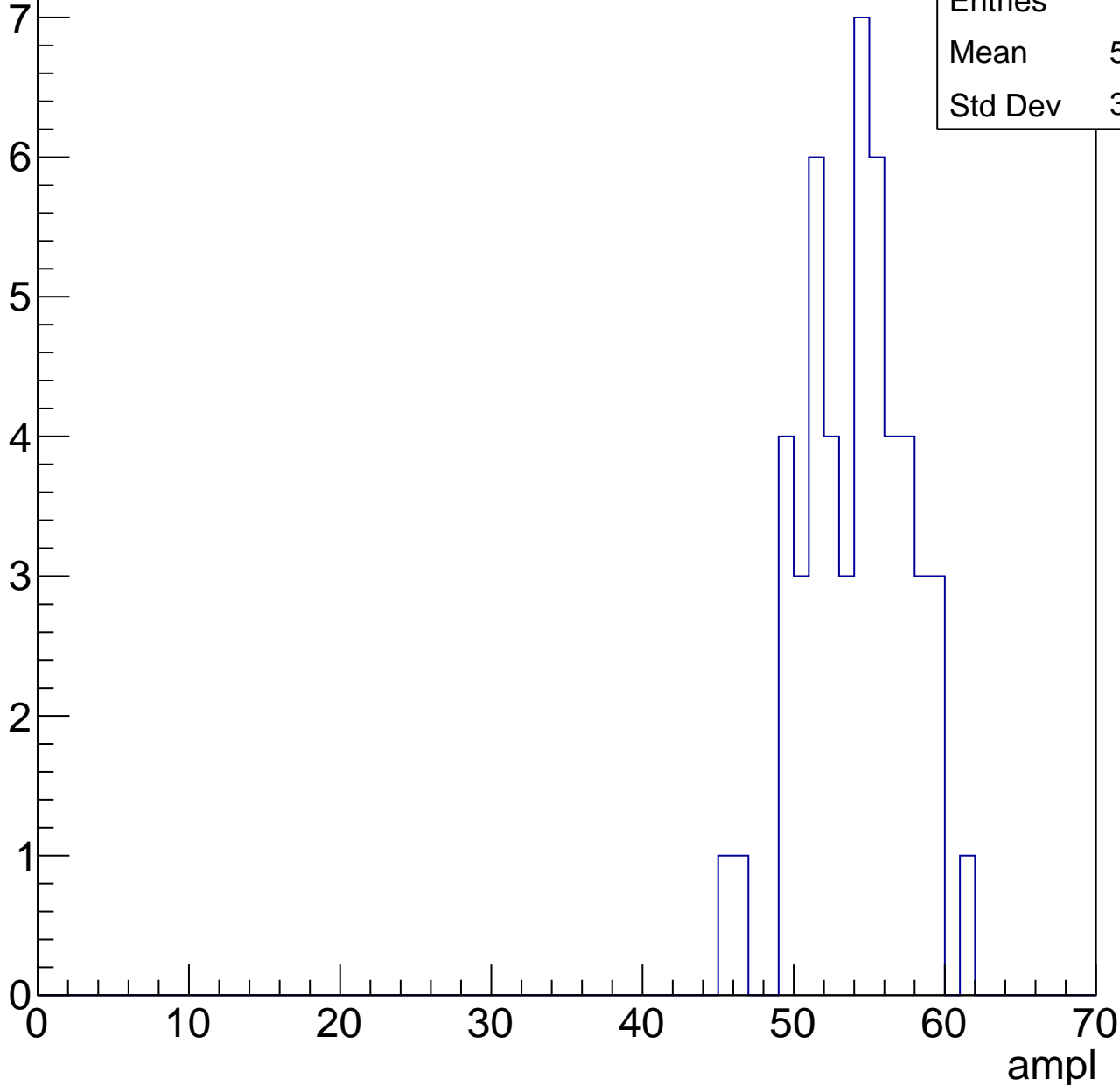


B0L102S, U5-ch45, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	53.64
Std Dev	3.434



B0L102S, U5-ch45, adc5

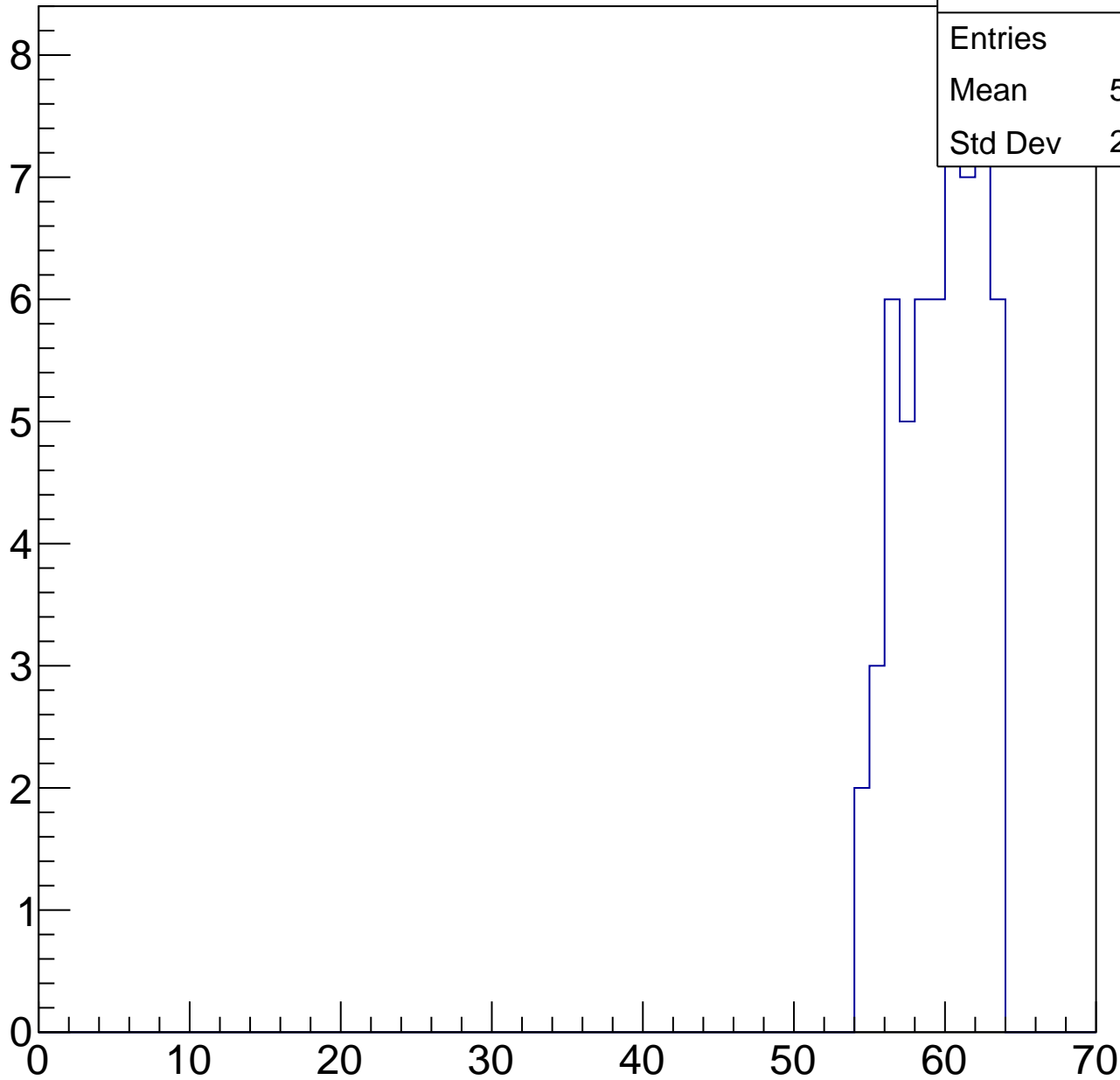
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	59.25
Std Dev	2.577

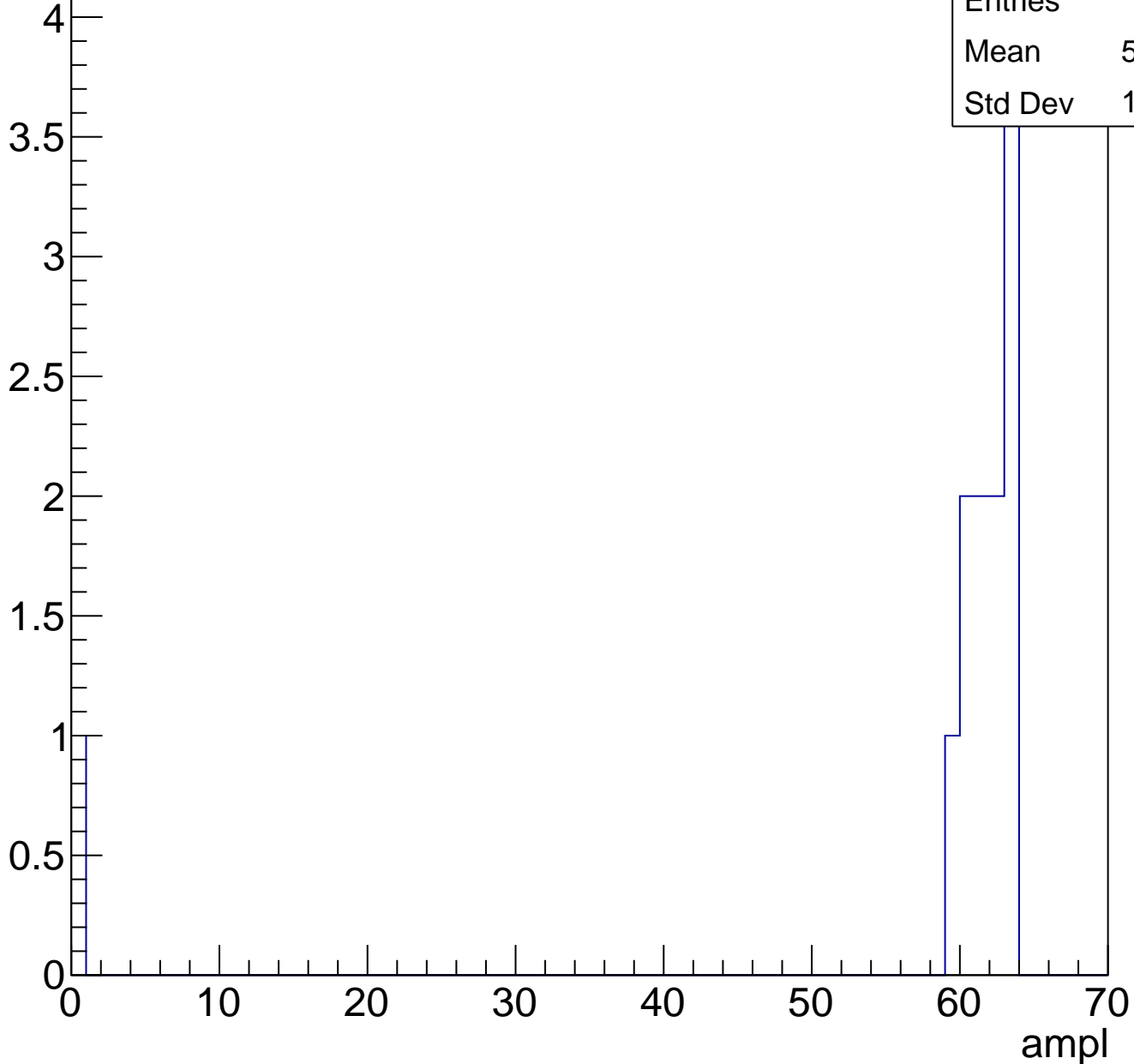
ampl



B0L102S, U5-ch45, adc6

calib_packv5_042523_0143.root, FC#12, port B1

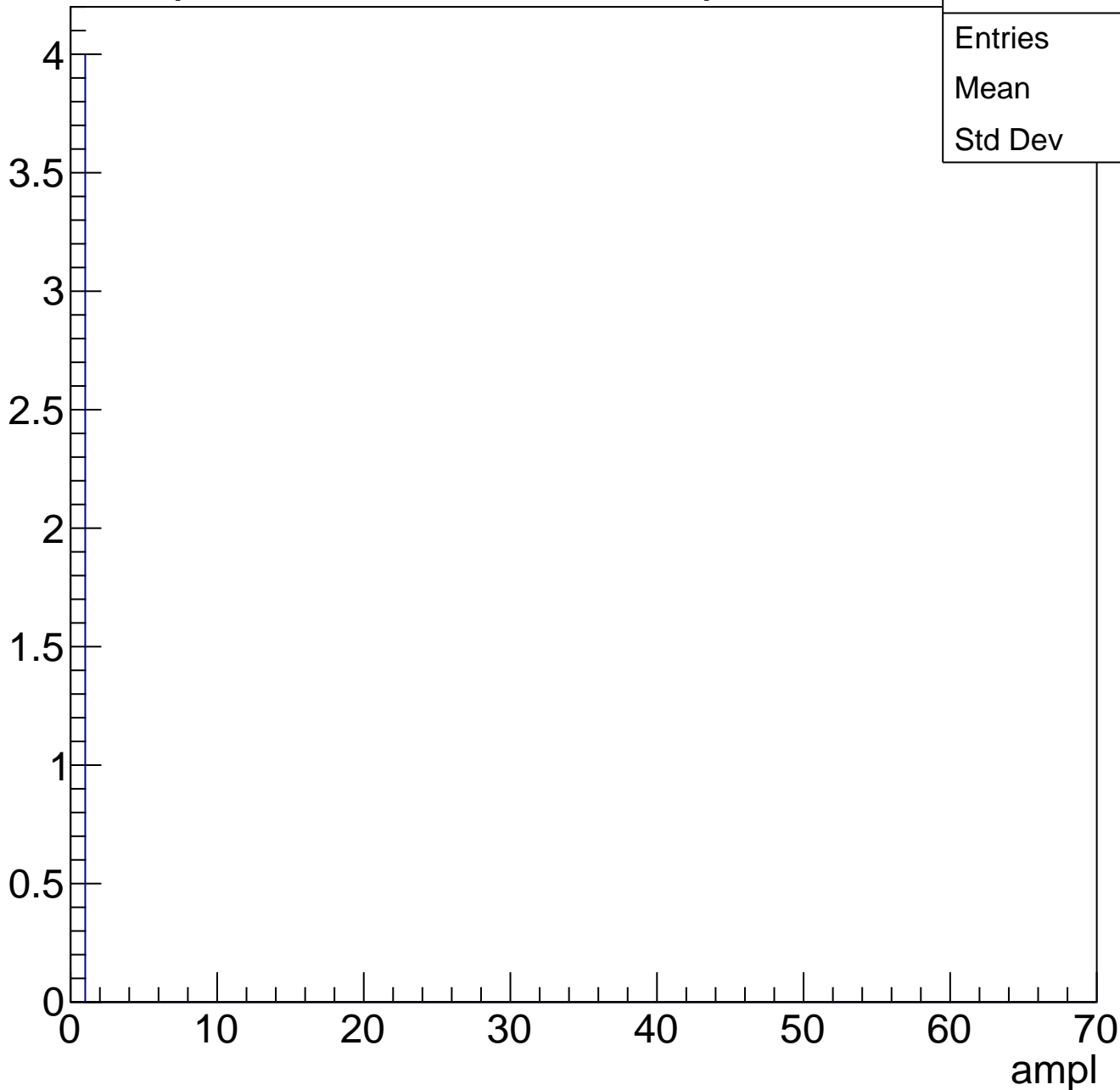
Entry



B0L102S, U5-ch45, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	4
Mean	0
Std Dev	0

B0L102S, U5-ch46, adc0

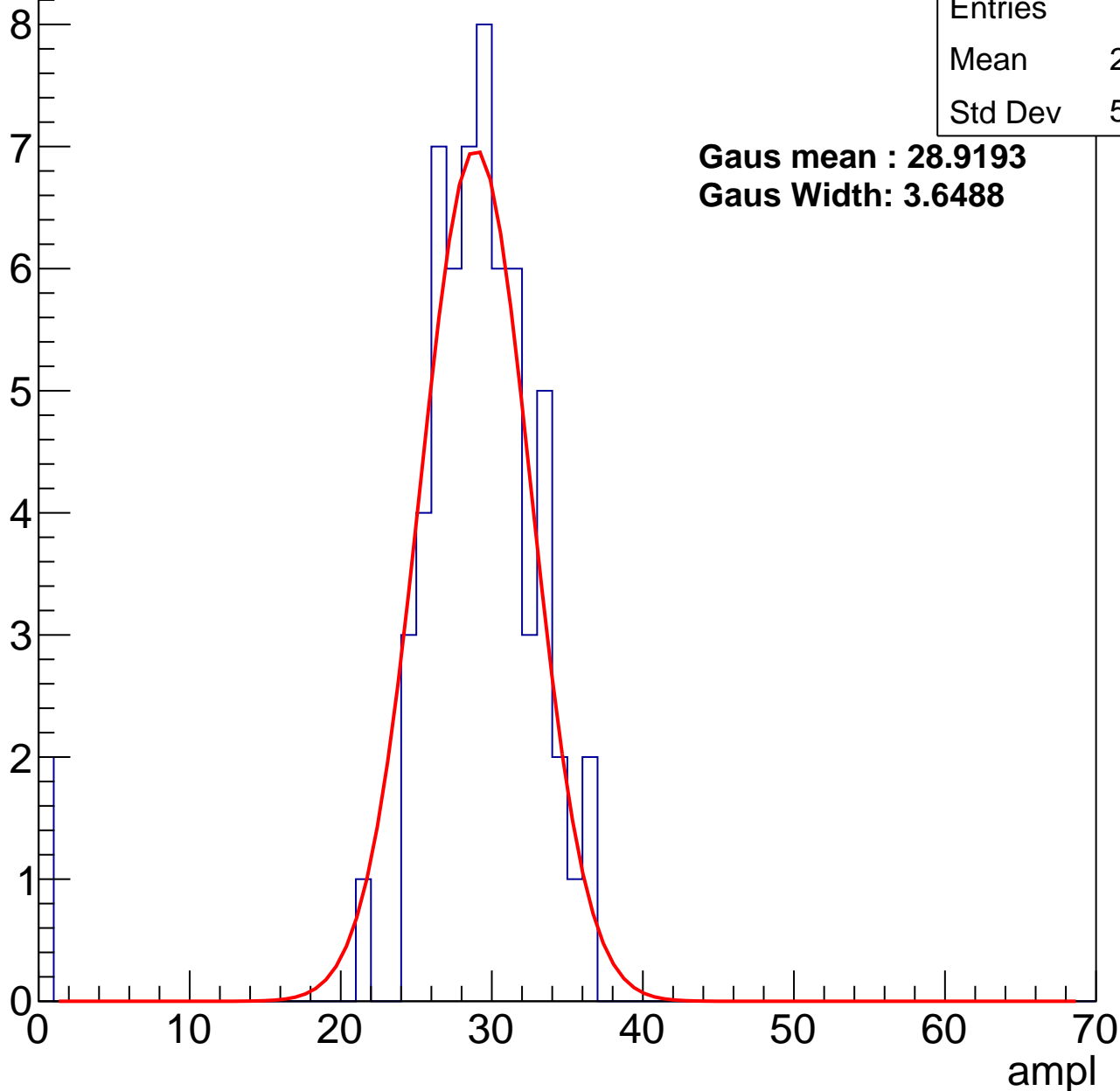
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.05
Std Dev	5.967

Gaus mean : 28.9193

Gaus Width: 3.6488



B0L102S, U5-ch46, adc1

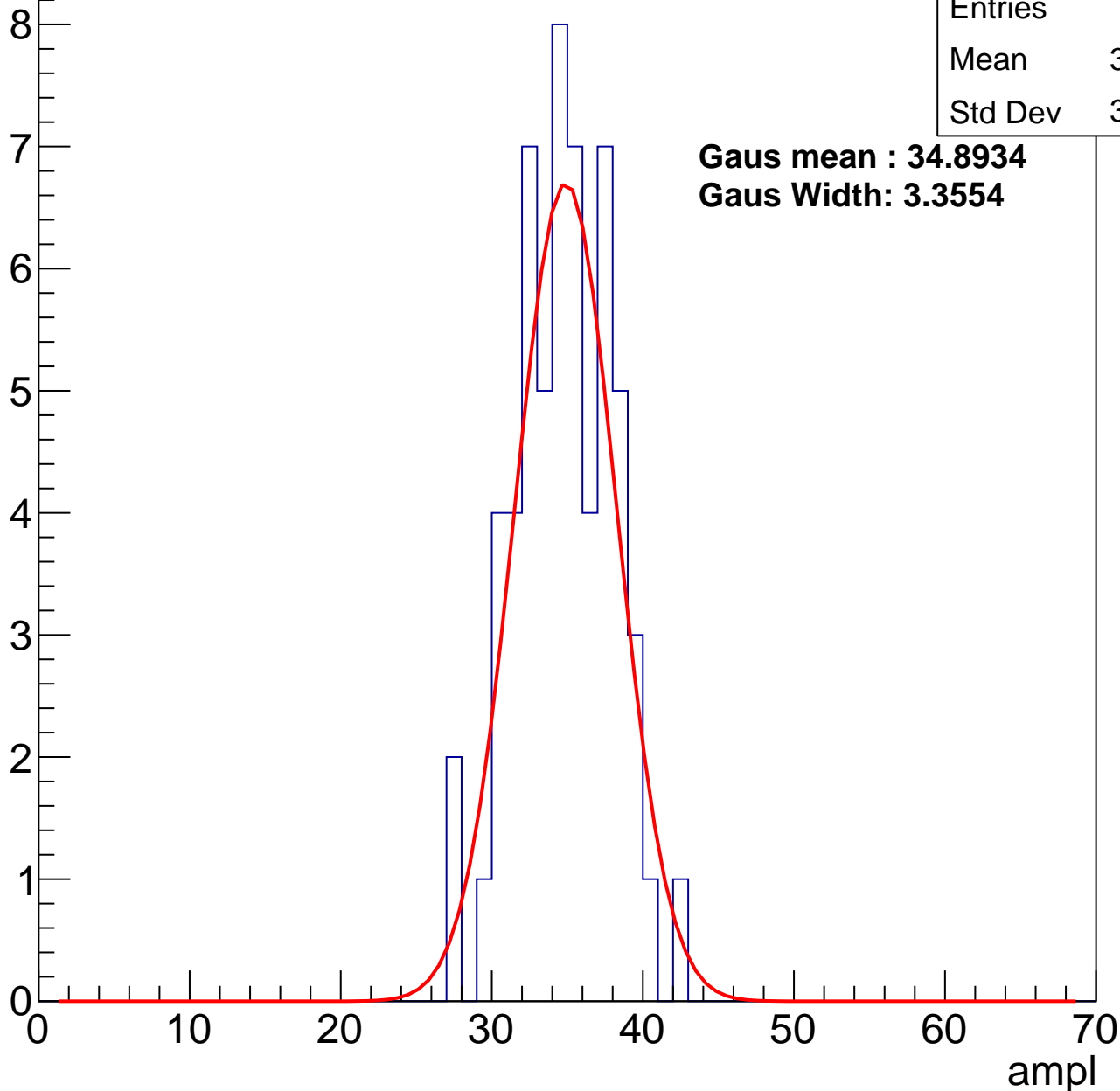
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	34.32
Std Dev	3.165

Gaus mean : 34.8934

Gaus Width: 3.3554



B0L102S, U5-ch46, adc2

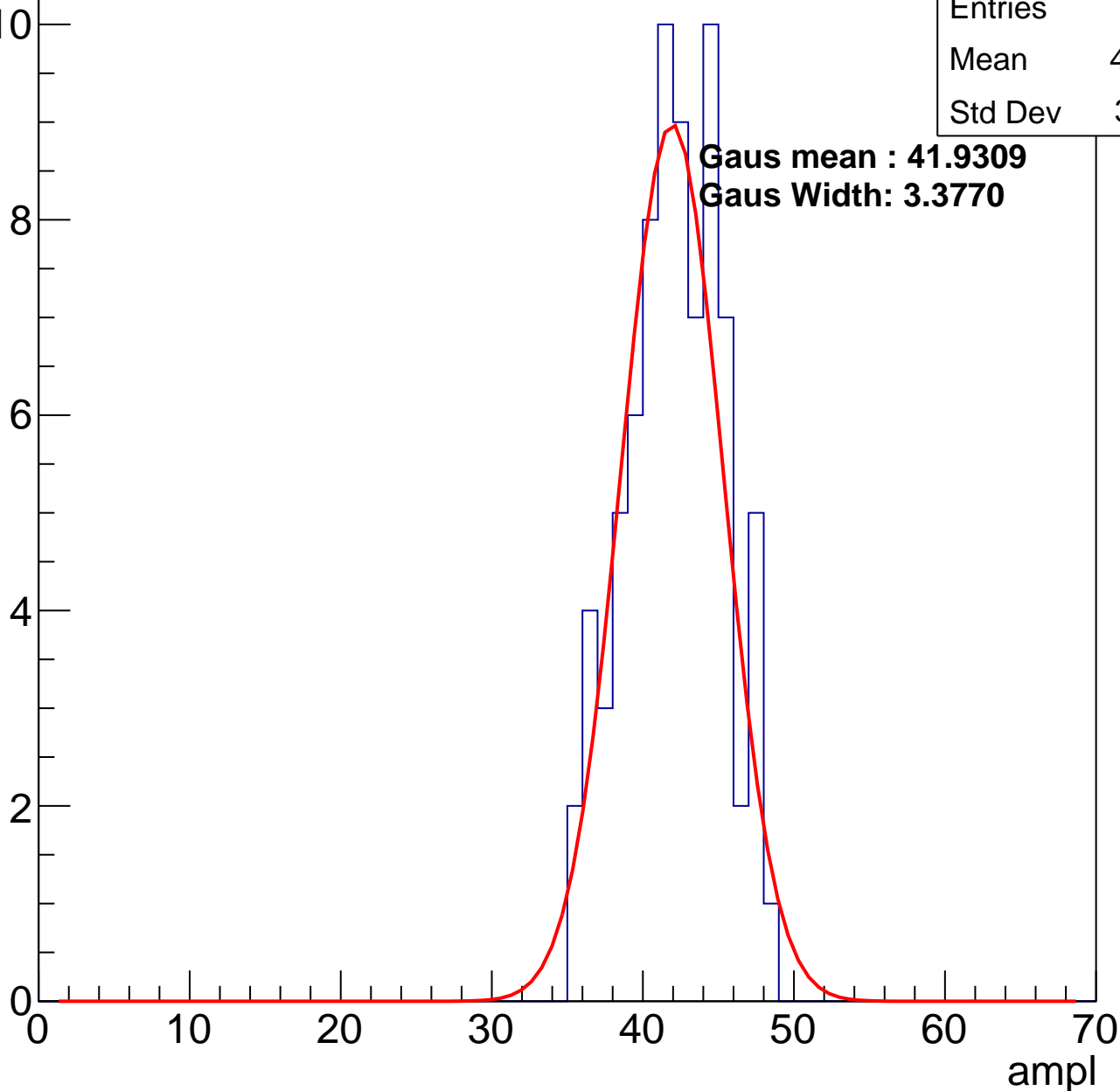
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	41.62
Std Dev	3.151

Gaus mean : 41.9309

Gaus Width: 3.3770

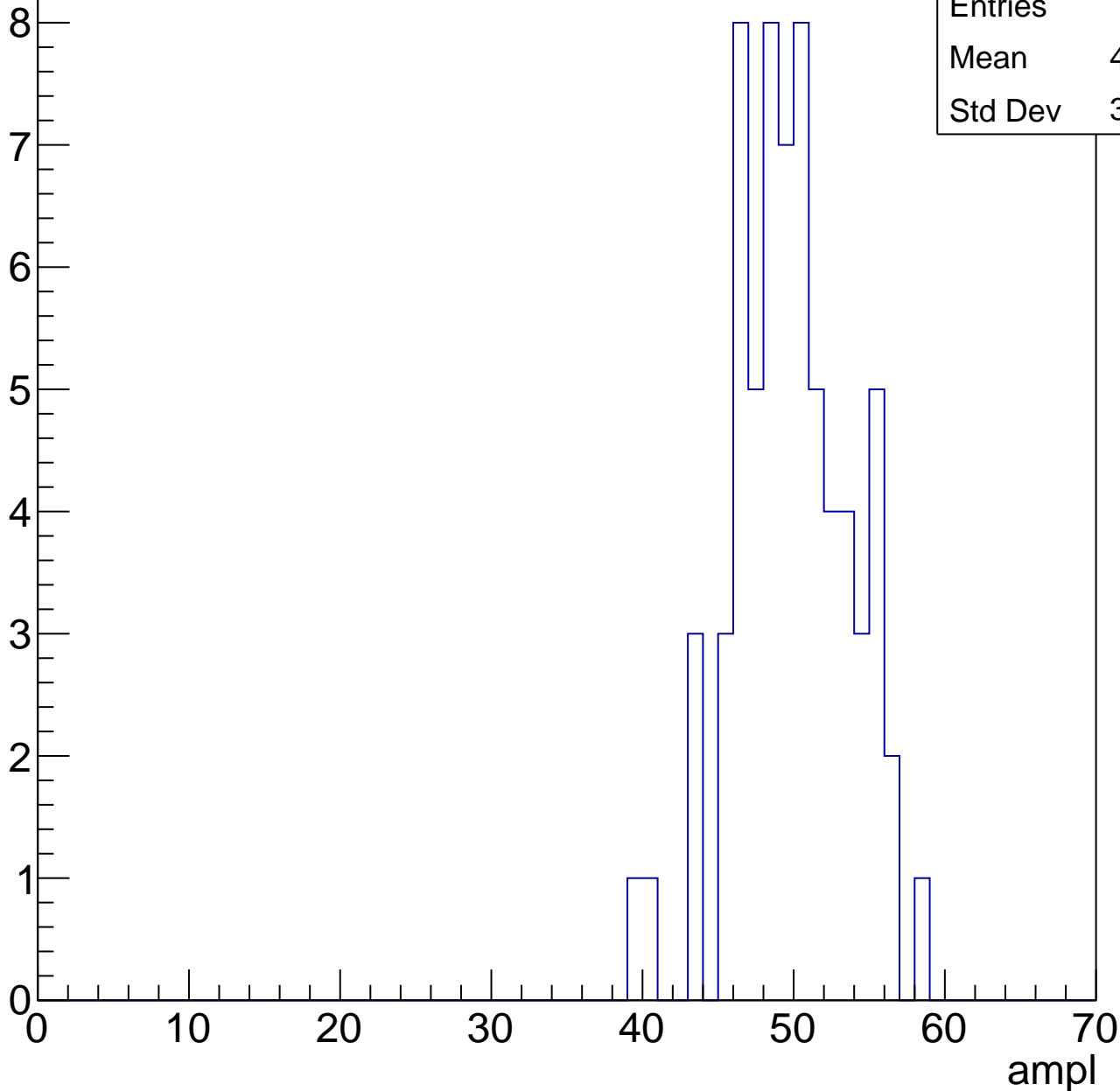


B0L102S, U5-ch46, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	49.34
Std Dev	3.818

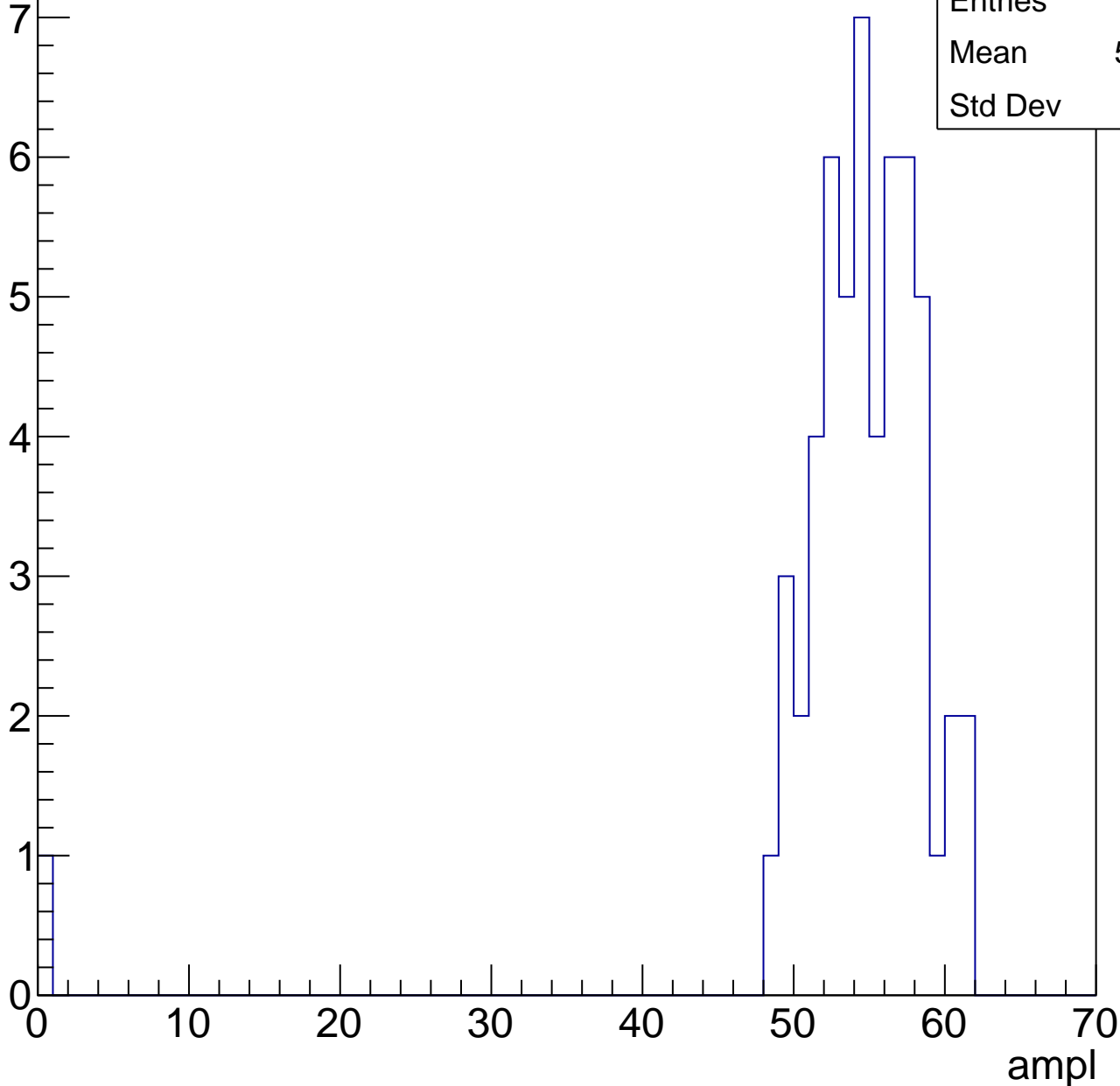


B0L102S, U5-ch46, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	53.51
Std Dev	7.94

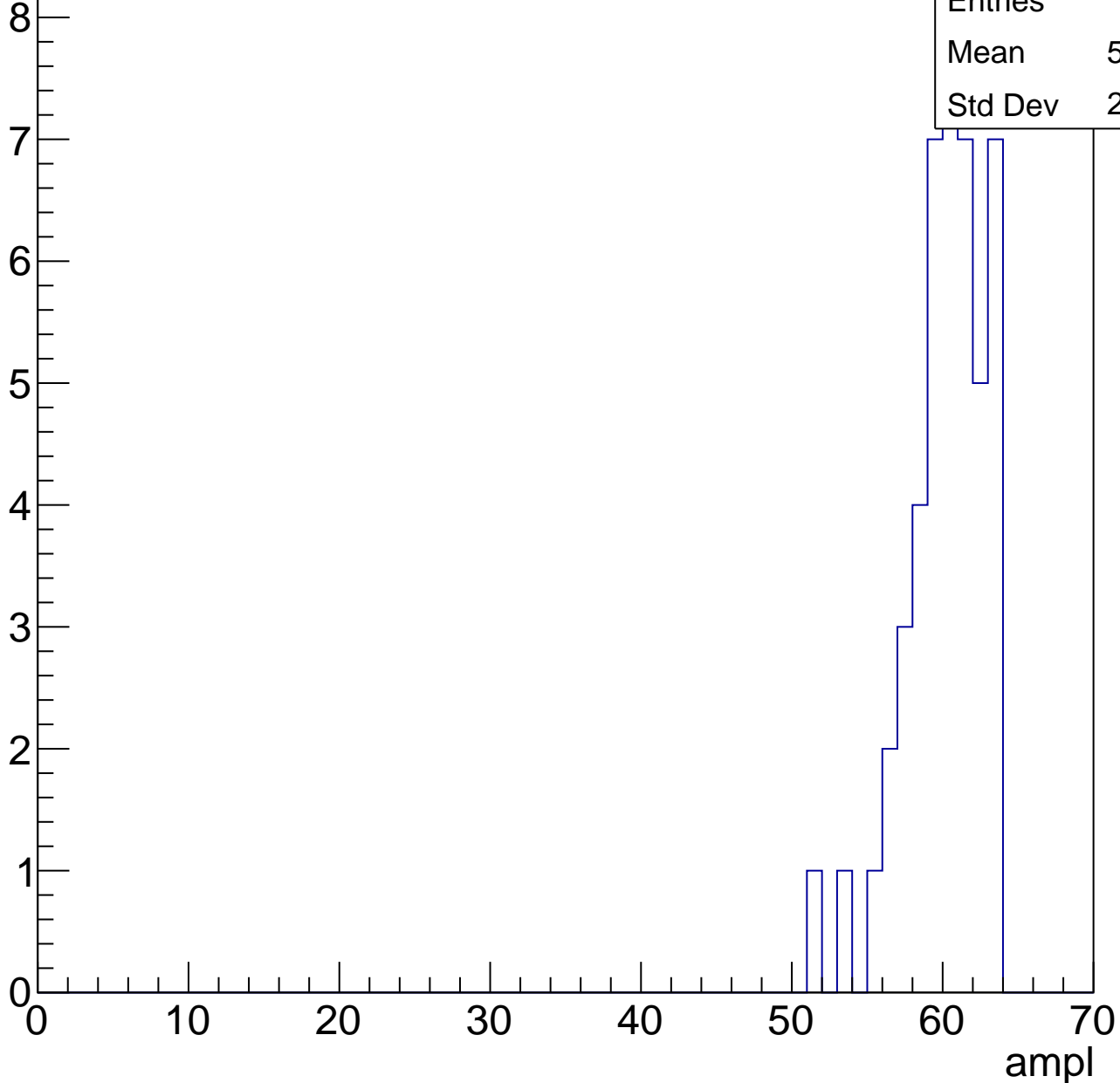


B0L102S, U5-ch46, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	59.67
Std Dev	2.646

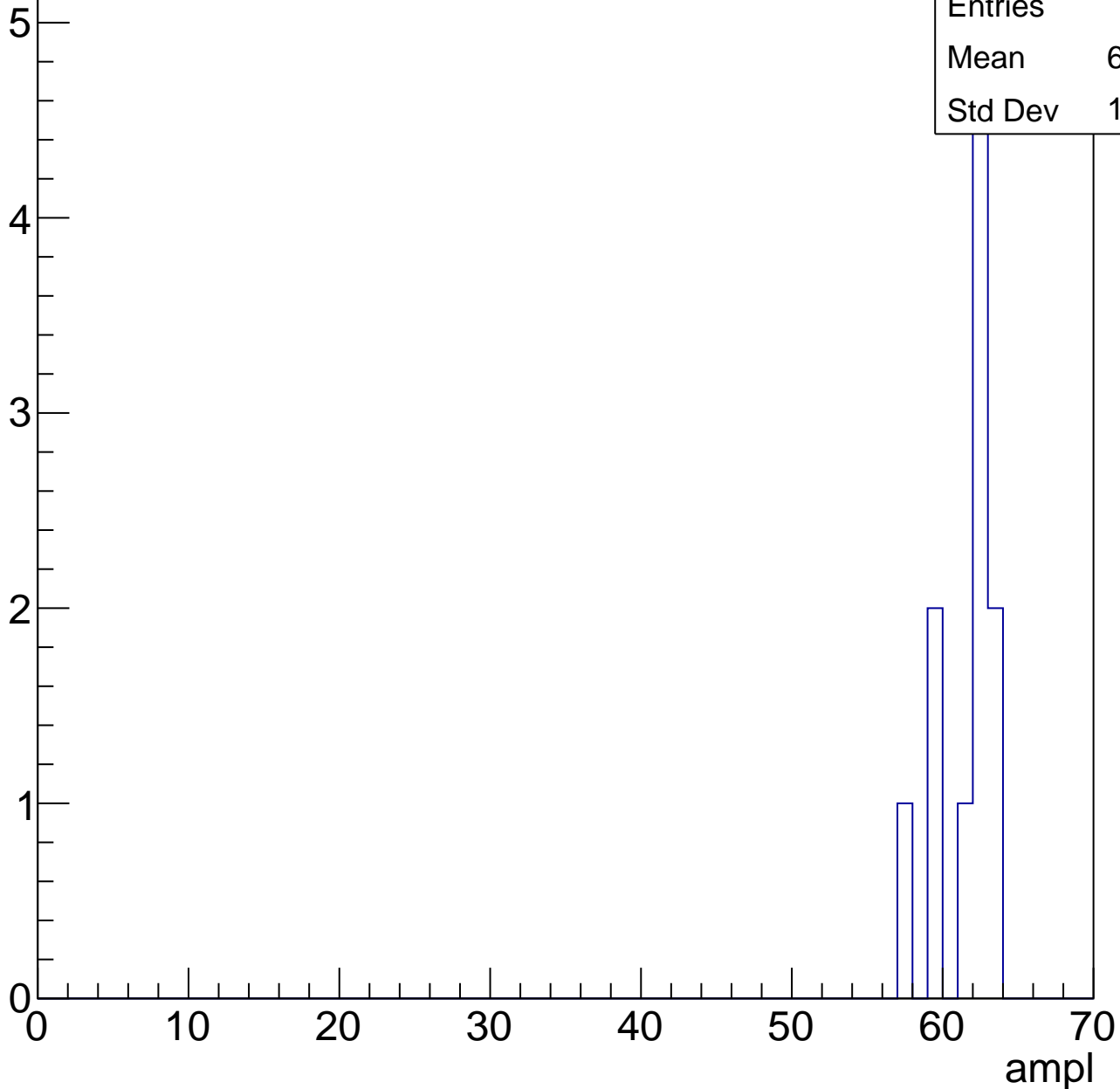


B0L102S, U5-ch46, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.09
Std Dev	1.832



B0L102S, U5-ch46, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch47, adc0

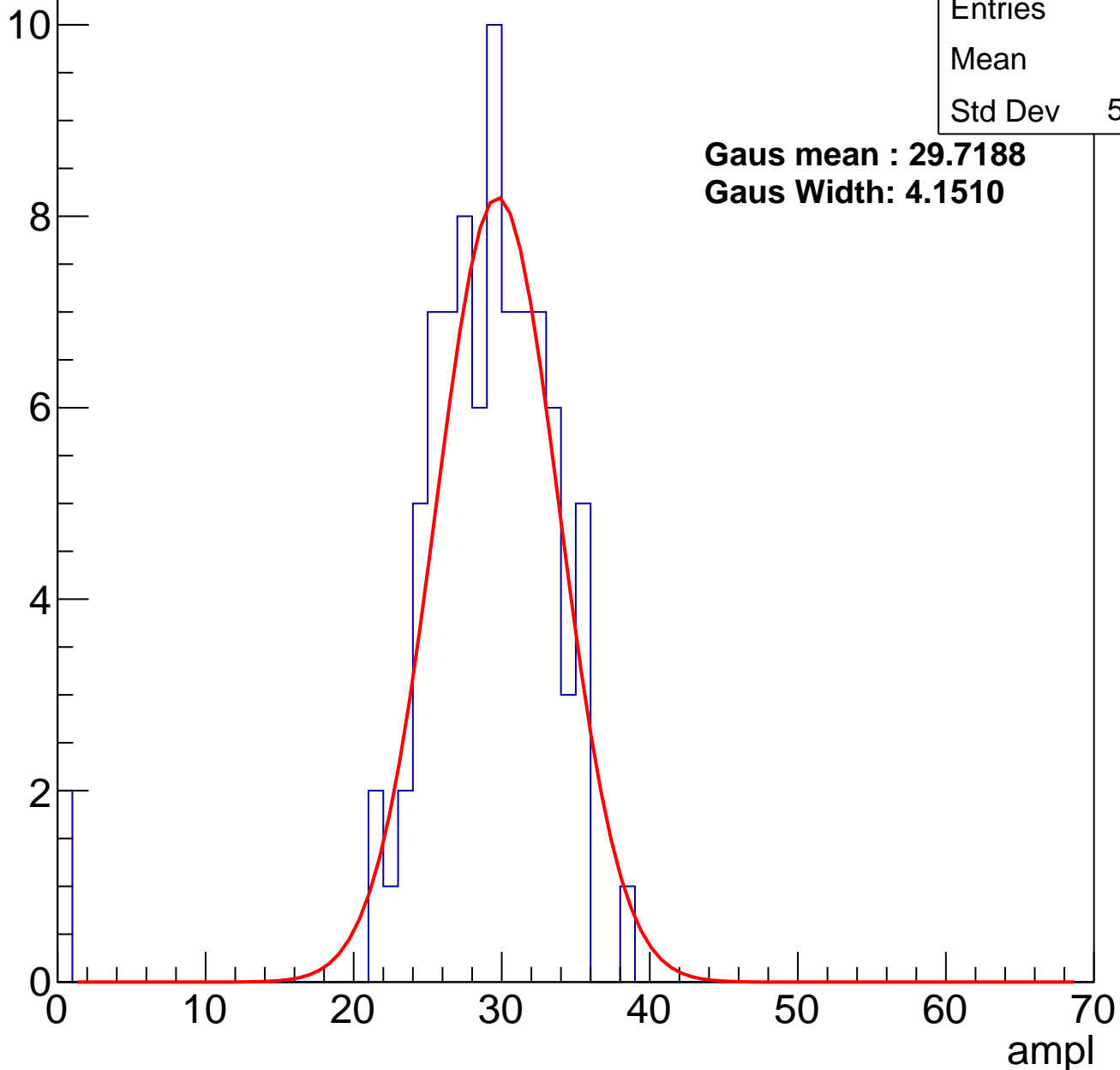
calib_packv5_042523_0143.root, FC#12, port B1

Entries	86
Mean	28.2
Std Dev	5.648

Gaus mean : 29.7188

Gaus Width: 4.1510

Entry



B0L102S, U5-ch47, adc1

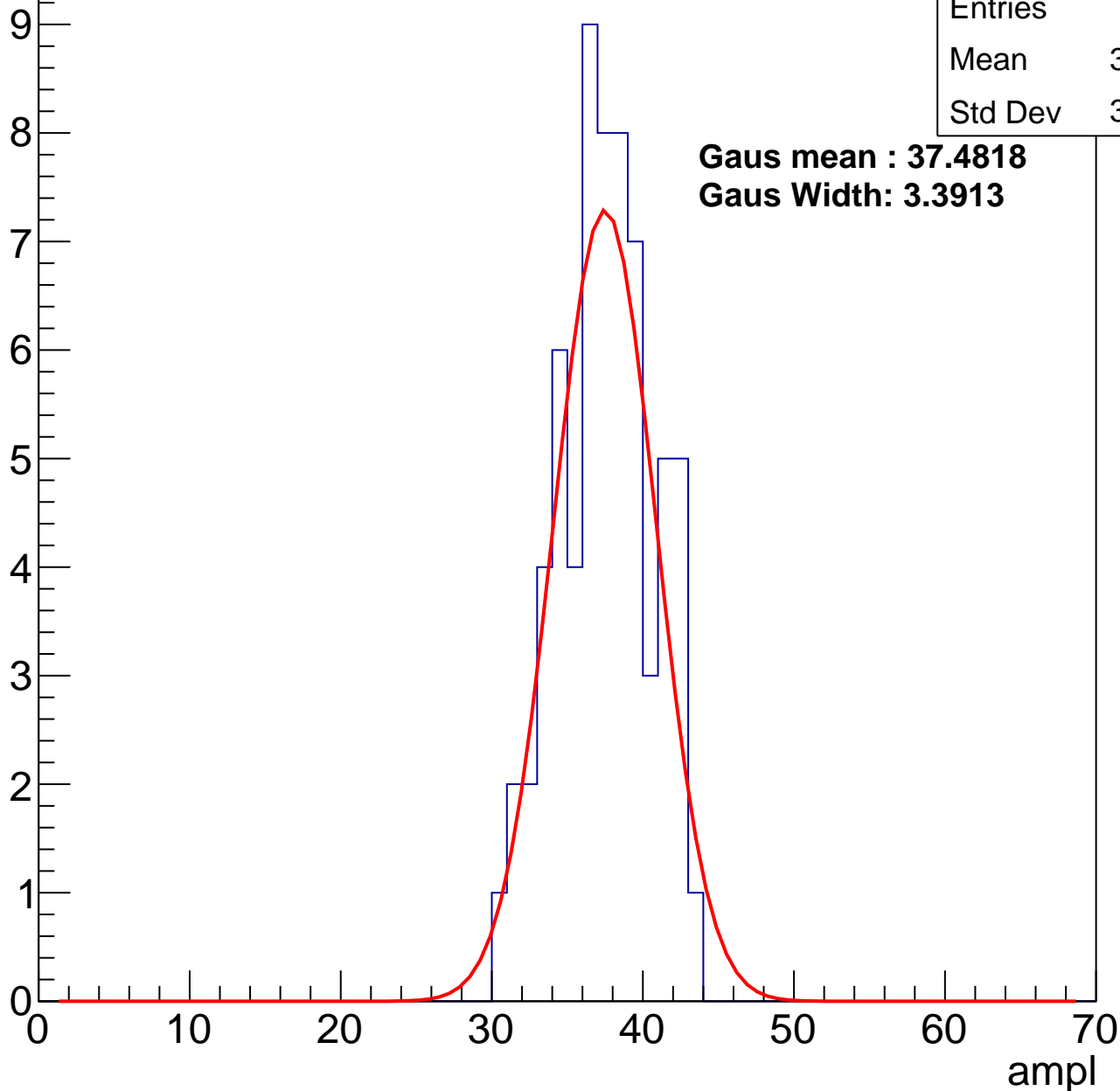
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	37.03
Std Dev	3.083

Gaus mean : 37.4818

Gaus Width: 3.3913



B0L102S, U5-ch47, adc2

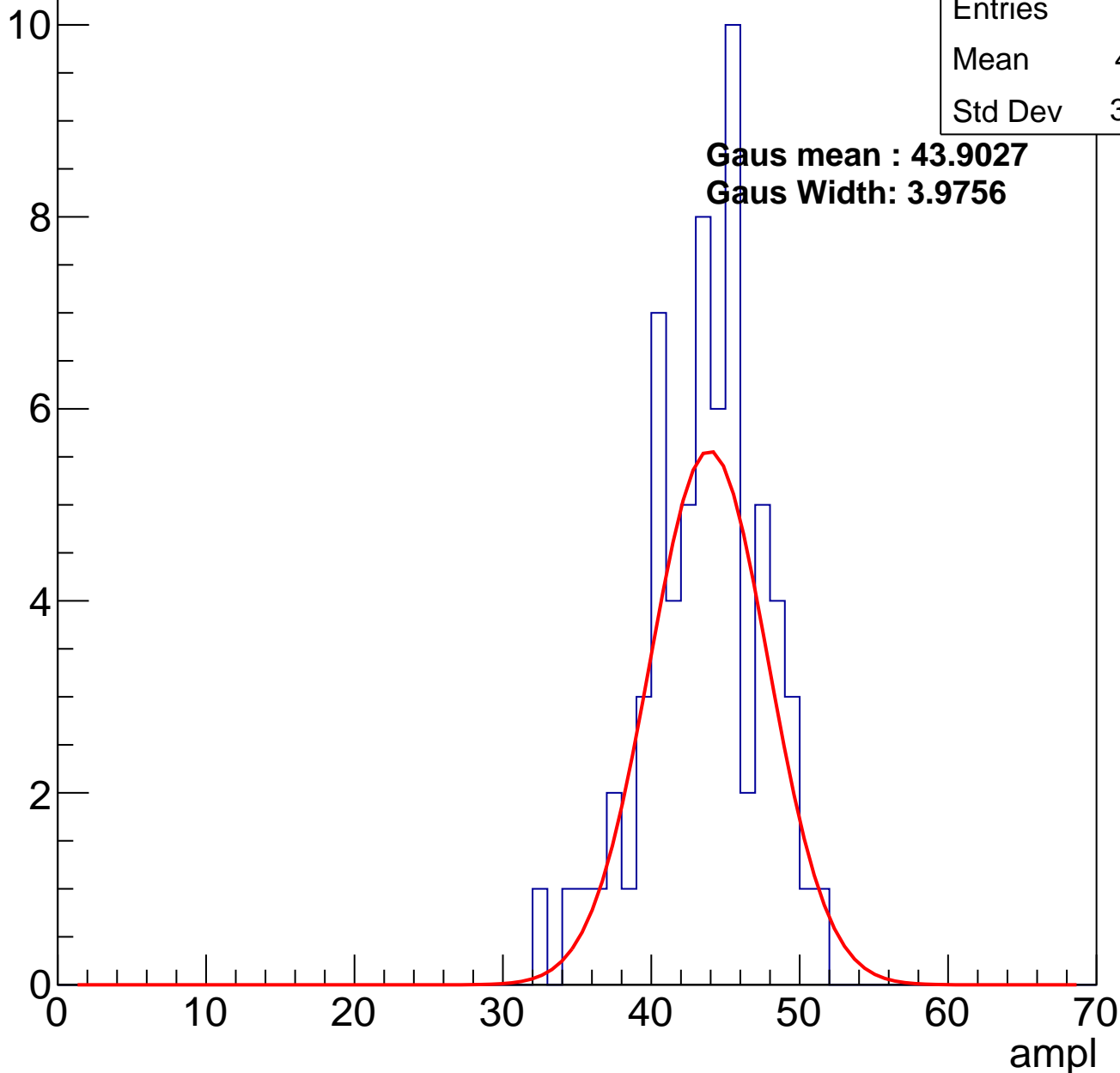
calib_packv5_042523_0143.root, FC#12, port B1

Entries	66
Mean	43.11
Std Dev	3.909

Gaus mean : 43.9027

Gaus Width: 3.9756

Entry

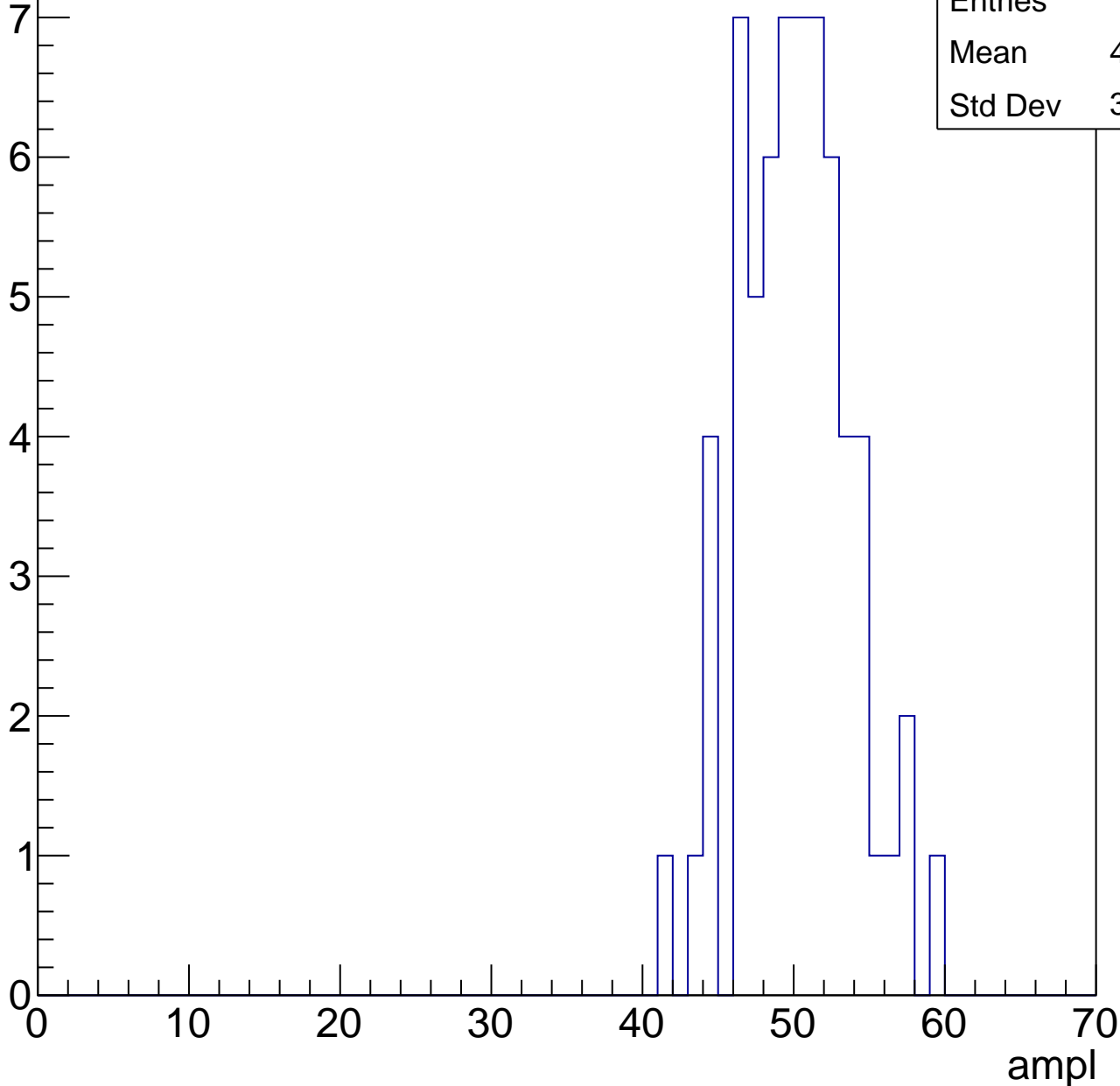


B0L102S, U5-ch47, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	49.67
Std Dev	3.593



B0L102S, U5-ch47, adc4

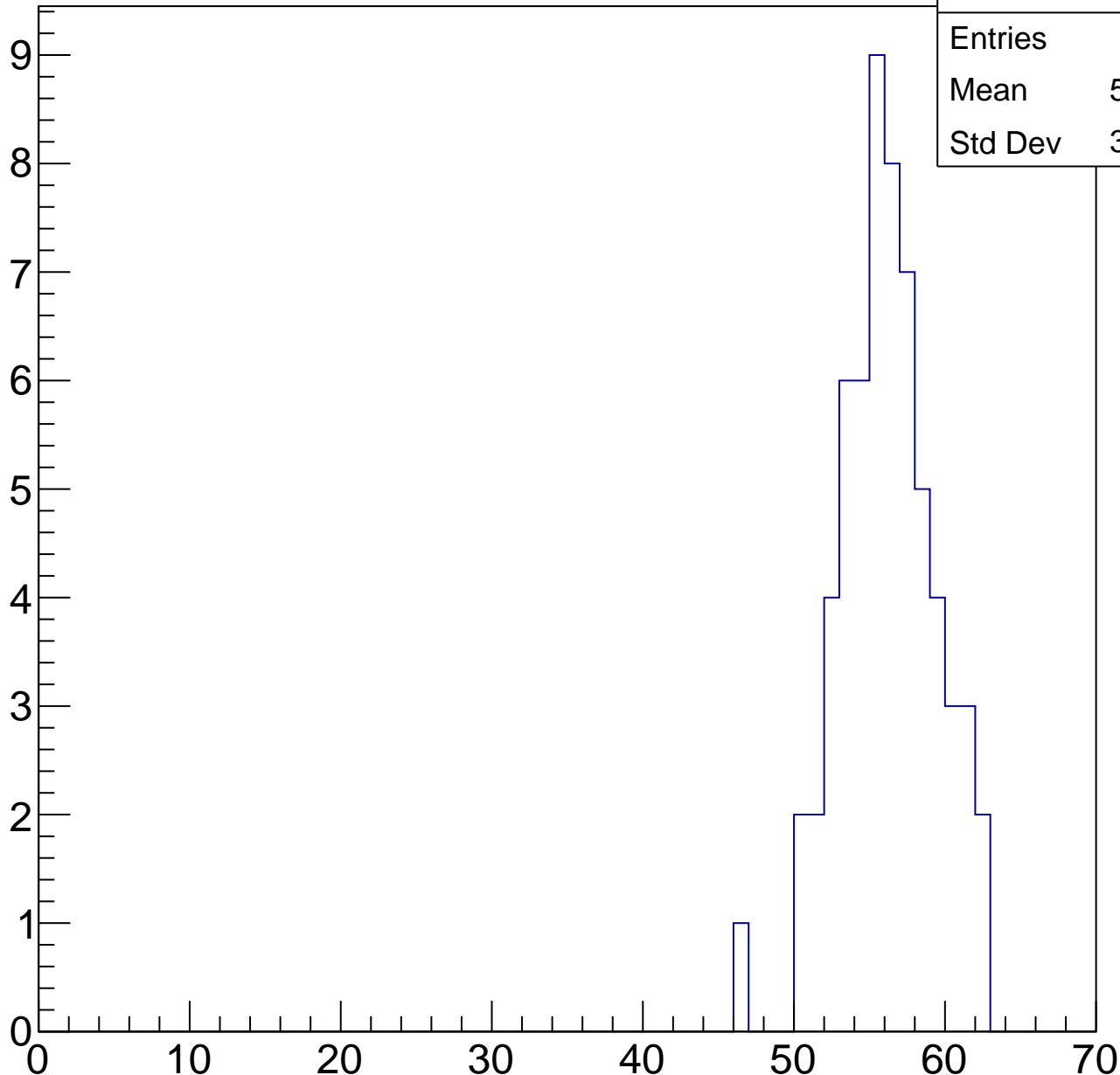
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	55.69
Std Dev	3.175

ampl



B0L102S, U5-ch47, adc5

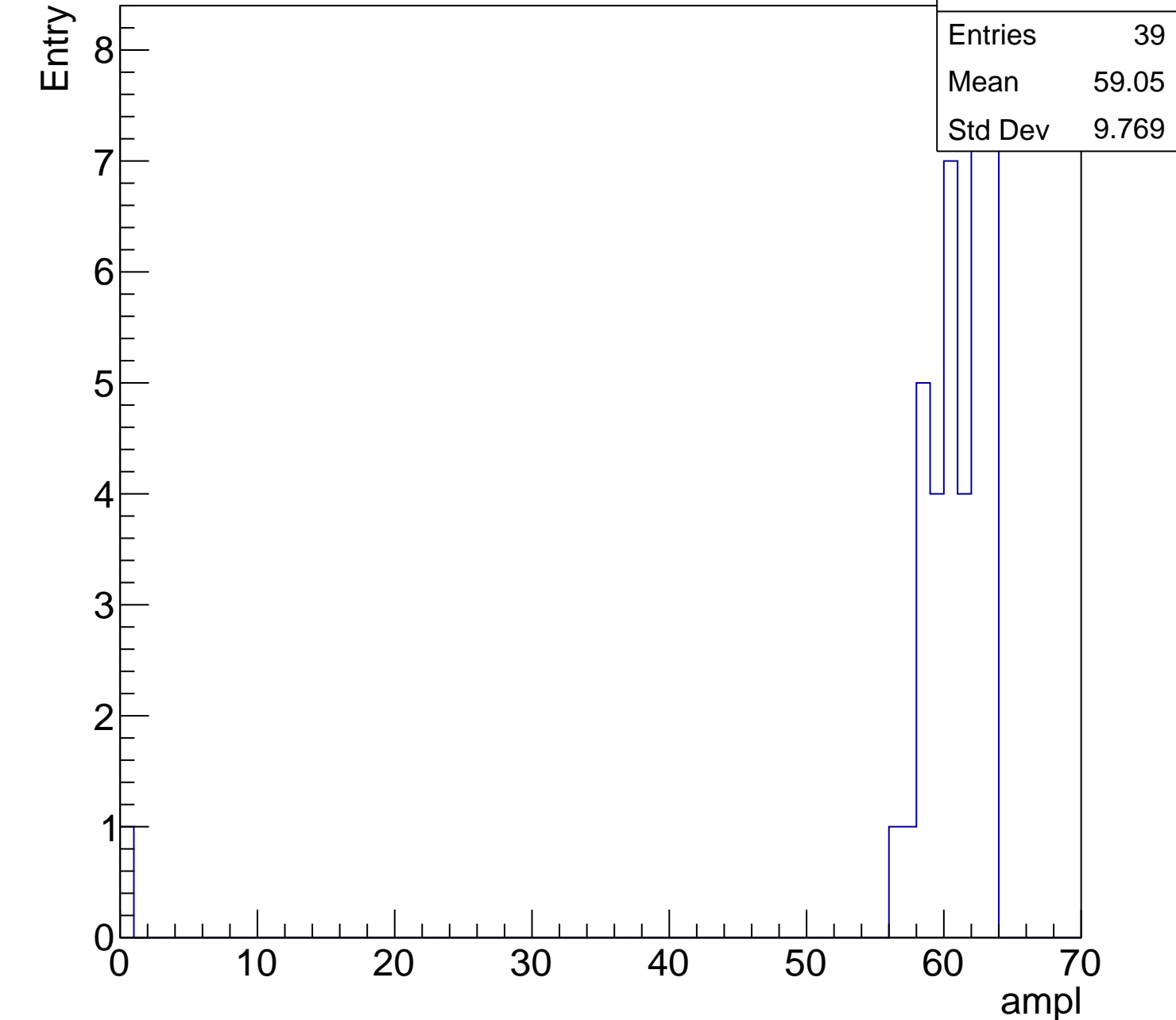
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.05
Std Dev	9.769

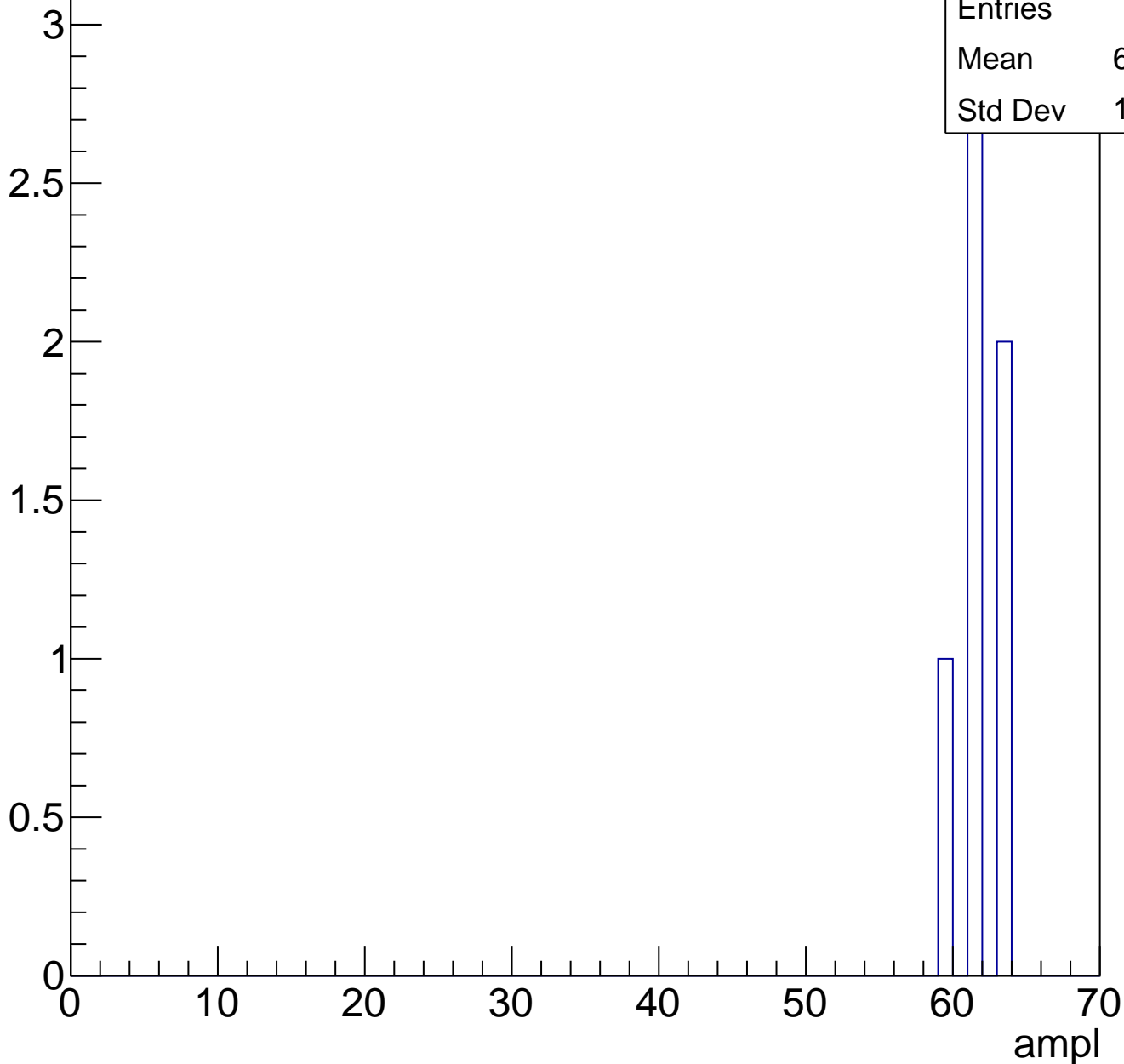
ampl



B0L102S, U5-ch47, adc6

calib_packv5_042523_0143.root, FC#12, port B1

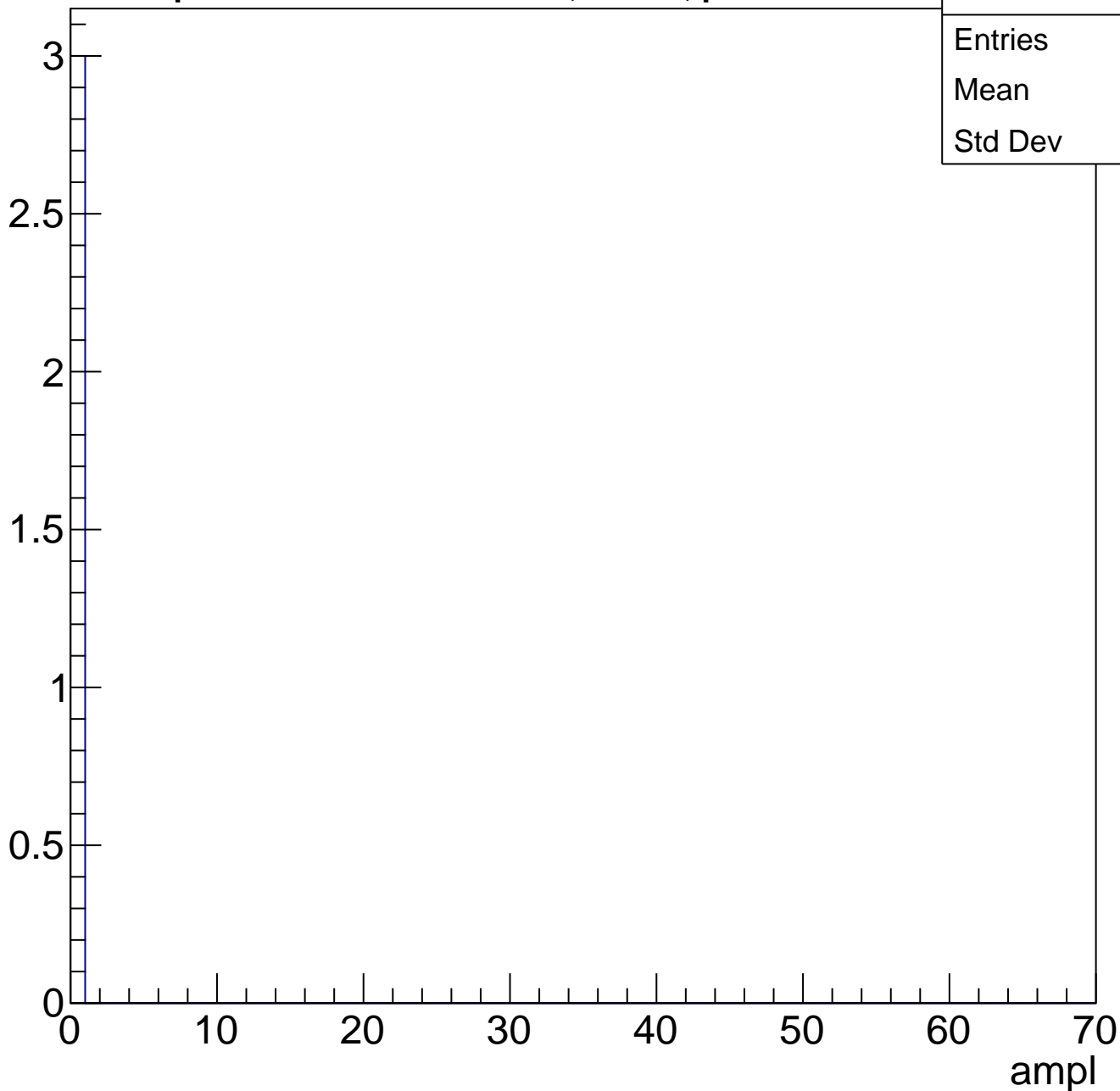
Entry



B0L102S, U5-ch47, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U5-ch48, adc0

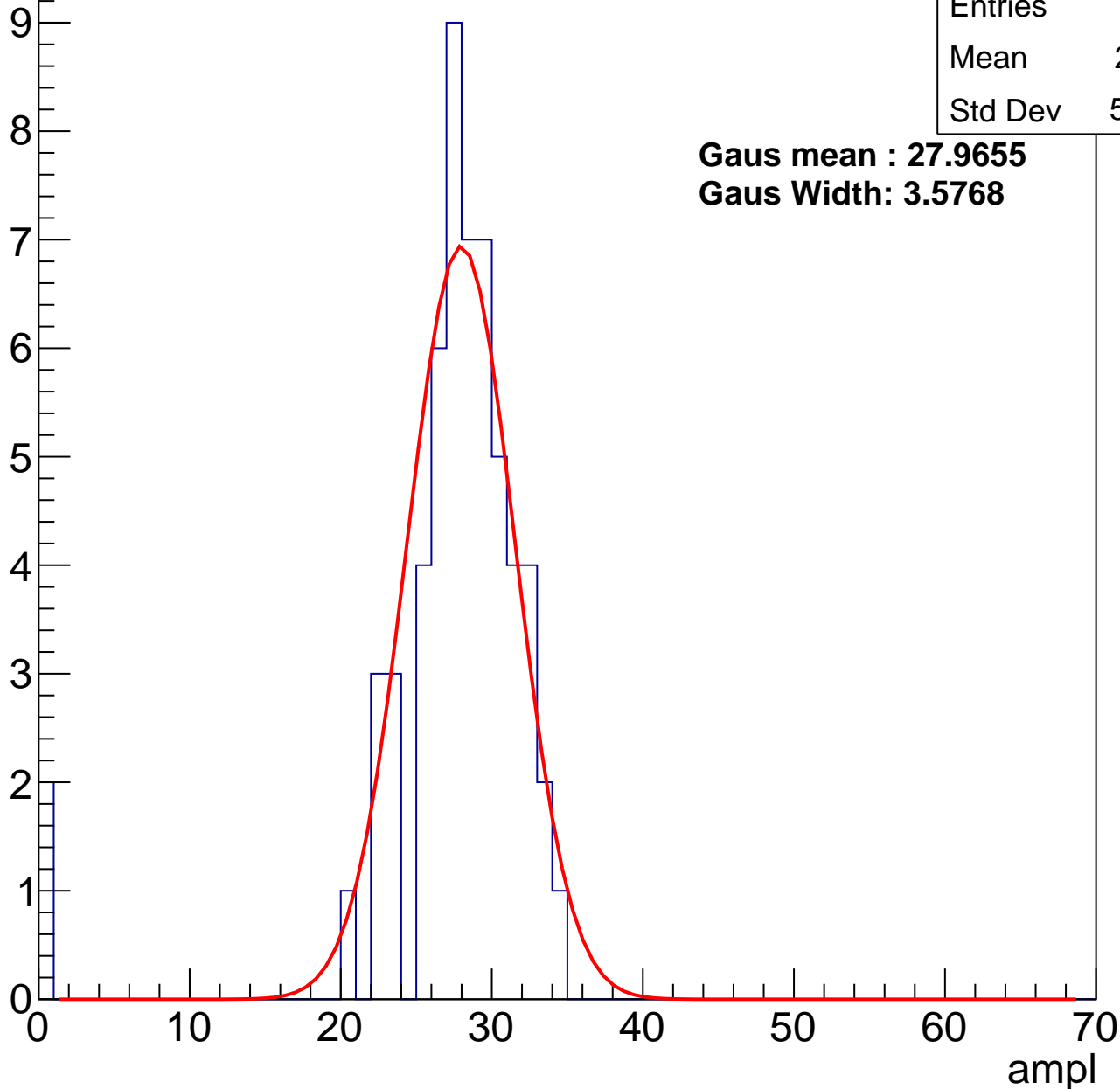
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	26.81
Std Dev	5.894

Gaus mean : 27.9655

Gaus Width: 3.5768



B0L102S, U5-ch48, adc1

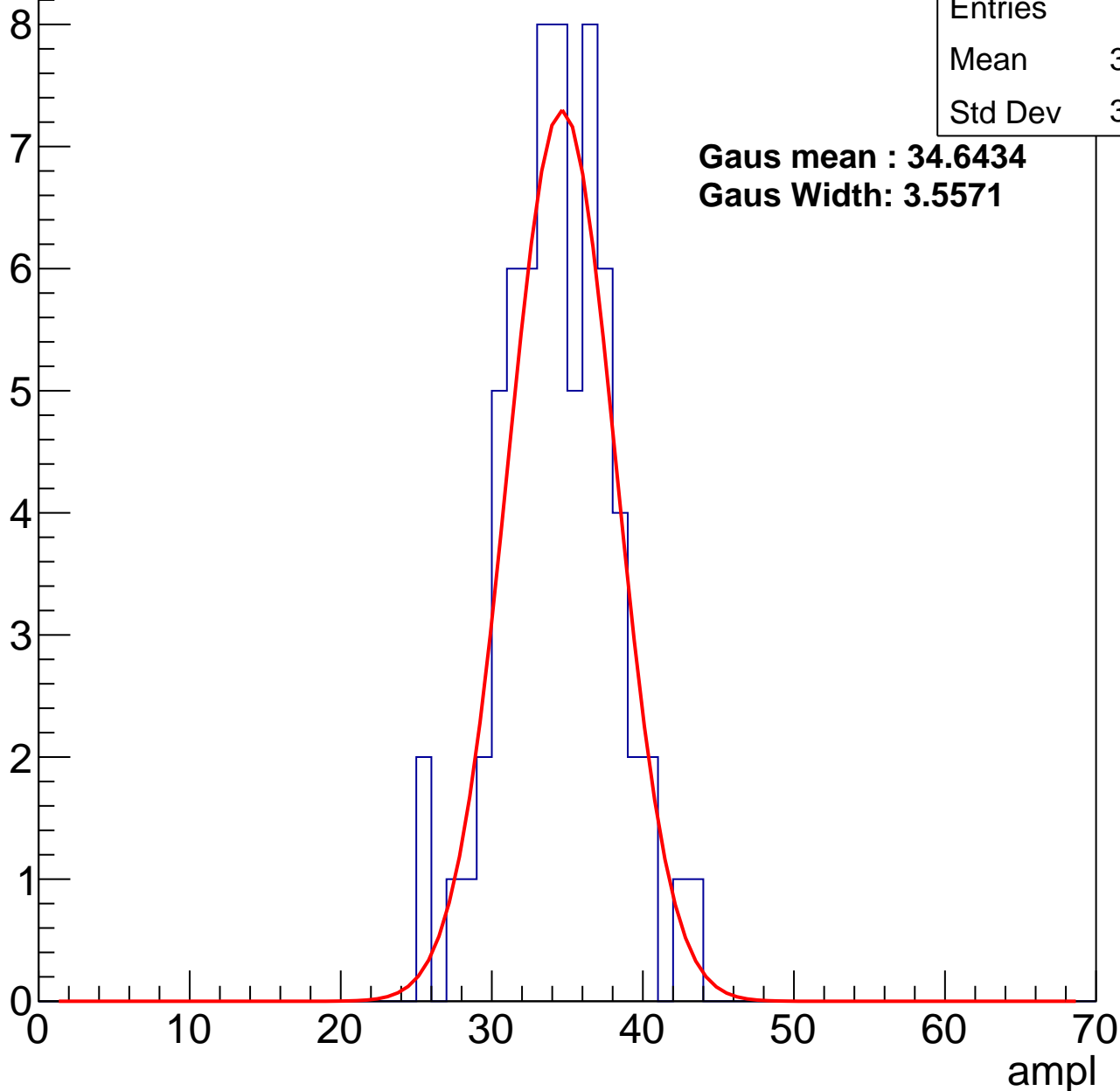
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	33.93
Std Dev	3.607

Gaus mean : 34.6434

Gaus Width: 3.5571



B0L102S, U5-ch48, adc2

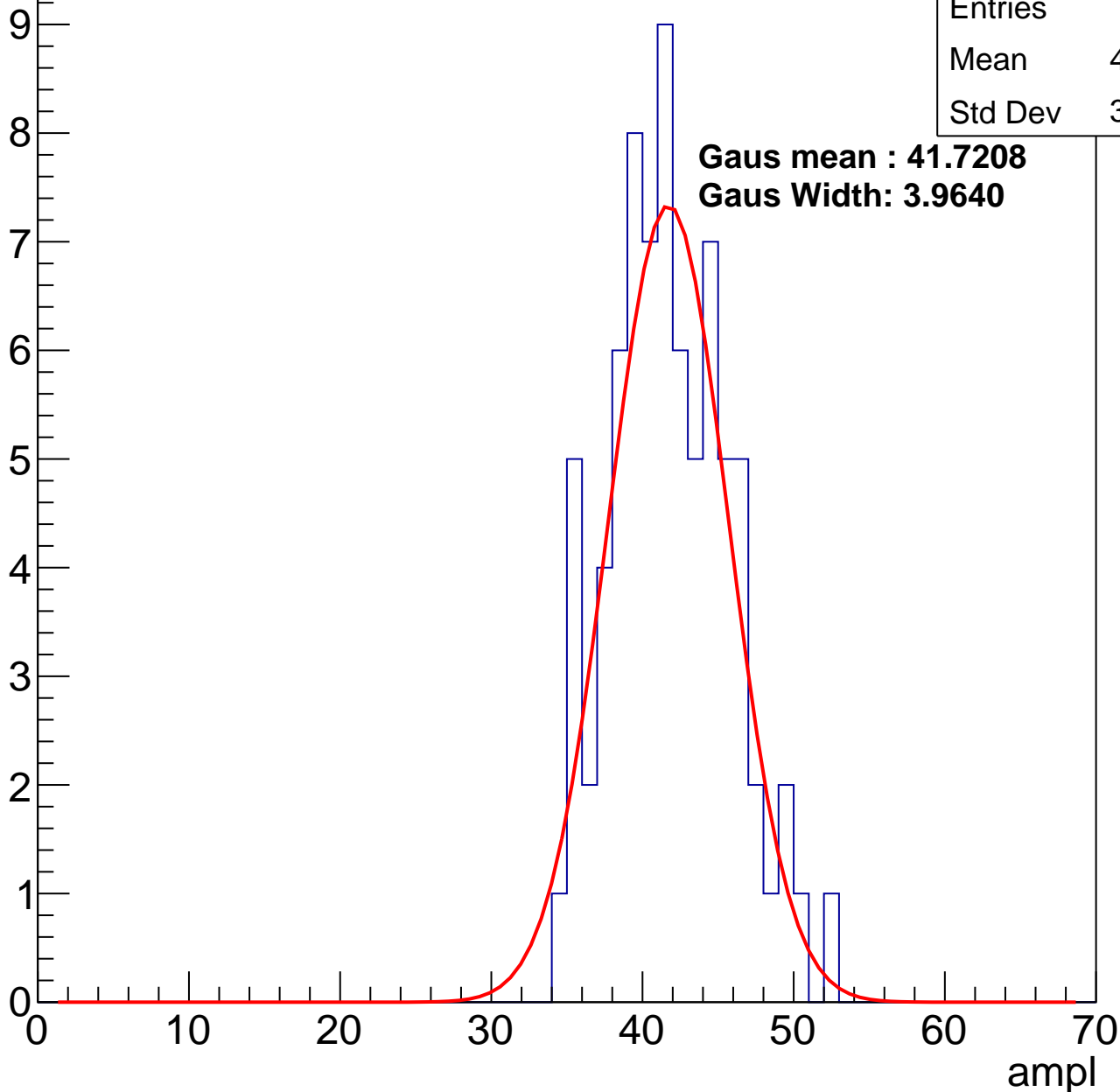
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	41.43
Std Dev	3.893

Gaus mean : 41.7208

Gaus Width: 3.9640

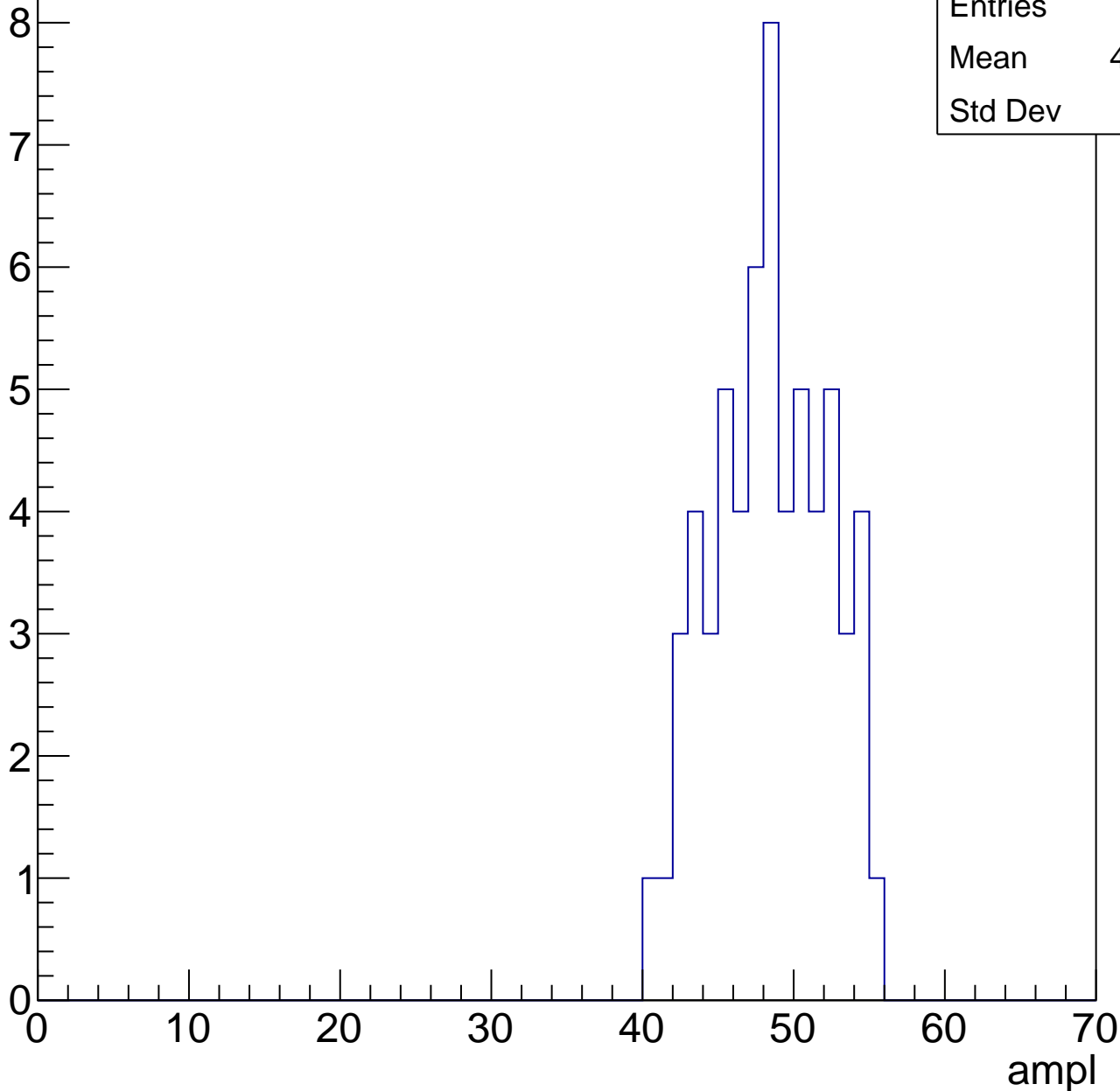


B0L102S, U5-ch48, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

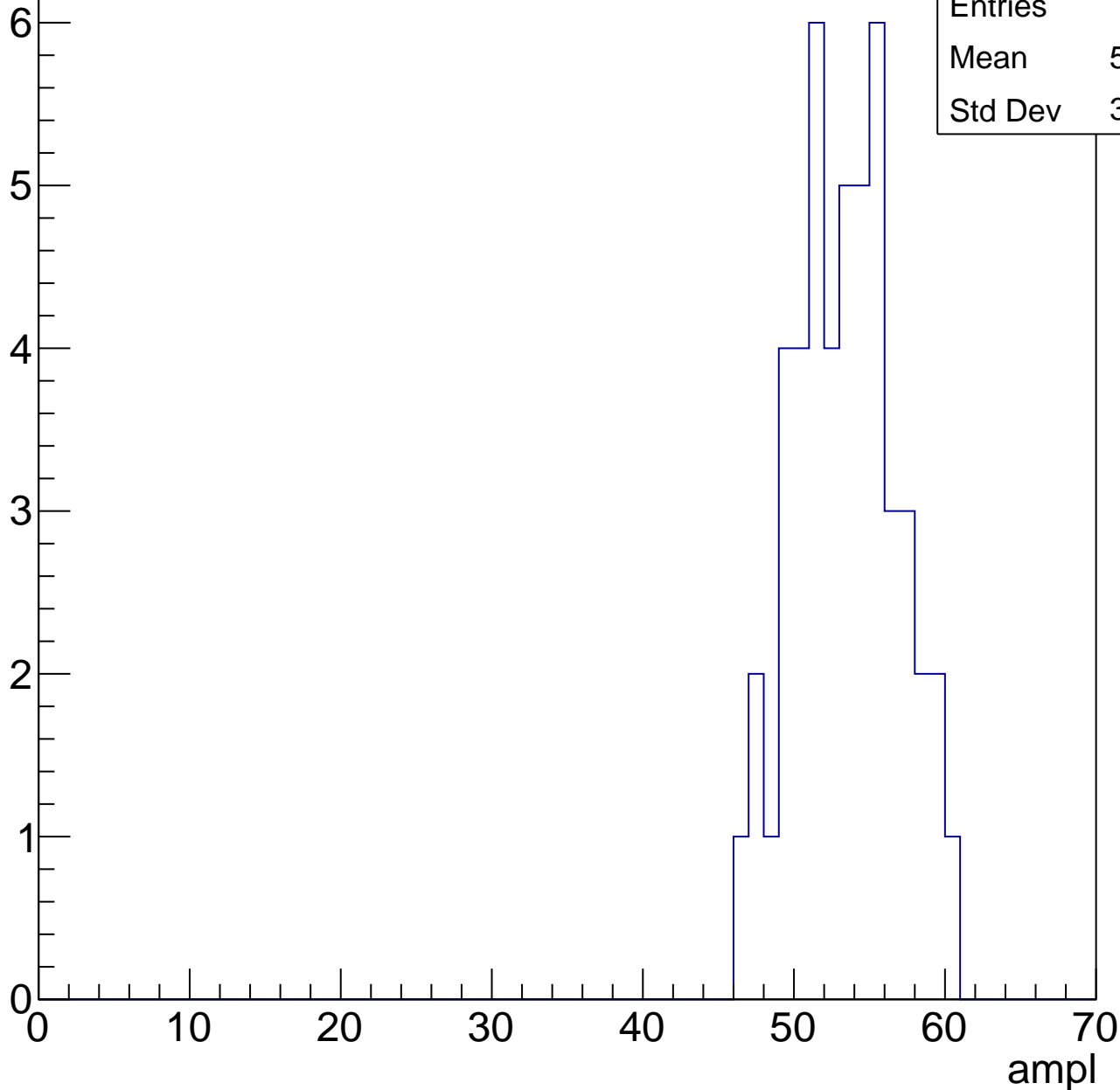
Entries	61
Mean	47.97
Std Dev	3.72



B0L102S, U5-ch48, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



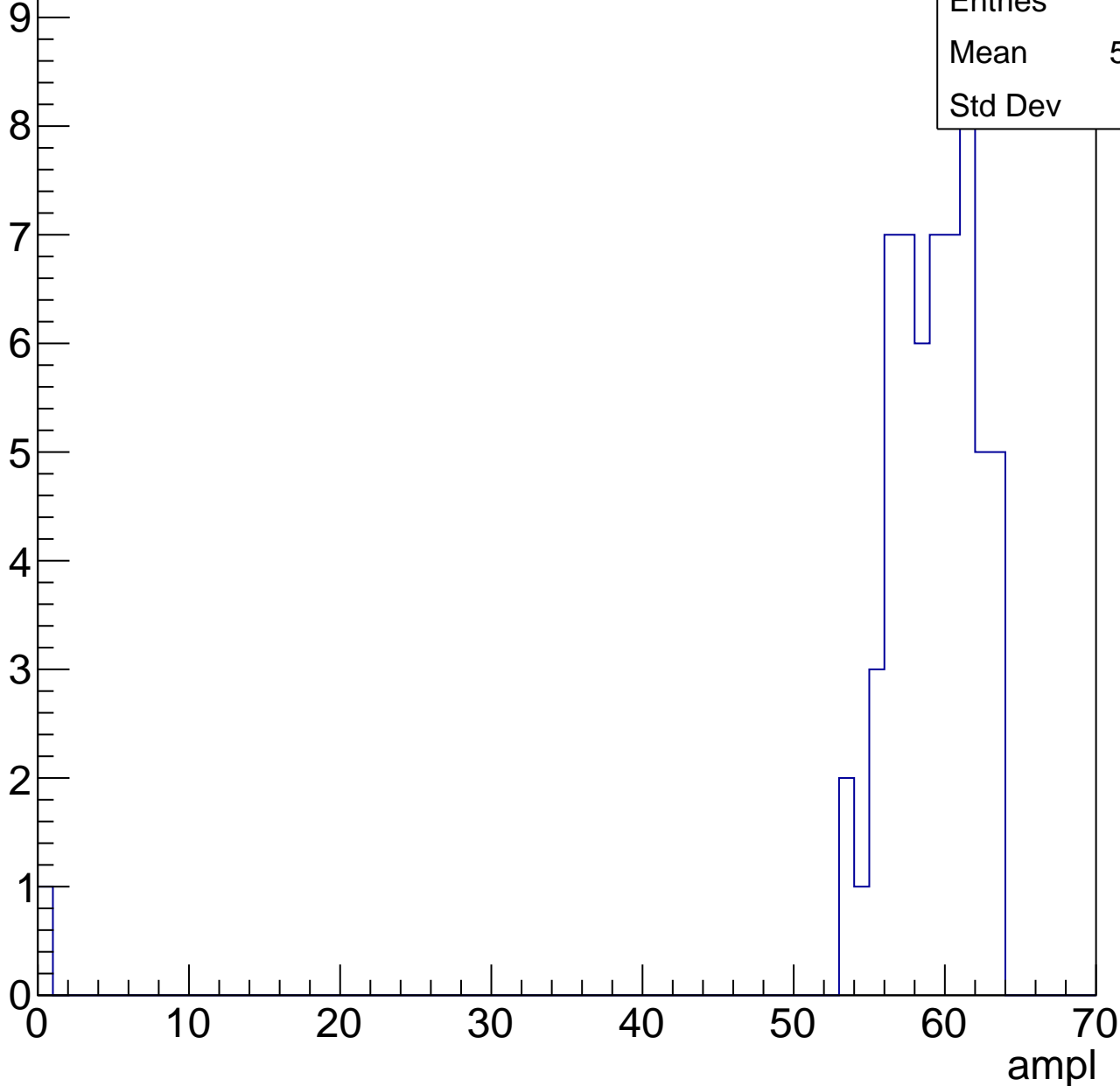
Entries	49
Mean	52.98
Std Dev	3.347

B0L102S, U5-ch48, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	57.85
Std Dev	7.97

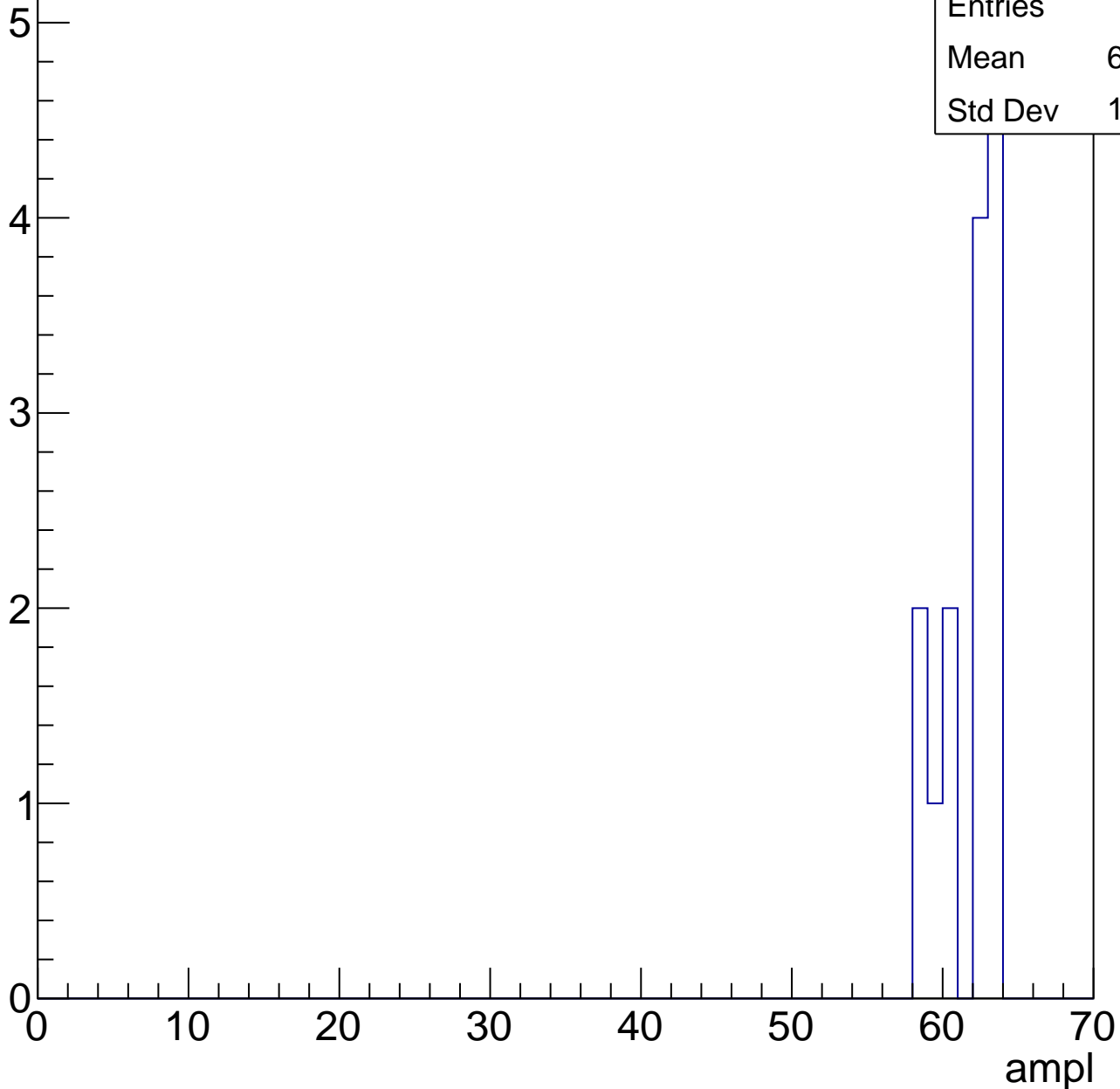


B0L102S, U5-ch48, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

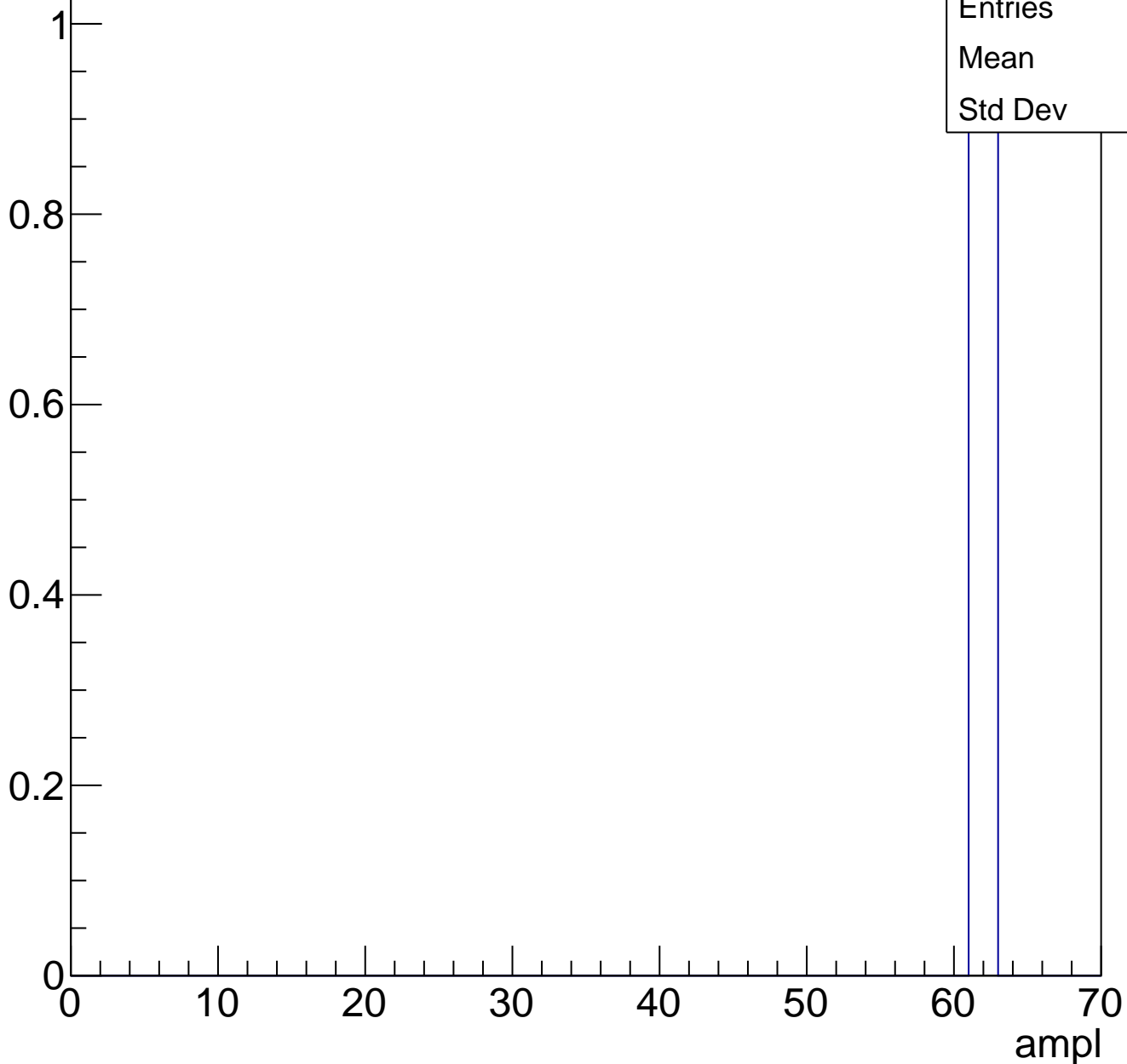
Entries	14
Mean	61.29
Std Dev	1.829



B0L102S, U5-ch48, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch49, adc0

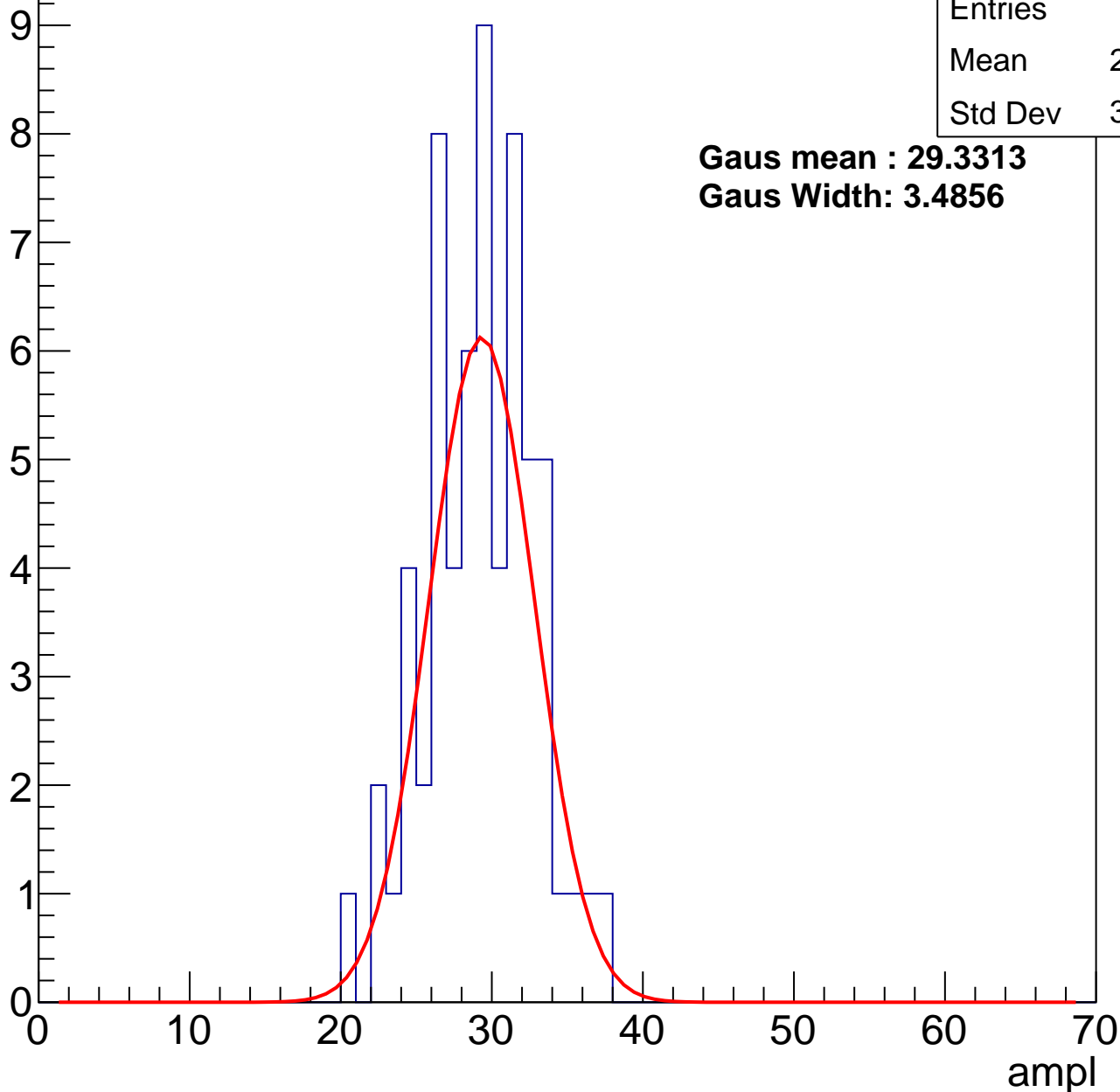
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.78
Std Dev	3.503

Gaus mean : 29.3313

Gaus Width: 3.4856



B0L102S, U5-ch49, adc1

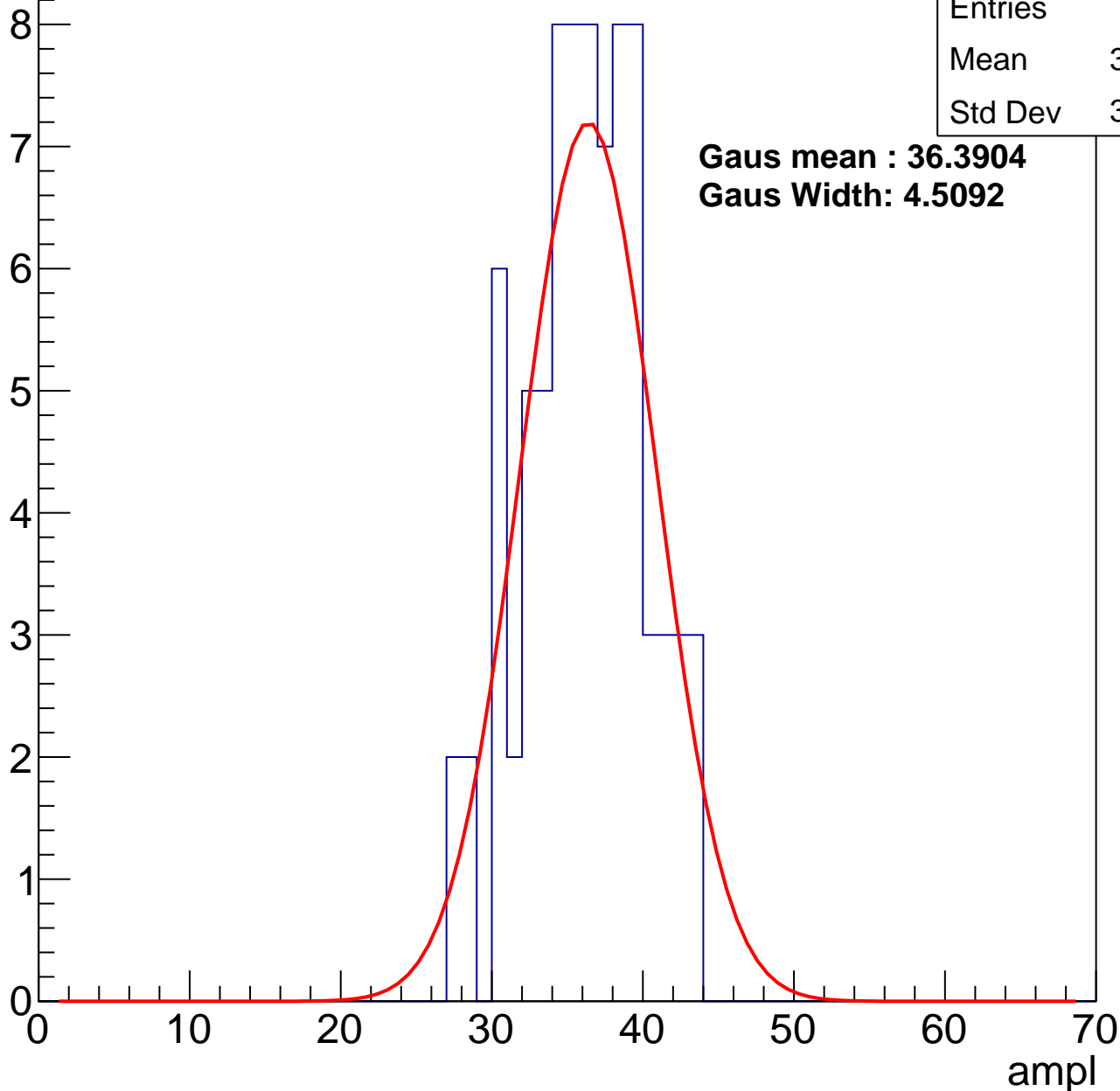
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	35.68
Std Dev	3.852

Gaus mean : 36.3904

Gaus Width: 4.5092



B0L102S, U5-ch49, adc2

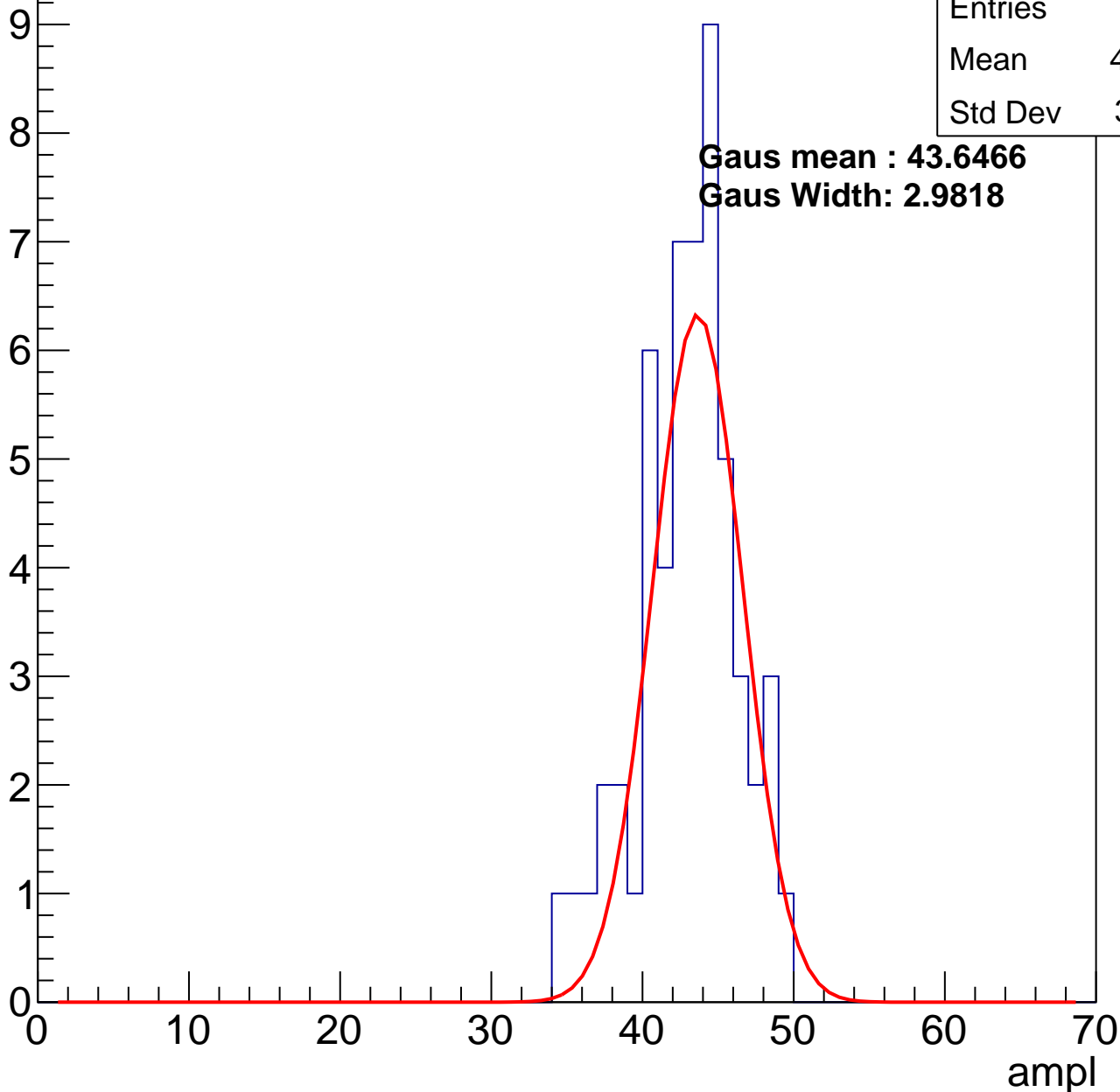
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	42.53
Std Dev	3.291

Gaus mean : 43.6466

Gaus Width: 2.9818

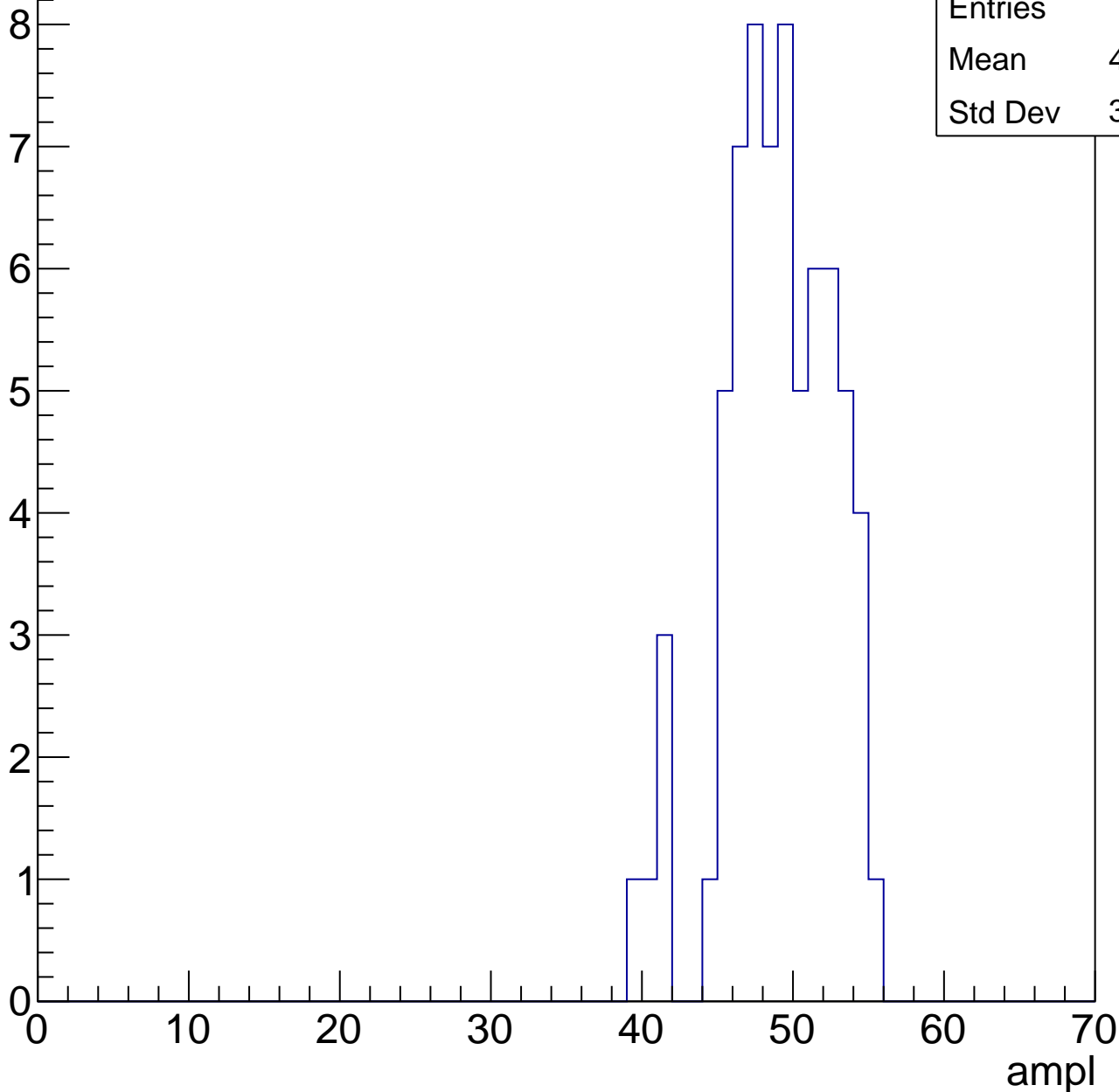


B0L102S, U5-ch49, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	48.54
Std Dev	3.566

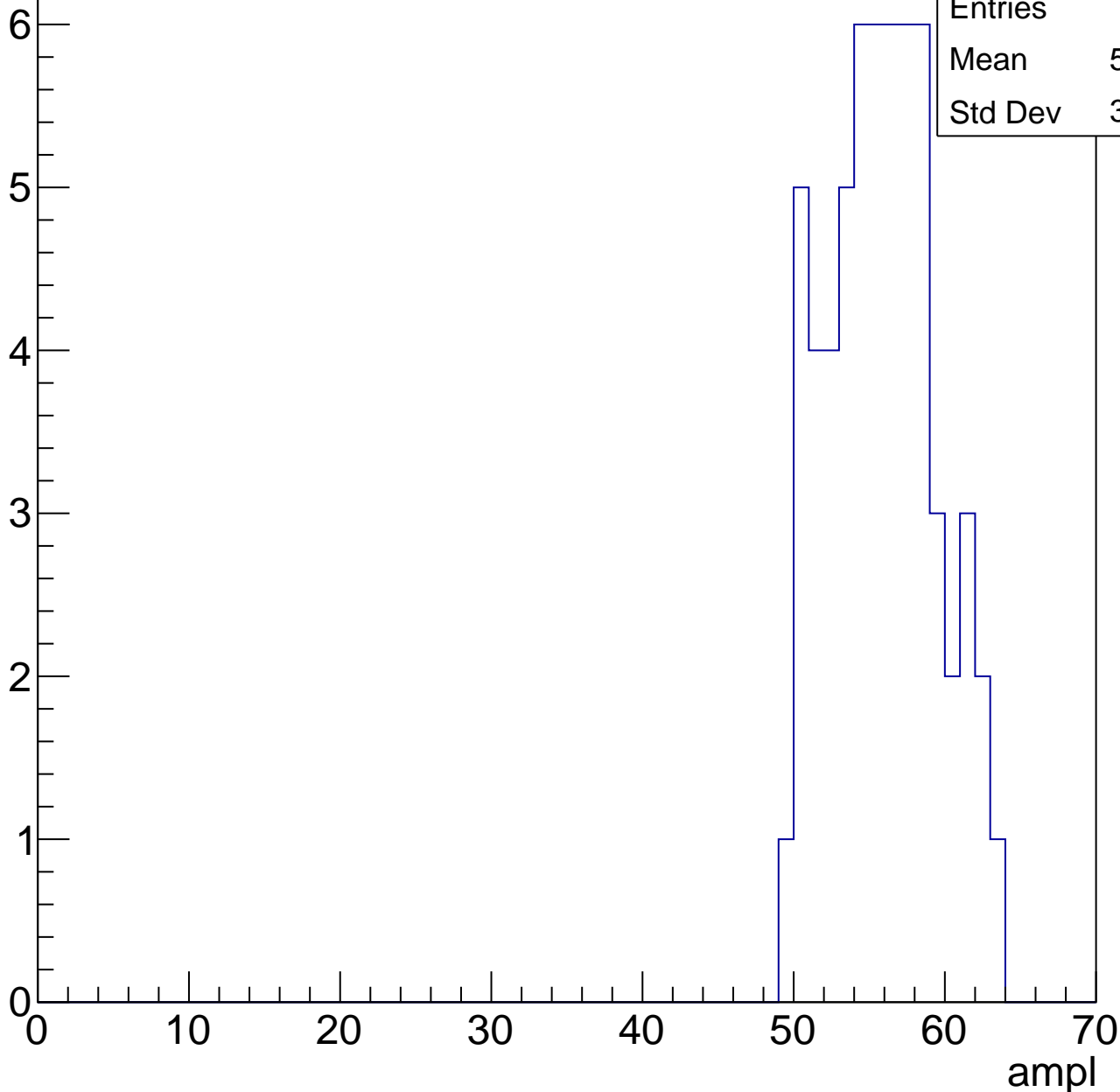


B0L102S, U5-ch49, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	55.38
Std Dev	3.489



B0L102S, U5-ch49, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 59.65

Std Dev 2.398

ampl

0

10

20

30

40

50

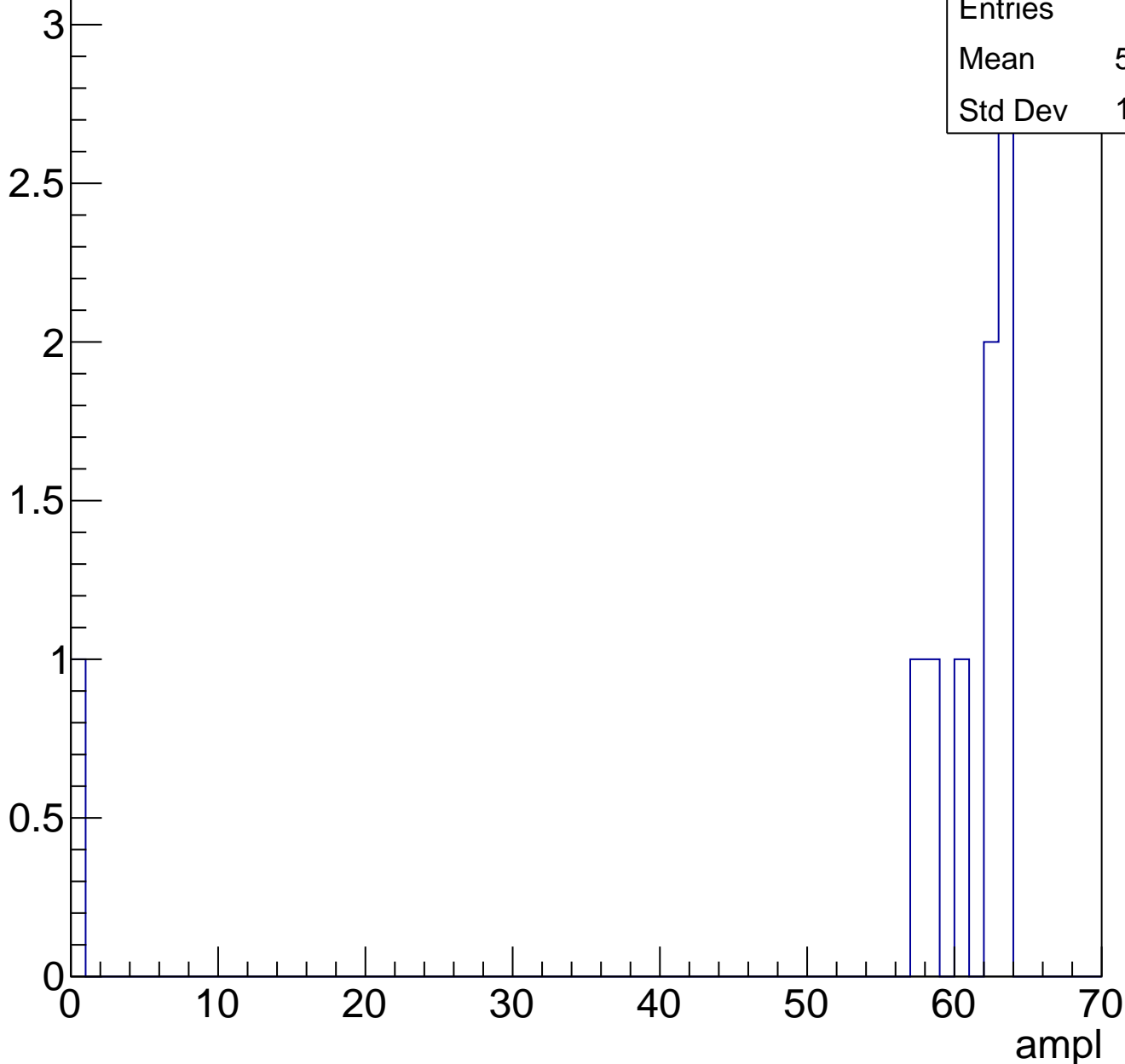
60

70

B0L102S, U5-ch49, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch49, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch50, adc0

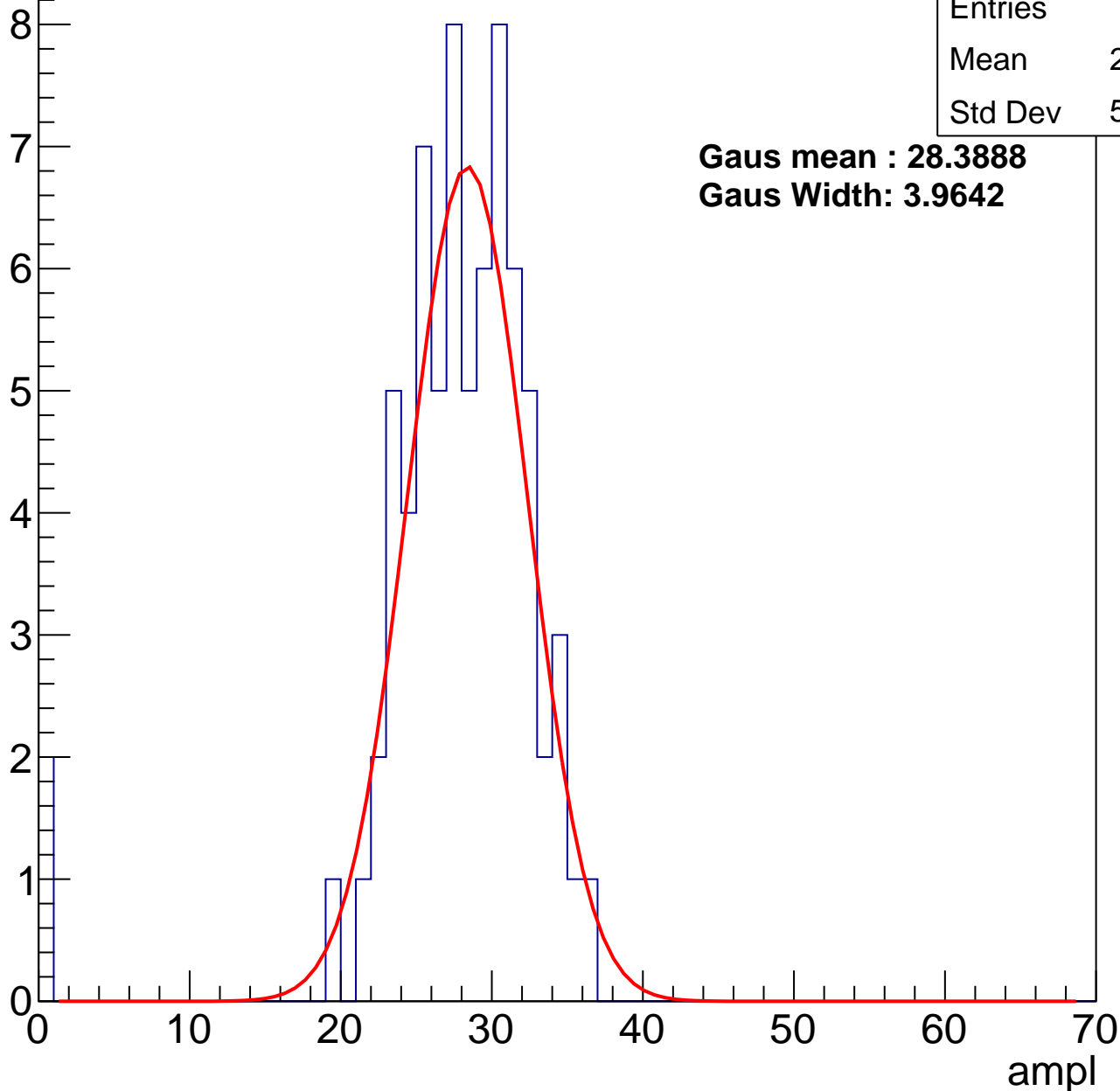
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	27.15
Std Dev	5.823

Gaus mean : 28.3888

Gaus Width: 3.9642



B0L102S, U5-ch50, adc1

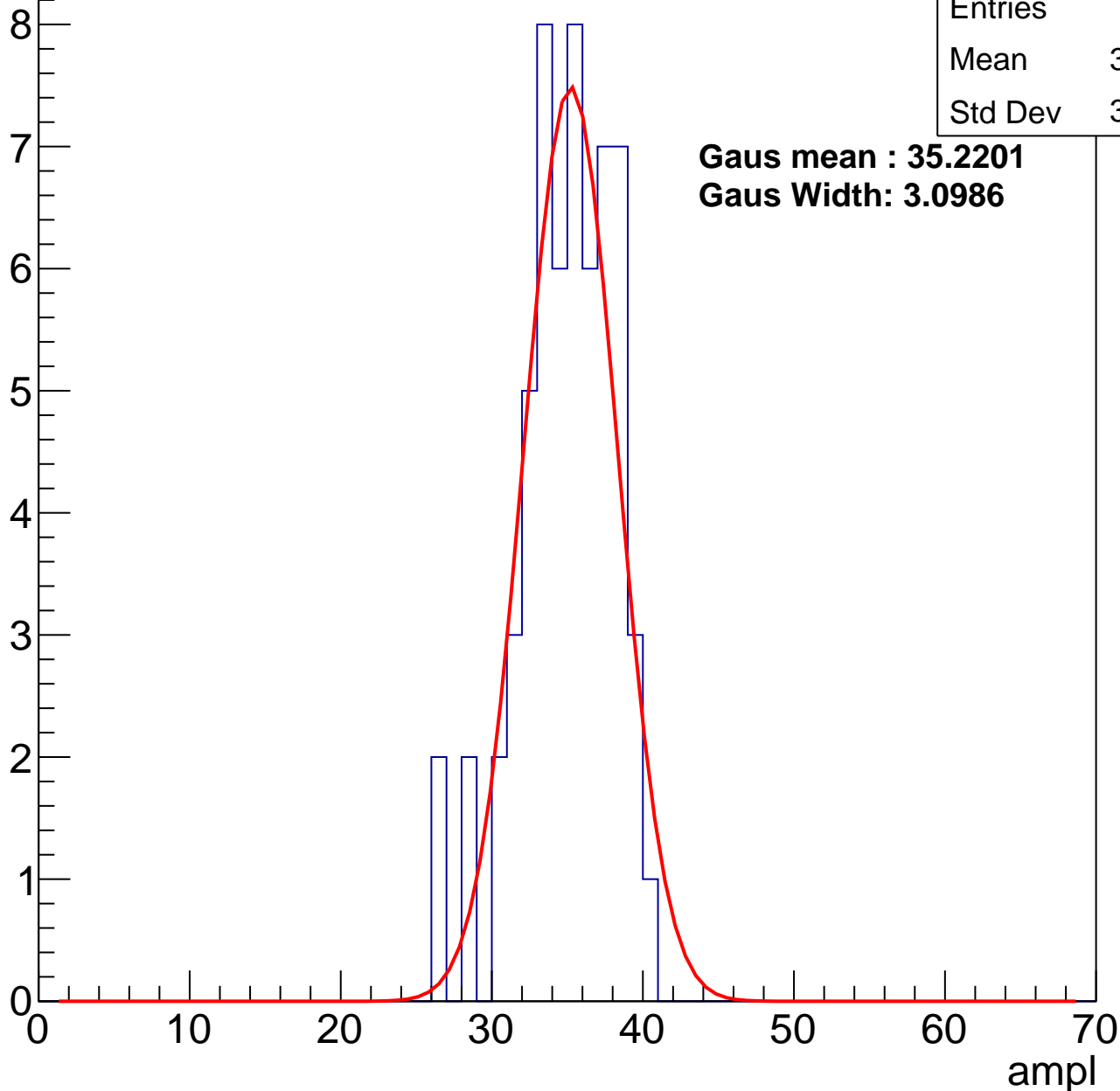
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	34.45
Std Dev	3.143

Gaus mean : 35.2201

Gaus Width: 3.0986



B0L102S, U5-ch50, adc2

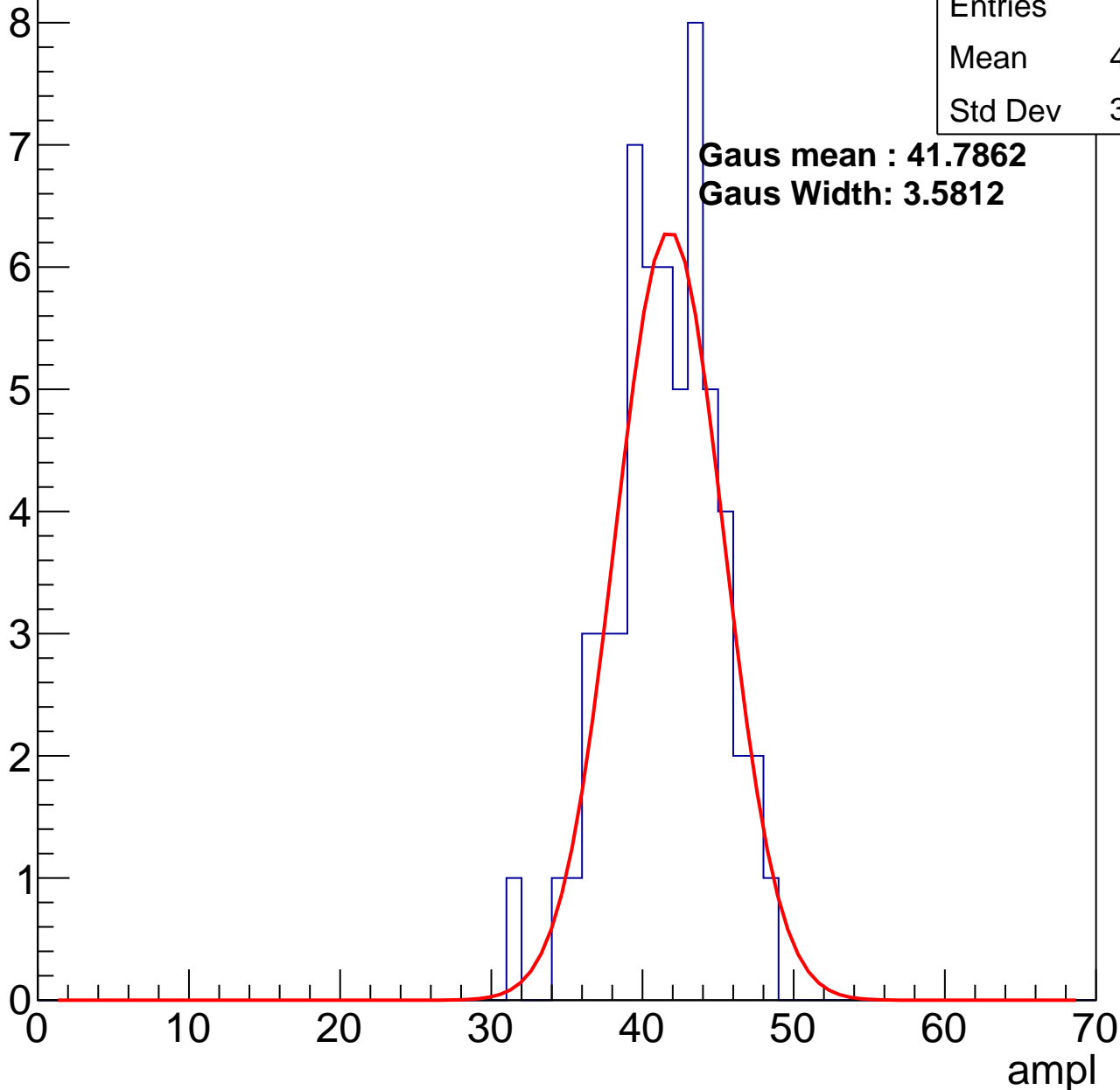
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	41.03
Std Dev	3.439

Gaus mean : 41.7862

Gaus Width: 3.5812

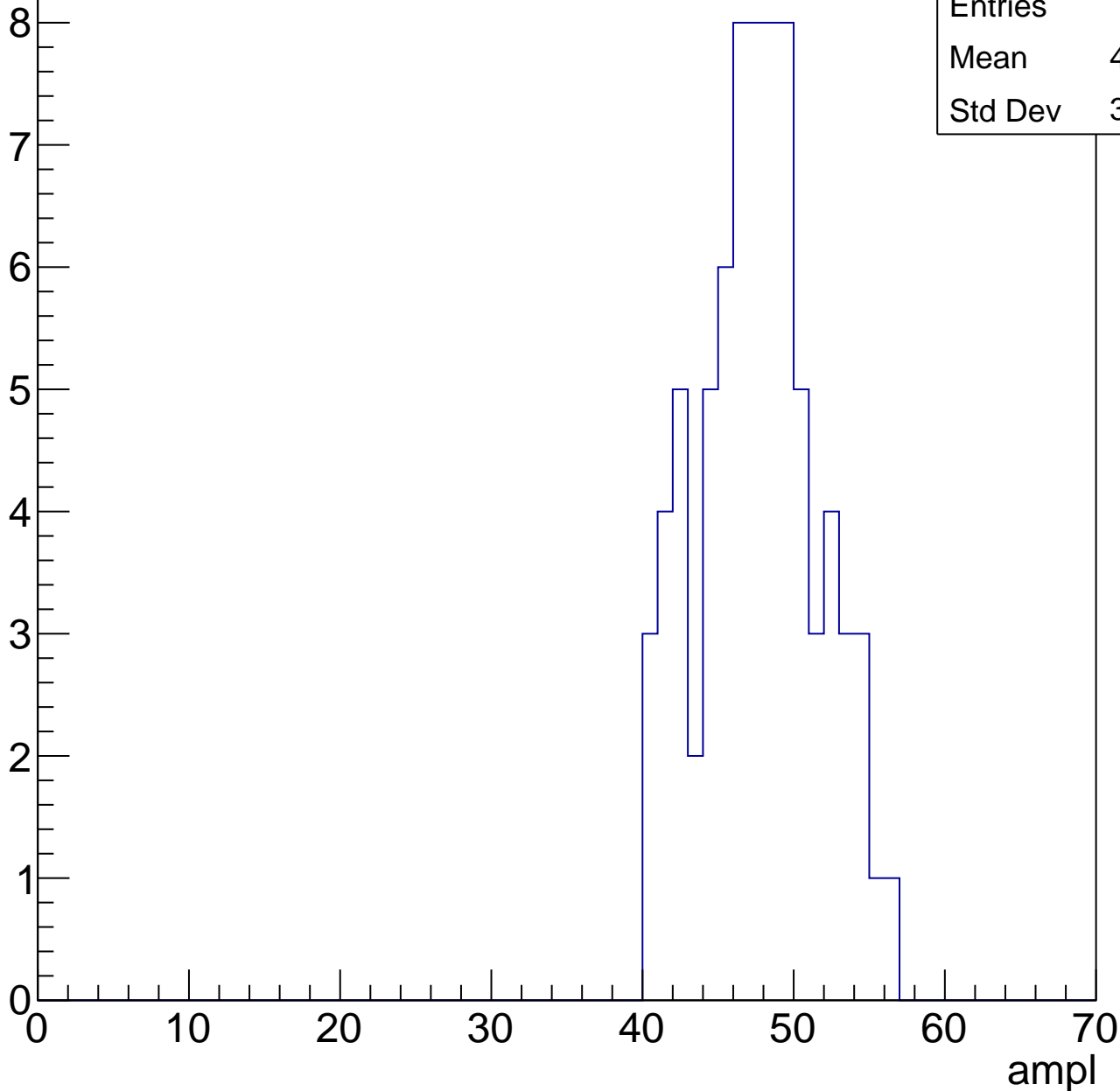


B0L102S, U5-ch50, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	47.18
Std Dev	3.874

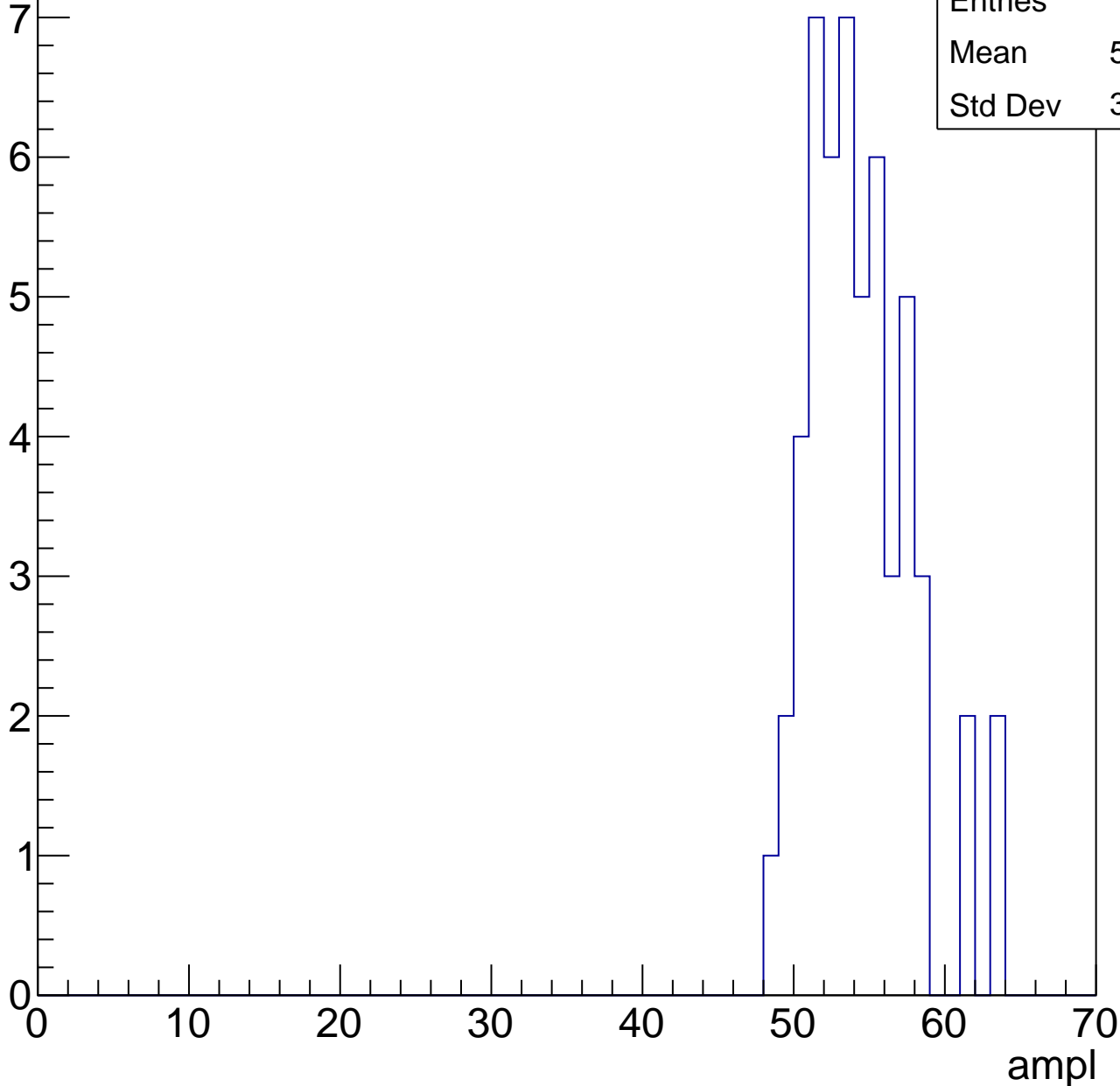


B0L102S, U5-ch50, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	53.98
Std Dev	3.406

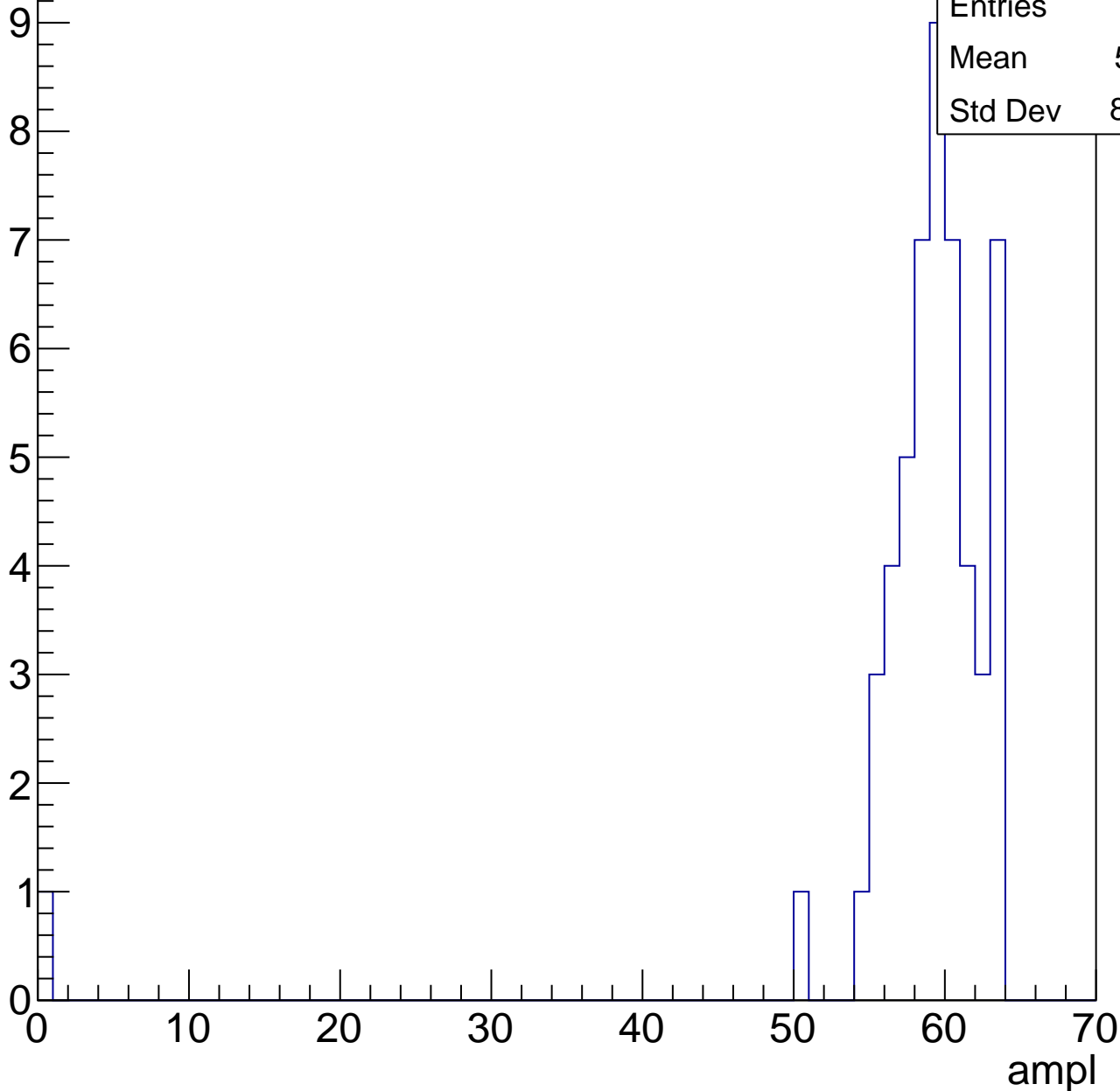


B0L102S, U5-ch50, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	57.81
Std Dev	8.533

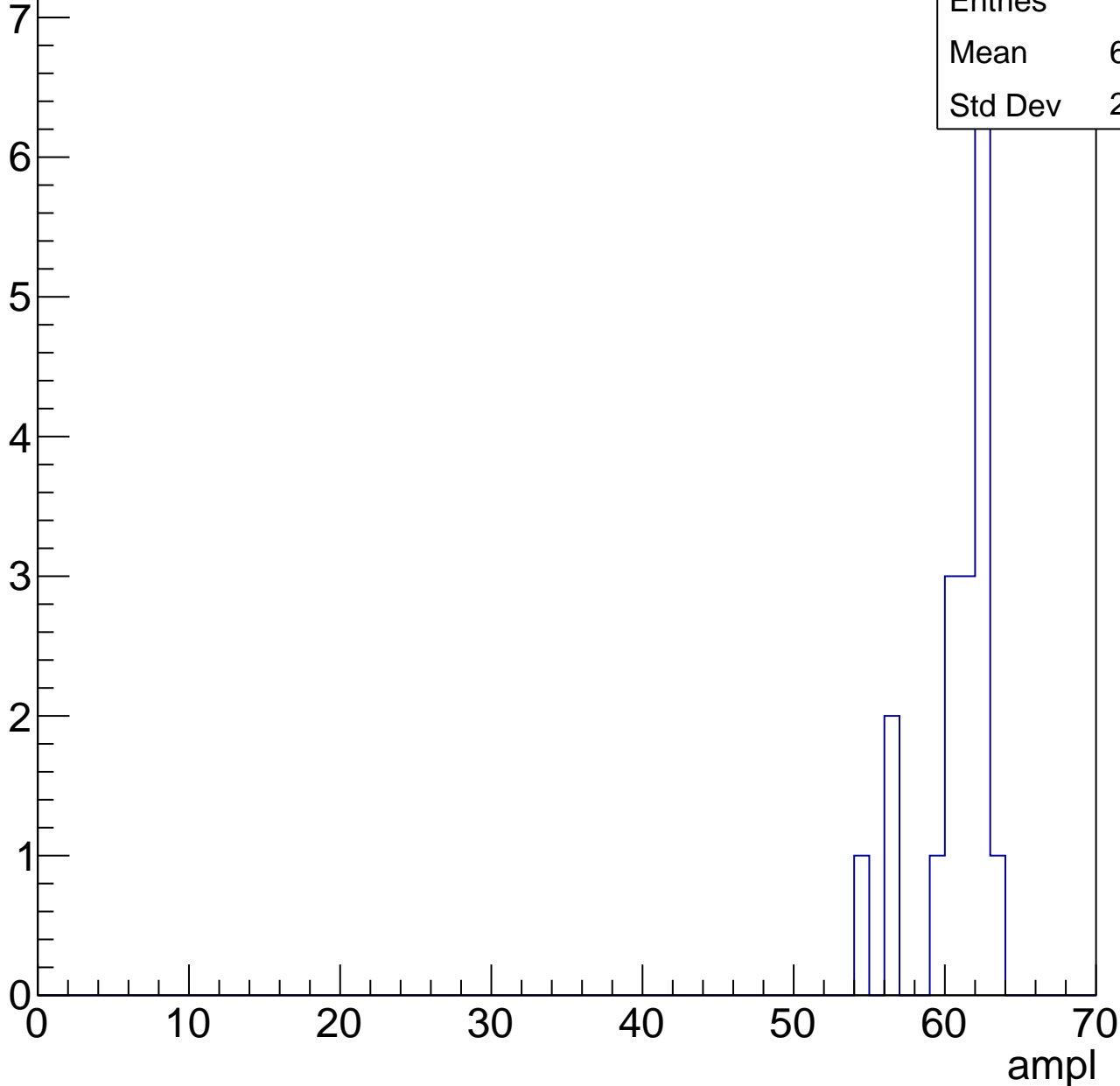


B0L102S, U5-ch50, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	18
Mean	60.28
Std Dev	2.445



B0L102S, U5-ch50, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch51, adc0

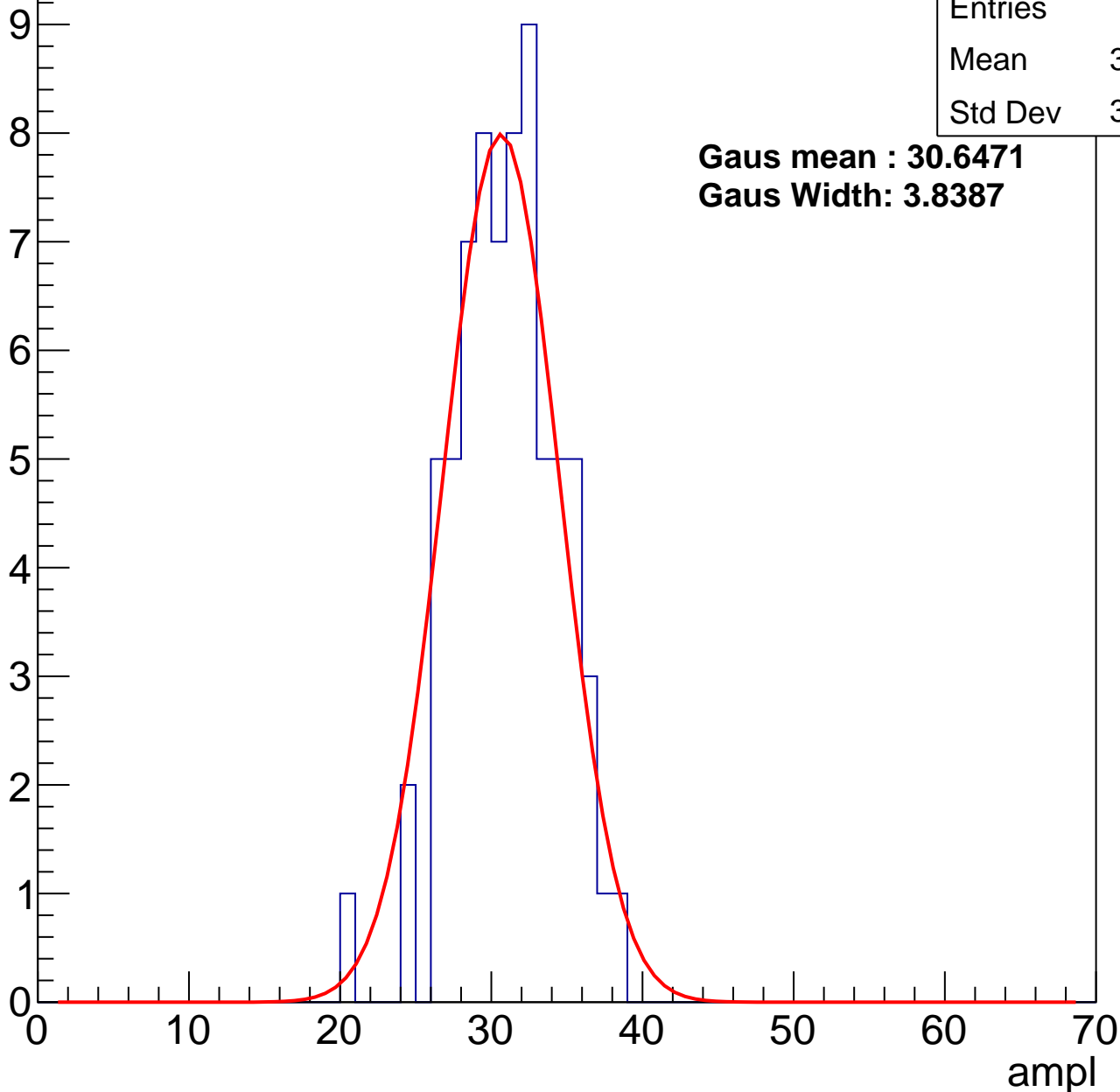
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	30.56
Std Dev	3.387

Gaus mean : 30.6471

Gaus Width: 3.8387



B0L102S, U5-ch51, adc1

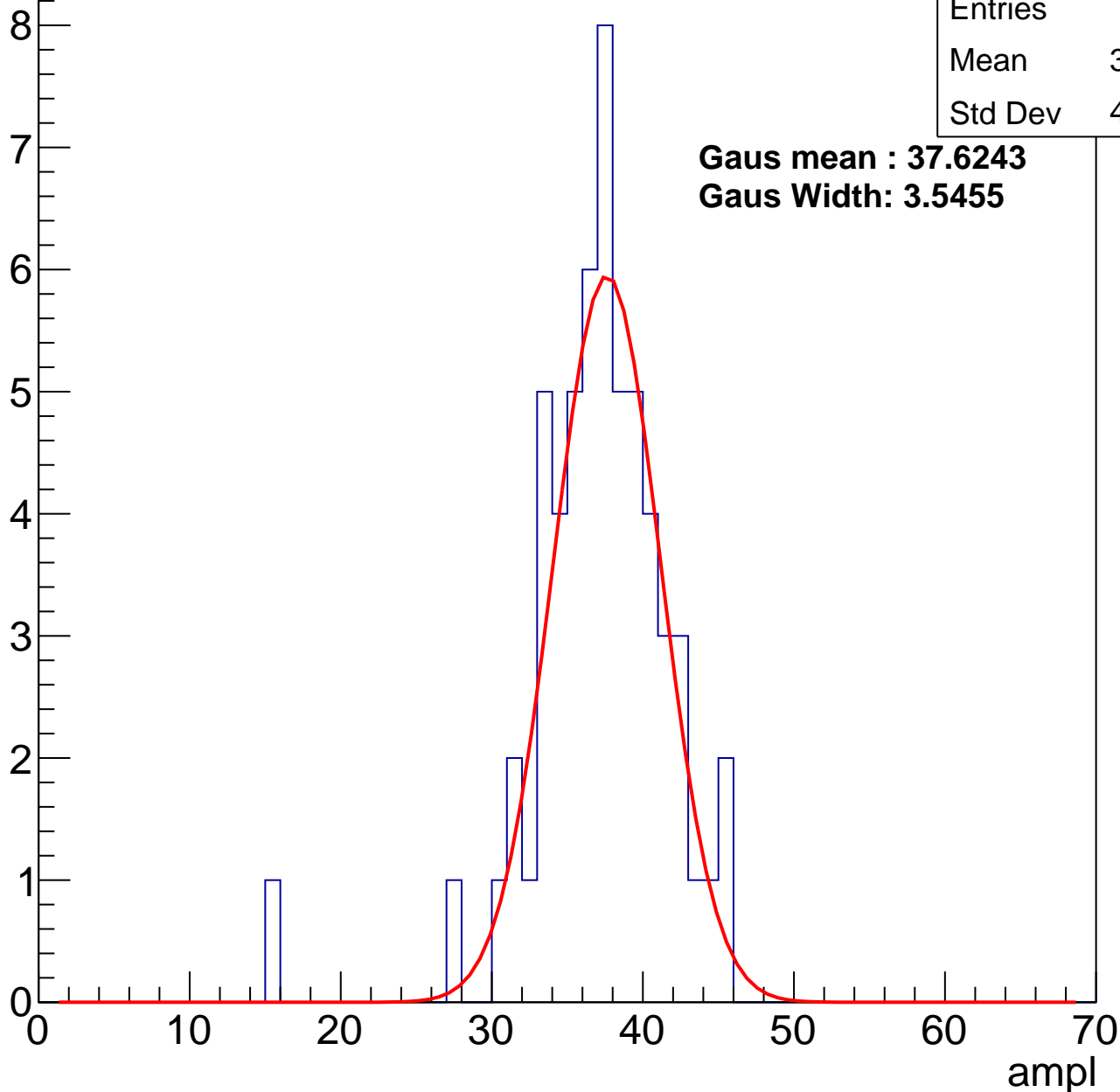
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	36.64
Std Dev	4.663

Gaus mean : 37.6243

Gaus Width: 3.5455



B0L102S, U5-ch51, adc2

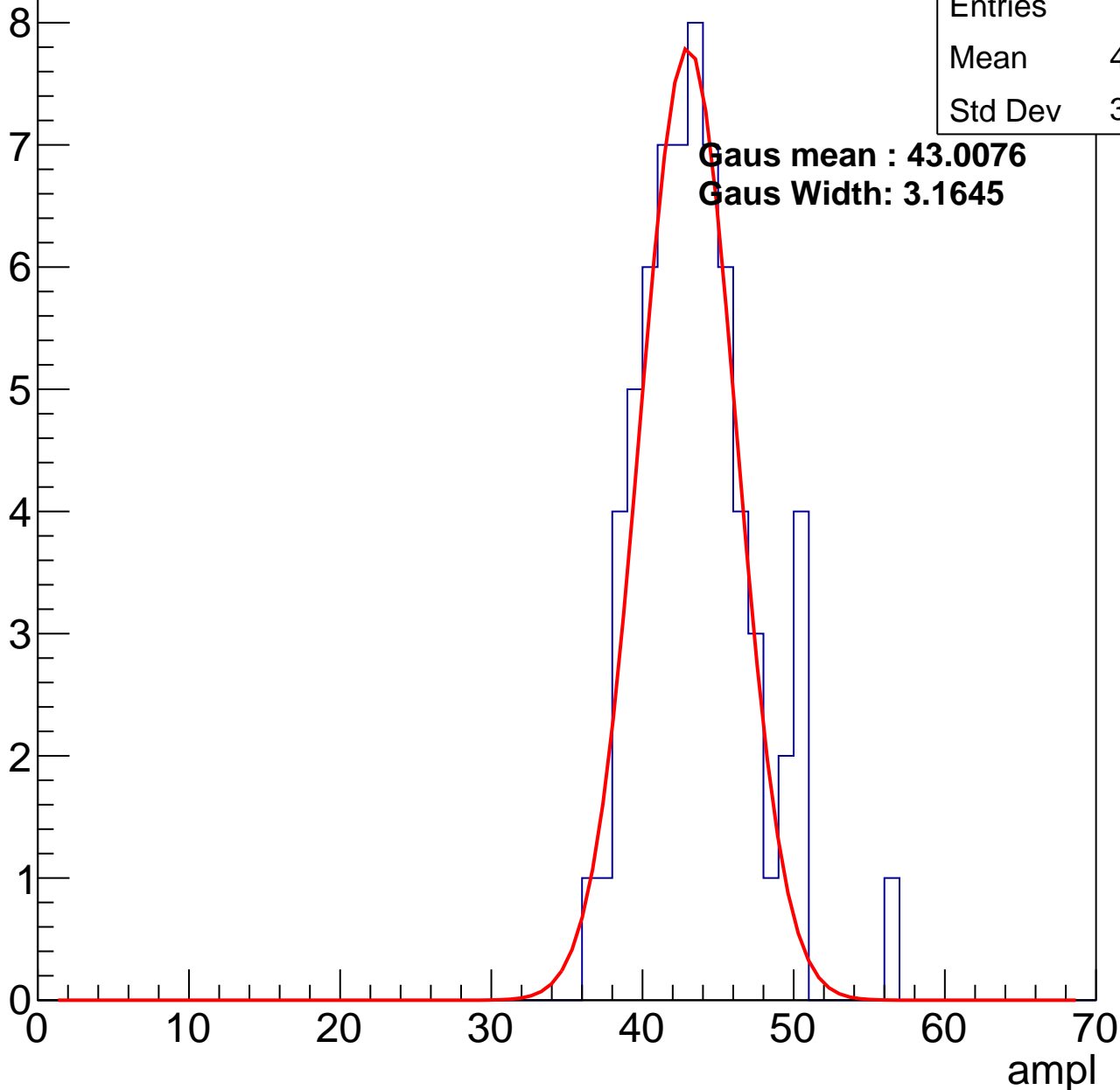
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	43.13
Std Dev	3.725

Gaus mean : 43.0076

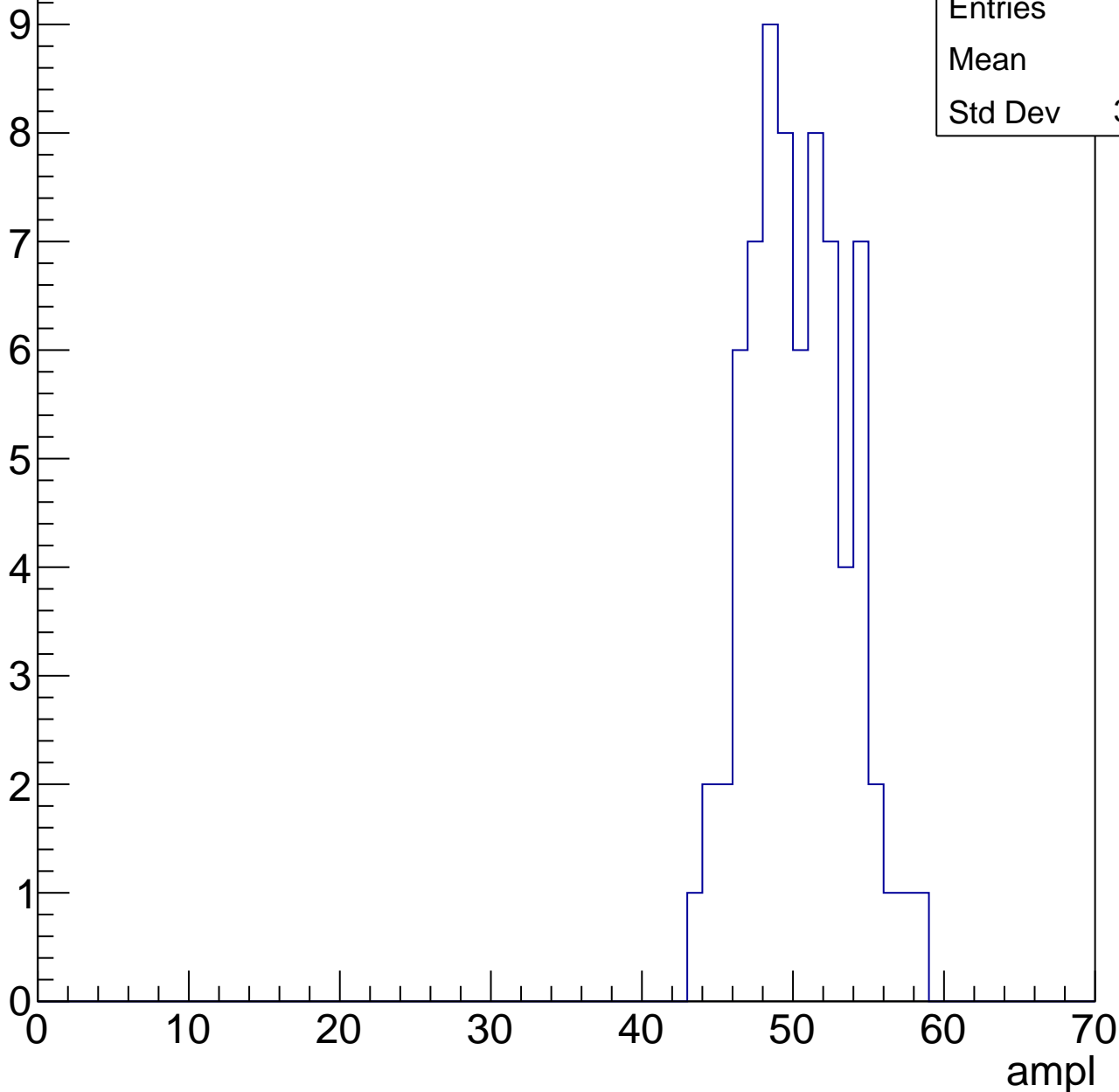
Gaus Width: 3.1645



B0L102S, U5-ch51, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

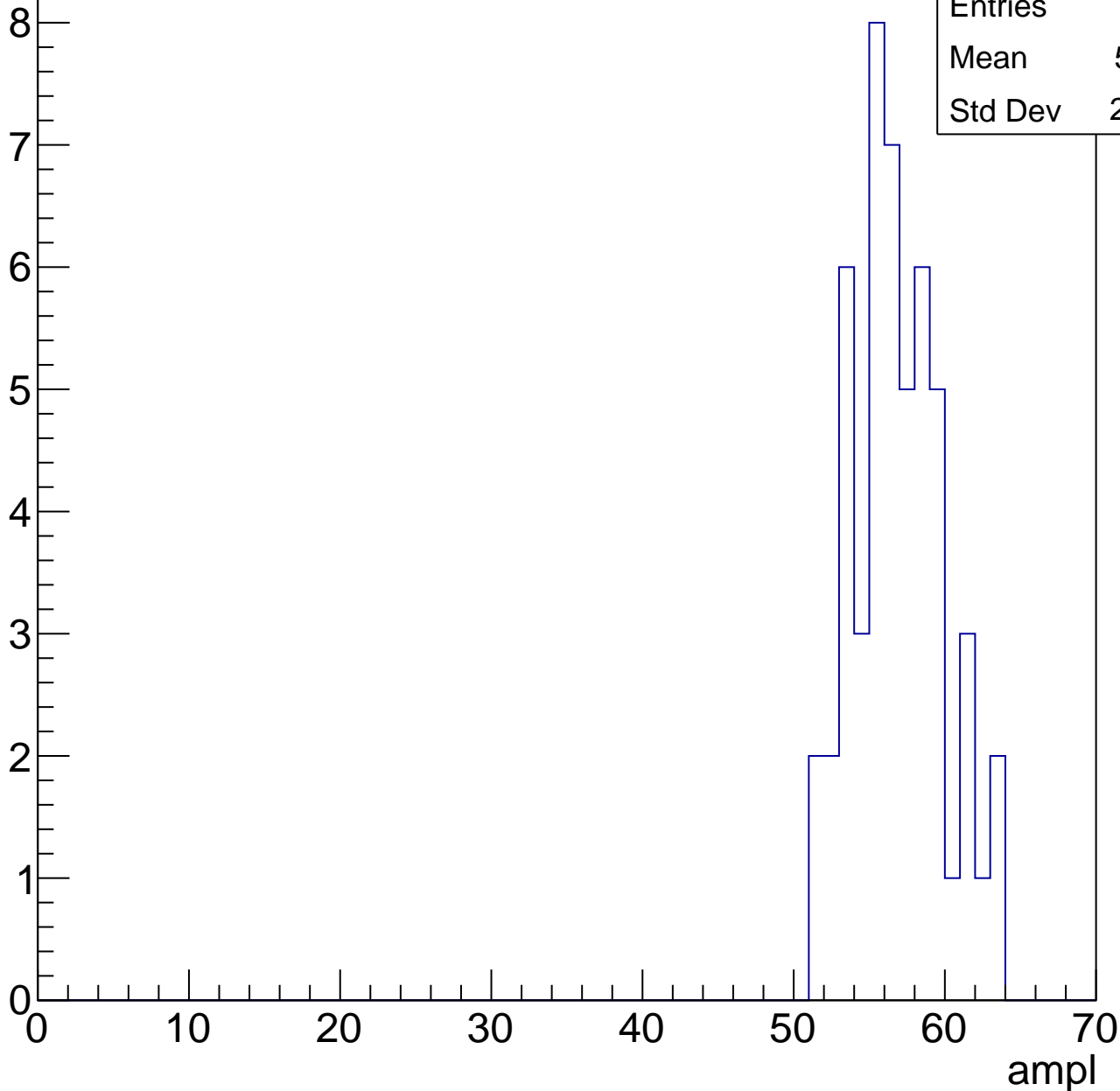


B0L102S, U5-ch51, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	56.41
Std Dev	2.958

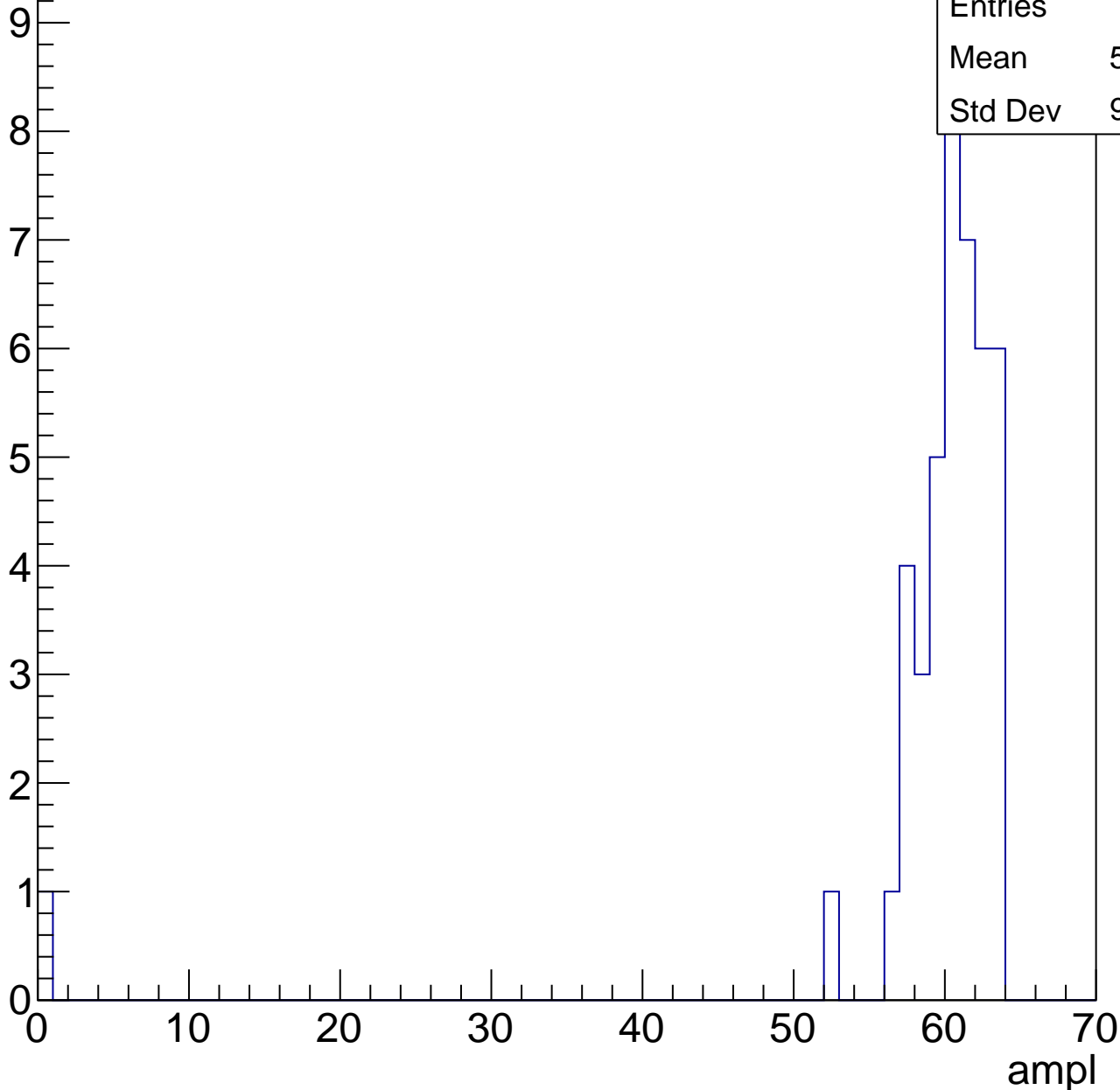


B0L102S, U5-ch51, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	58.65
Std Dev	9.326



B0L102S, U5-ch51, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch51, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch52, adc0

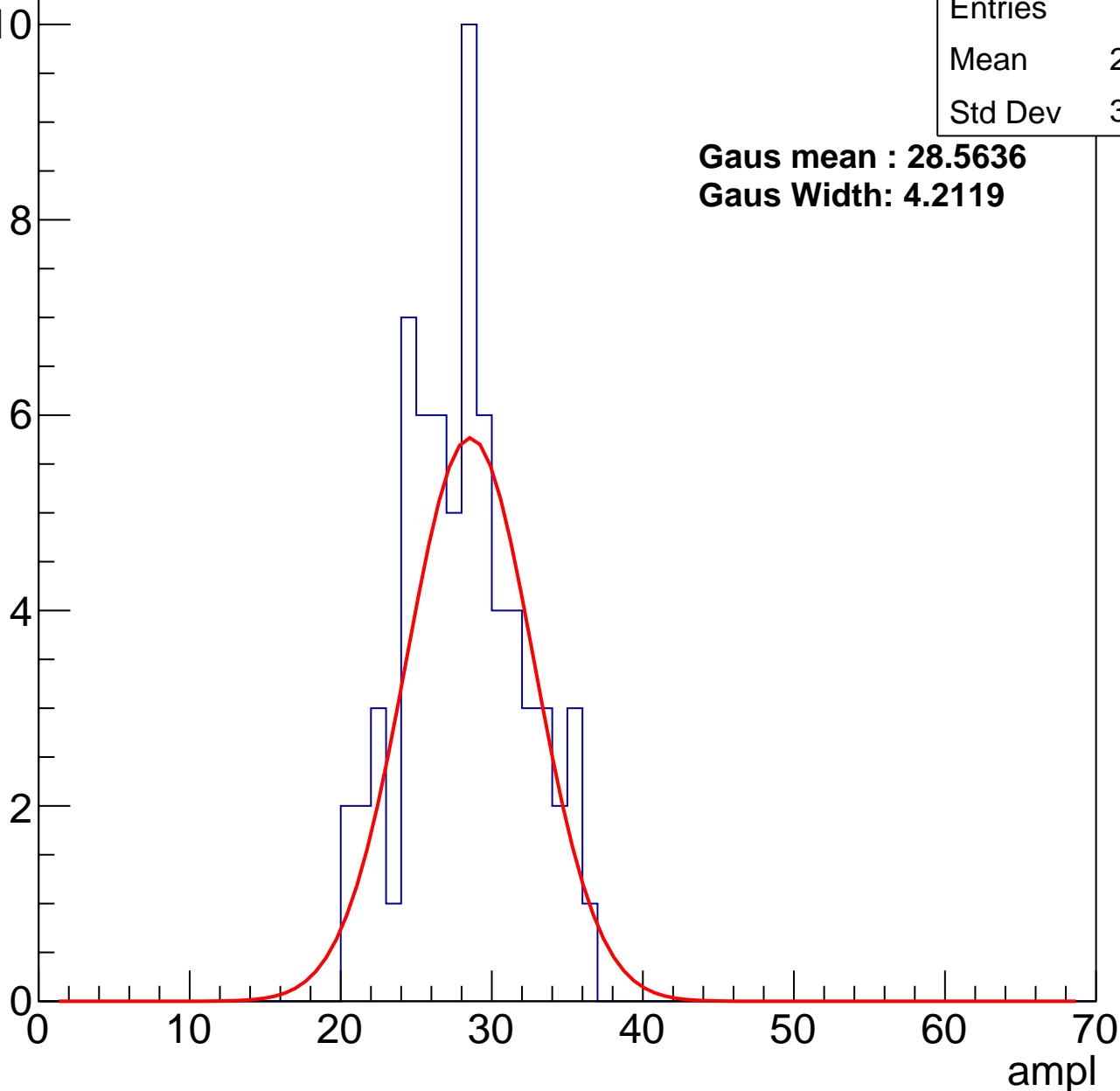
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	27.68
Std Dev	3.856

Gaus mean : 28.5636

Gaus Width: 4.2119



B0L102S, U5-ch52, adc1

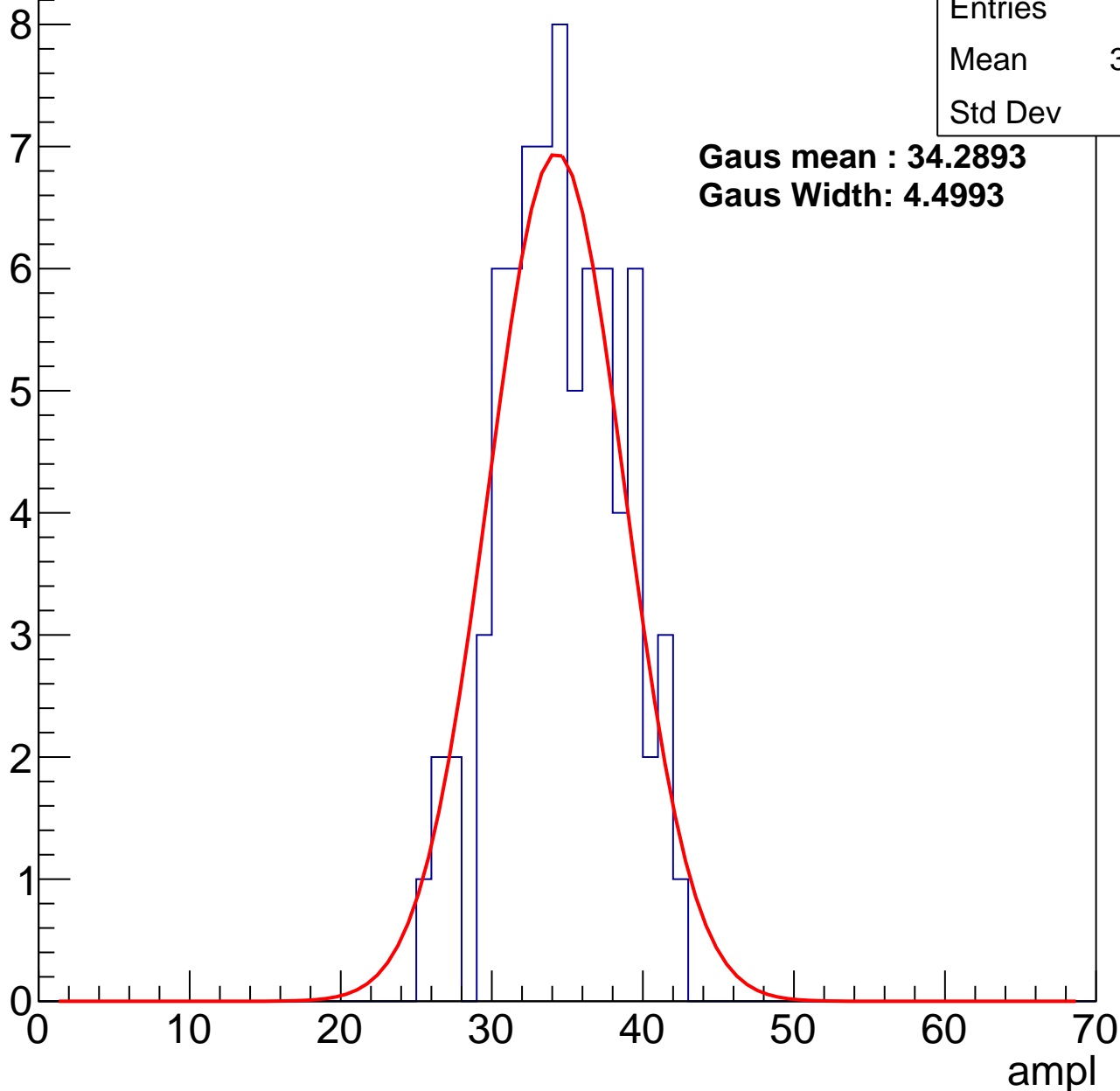
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	34.07
Std Dev	3.91

Gaus mean : 34.2893

Gaus Width: 4.4993



B0L102S, U5-ch52, adc2

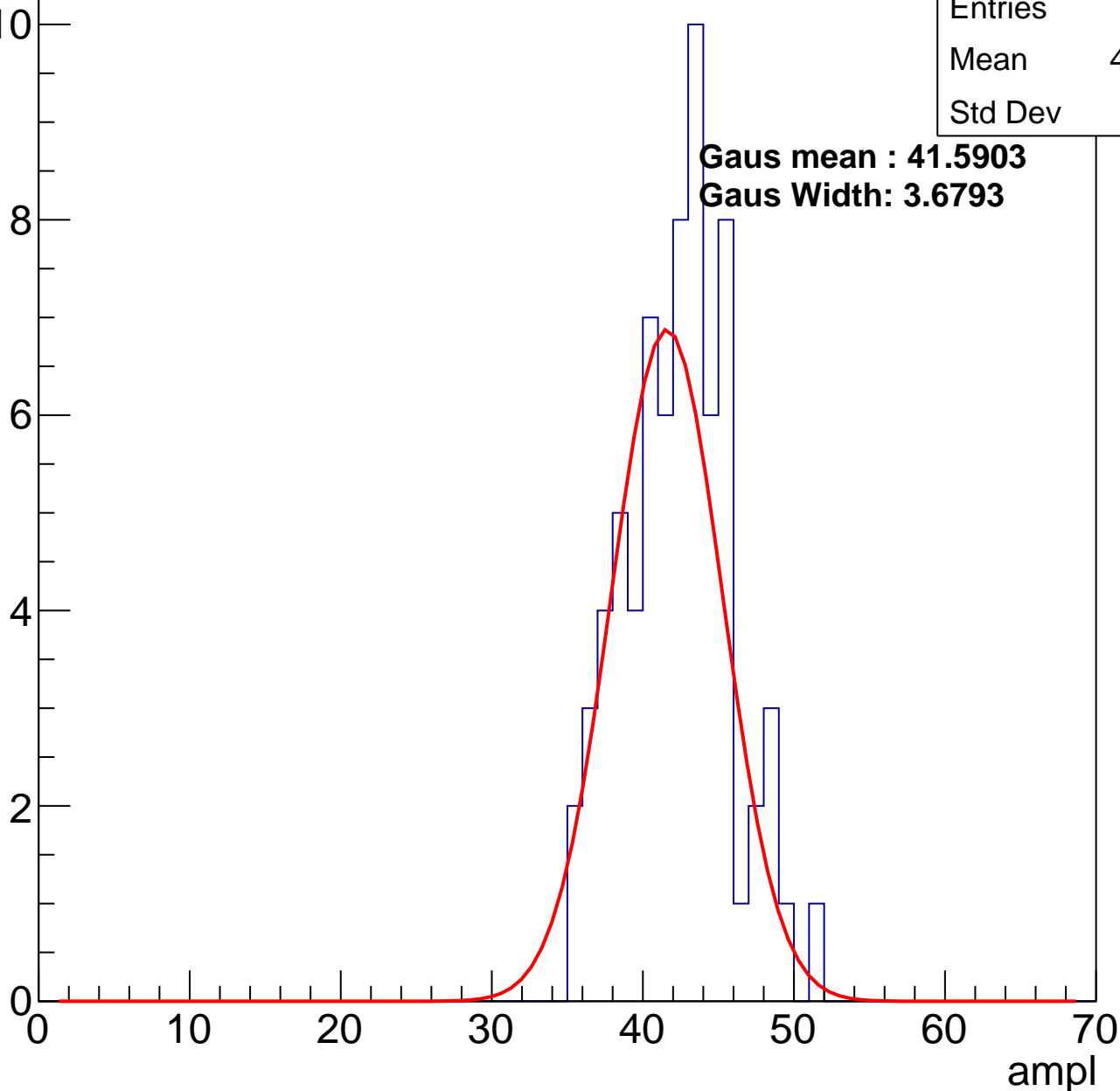
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	41.86
Std Dev	3.49

Gaus mean : 41.5903

Gaus Width: 3.6793

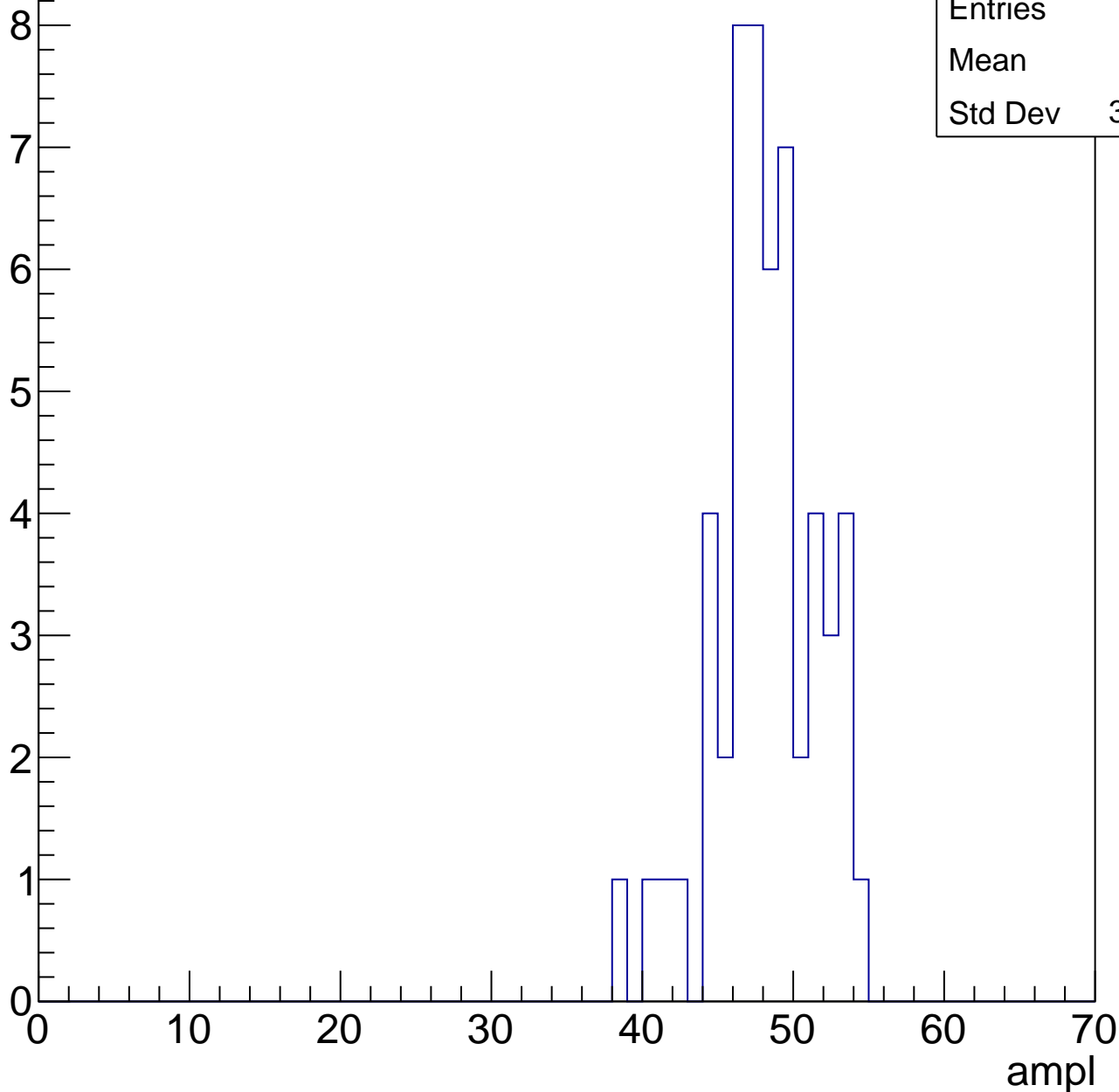


B0L102S, U5-ch52, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	47.7
Std Dev	3.368

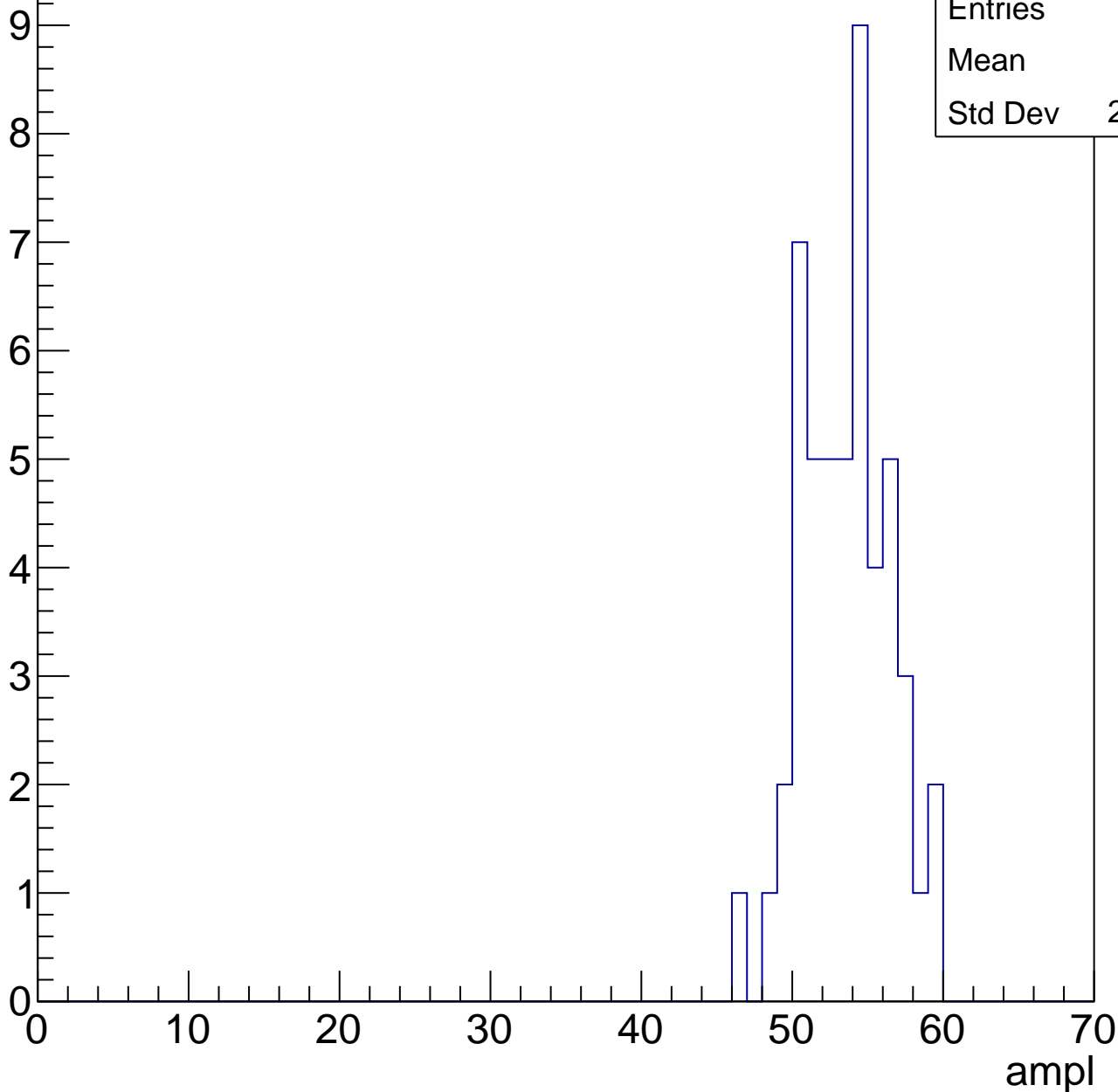


B0L102S, U5-ch52, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	53.1
Std Dev	2.858

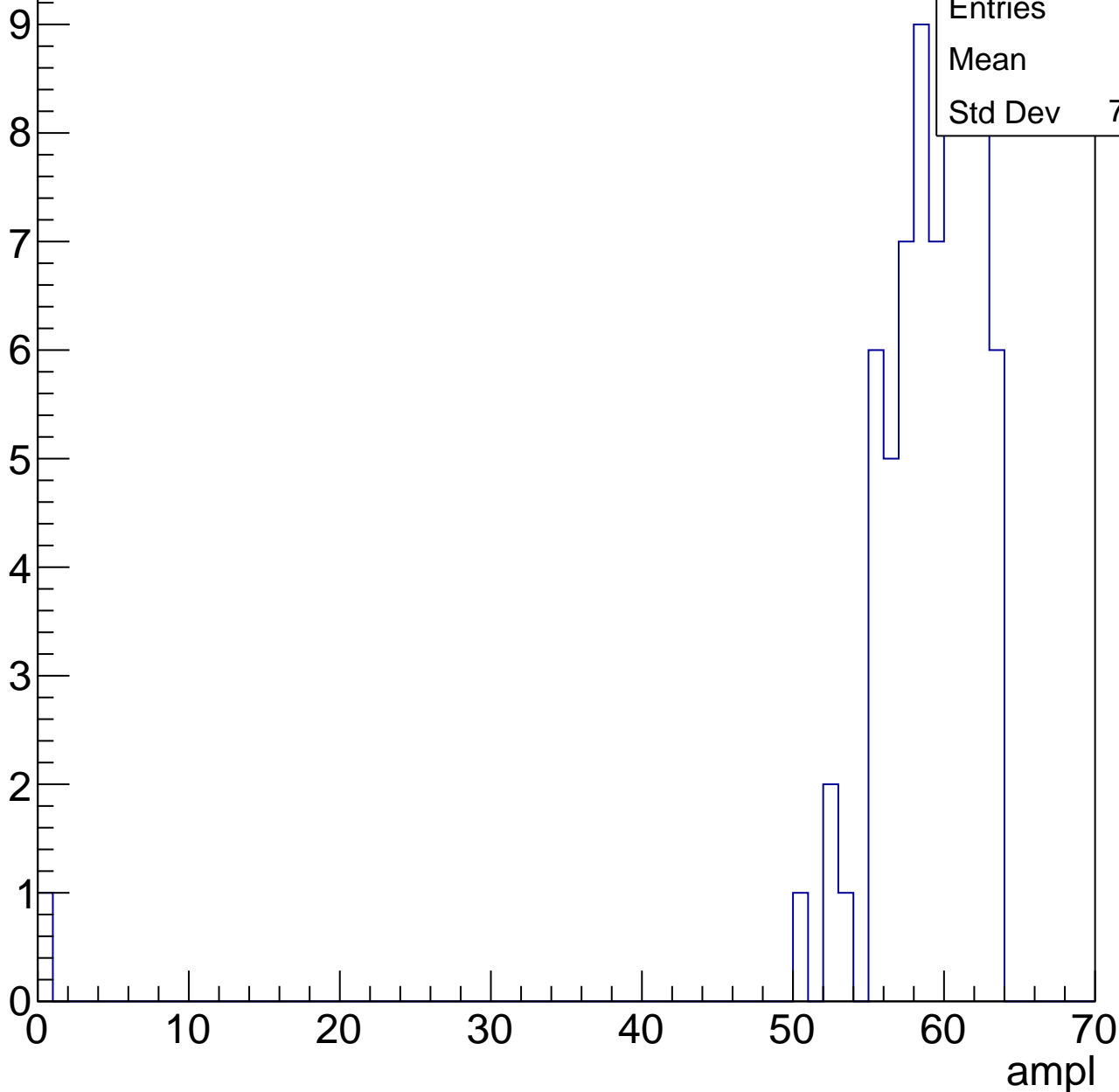


B0L102S, U5-ch52, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

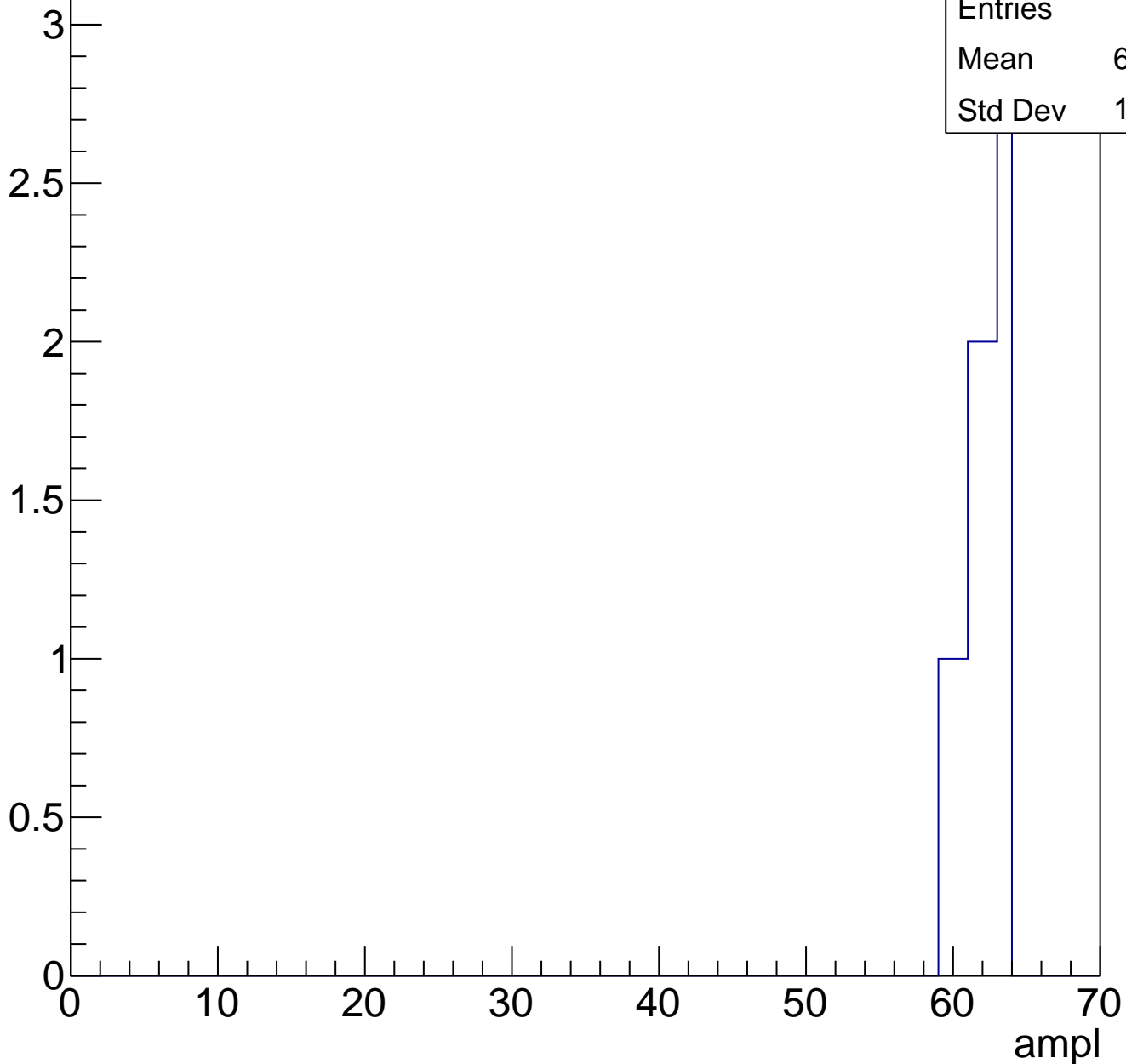
Entries	70
Mean	57.9
Std Dev	7.558



B0L102S, U5-ch52, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch52, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch53, adc0

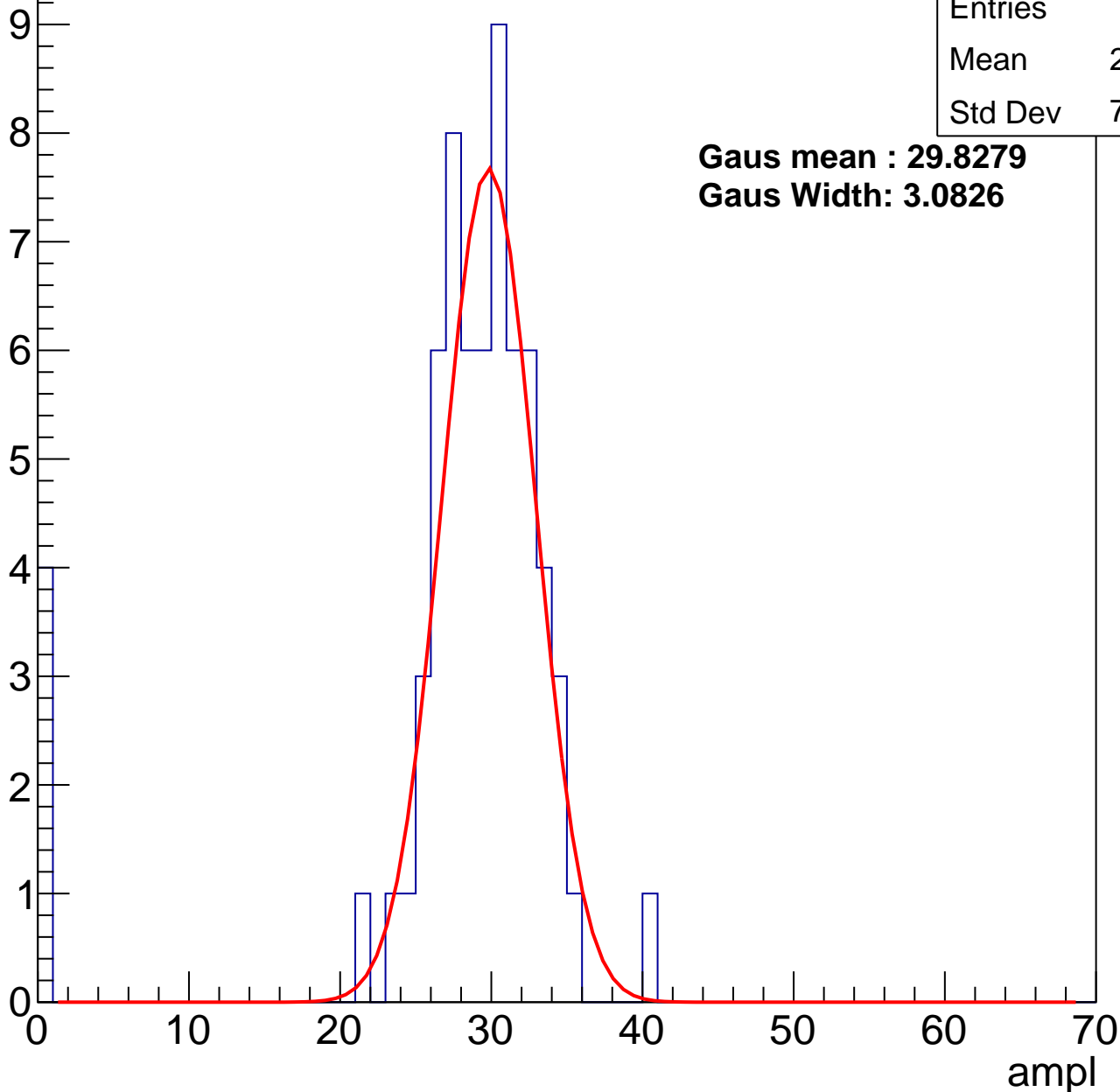
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.48
Std Dev	7.648

Gaus mean : 29.8279

Gaus Width: 3.0826



B0L102S, U5-ch53, adc1

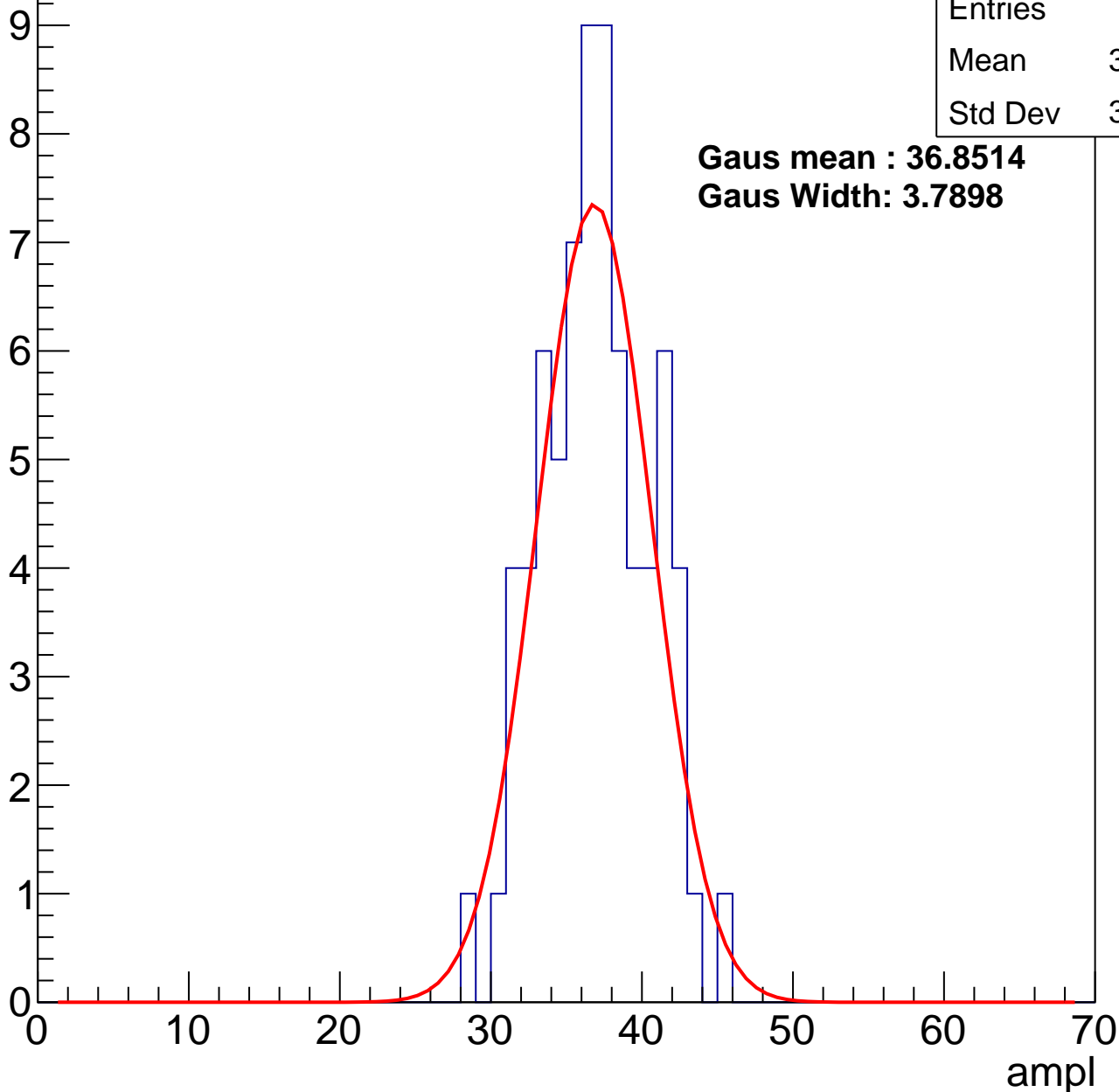
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	36.47
Std Dev	3.508

Gaus mean : 36.8514

Gaus Width: 3.7898



B0L102S, U5-ch53, adc2

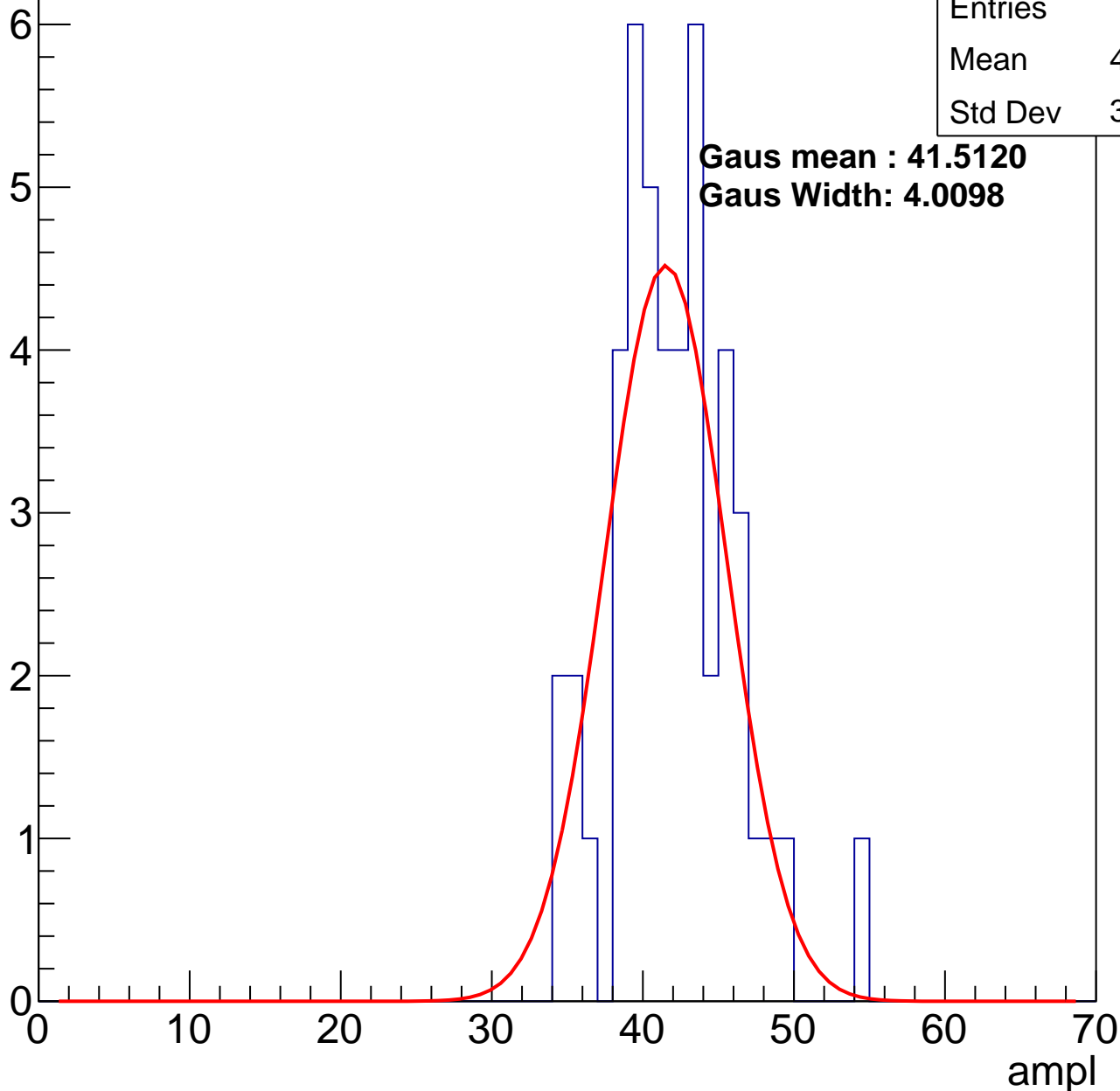
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	41.57
Std Dev	3.988

Gaus mean : 41.5120

Gaus Width: 4.0098

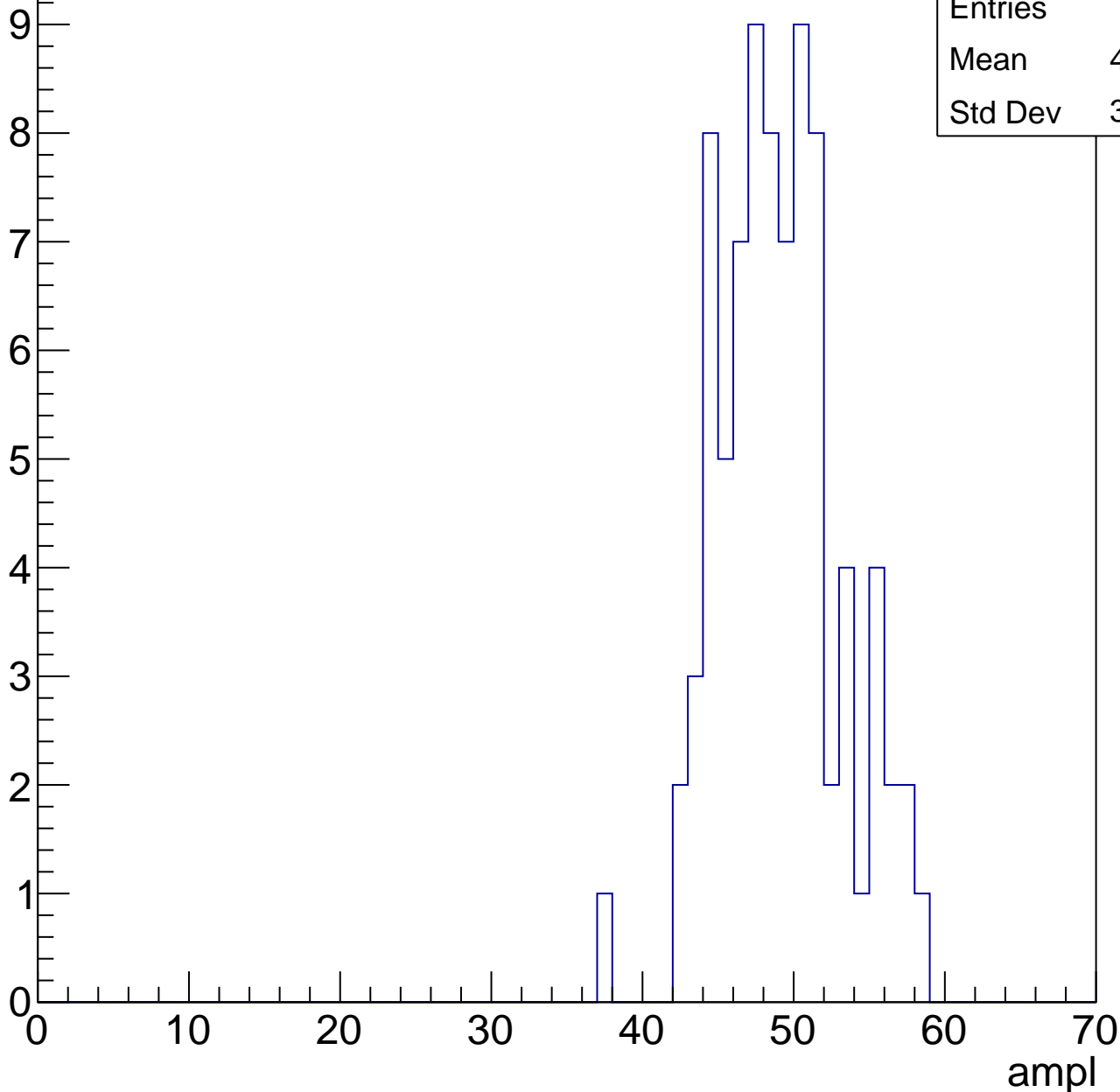


B0L102S, U5-ch53, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	48.57
Std Dev	3.989

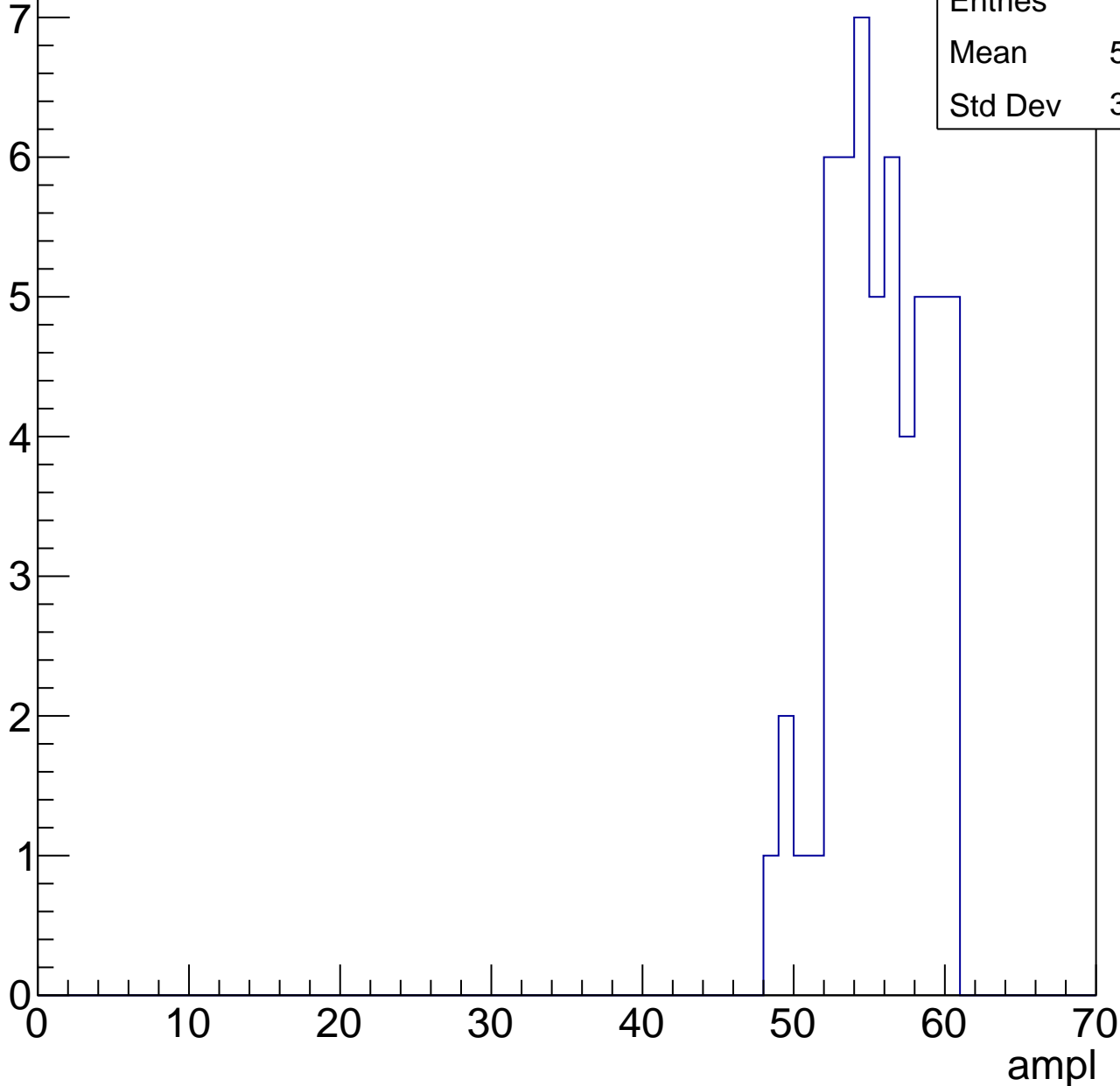


B0L102S, U5-ch53, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	55.17
Std Dev	3.096

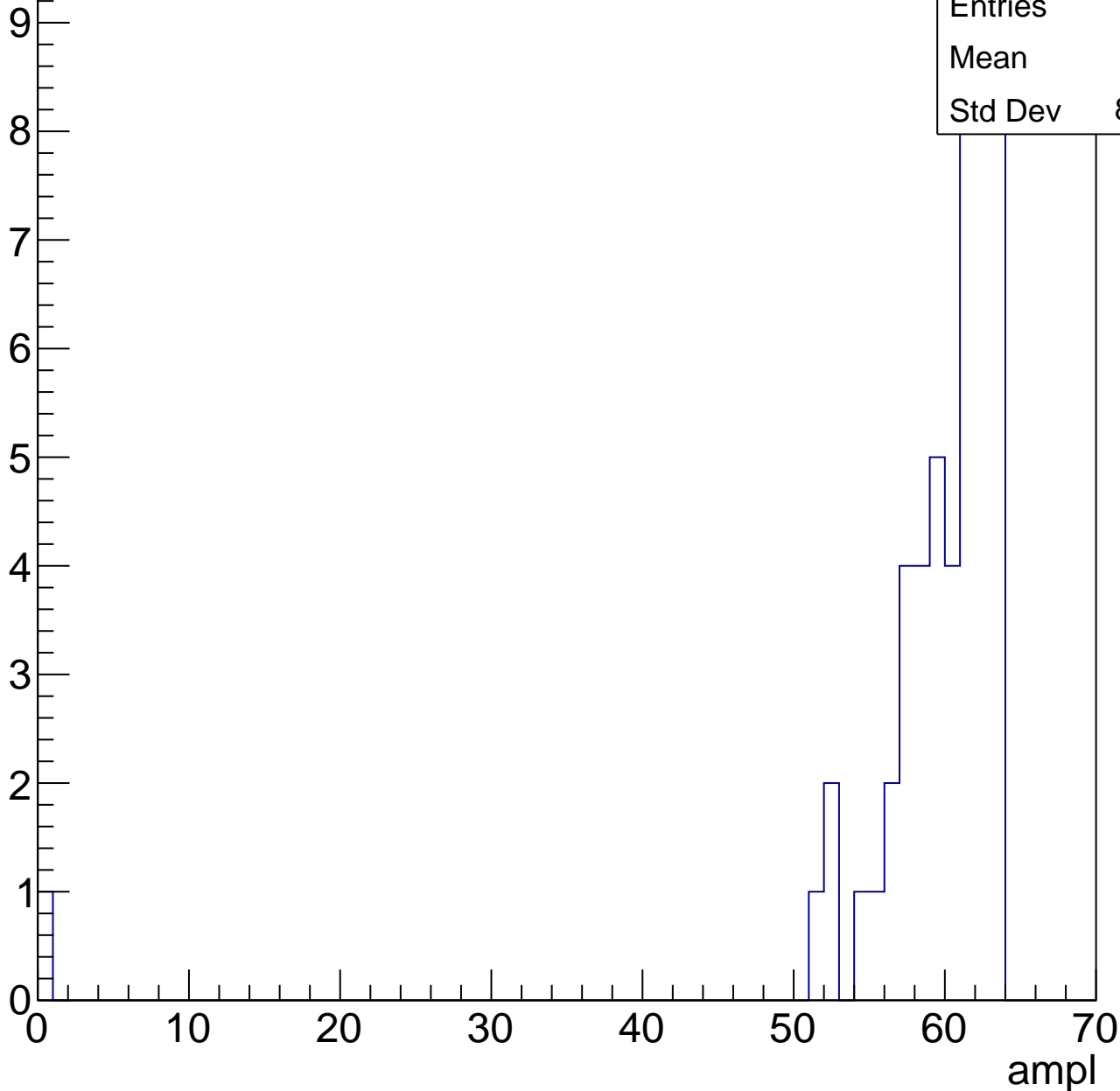


B0L102S, U5-ch53, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	58.4
Std Dev	8.881



B0L102S, U5-ch53, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	5
Mean	61.6
Std Dev	1.02

B0L102S, U5-ch53, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch54, adc0

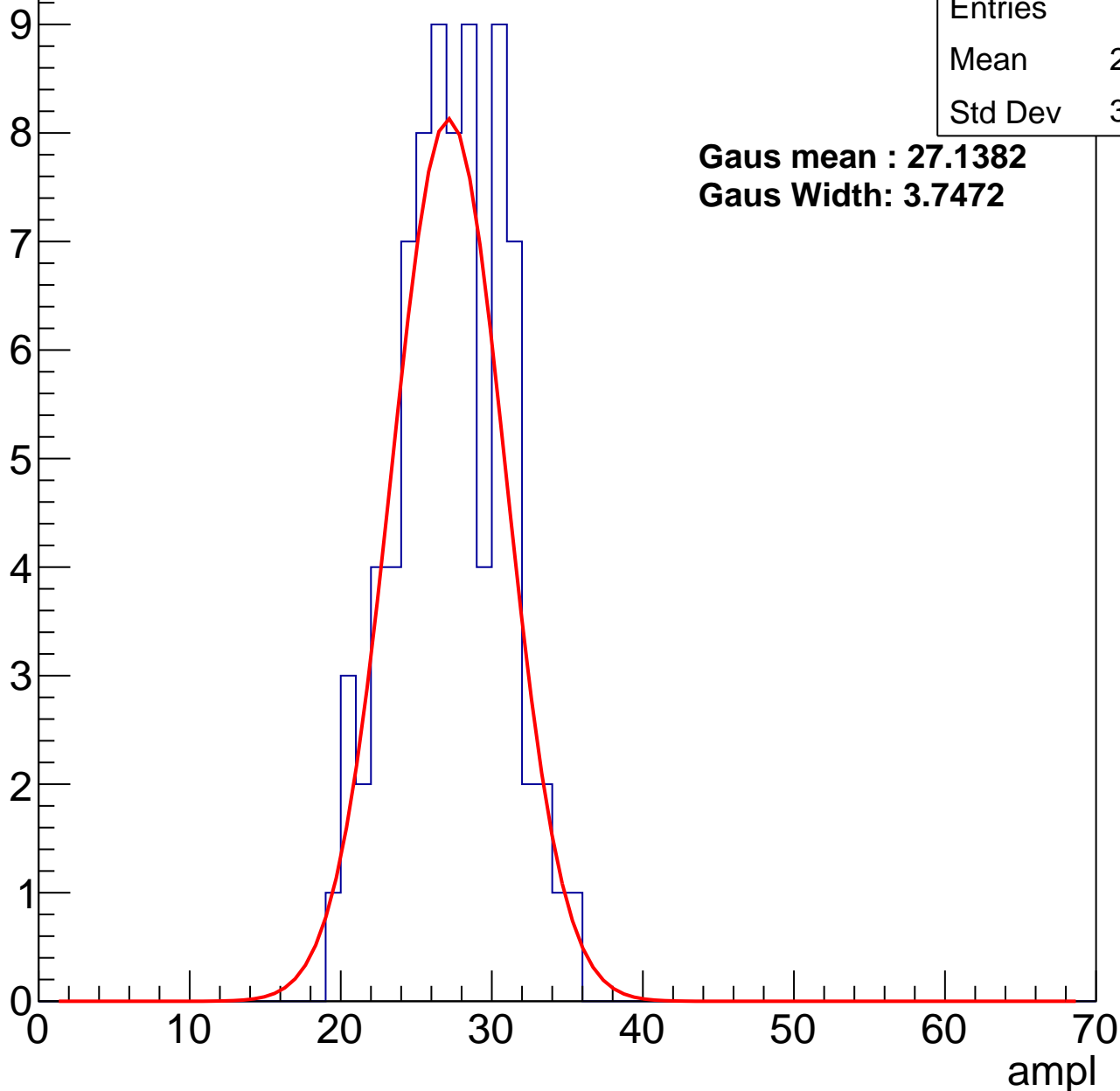
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	26.83
Std Dev	3.516

Gaus mean : 27.1382

Gaus Width: 3.7472



B0L102S, U5-ch54, adc1

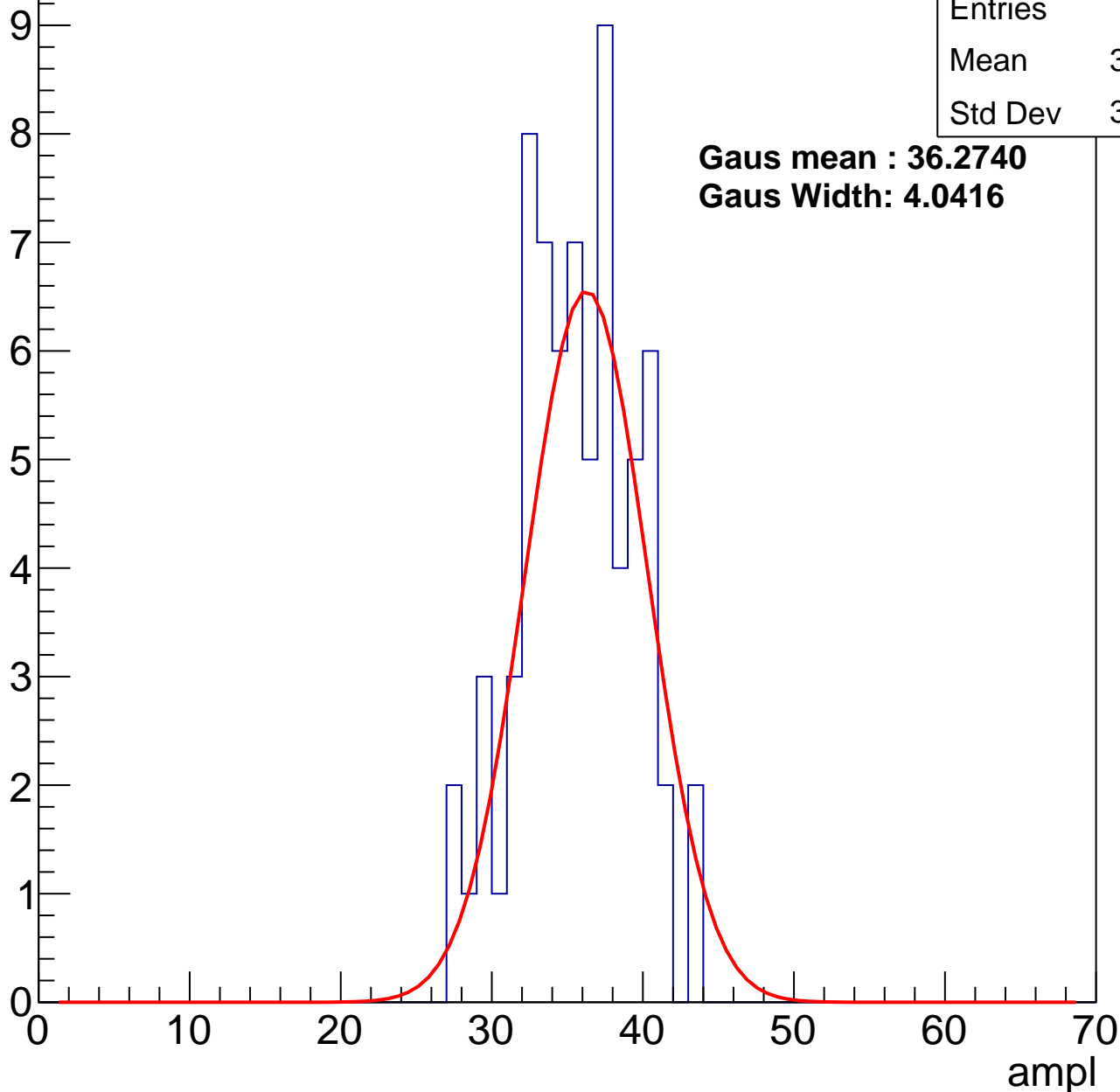
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	35.15
Std Dev	3.695

Gaus mean : 36.2740

Gaus Width: 4.0416



B0L102S, U5-ch54, adc2

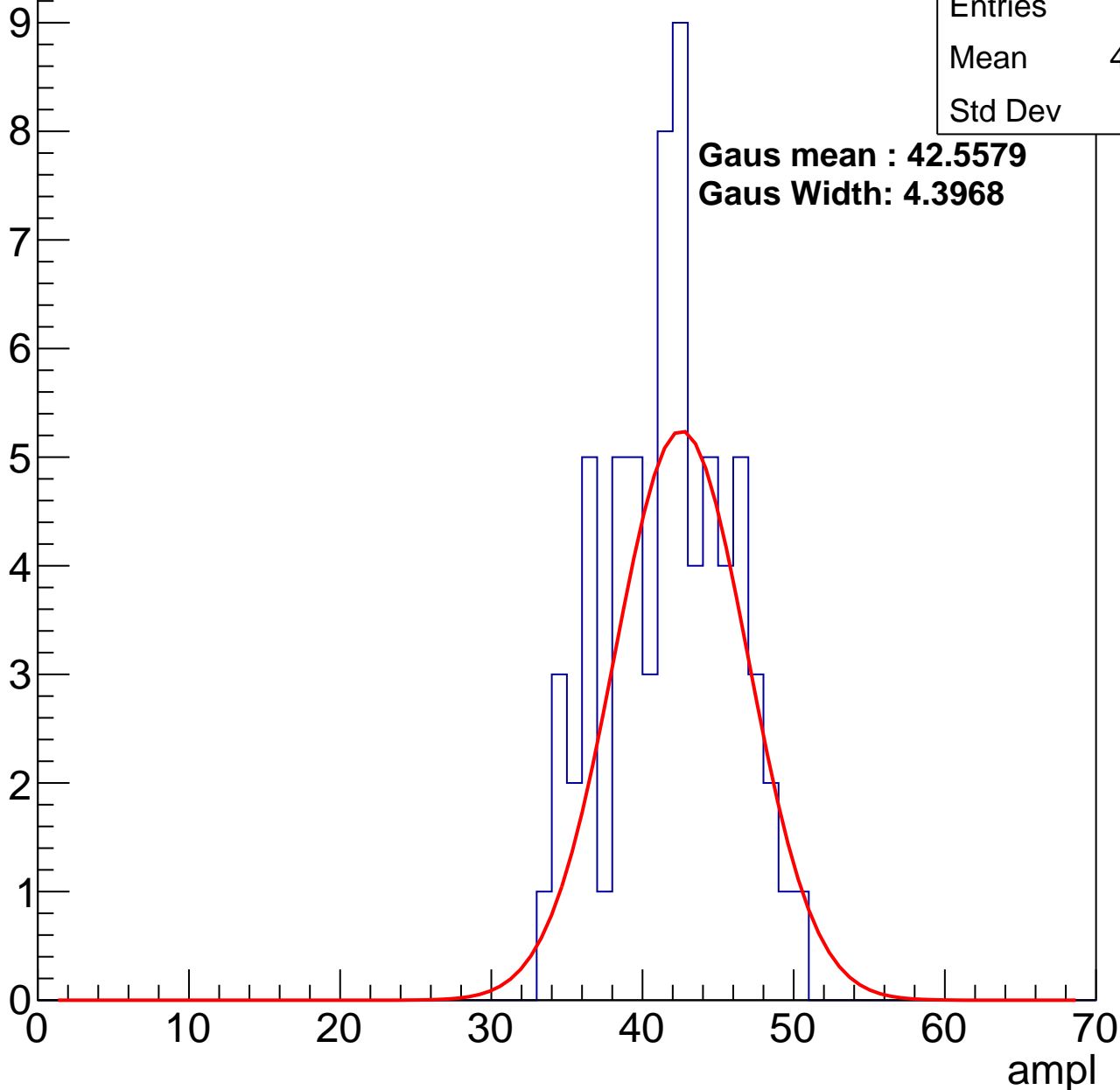
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	41.36
Std Dev	4.04

Gaus mean : 42.5579

Gaus Width: 4.3968

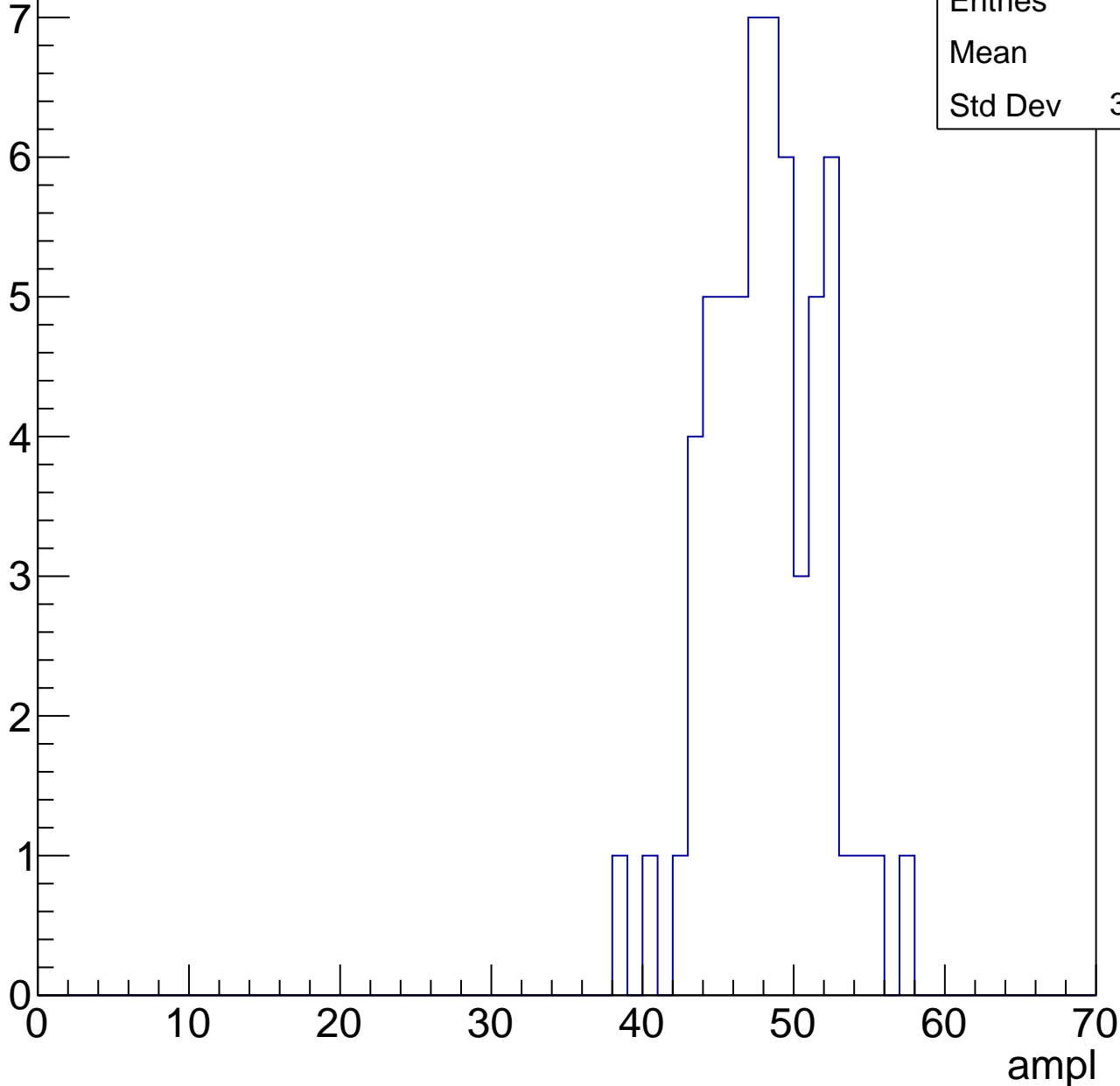


B0L102S, U5-ch54, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	47.7
Std Dev	3.644

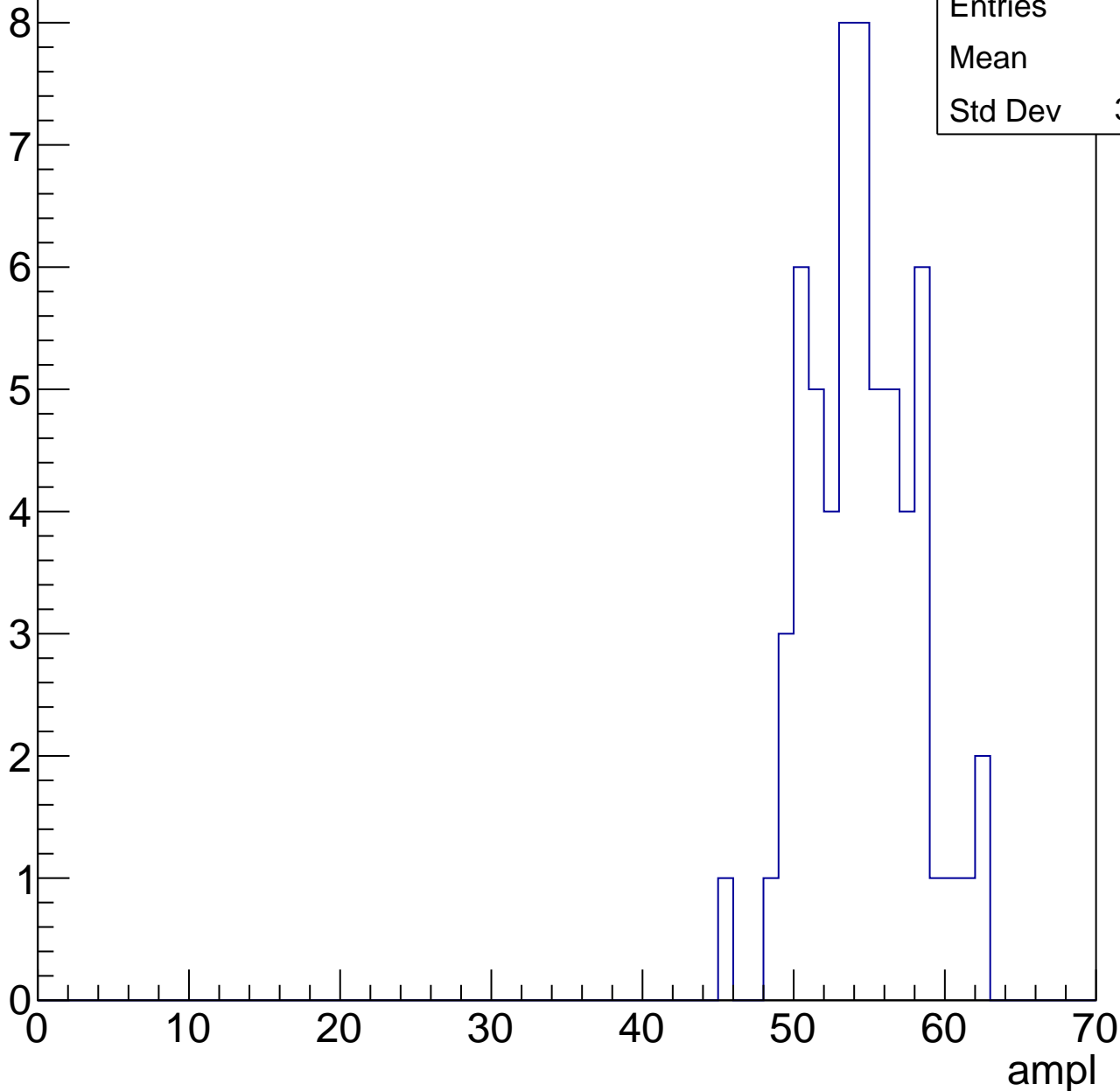


B0L102S, U5-ch54, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	54
Std Dev	3.511



B0L102S, U5-ch54, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7
6
5
4
3
2
1
0

Entries	43
Mean	58.63
Std Dev	2.633

ampl

0

10

20

30

40

50

60

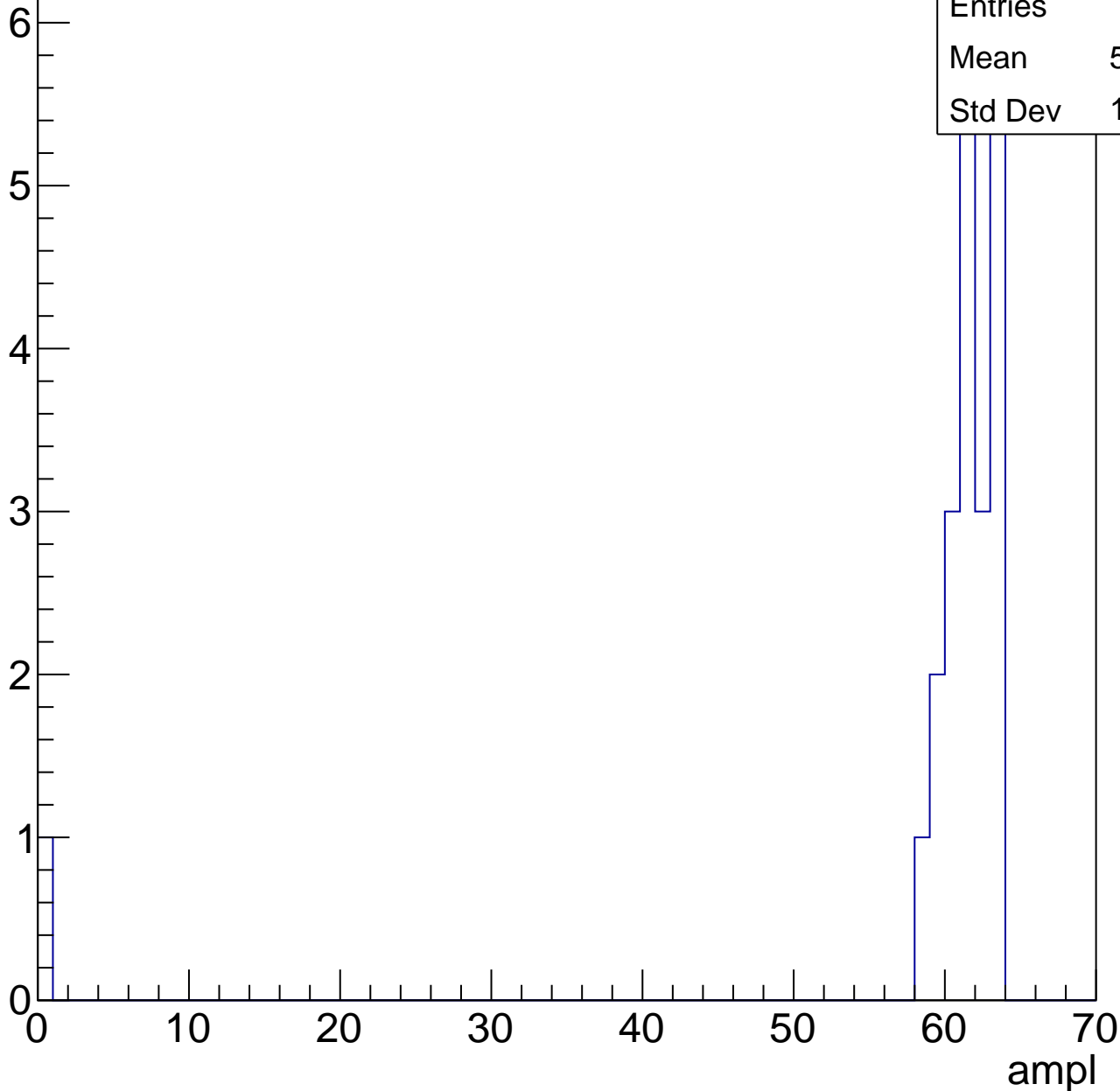
70

B0L102S, U5-ch54, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

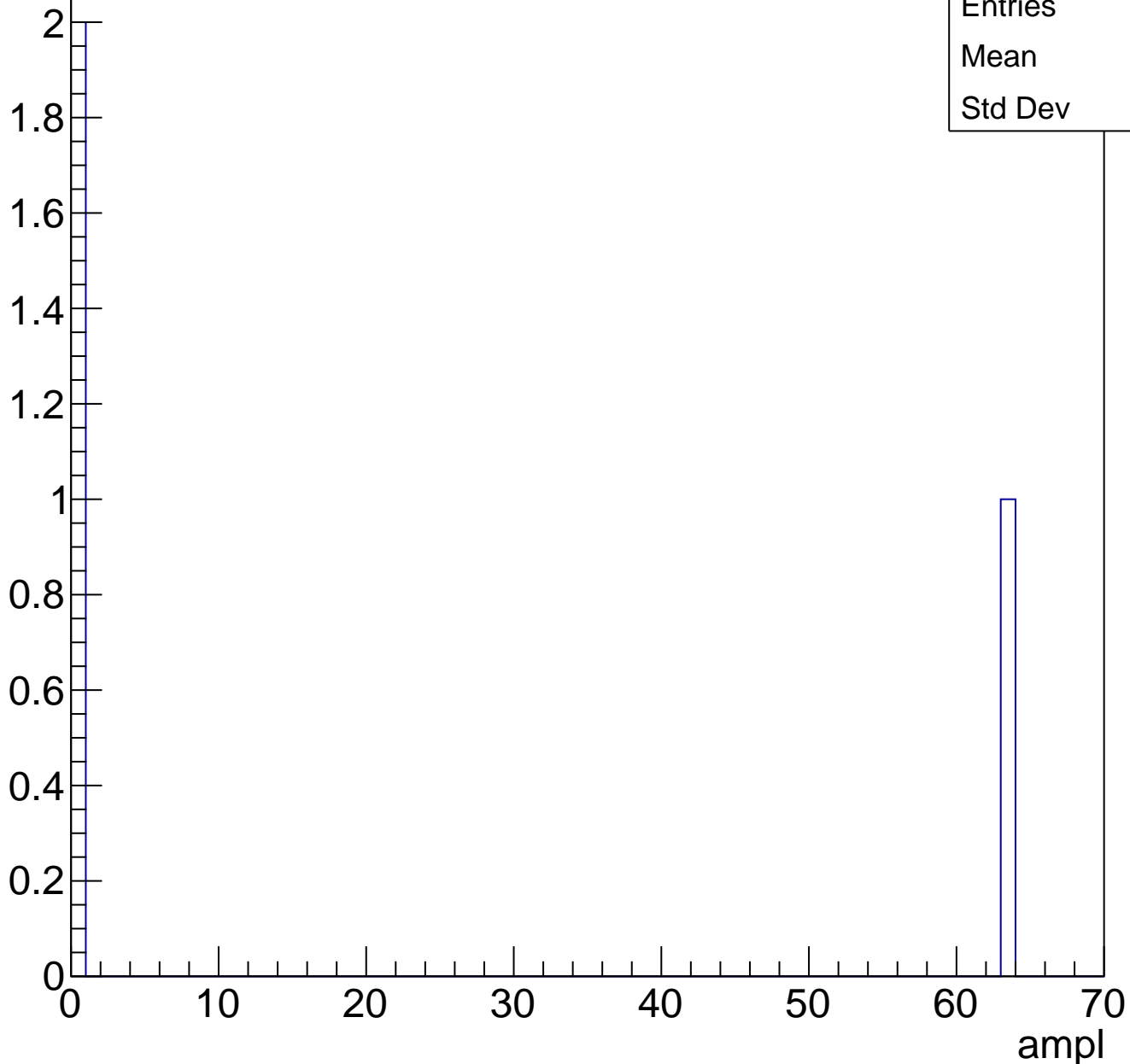
Entries	22
Mean	58.45
Std Dev	12.84



B0L102S, U5-ch54, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L102S, U5-ch55, adc0

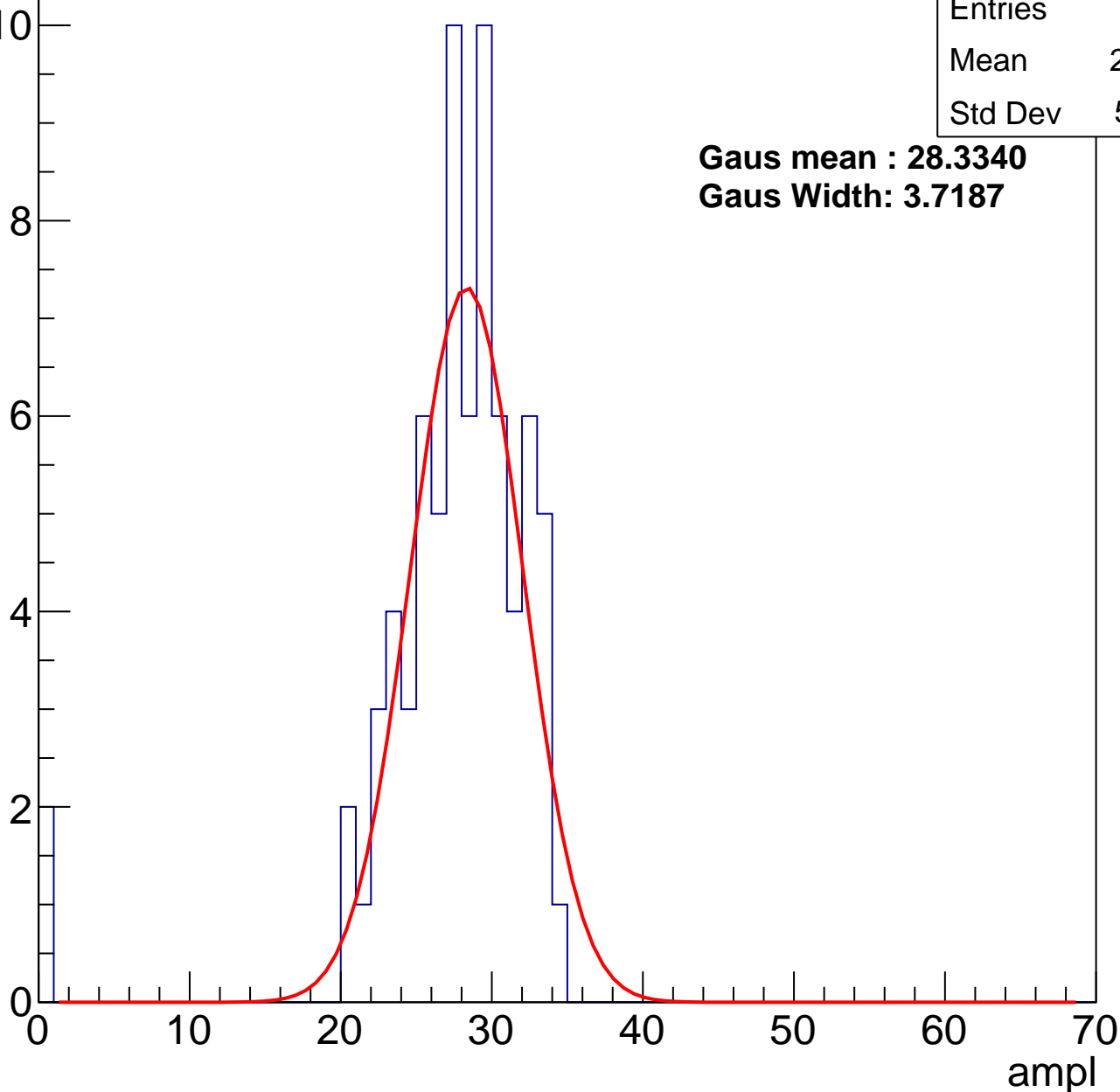
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	74
Mean	26.95
Std Dev	5.611

Gaus mean : 28.3340

Gaus Width: 3.7187



B0L102S, U5-ch55, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	75
Mean	35.01
Std Dev	3.617

Gaus mean : 35.0505

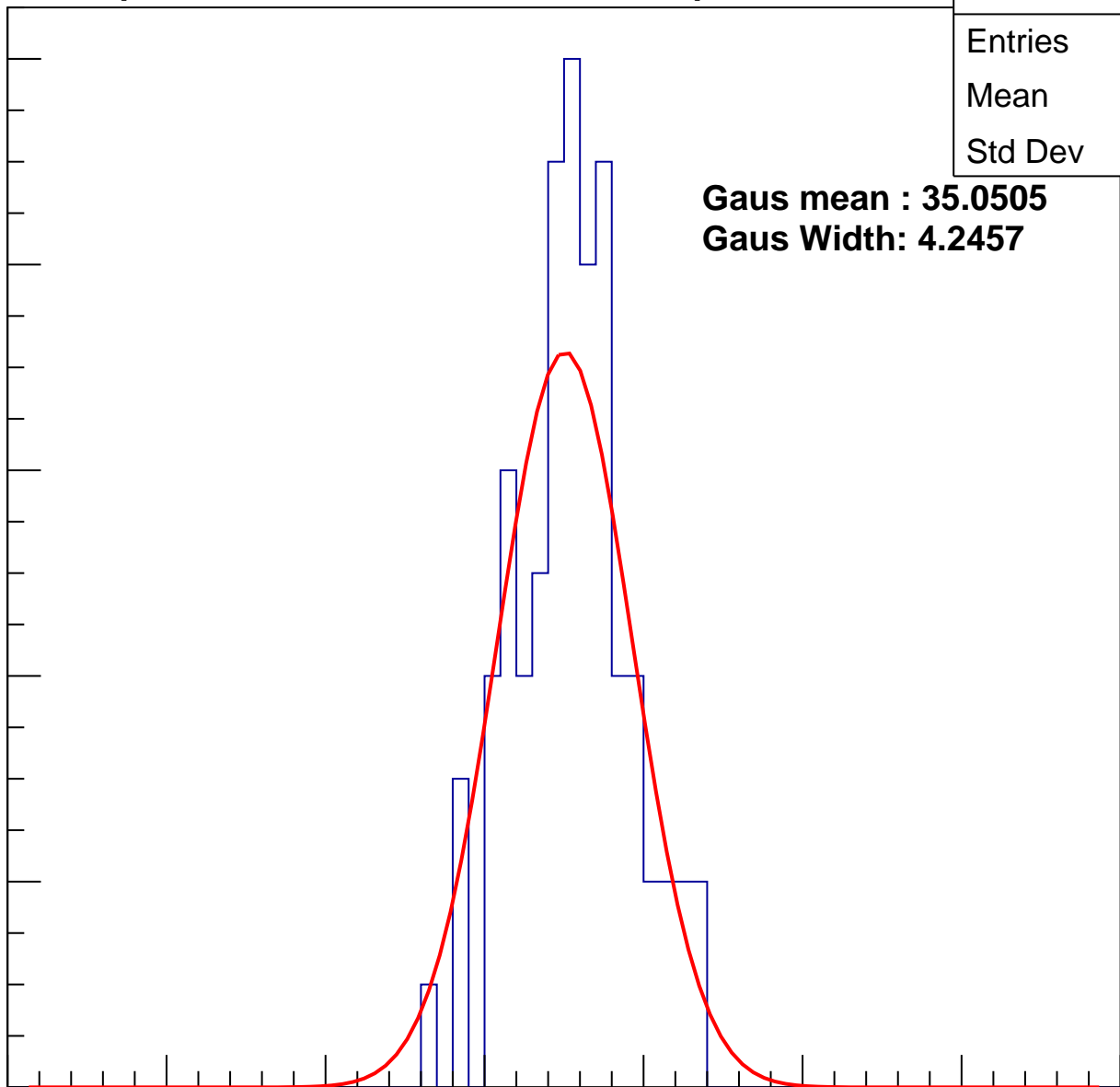
Gaus Width: 4.2457

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch55, adc2

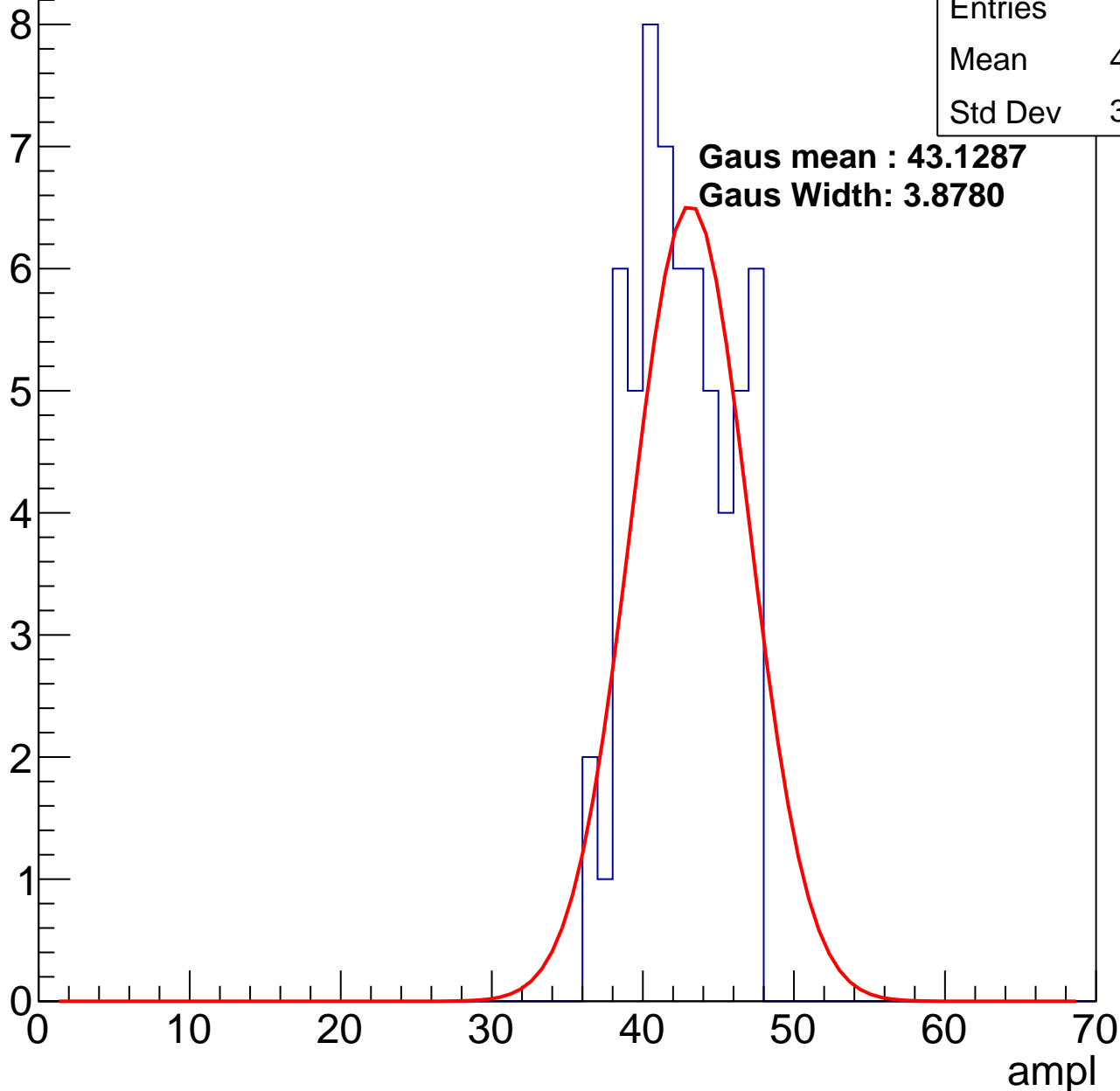
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	41.98
Std Dev	3.054

Gaus mean : 43.1287

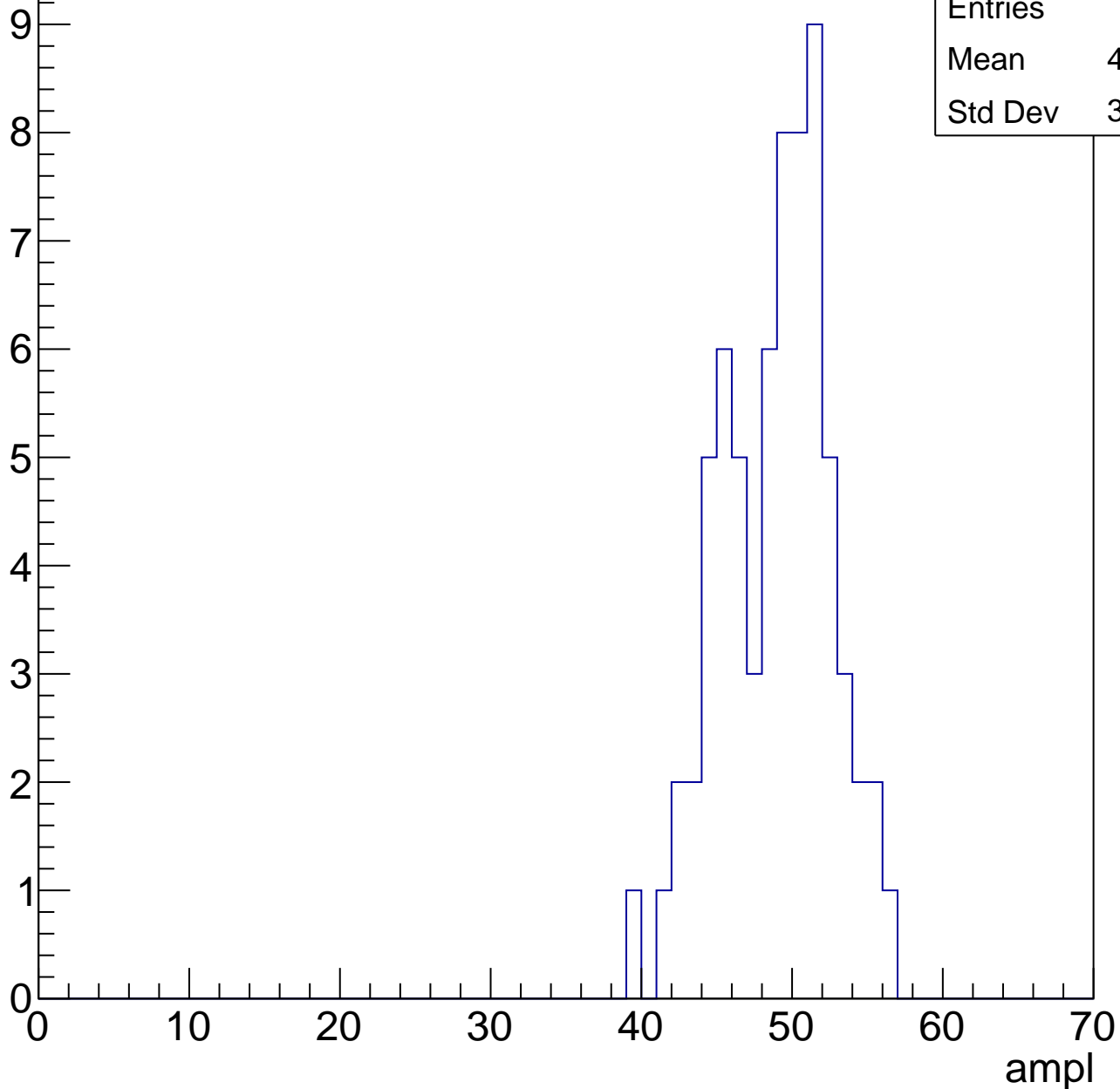
Gaus Width: 3.8780



B0L102S, U5-ch55, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

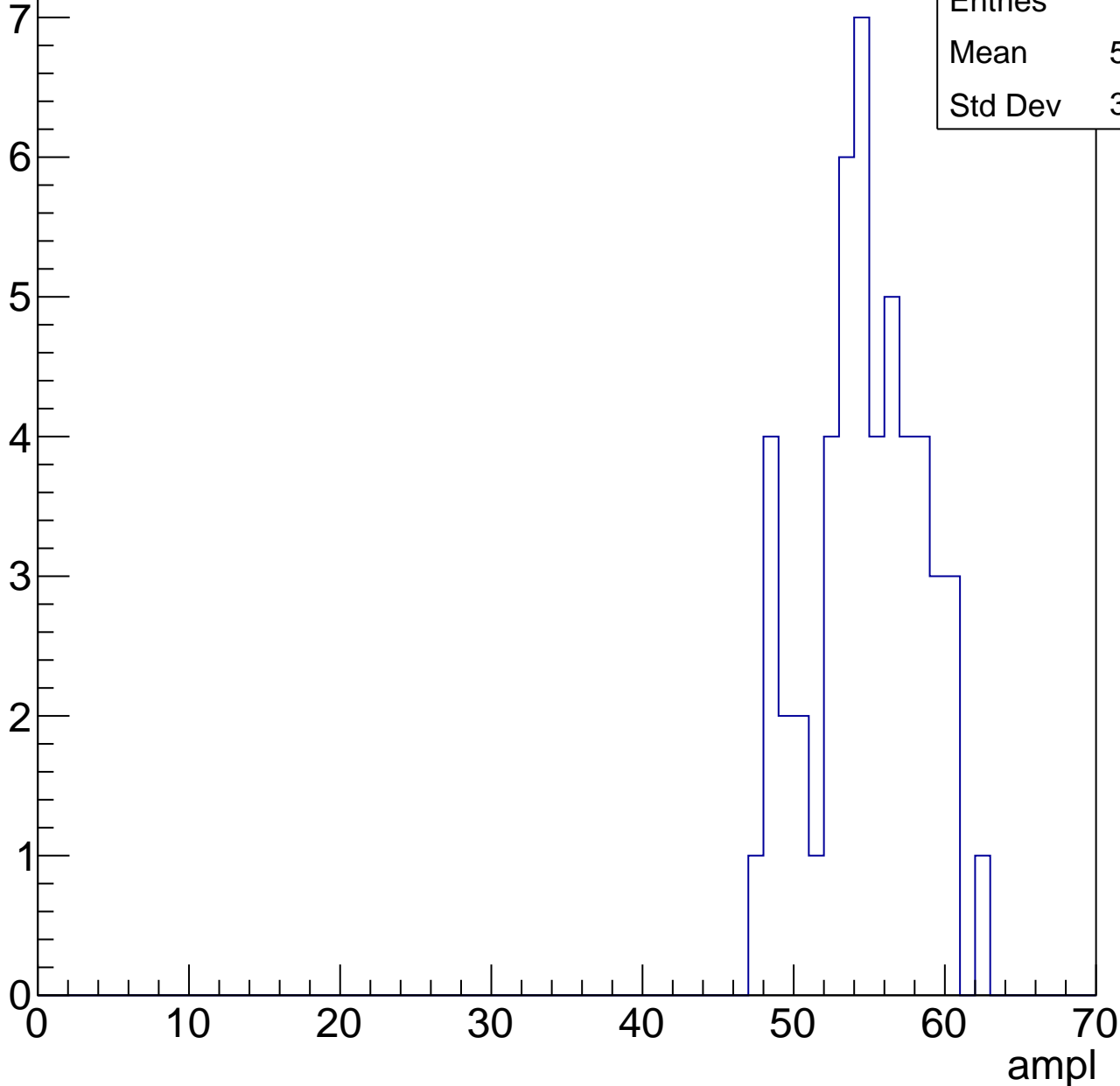


B0L102S, U5-ch55, adc4

calib_packv5_042523_0143.root, FC#12, port B1

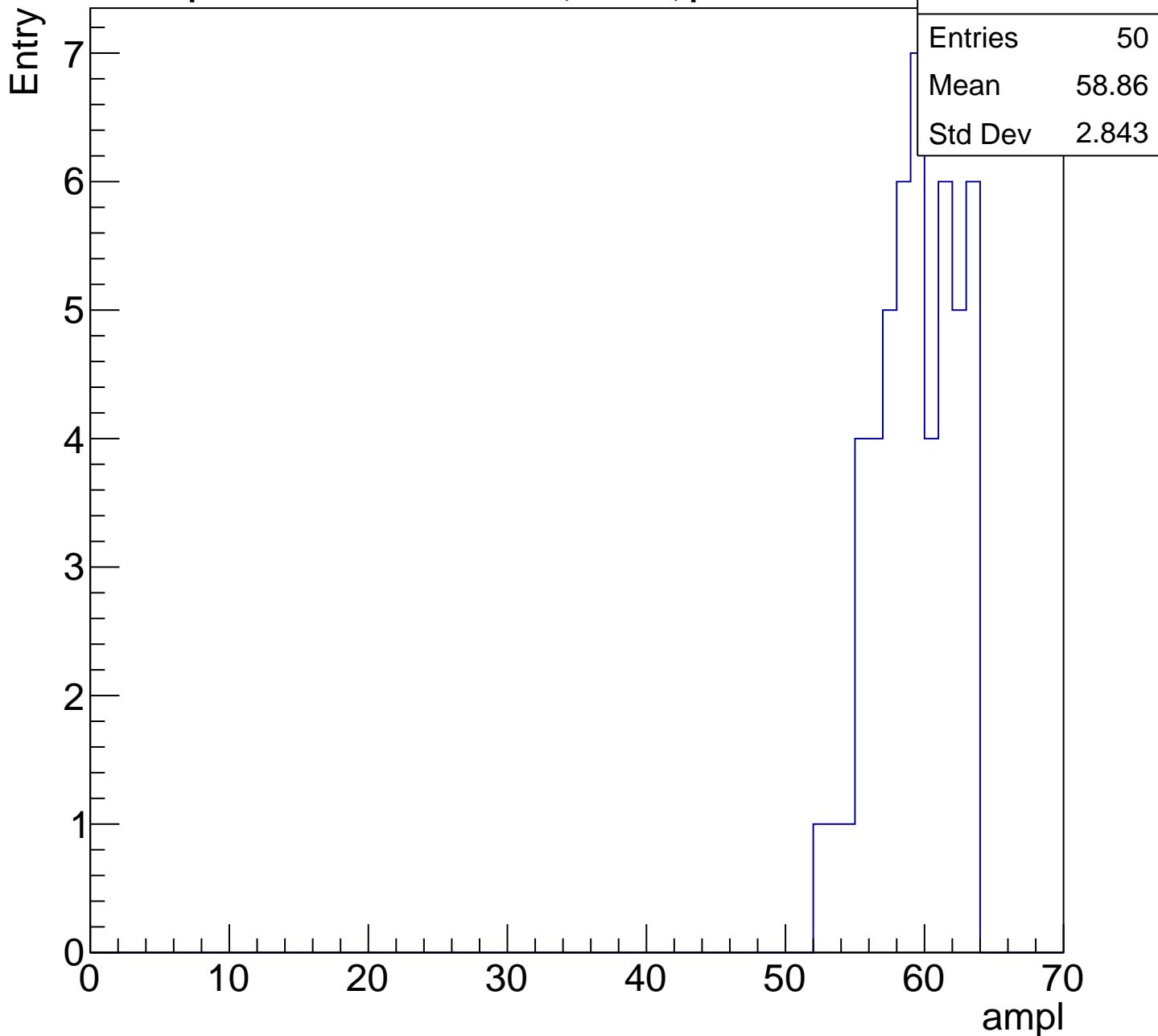
Entry

Entries	51
Mean	54.33
Std Dev	3.628



B0L102S, U5-ch55, adc5

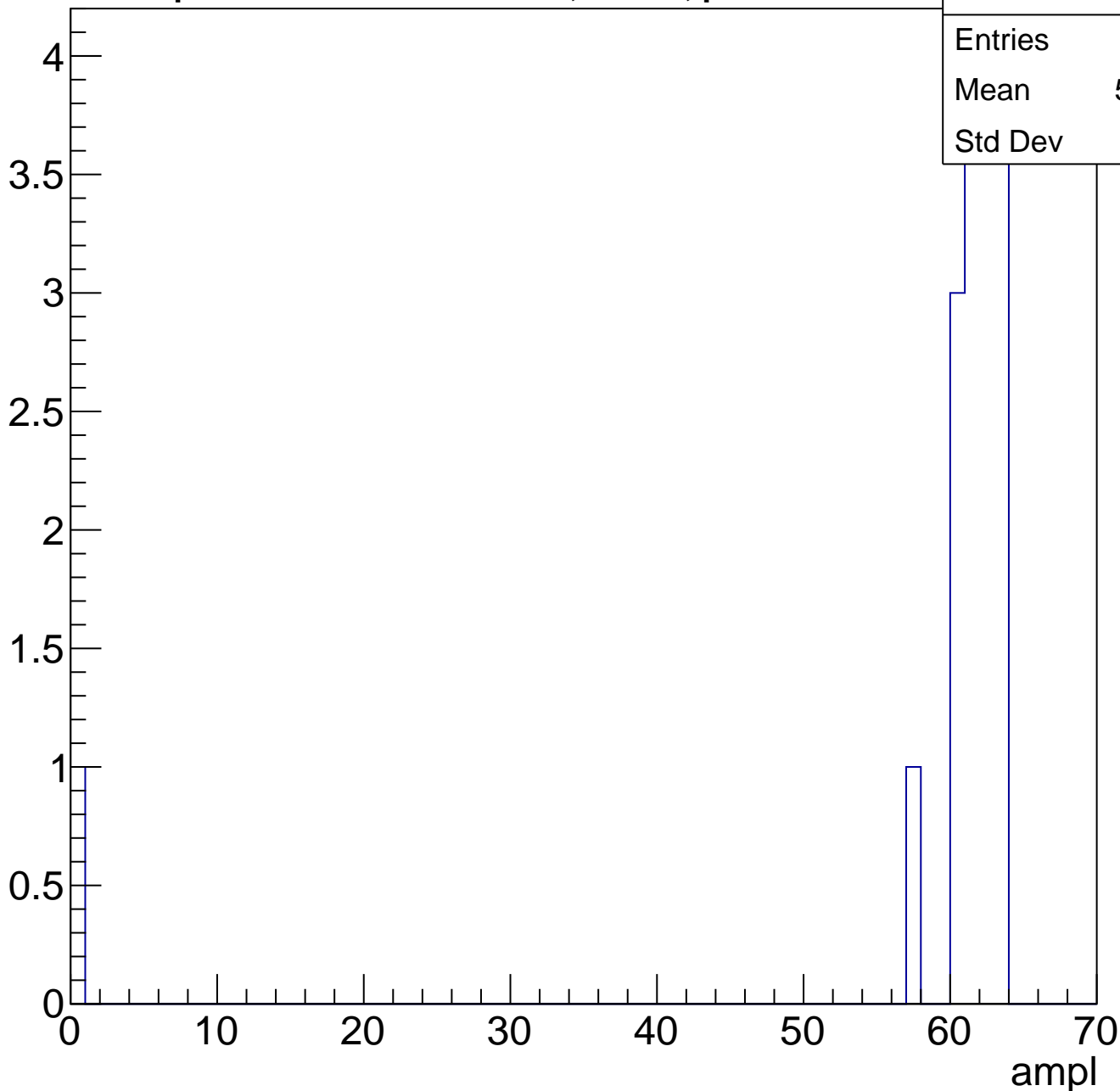
calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch55, adc6

calib_packv5_042523_0143.root, FC#12, port B1

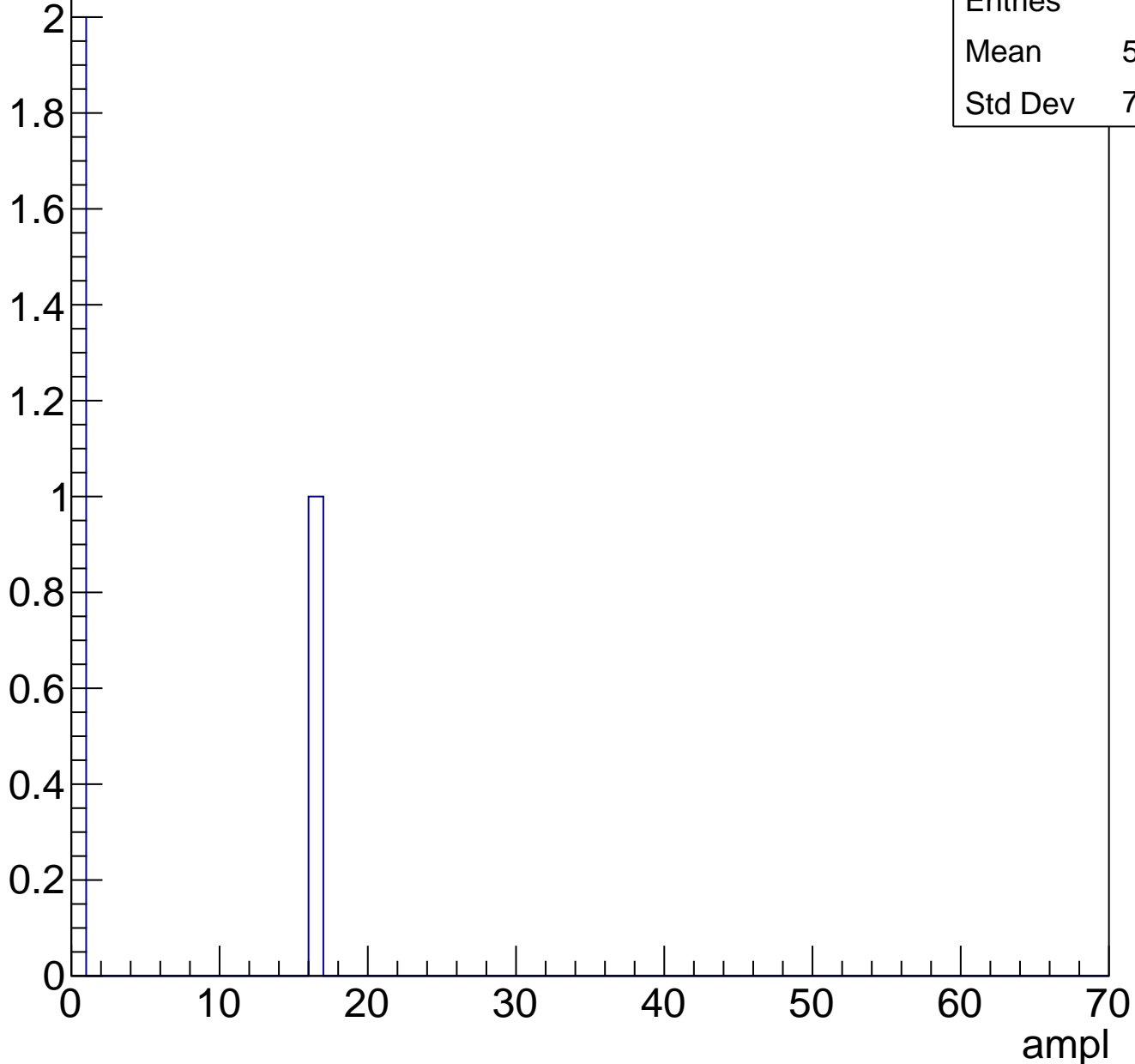
Entry



B0L102S, U5-ch55, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	5.333
Std Dev	7.542

B0L102S, U5-ch56, adc0

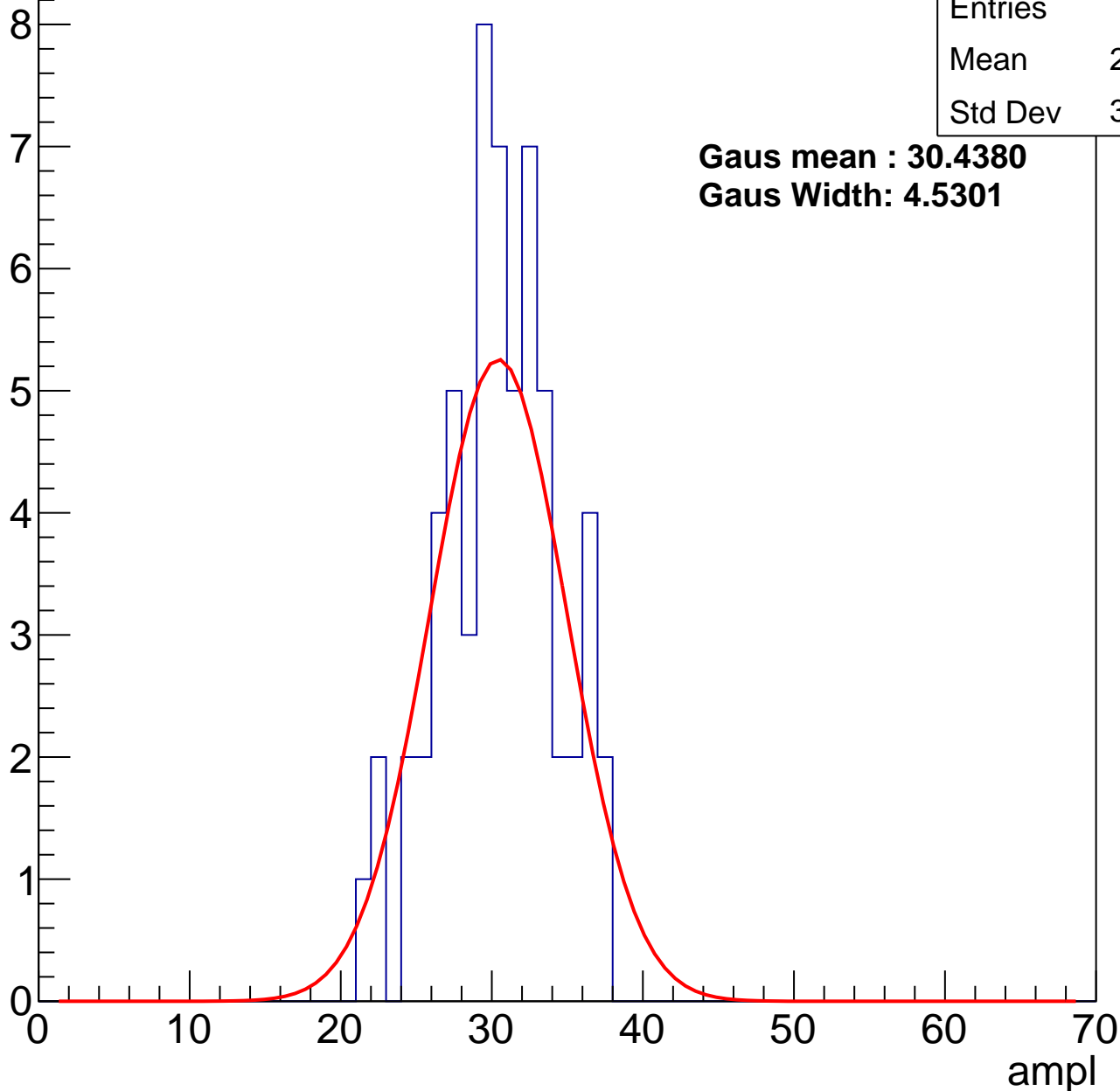
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	29.97
Std Dev	3.759

Gaus mean : 30.4380

Gaus Width: 4.5301



B0L102S, U5-ch56, adc1

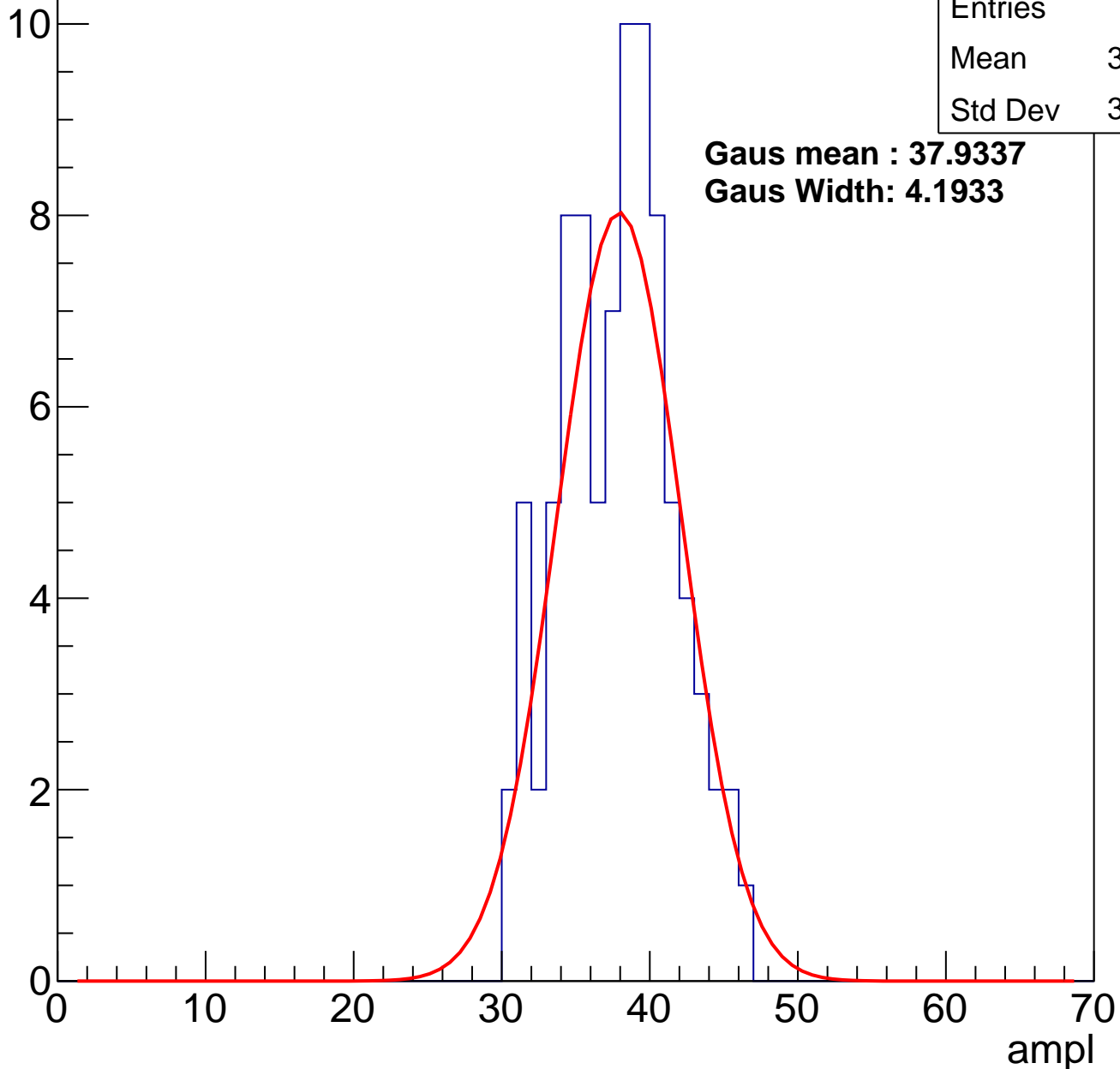
calib_packv5_042523_0143.root, FC#12, port B1

Entries	87
Mean	37.37
Std Dev	3.748

Gaus mean : 37.9337

Gaus Width: 4.1933

Entry



B0L102S, U5-ch56, adc2

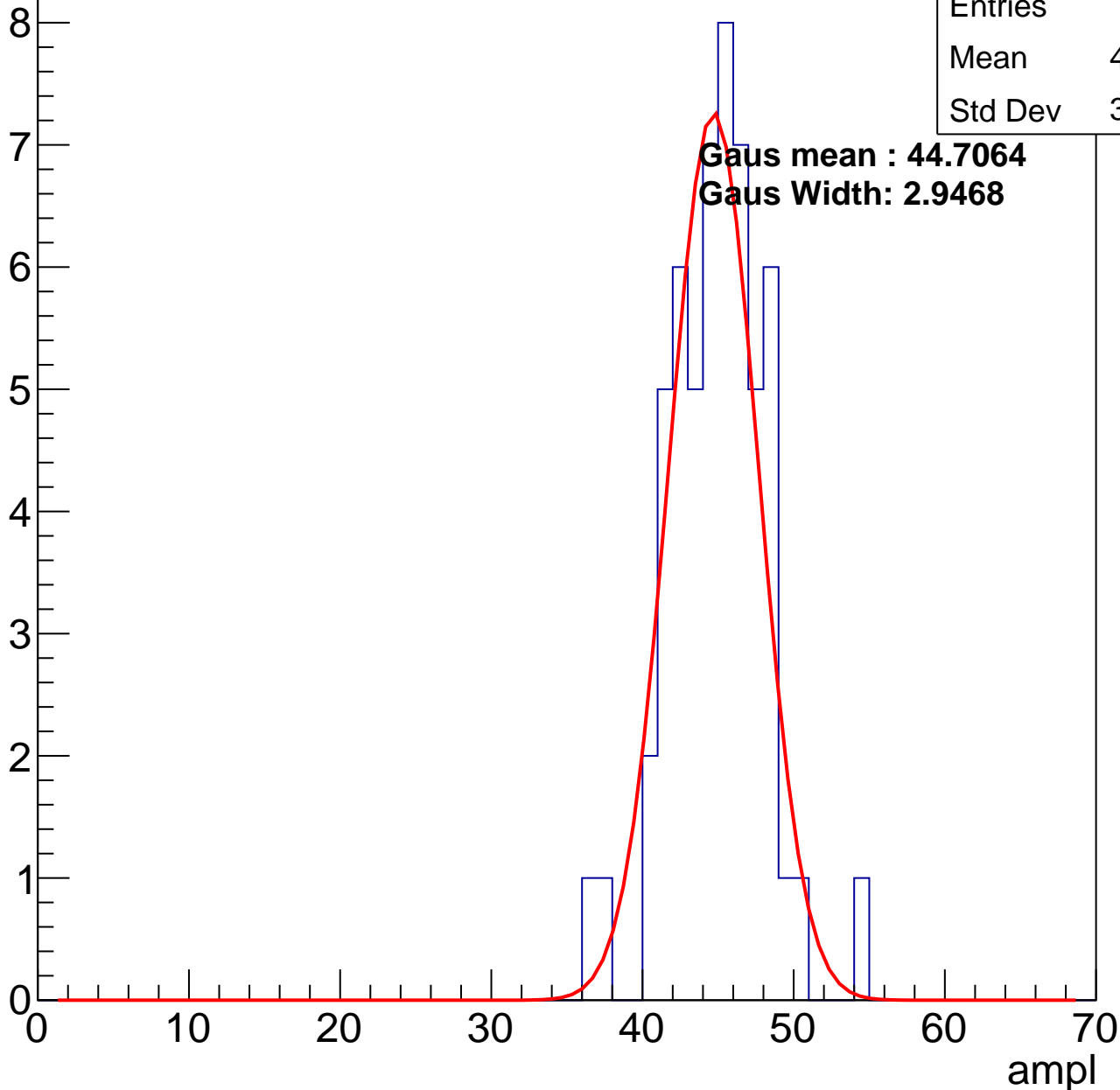
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	44.48
Std Dev	3.117

Gaus mean : 44.7064

Gaus Width: 2.9468

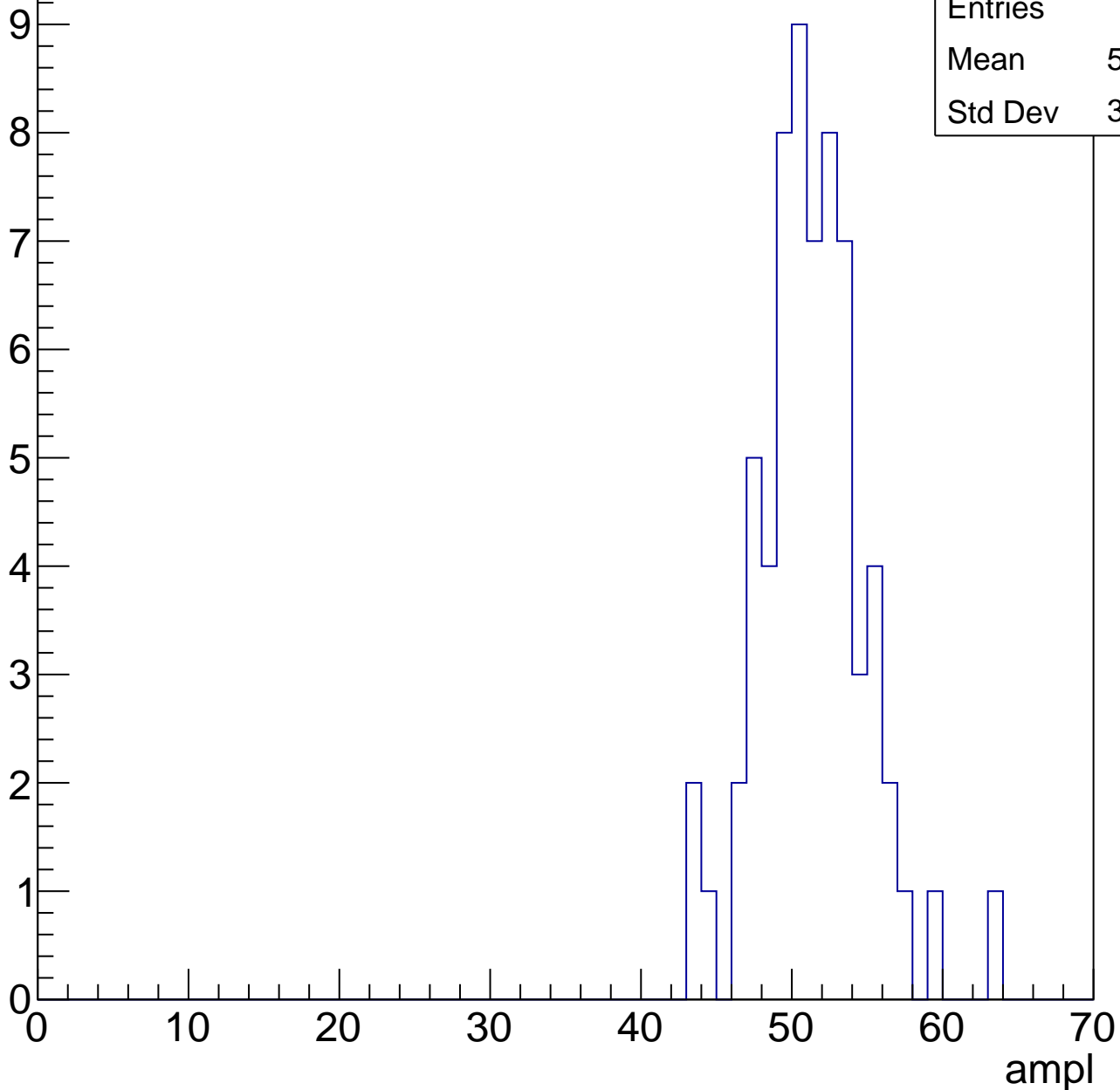


B0L102S, U5-ch56, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	50.89
Std Dev	3.518

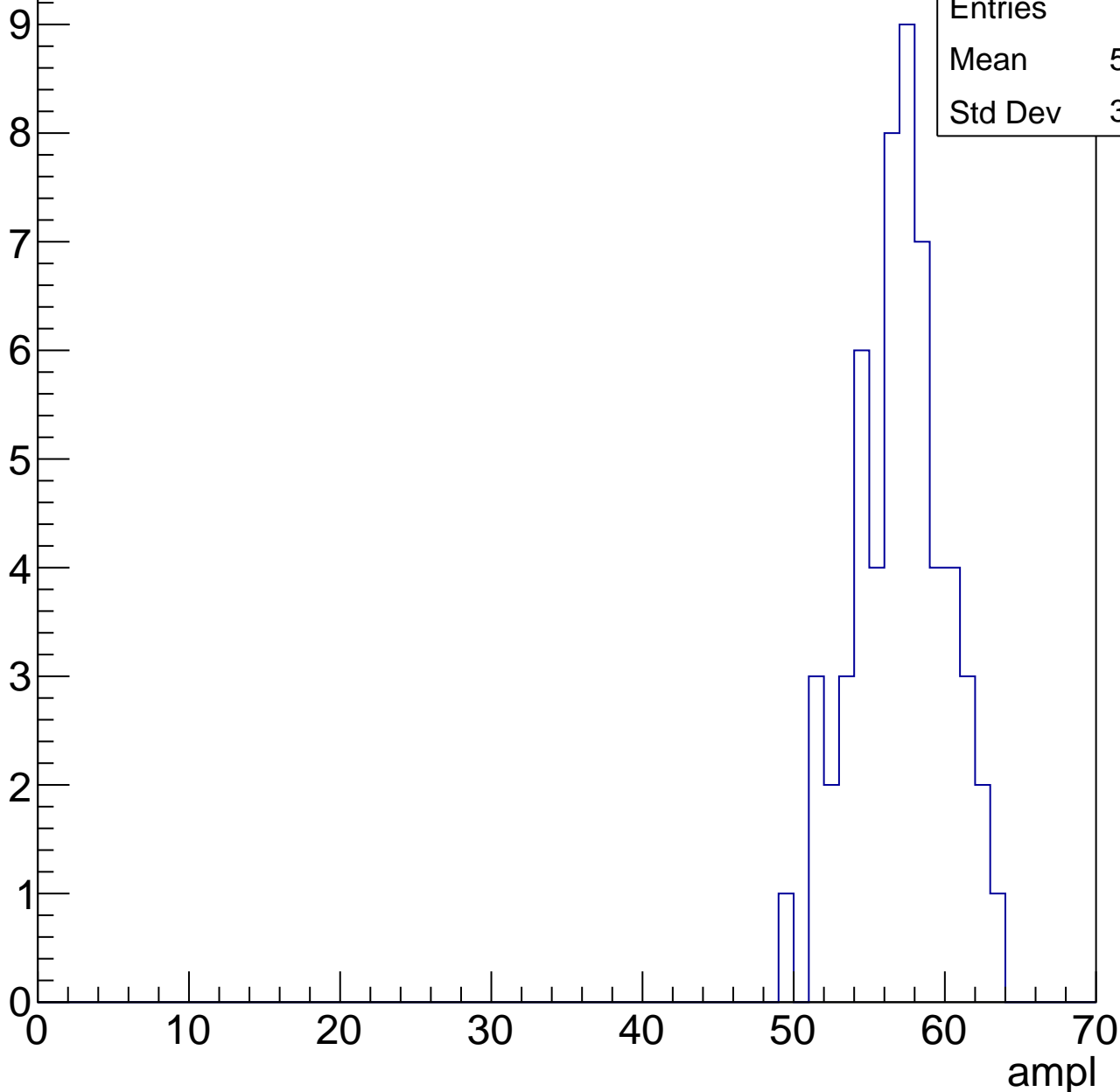


B0L102S, U5-ch56, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	56.53
Std Dev	3.044

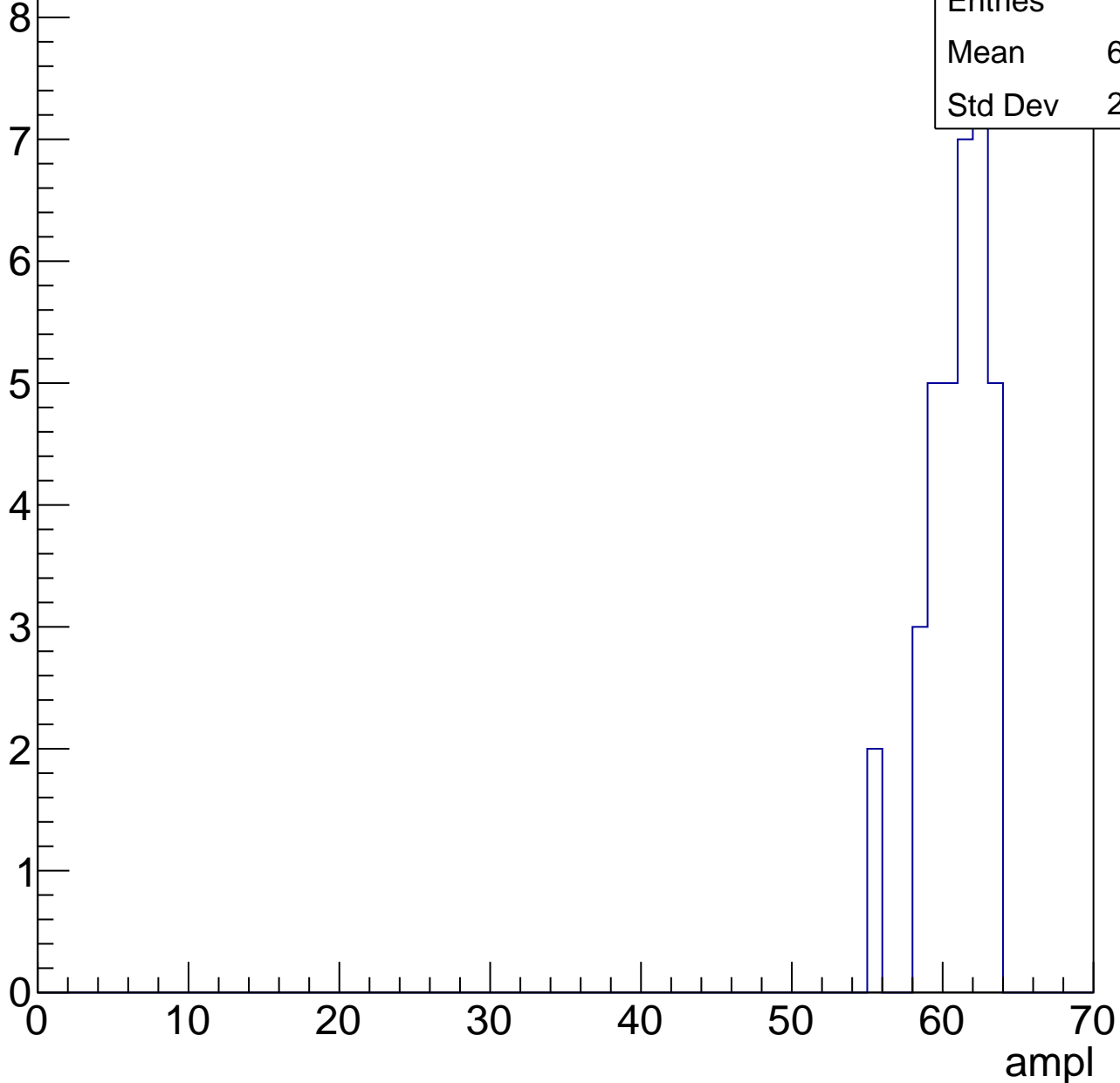


B0L102S, U5-ch56, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

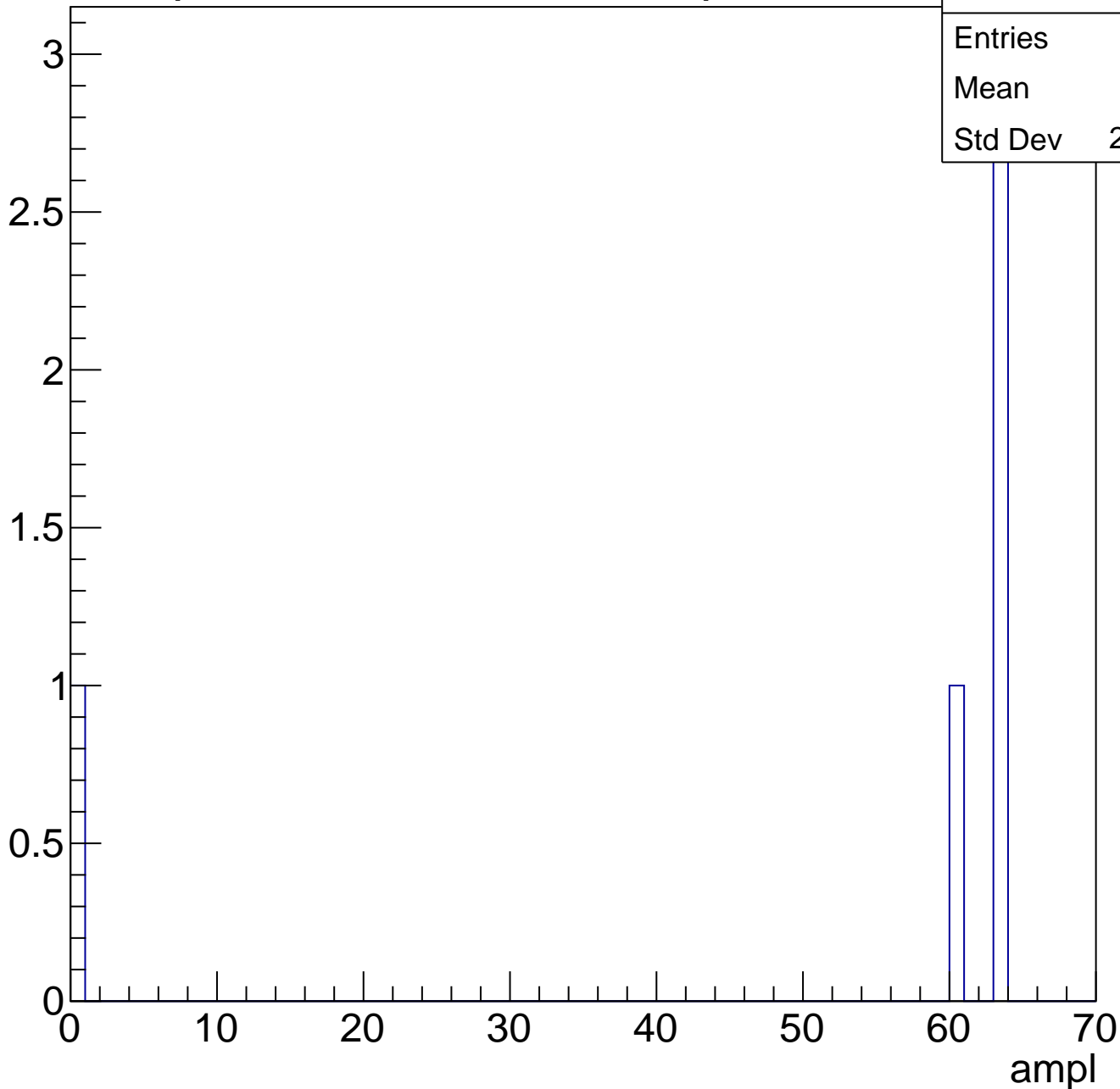
Entries	35
Mean	60.49
Std Dev	2.019



B0L102S, U5-ch56, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch56, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch57, adc0

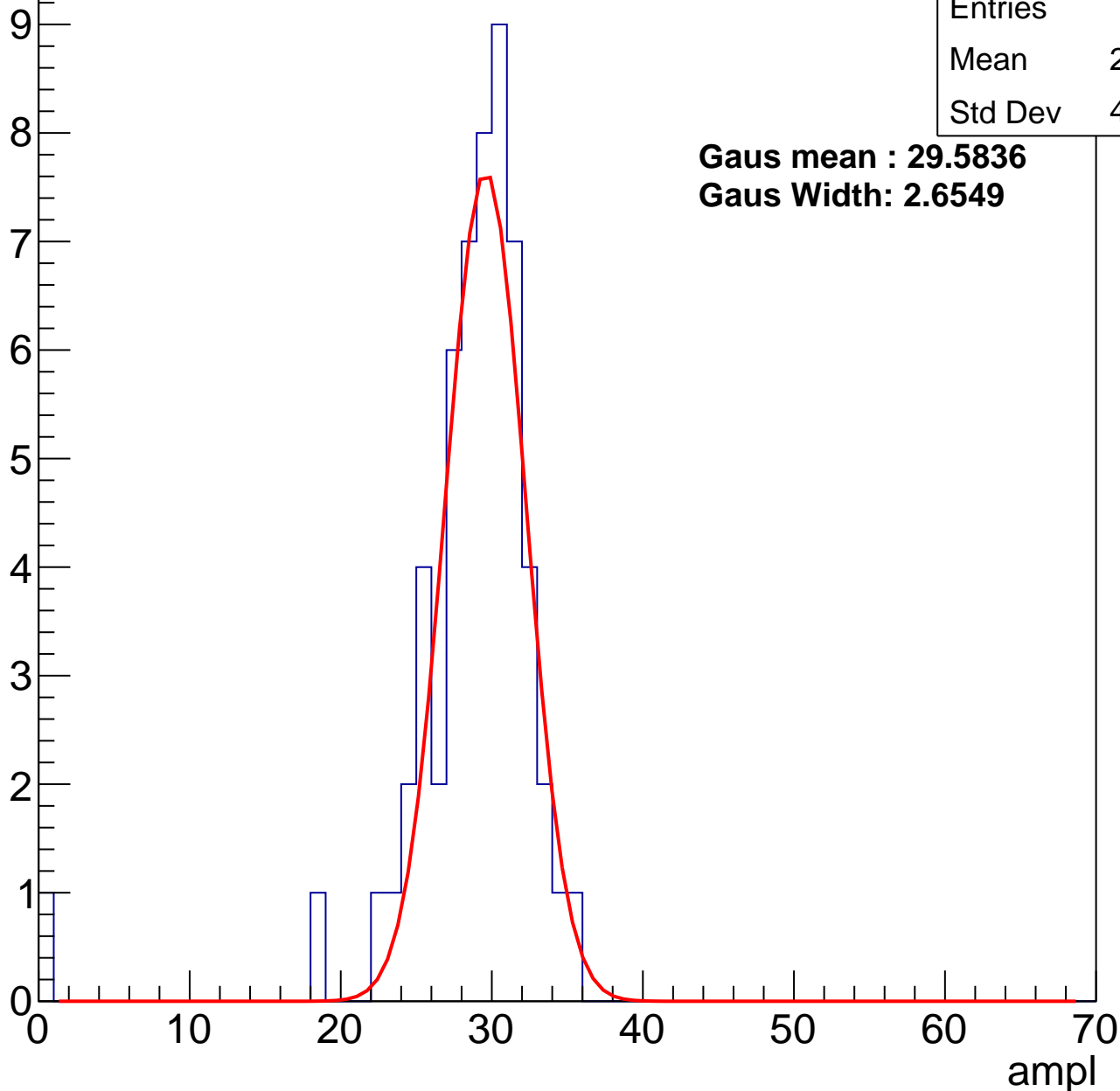
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	28.12
Std Dev	4.834

Gaus mean : 29.5836

Gaus Width: 2.6549



B0L102S, U5-ch57, adc1

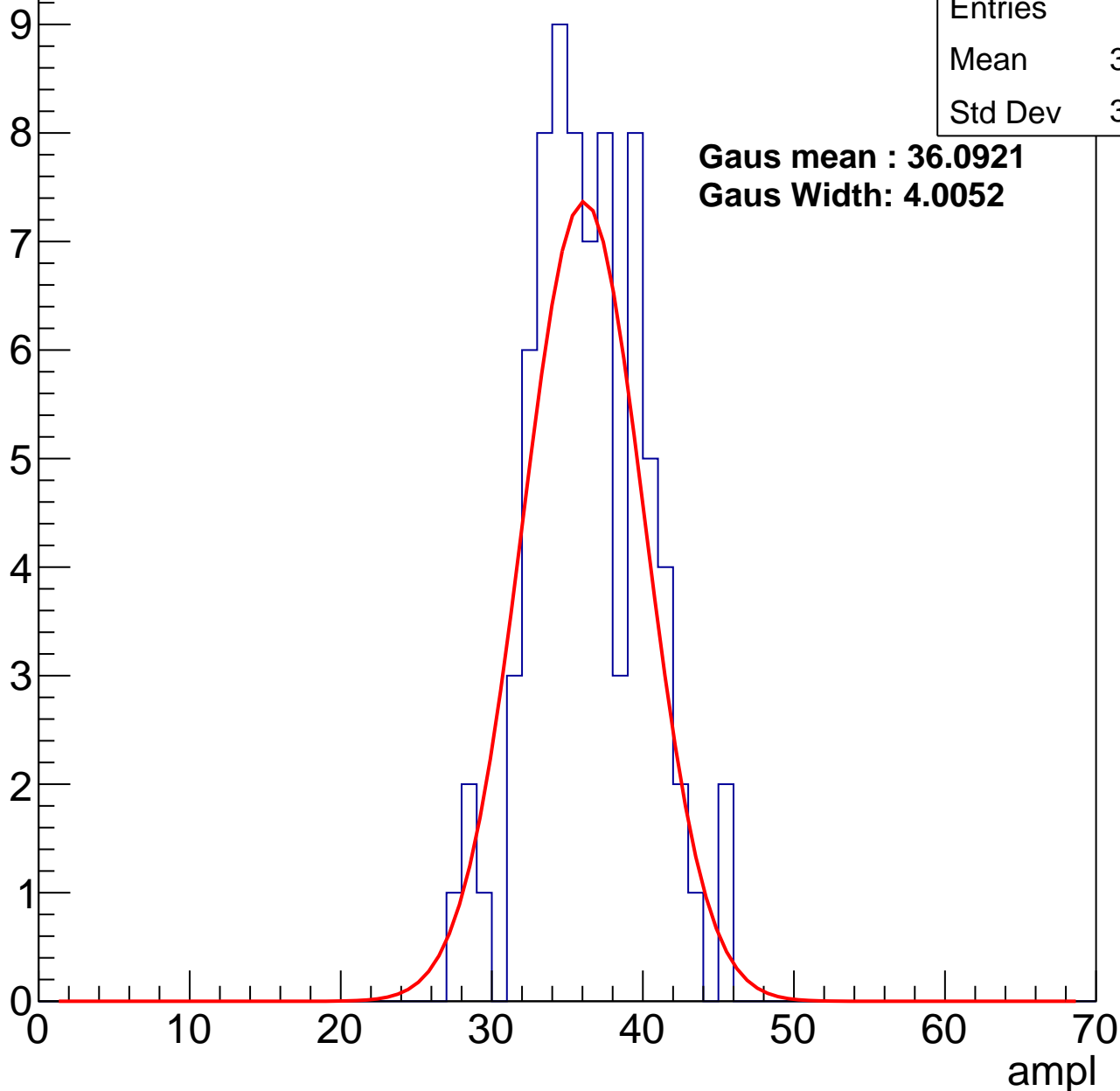
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	35.92
Std Dev	3.755

Gaus mean : 36.0921

Gaus Width: 4.0052



B0L102S, U5-ch57, adc2

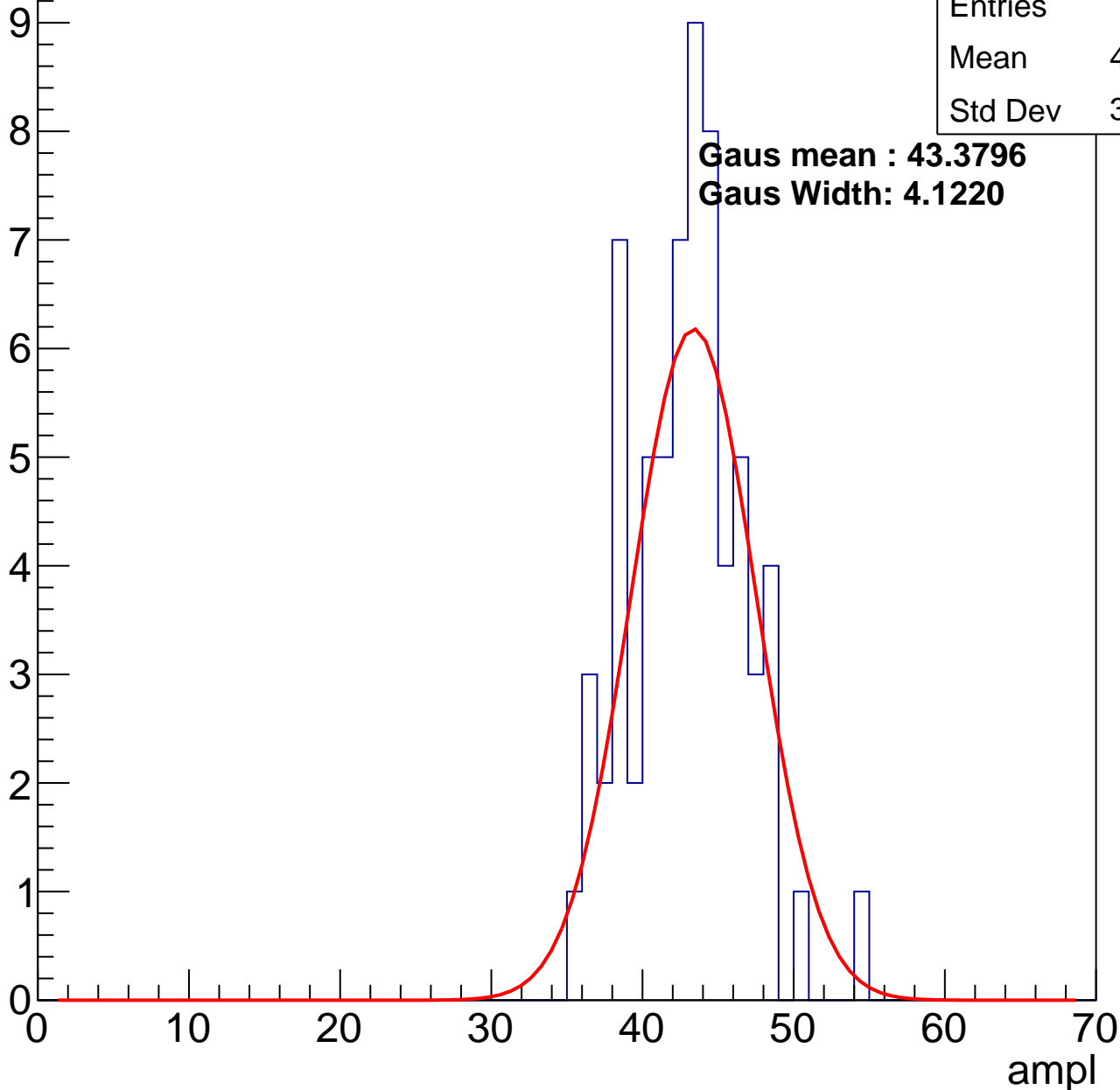
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	42.48
Std Dev	3.727

Gaus mean : 43.3796

Gaus Width: 4.1220

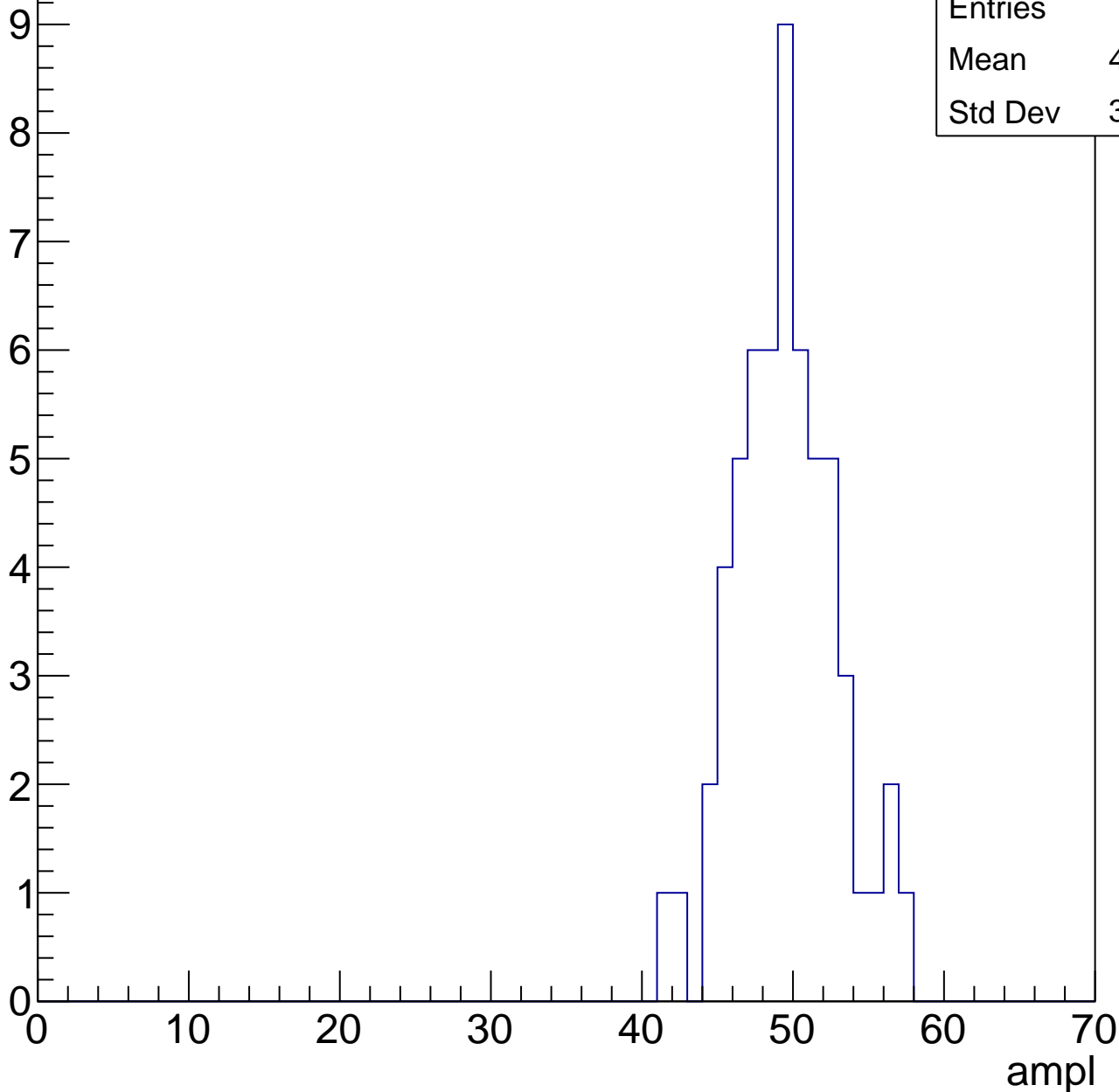


B0L102S, U5-ch57, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	49.03
Std Dev	3.332

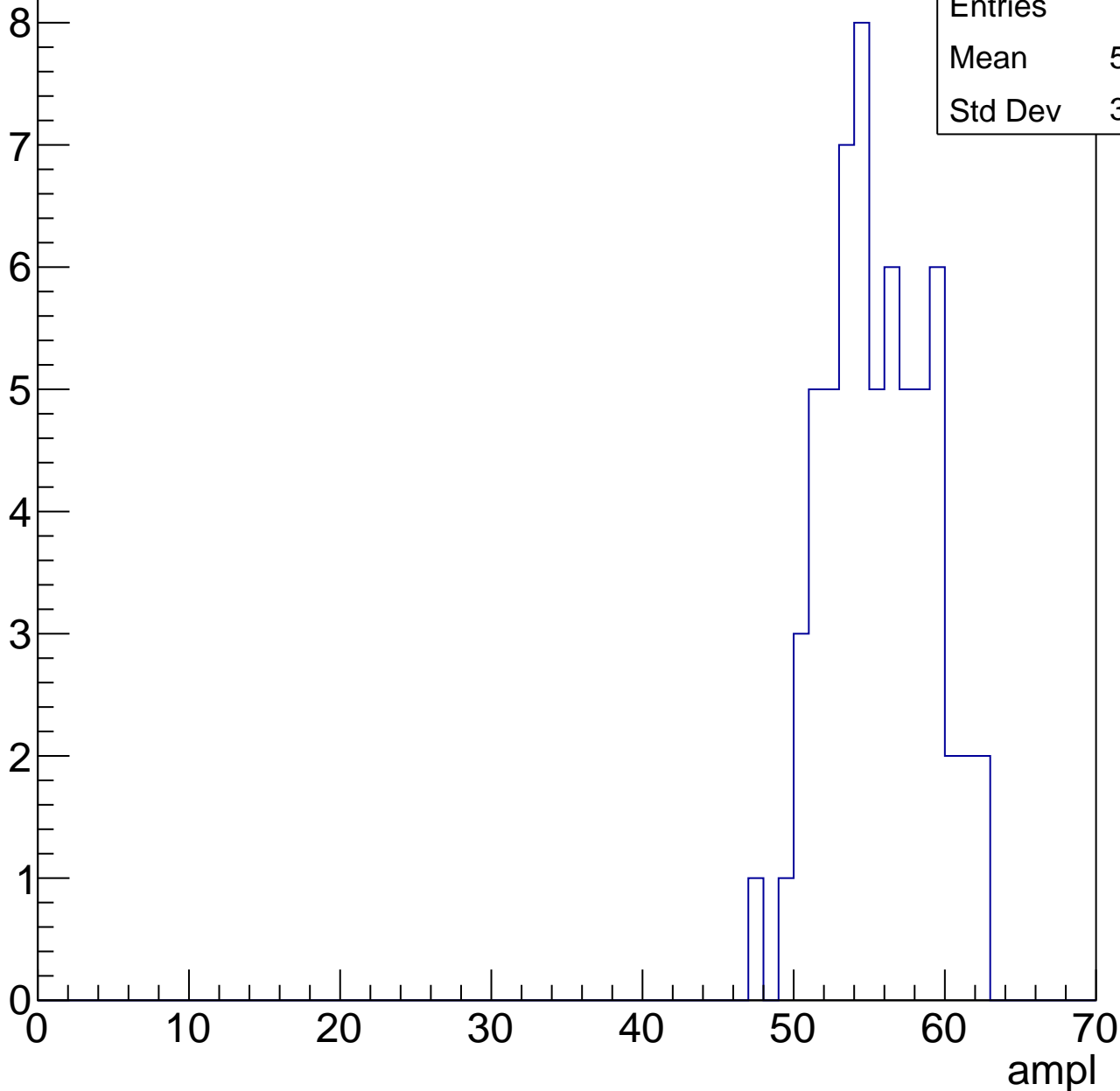


B0L102S, U5-ch57, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	55.08
Std Dev	3.387

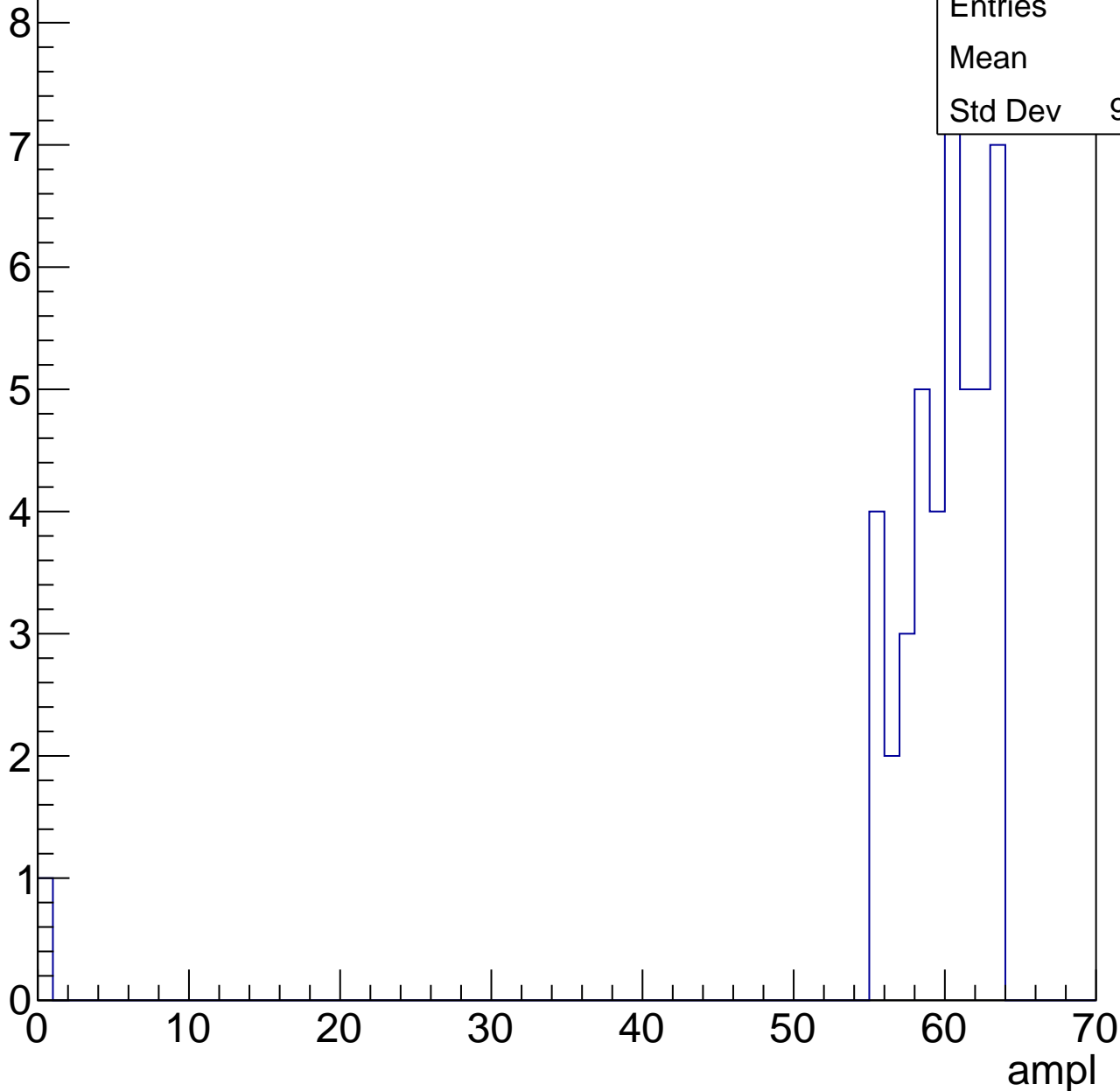


B0L102S, U5-ch57, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

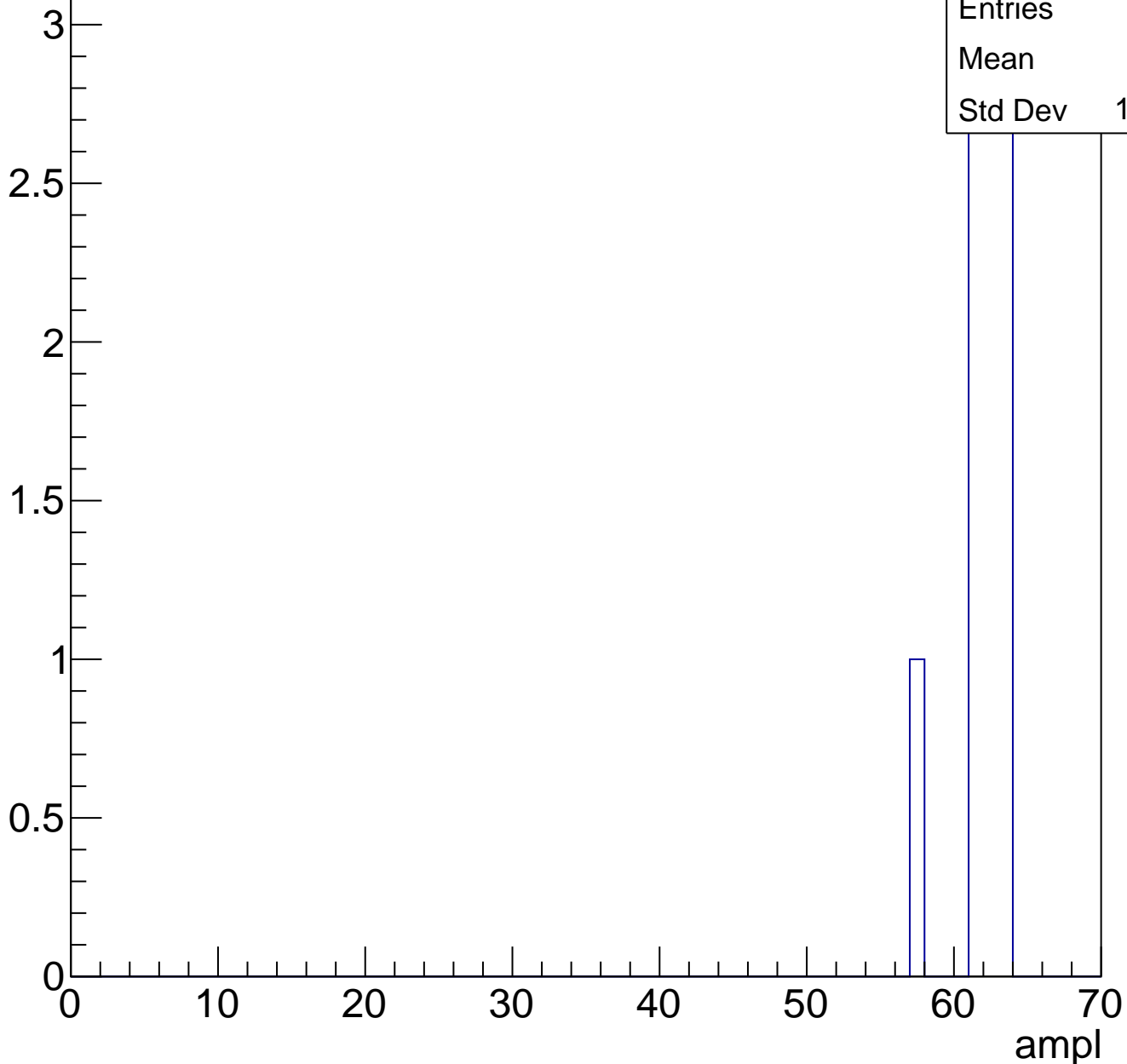
Entries	44
Mean	58.3
Std Dev	9.223



B0L102S, U5-ch57, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch57, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch58, adc0

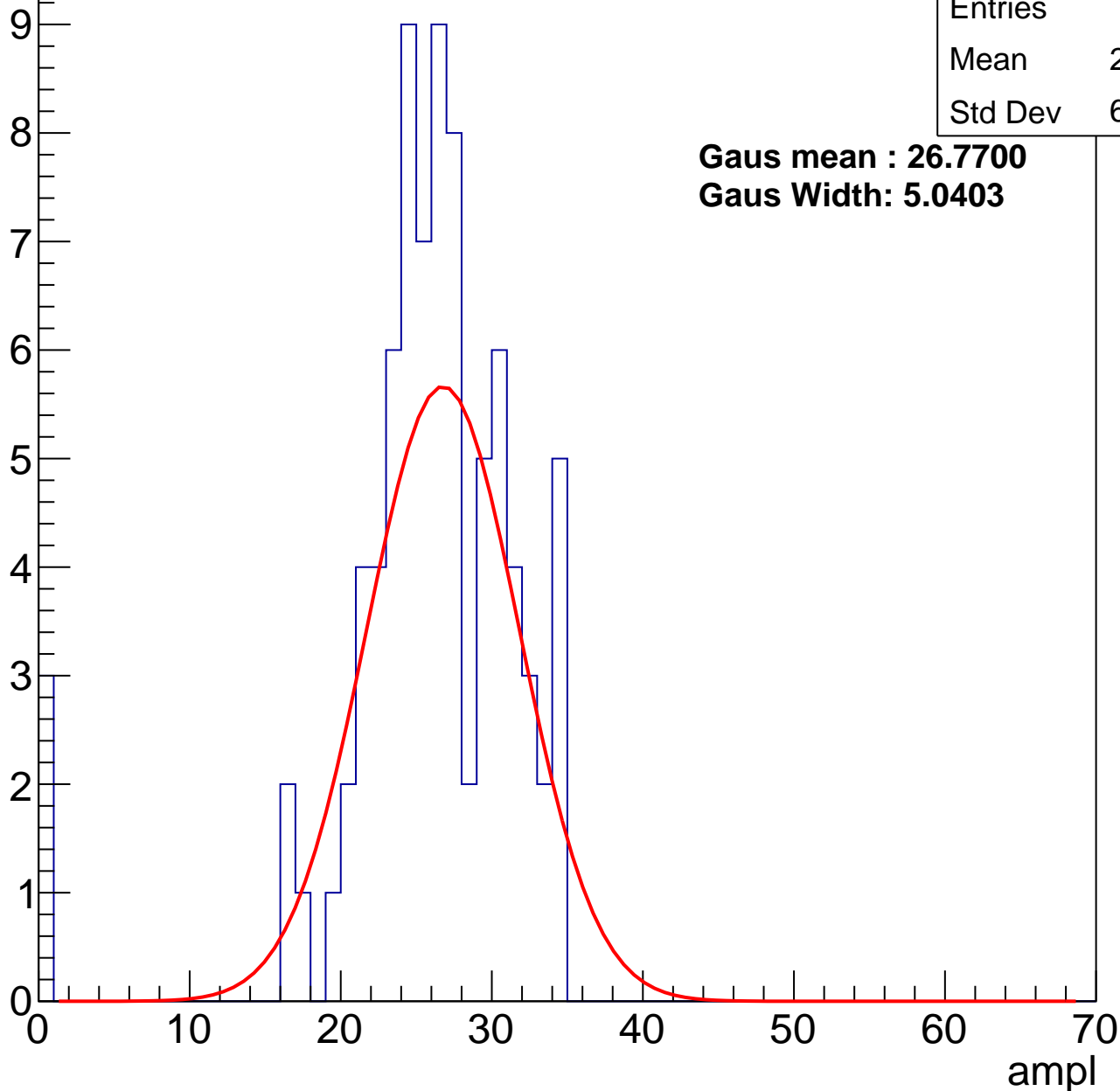
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	25.25
Std Dev	6.426

Gaus mean : 26.7700

Gaus Width: 5.0403



B0L102S, U5-ch58, adc1

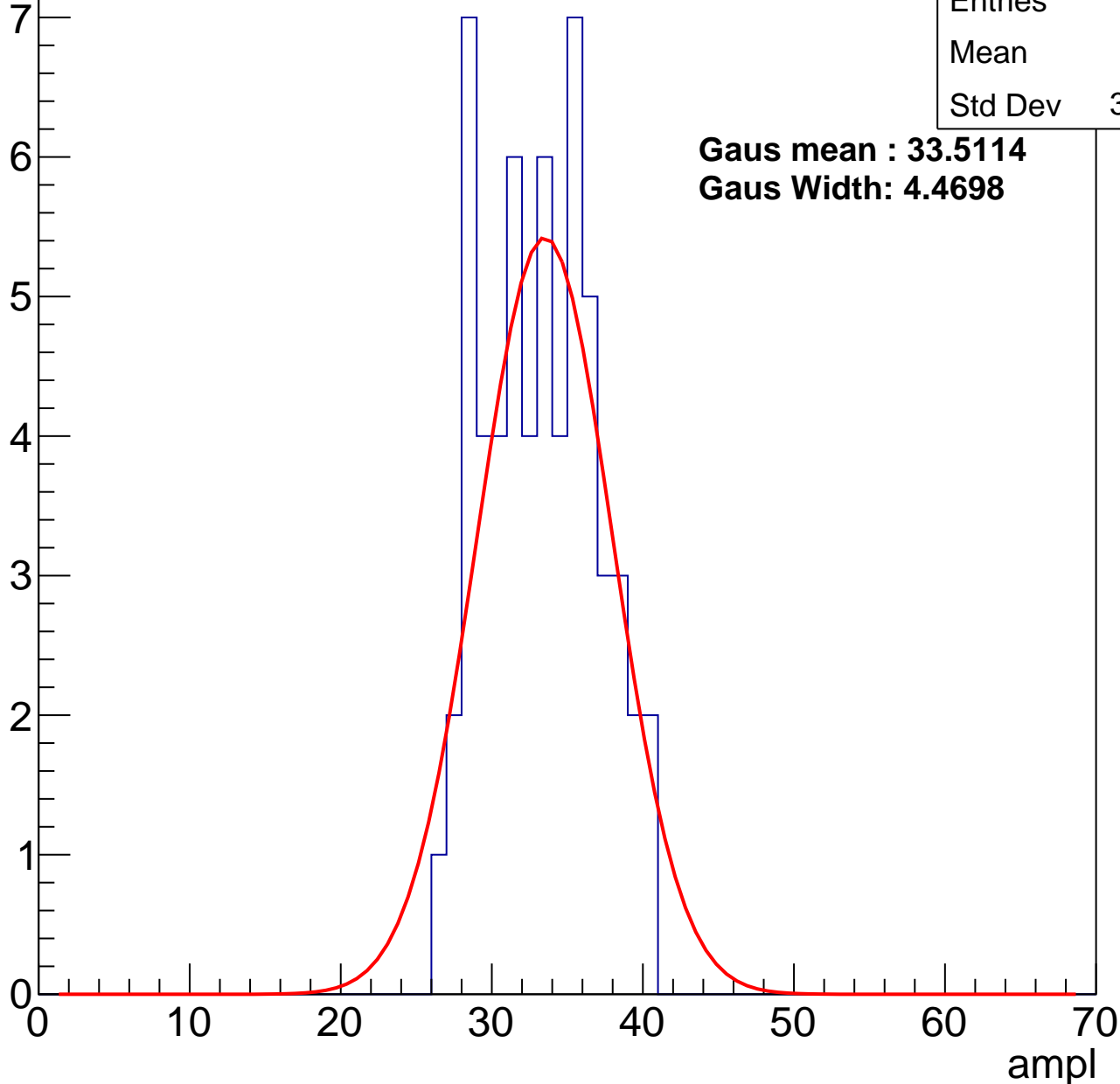
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	32.8
Std Dev	3.632

Gaus mean : 33.5114

Gaus Width: 4.4698



B0L102S, U5-ch58, adc2

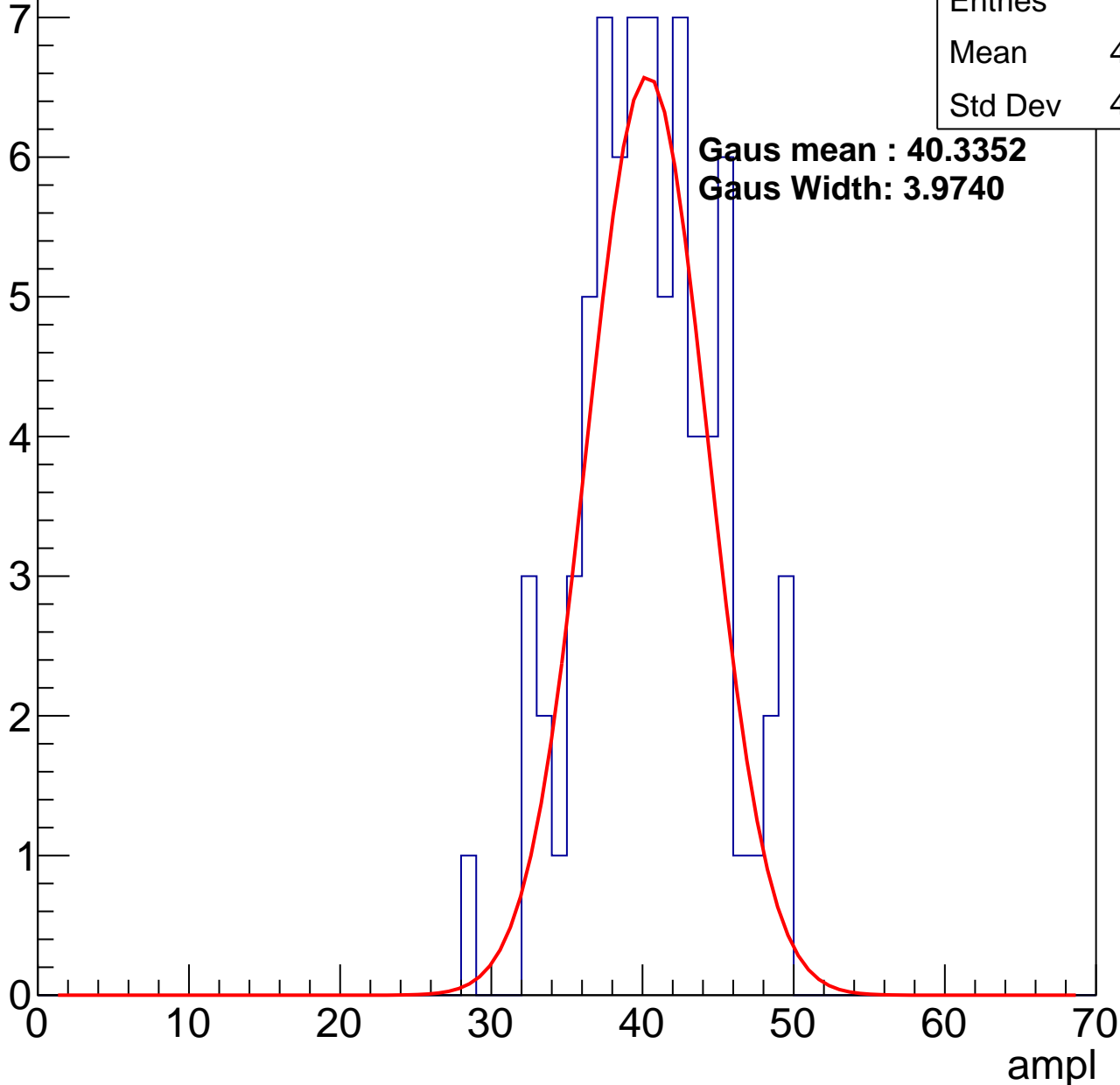
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	40.03
Std Dev	4.409

Gaus mean : 40.3352

Gaus Width: 3.9740

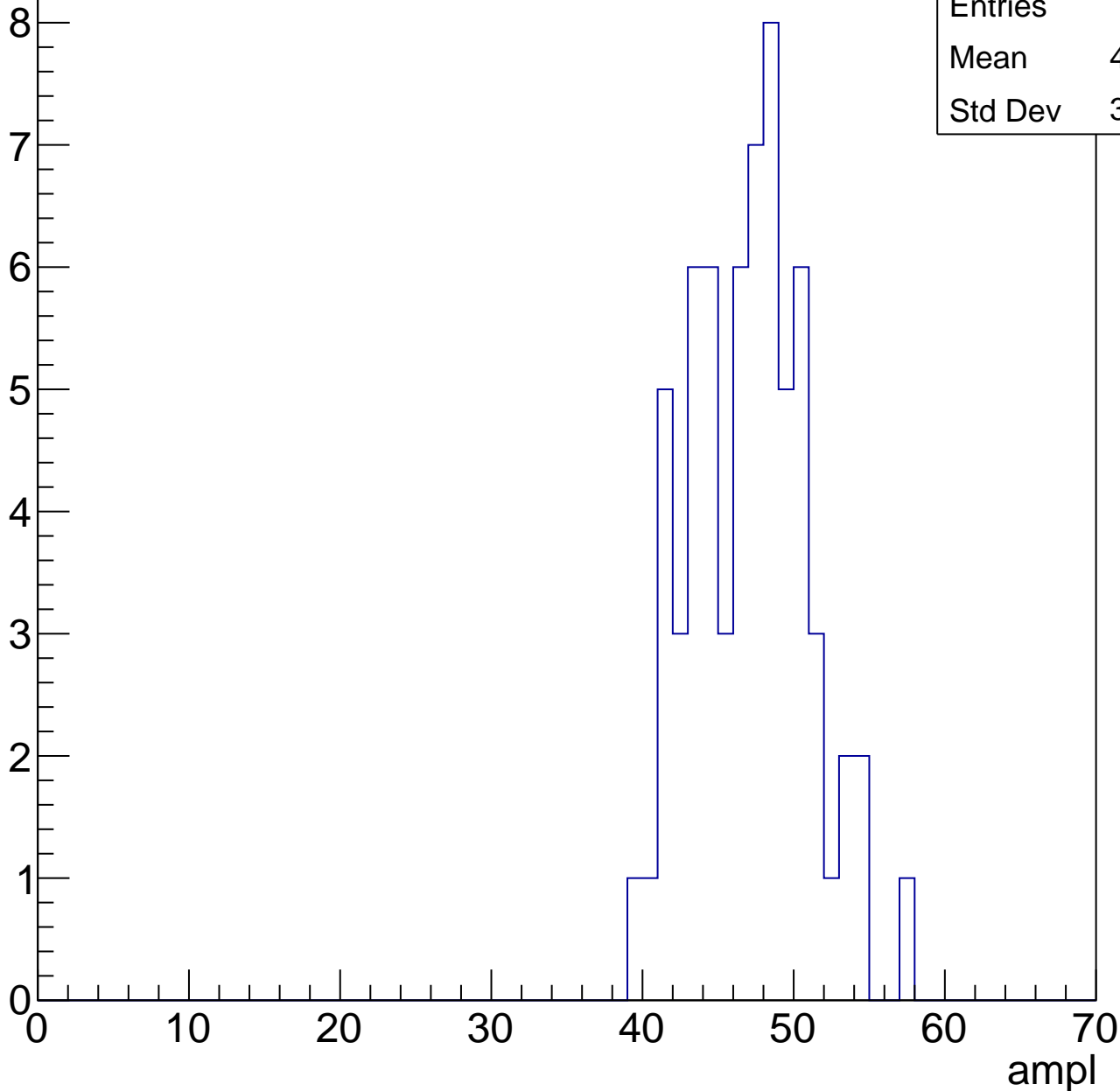


B0L102S, U5-ch58, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	46.62
Std Dev	3.797

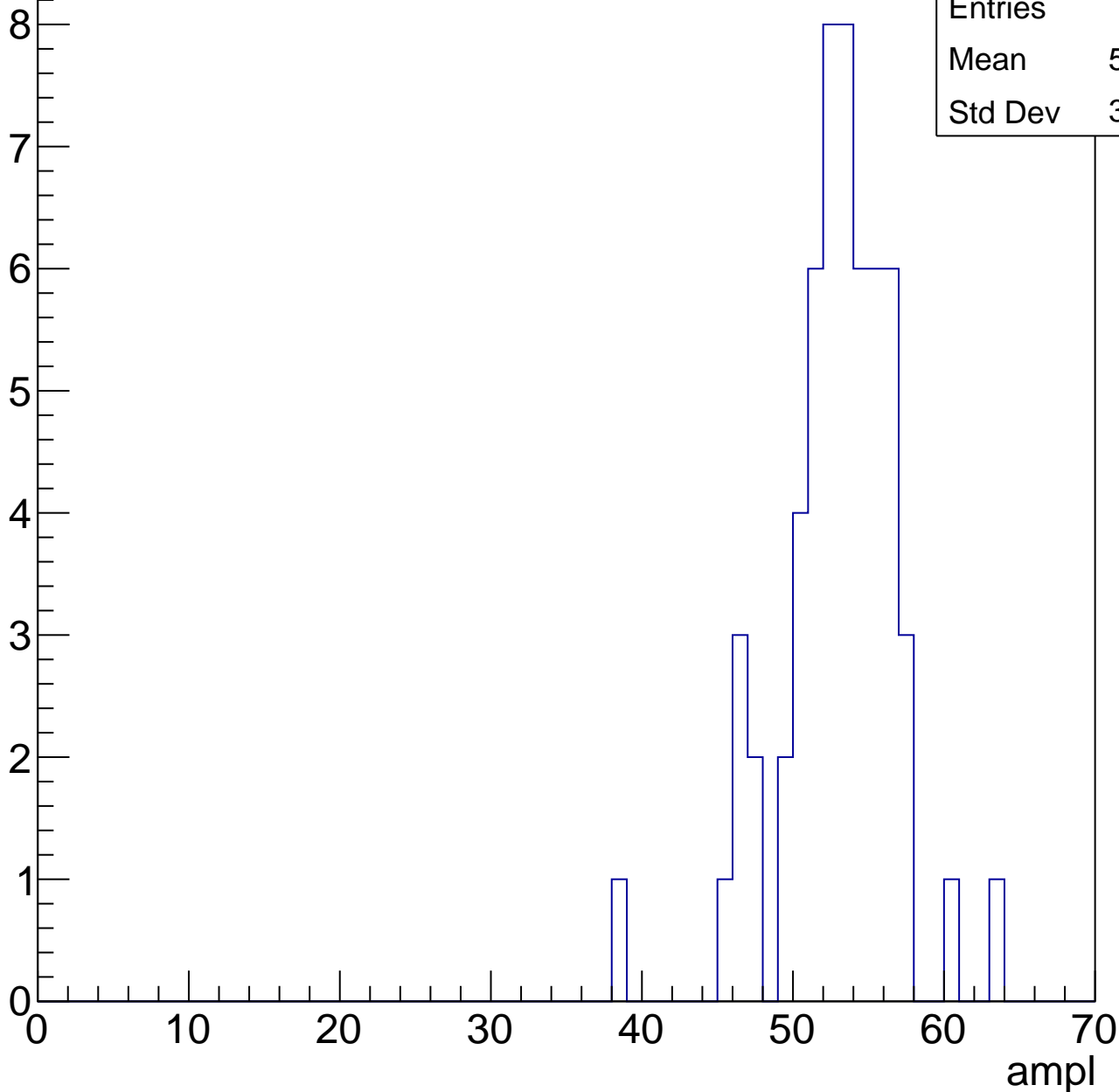


B0L102S, U5-ch58, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	52.47
Std Dev	3.878



B0L102S, U5-ch58, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

10

Entries 68

Mean 58.13

Std Dev 7.618

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B0L102S, U5-ch58, adc6

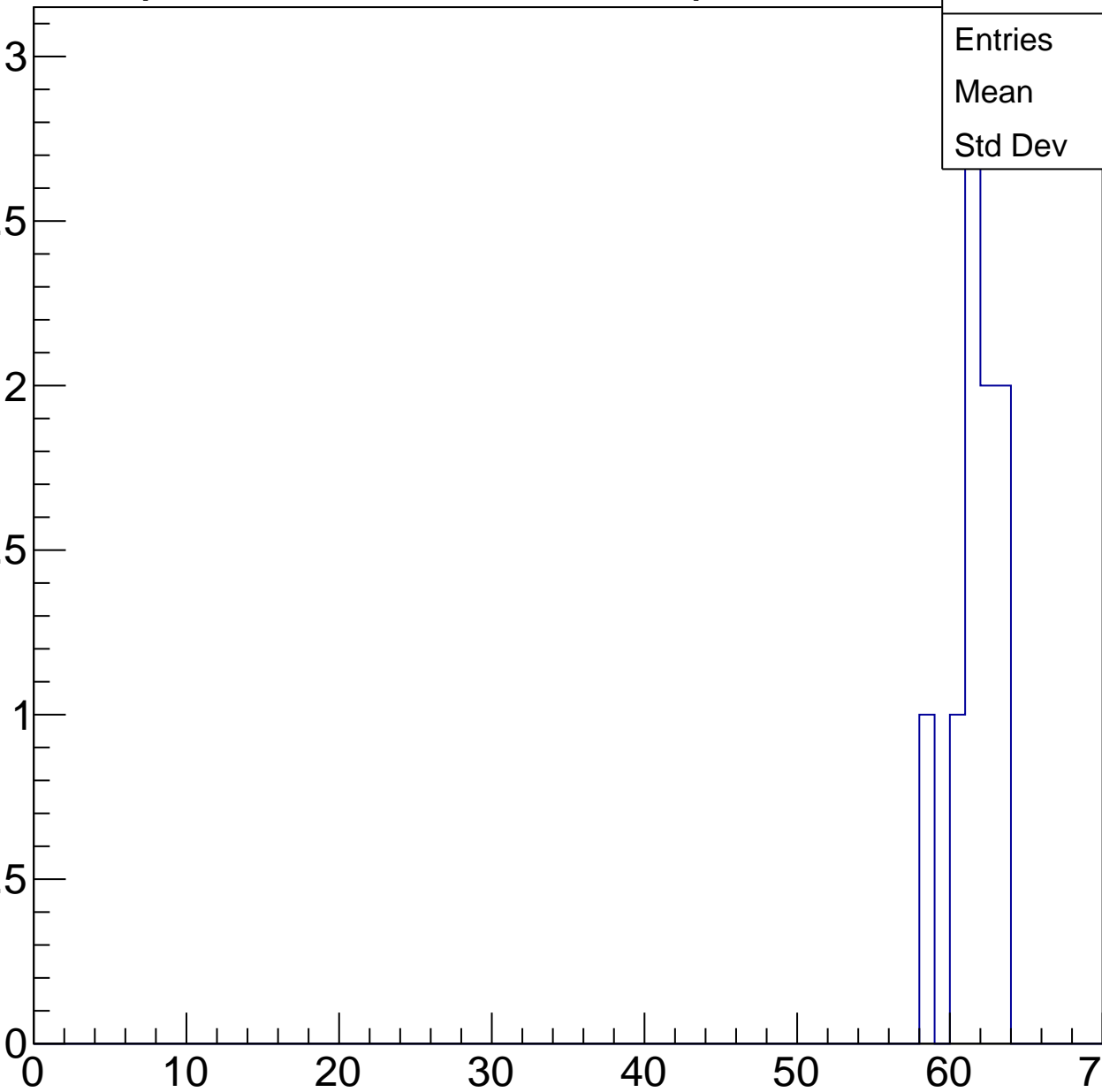
calib_packv5_042523_0143.root, FC#12, port B1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.22
Std Dev	1.474

ampl



B0L102S, U5-ch58, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch59, adc0

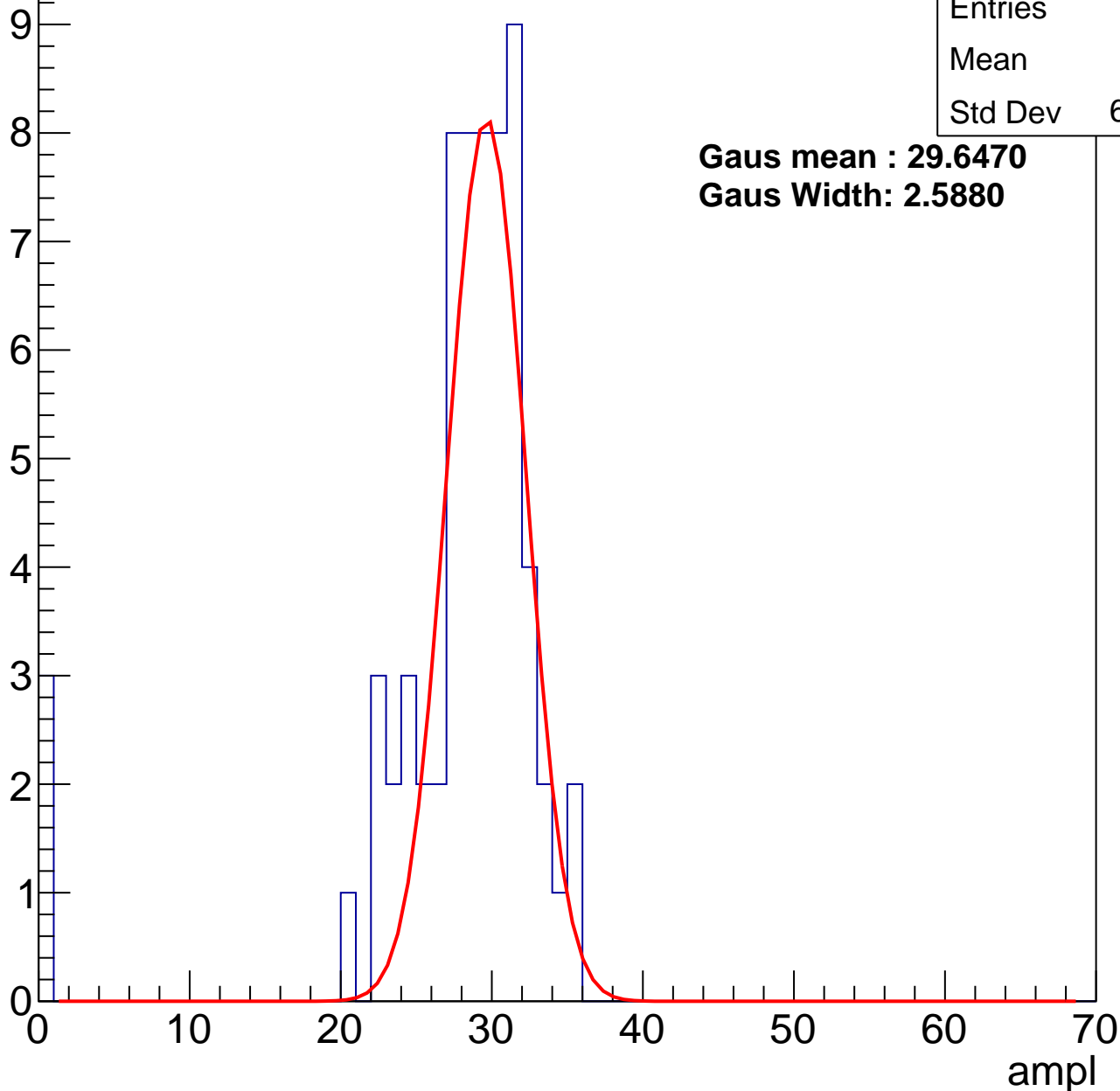
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.2
Std Dev	6.722

Gaus mean : 29.6470

Gaus Width: 2.5880



B0L102S, U5-ch59, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	84
Mean	35.75
Std Dev	3.44

Gaus mean : 36.7928

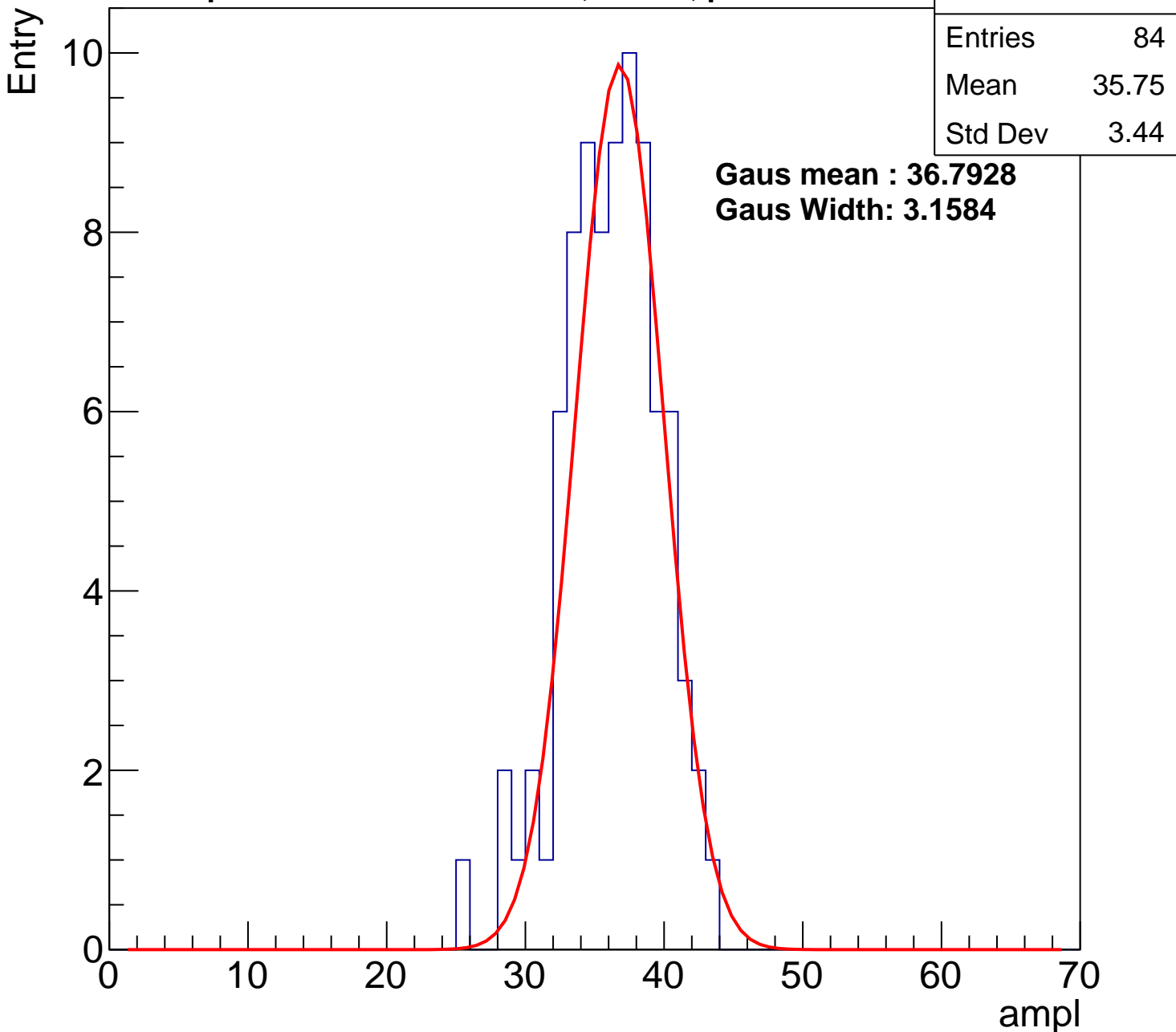
Gaus Width: 3.1584

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch59, adc2

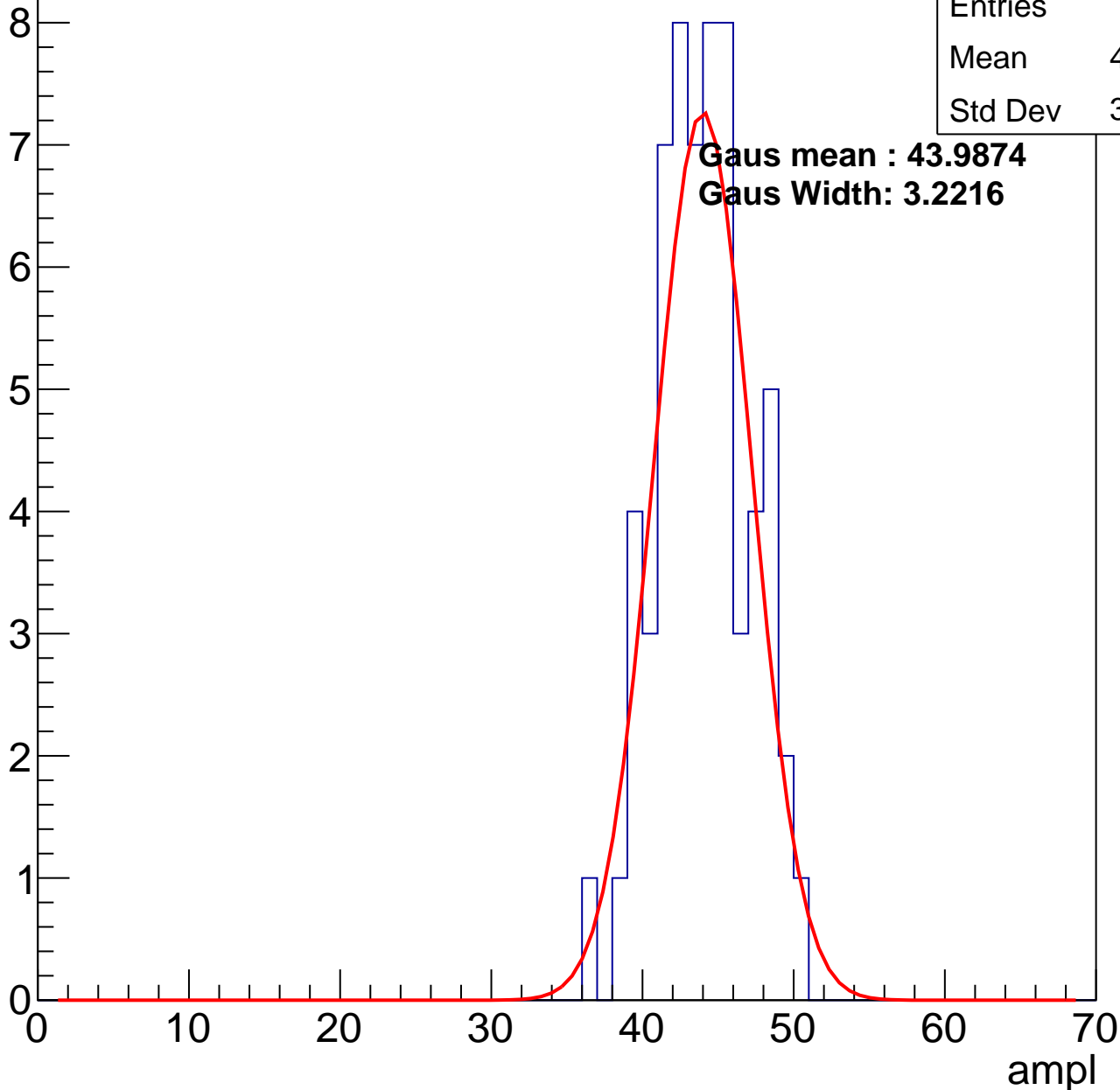
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	43.55
Std Dev	3.004

Gaus mean : 43.9874

Gaus Width: 3.2216

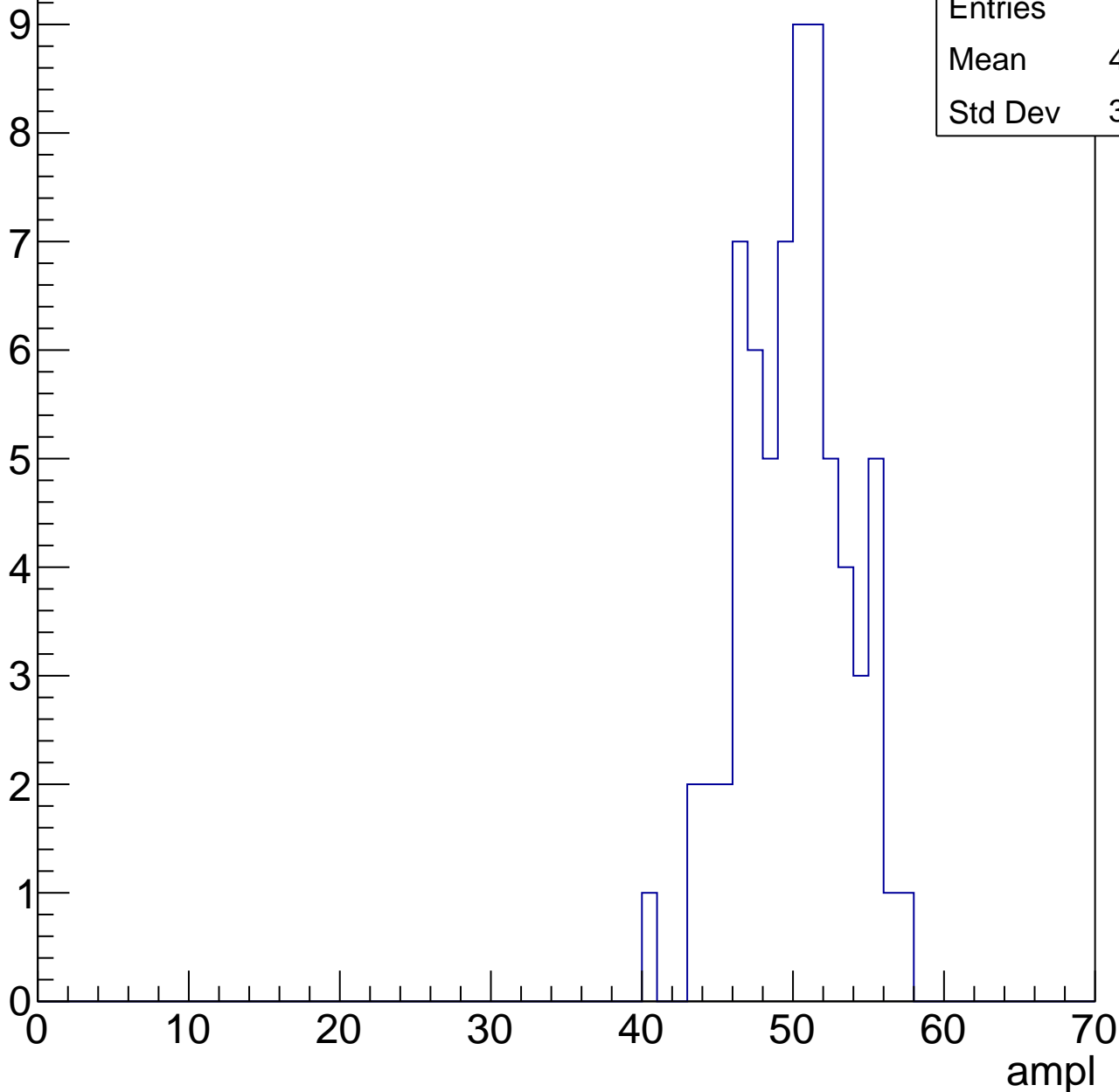


B0L102S, U5-ch59, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	49.59
Std Dev	3.457

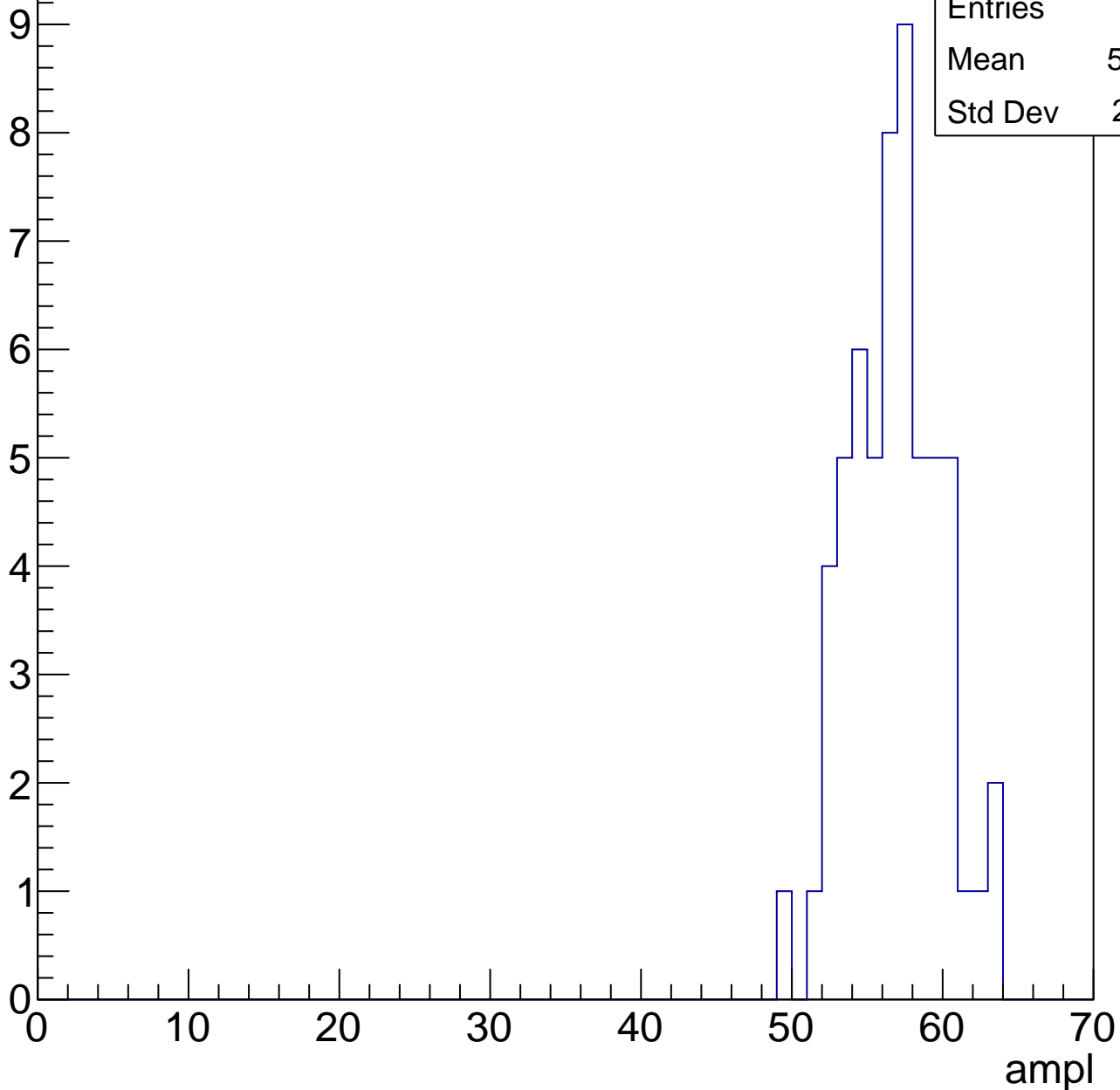


B0L102S, U5-ch59, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	56.33
Std Dev	2.991

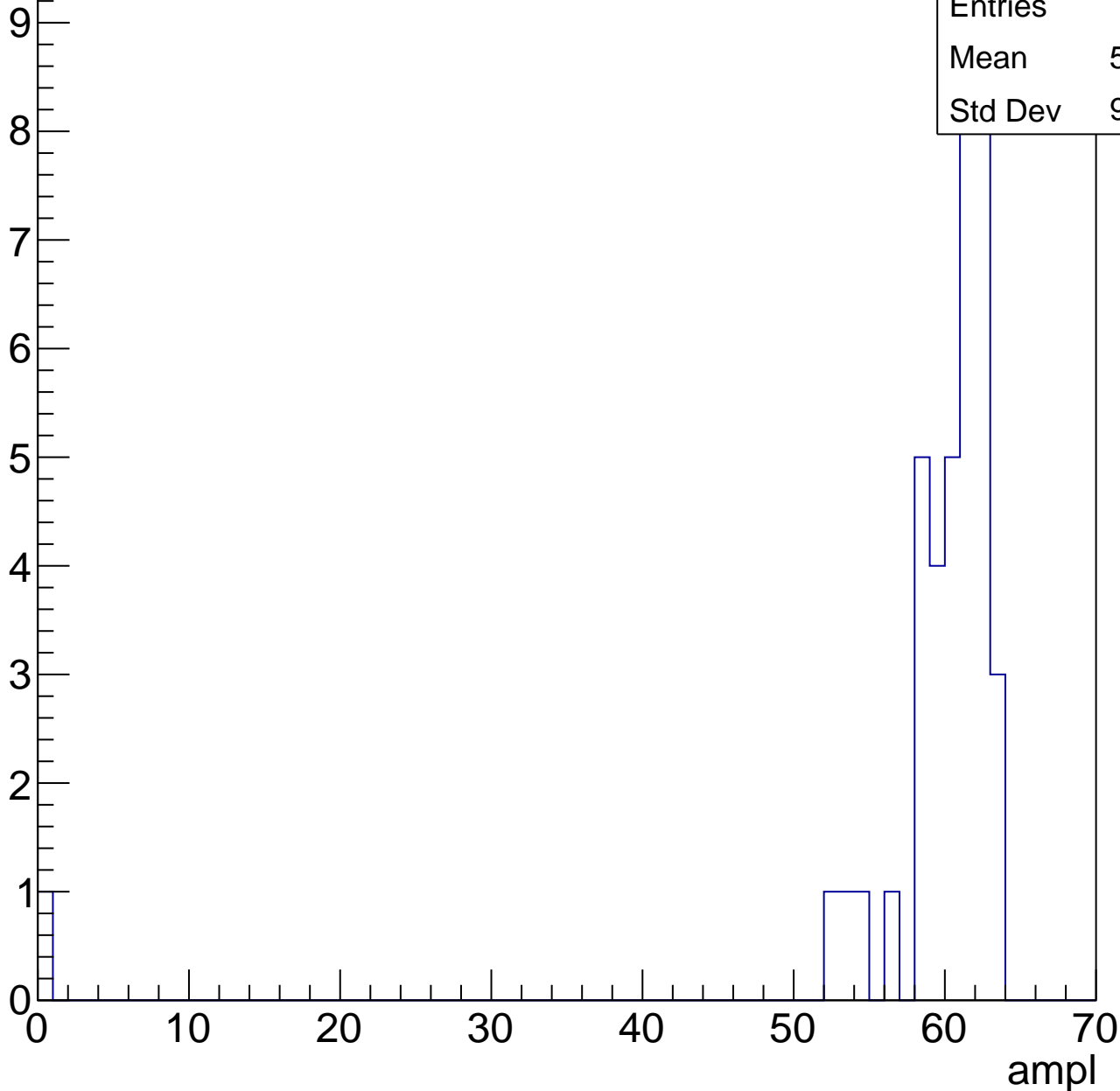


B0L102S, U5-ch59, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.36
Std Dev	9.812

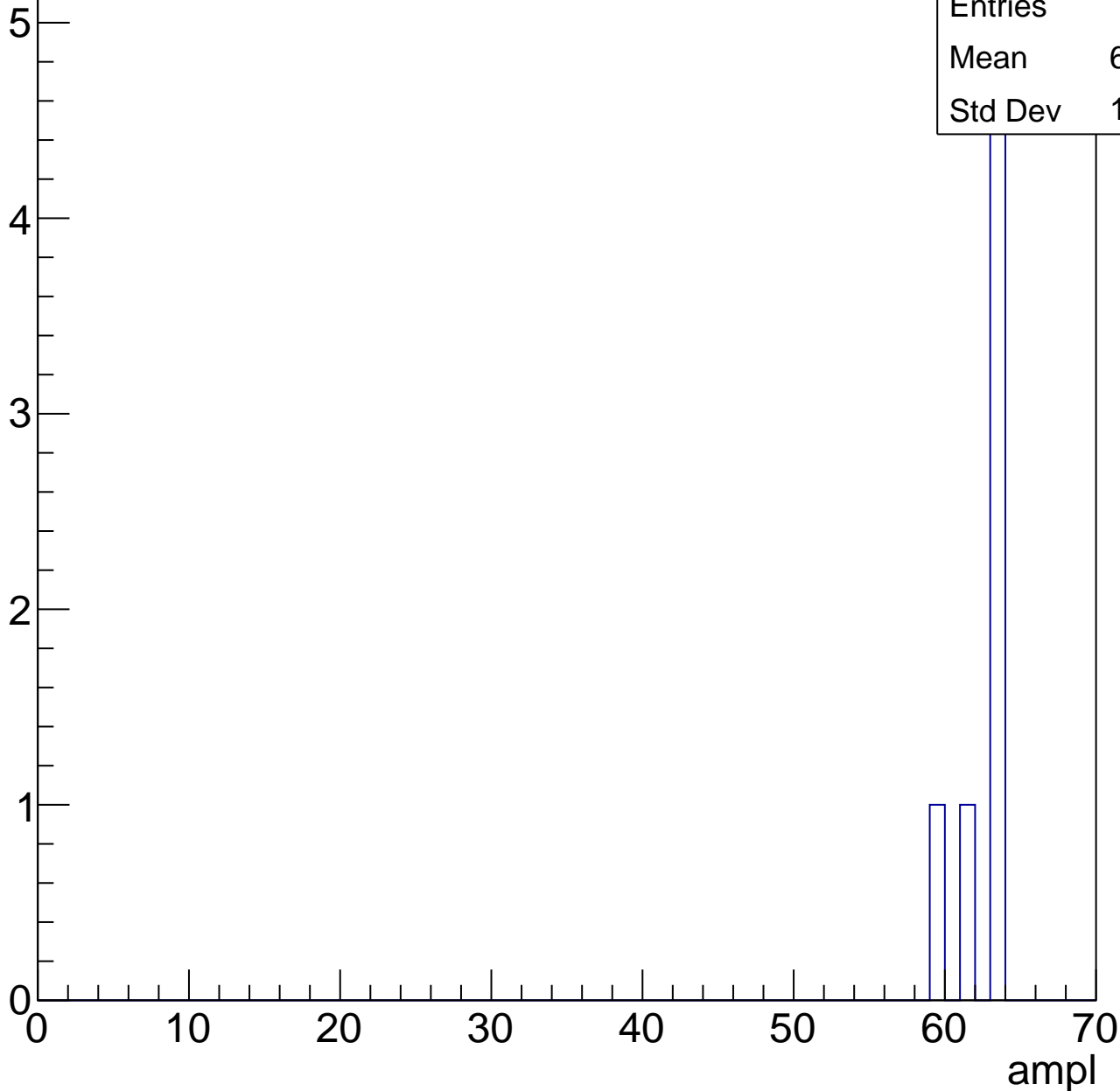


B0L102S, U5-ch59, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	7
Mean	62.14
Std Dev	1.457



B0L102S, U5-ch59, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	0
Std Dev	0

B0L102S, U5-ch60, adc0

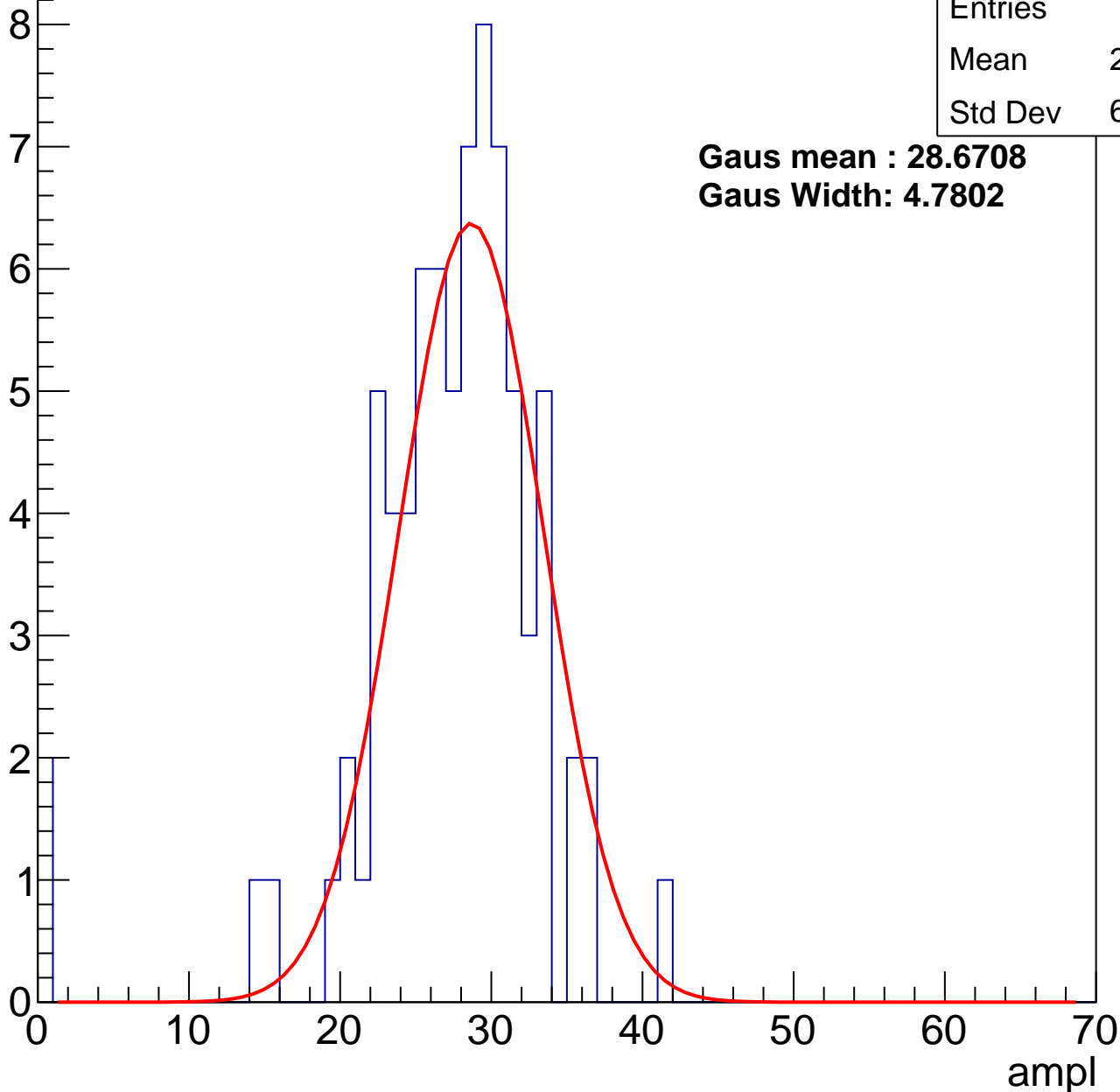
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	26.73
Std Dev	6.346

Gaus mean : 28.6708

Gaus Width: 4.7802



B0L102S, U5-ch60, adc1

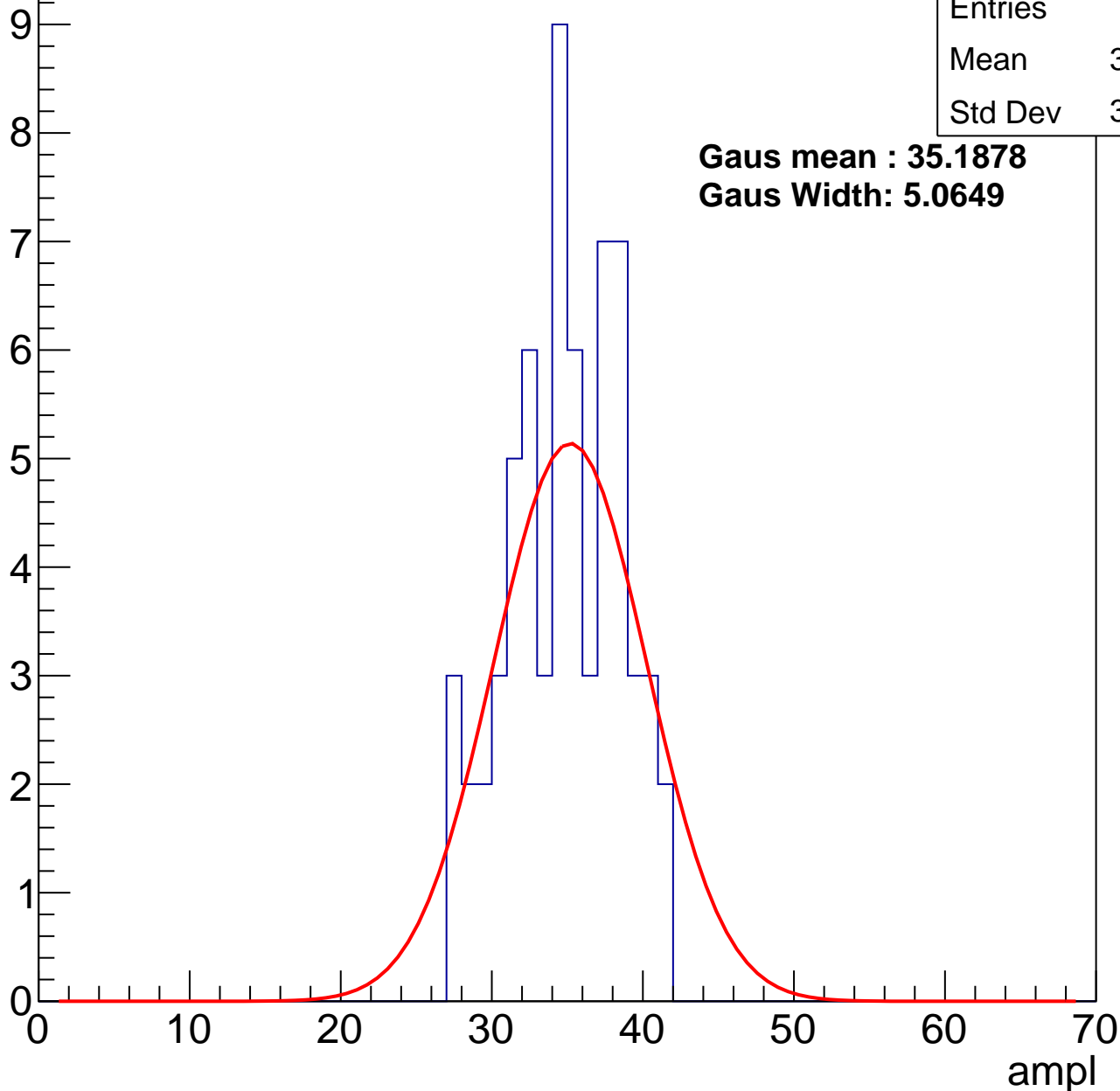
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	34.36
Std Dev	3.654

Gaus mean : 35.1878

Gaus Width: 5.0649



B0L102S, U5-ch60, adc2

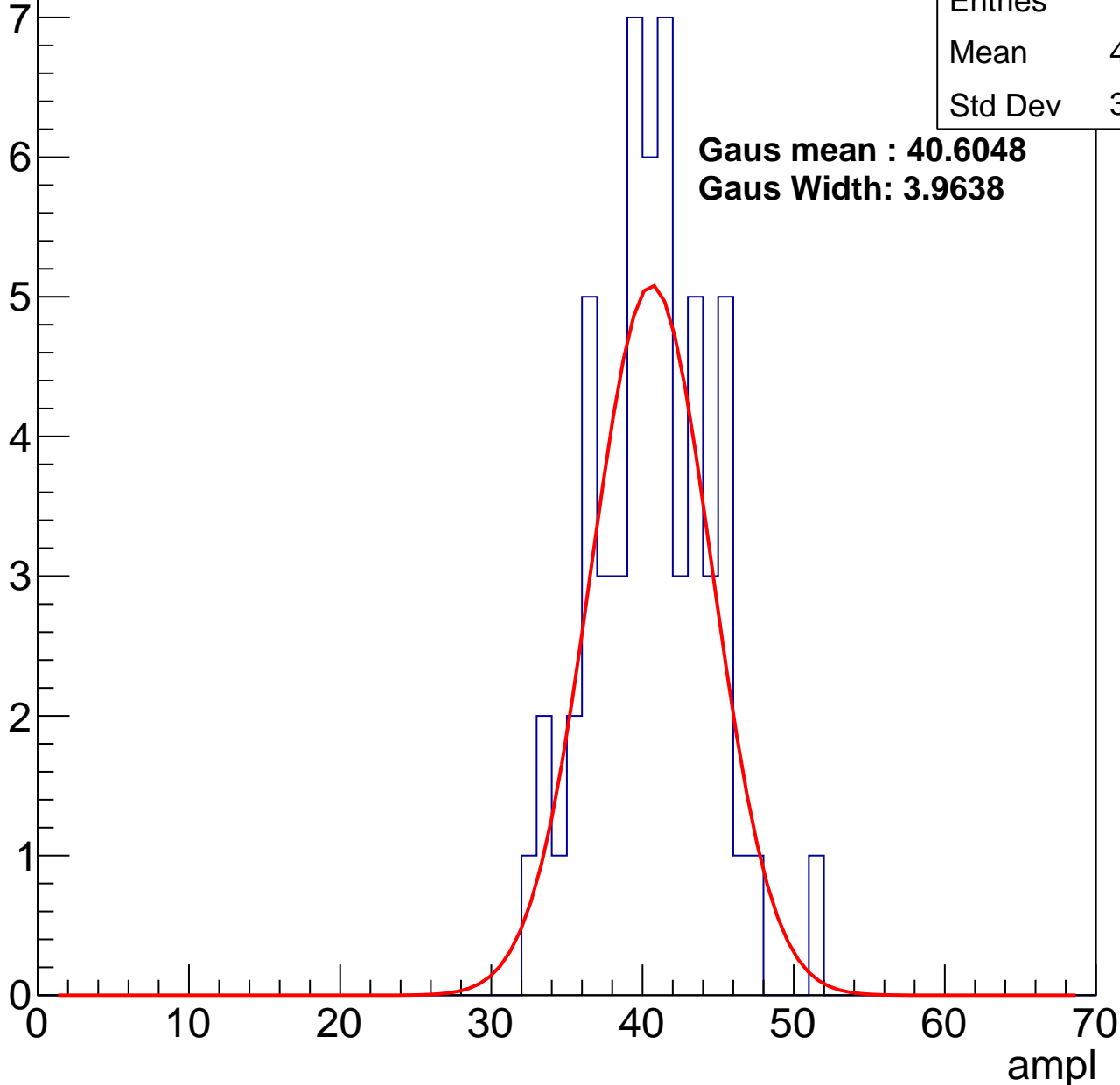
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	40.16
Std Dev	3.816

Gaus mean : 40.6048

Gaus Width: 3.9638

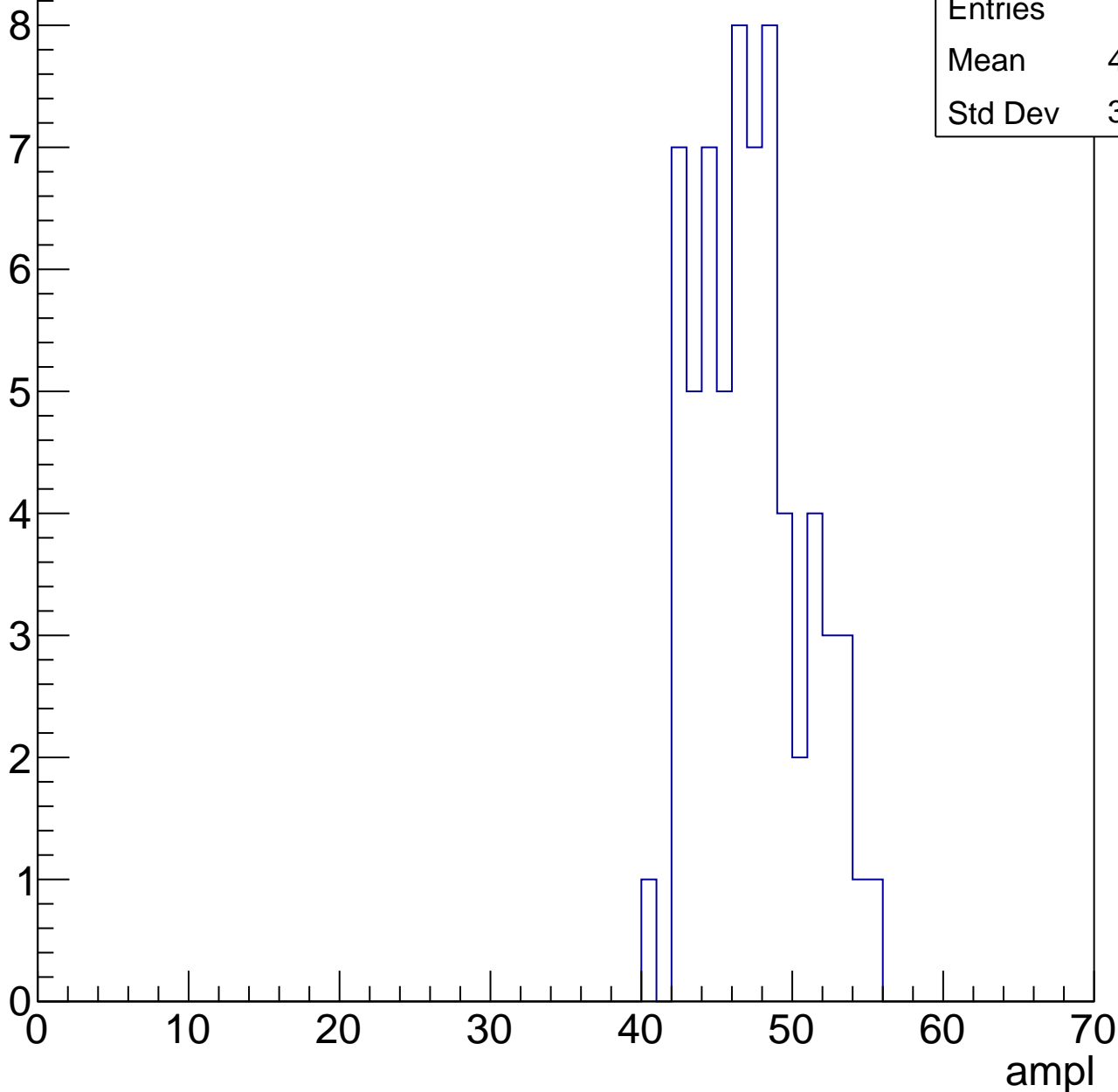


B0L102S, U5-ch60, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	46.77
Std Dev	3.468

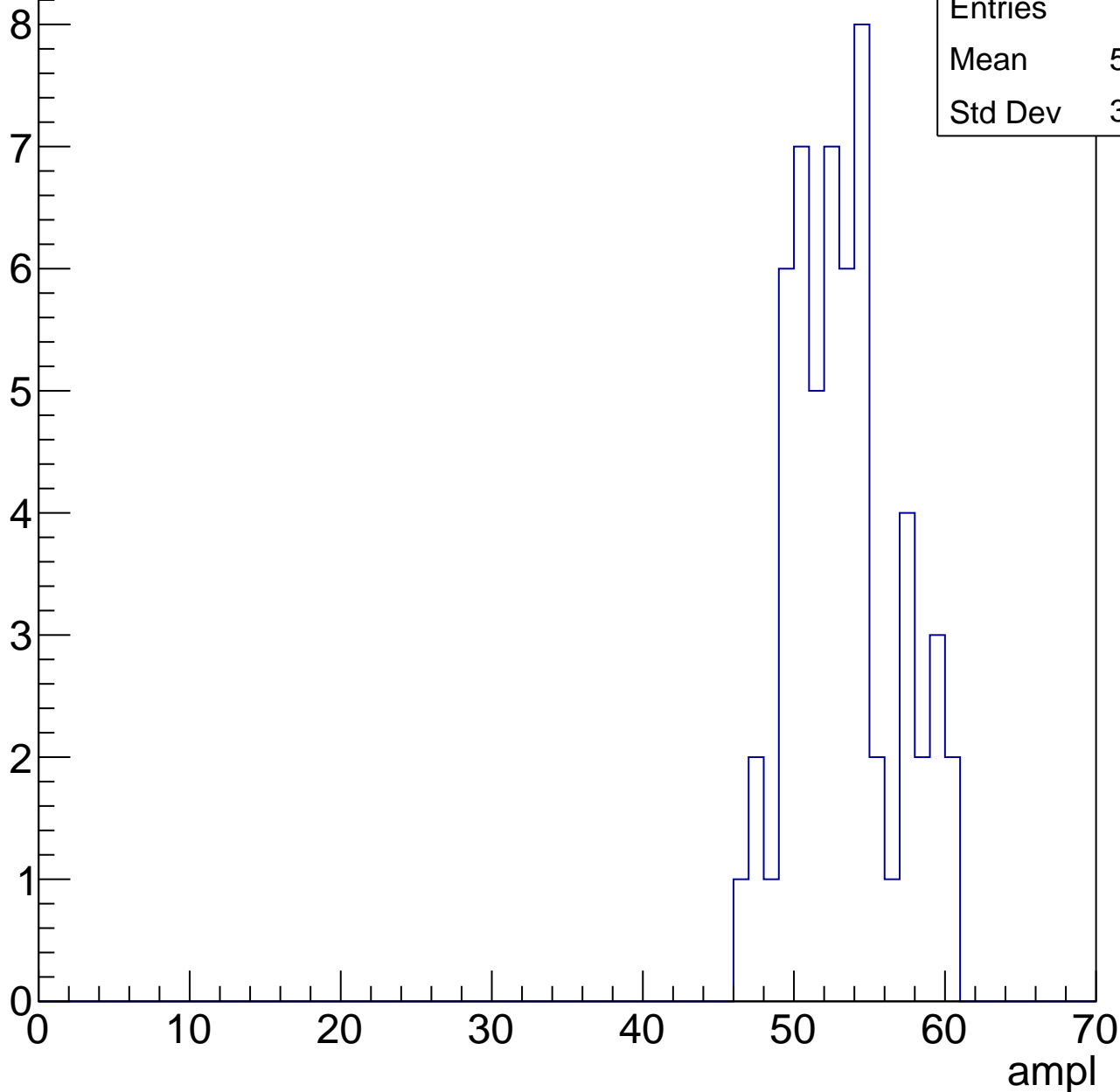


B0L102S, U5-ch60, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	52.77
Std Dev	3.439

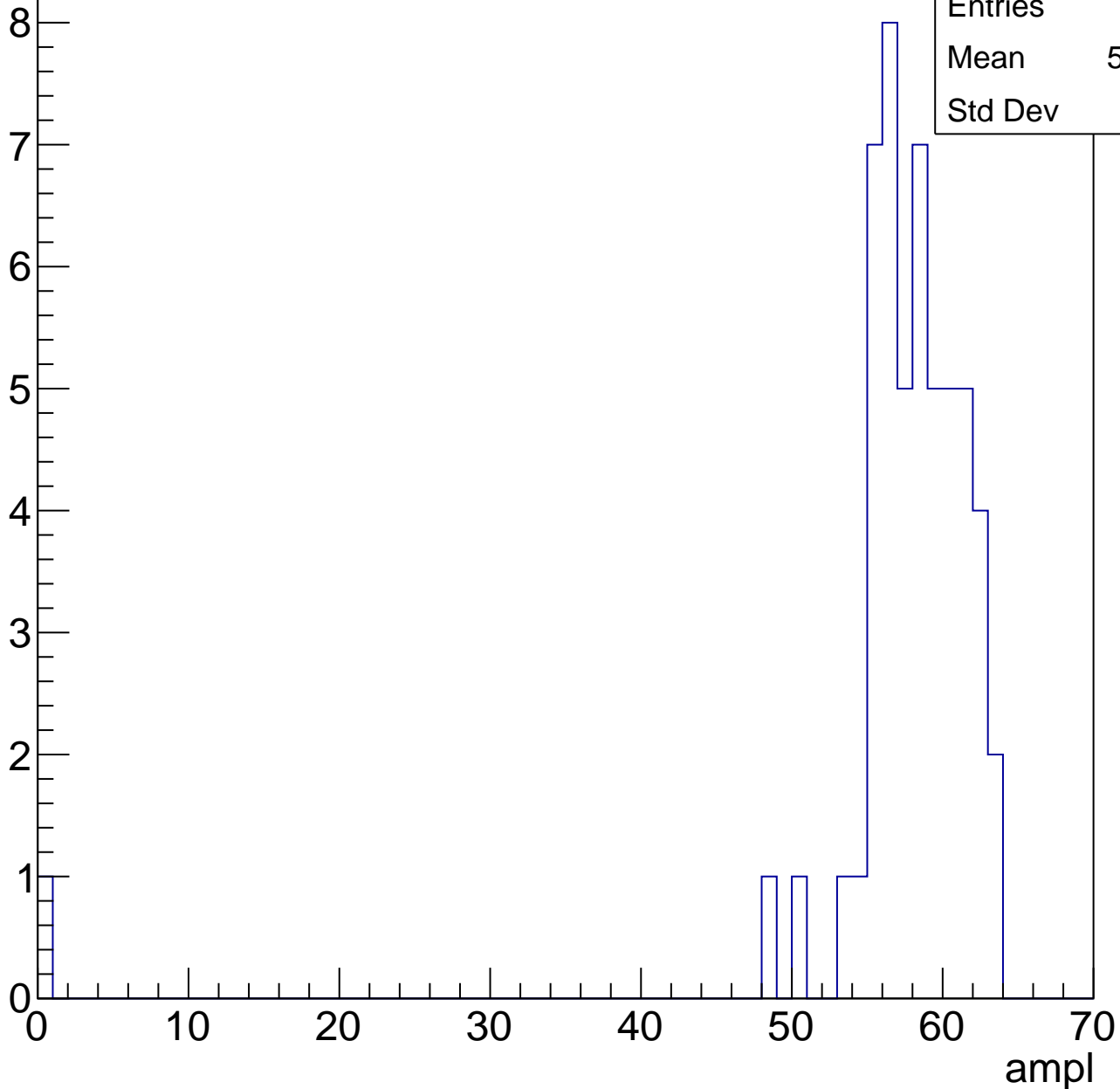


B0L102S, U5-ch60, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	56.66
Std Dev	8.42

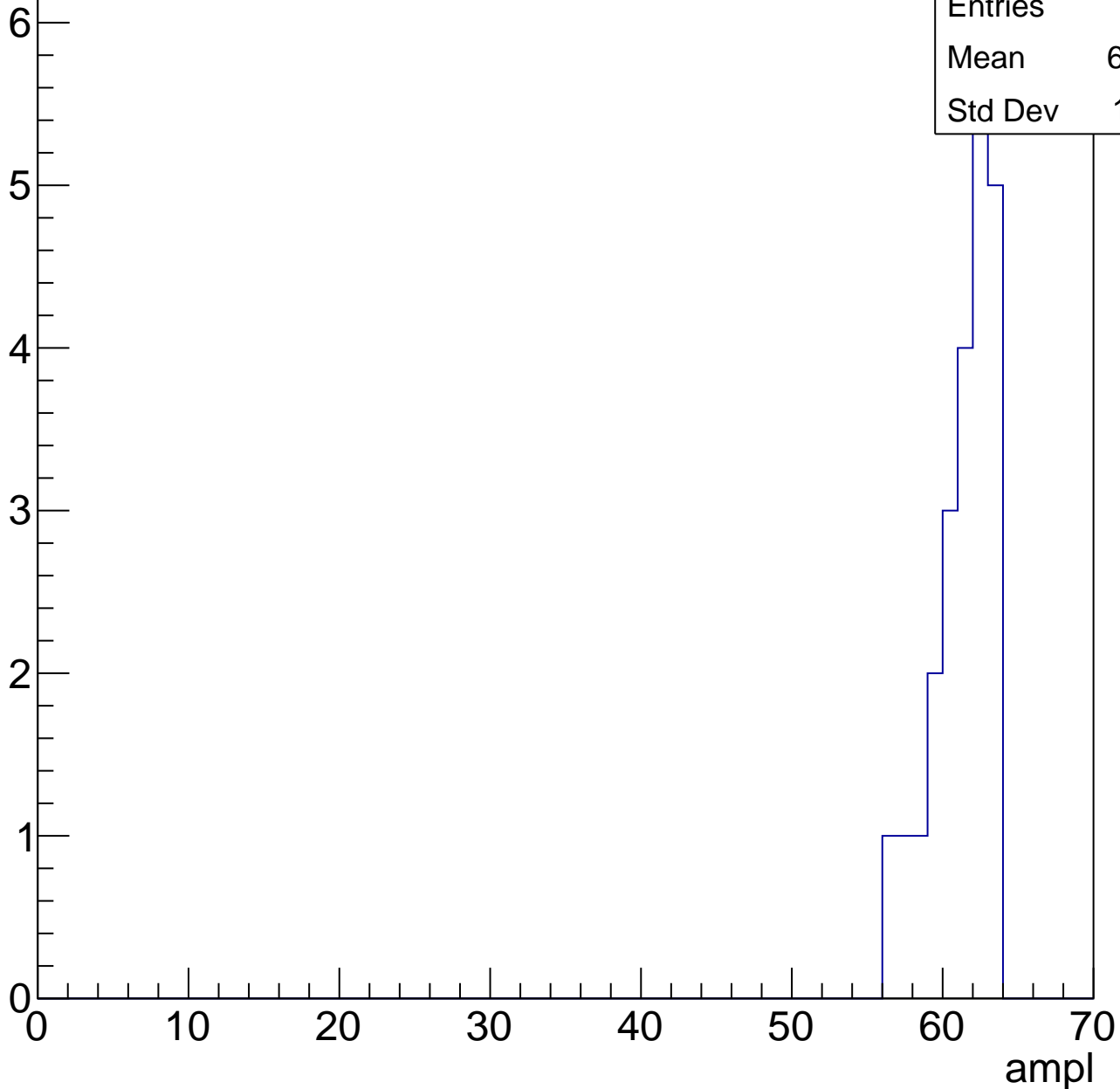


B0L102S, U5-ch60, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

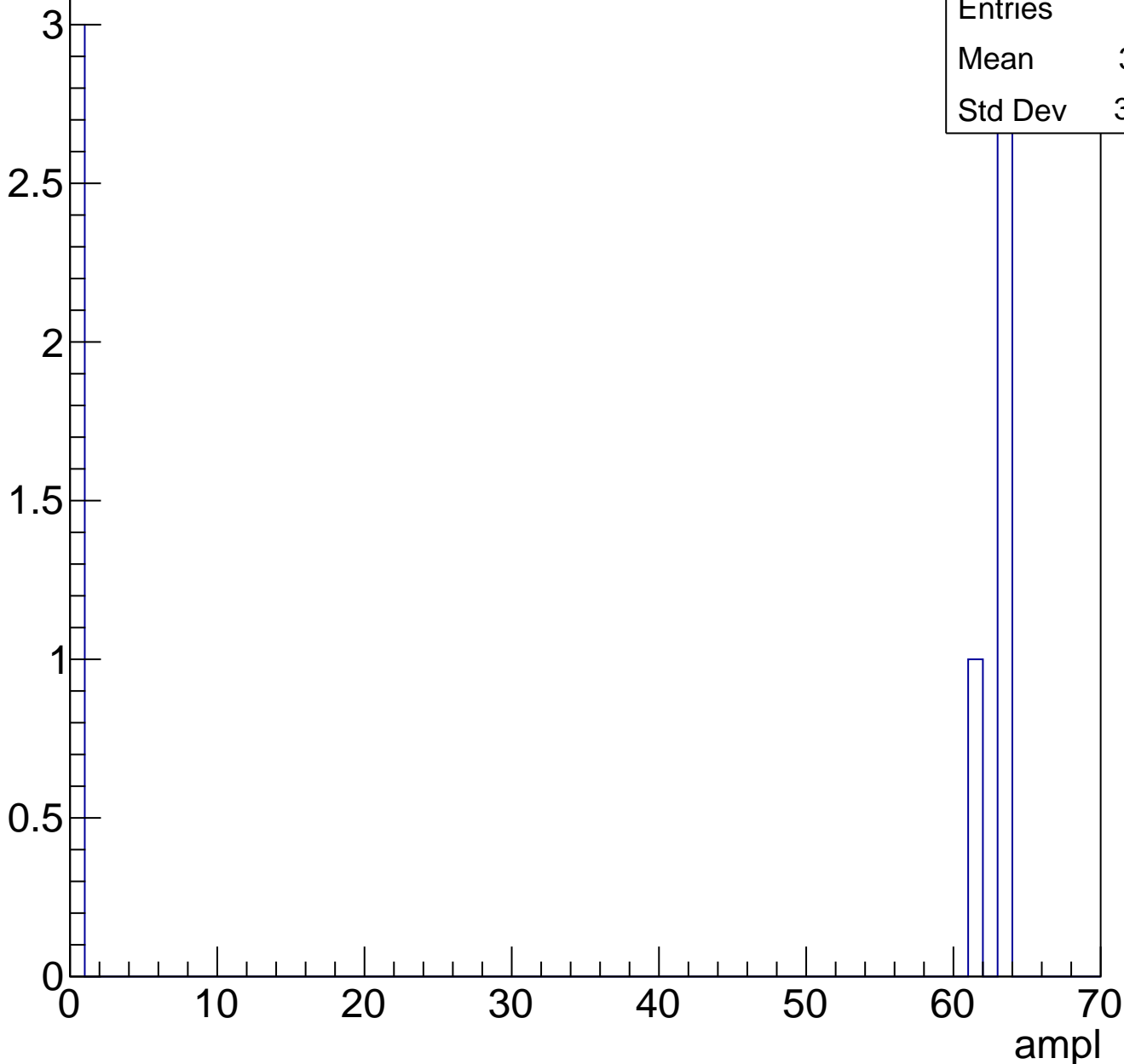
Entries	23
Mean	60.87
Std Dev	1.941



B0L102S, U5-ch60, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch61, adc0

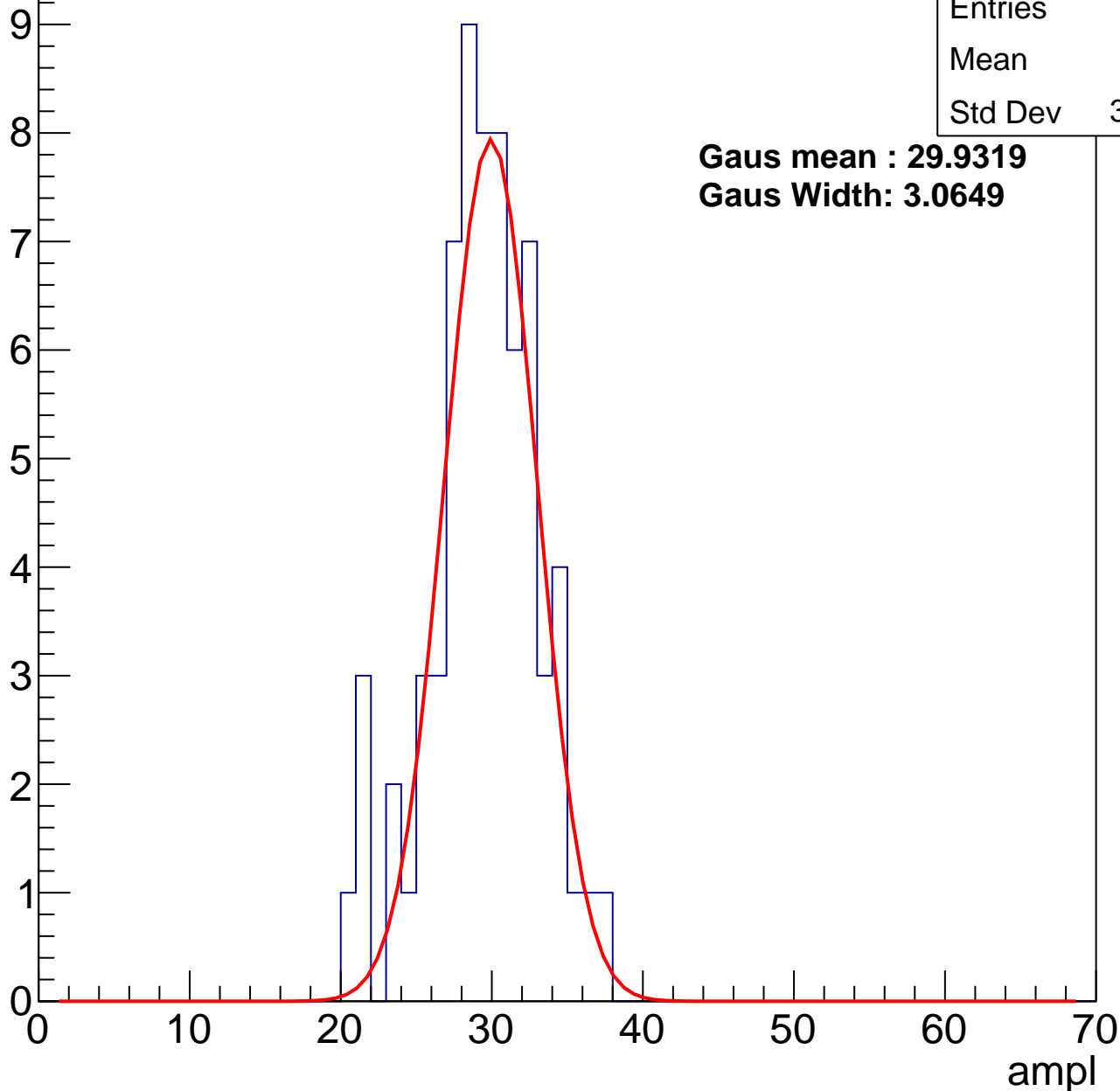
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29
Std Dev	3.585

Gaus mean : 29.9319

Gaus Width: 3.0649



B0L102S, U5-ch61, adc1

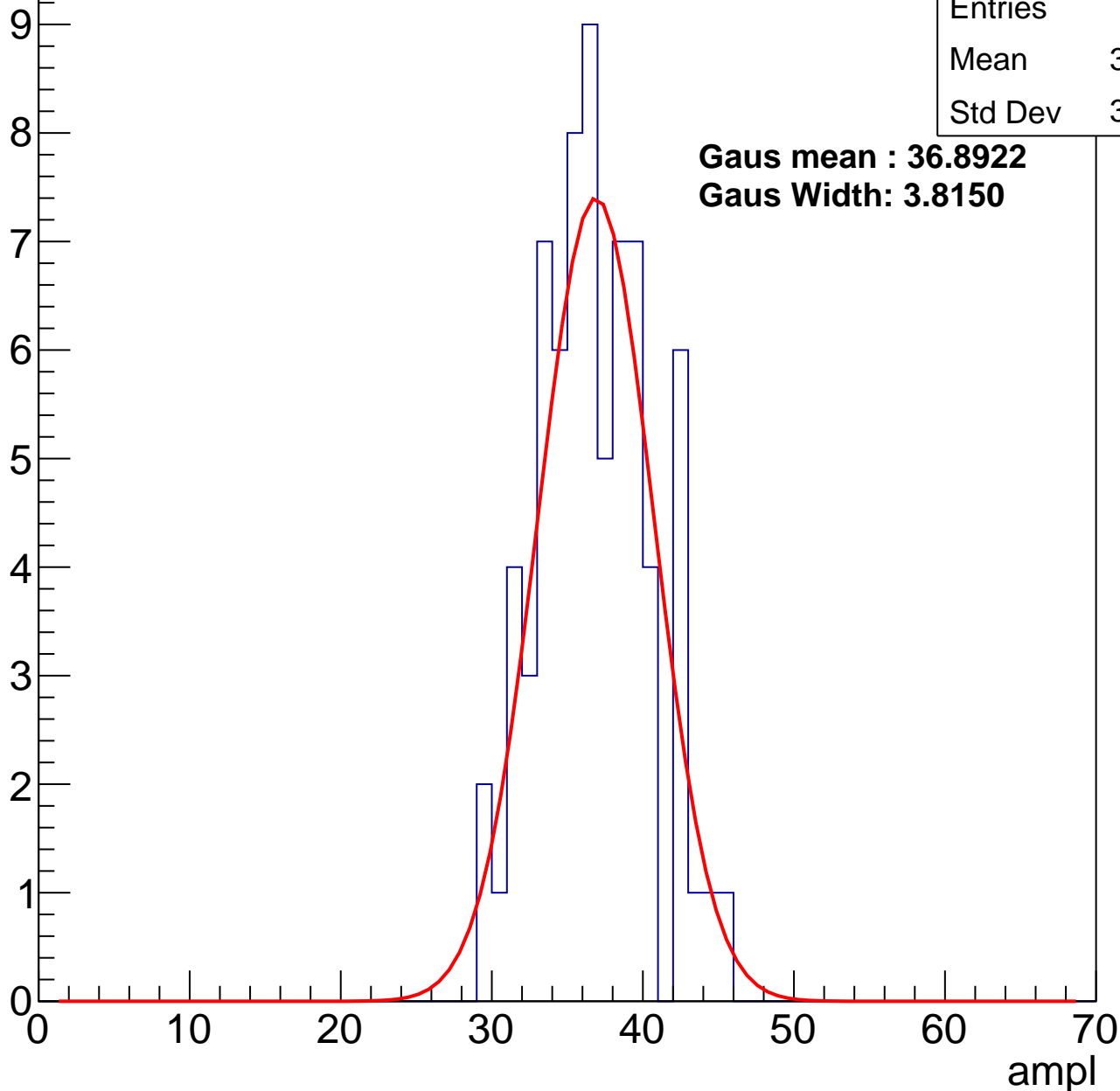
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	36.32
Std Dev	3.613

Gaus mean : 36.8922

Gaus Width: 3.8150



B0L102S, U5-ch61, adc2

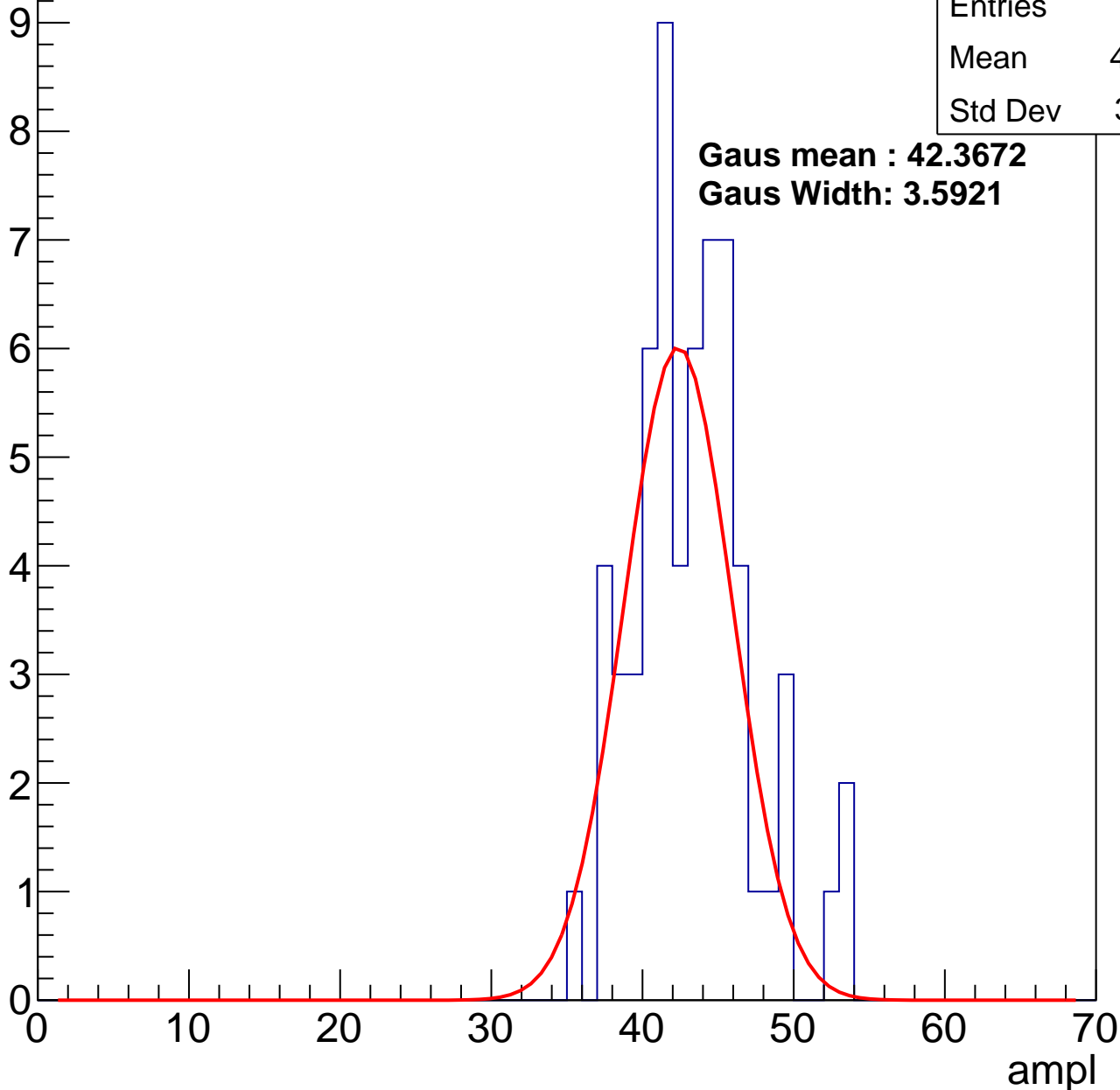
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	42.84
Std Dev	3.861

Gaus mean : 42.3672

Gaus Width: 3.5921

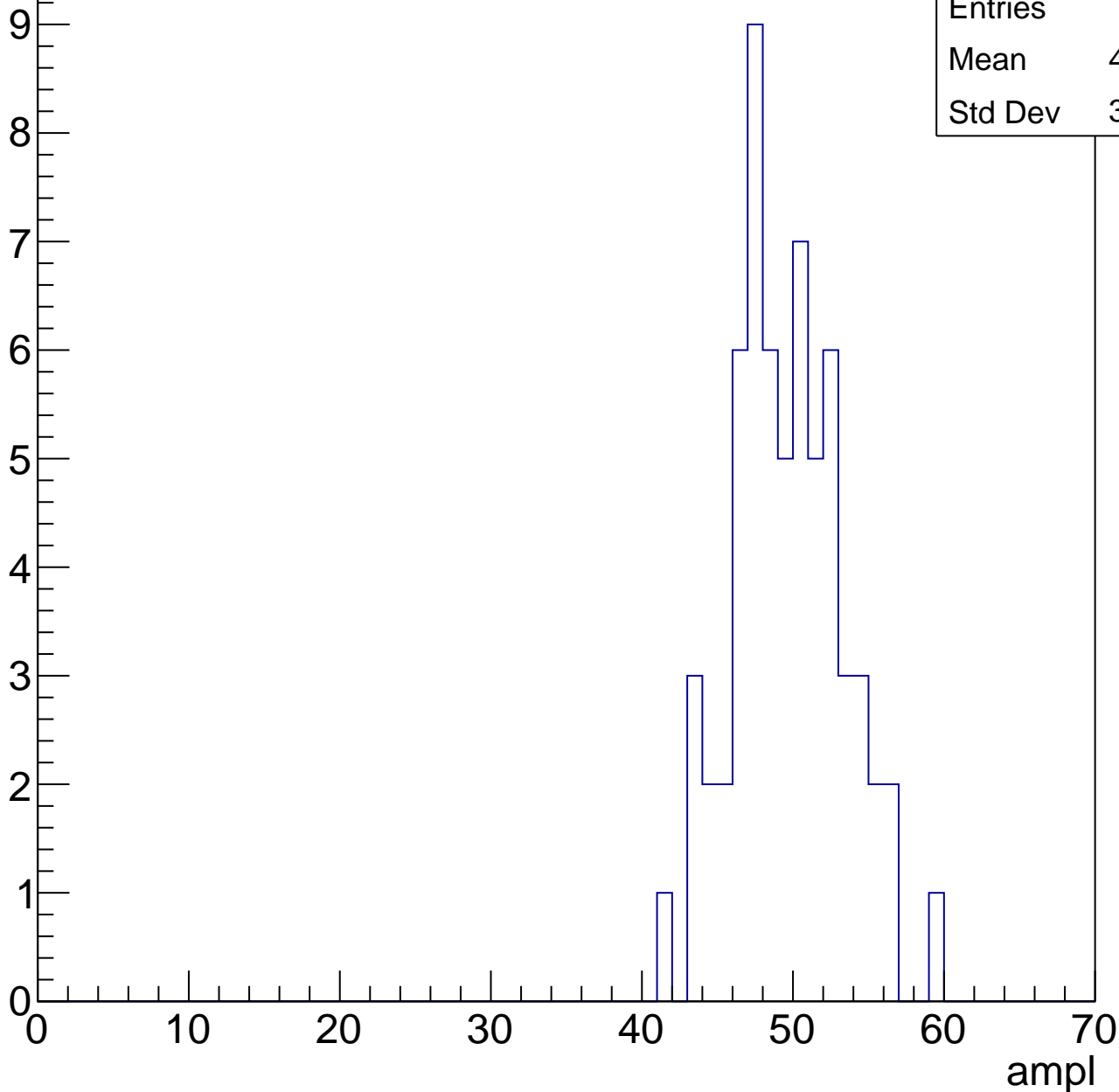


B0L102S, U5-ch61, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.19
Std Dev	3.612

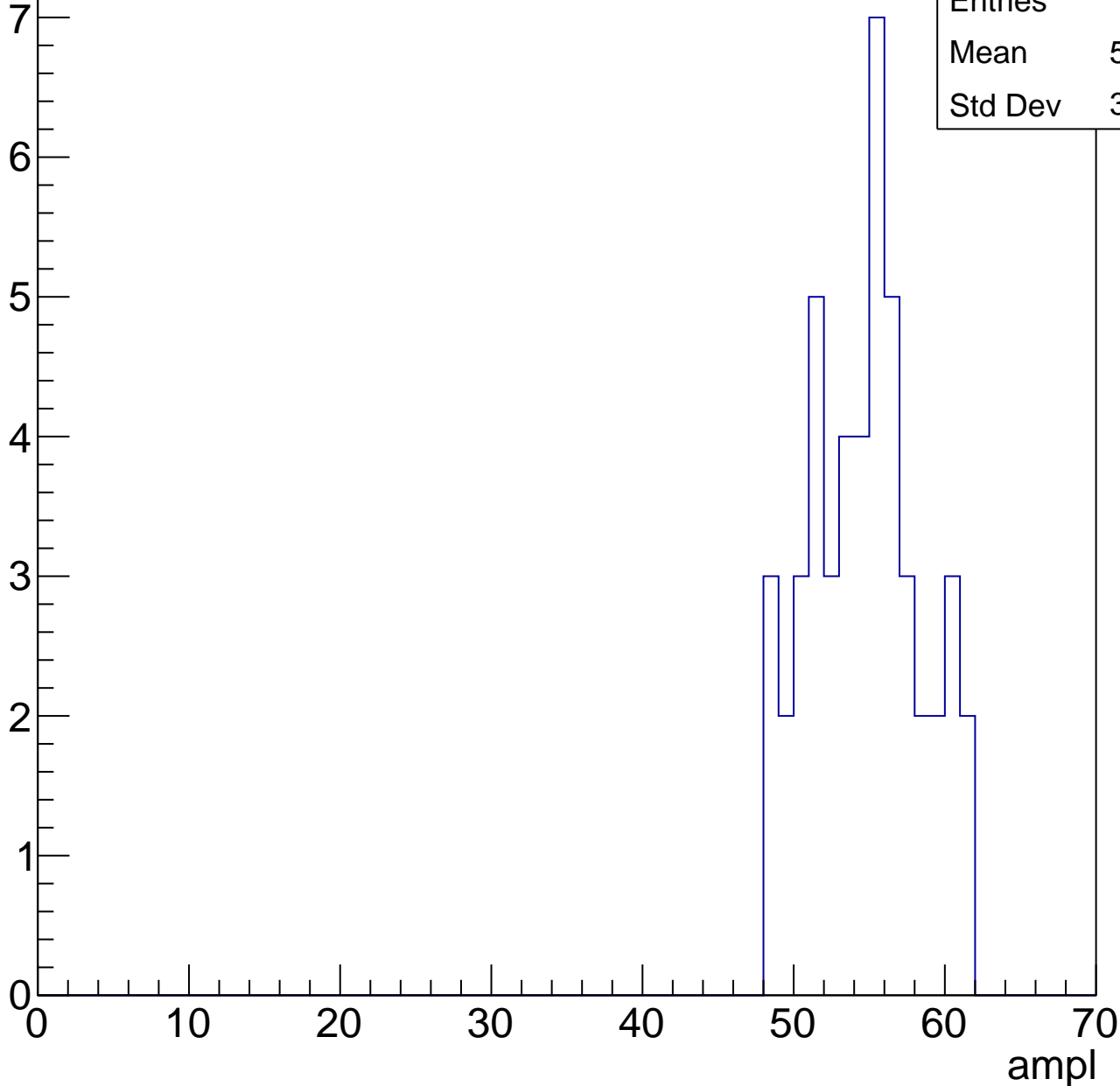


B0L102S, U5-ch61, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	54.23
Std Dev	3.555

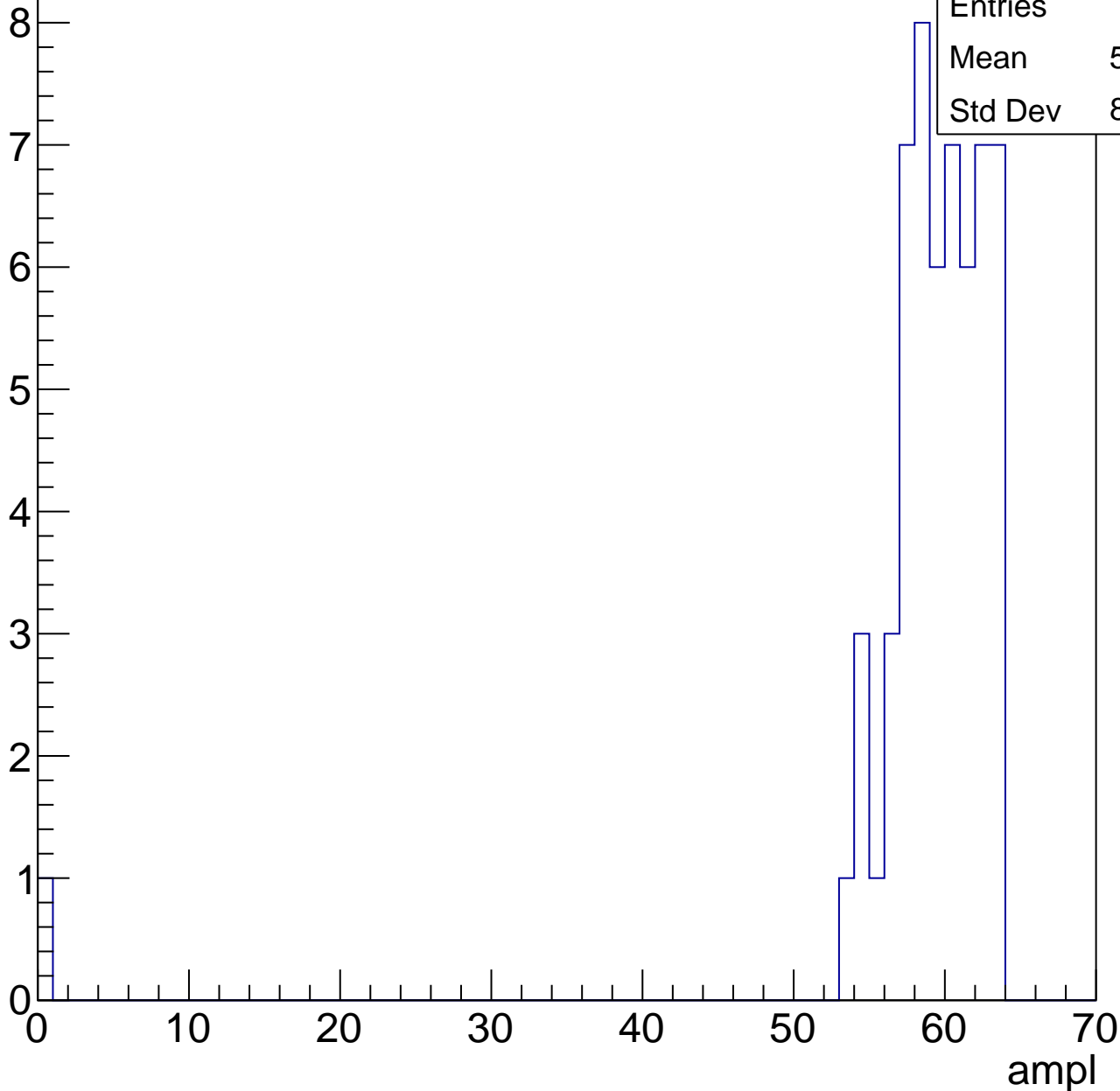


B0L102S, U5-ch61, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

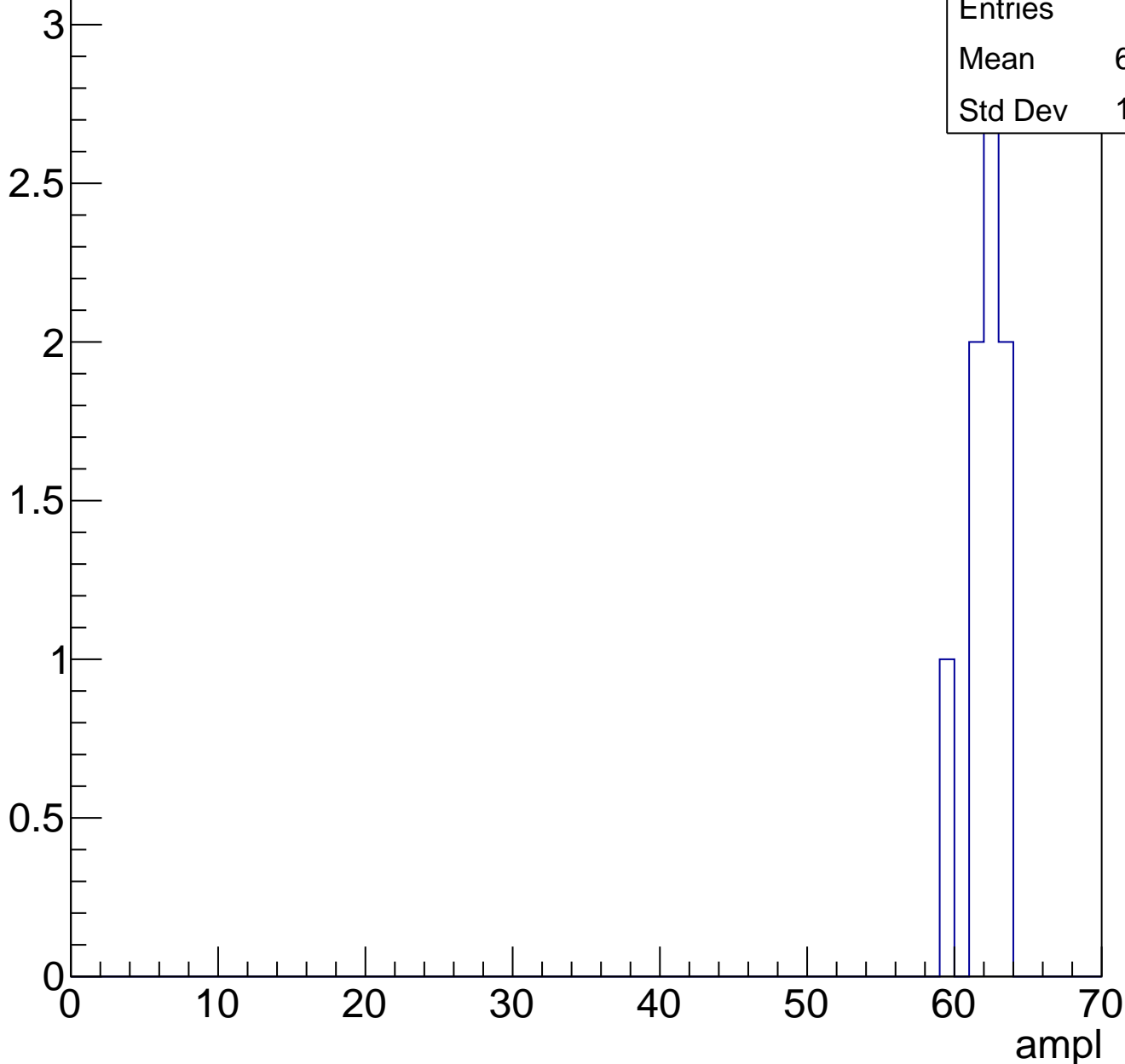
Entries	57
Mean	58.18
Std Dev	8.206



B0L102S, U5-ch61, adc6

calib_packv5_042523_0143.root, FC#12, port B1

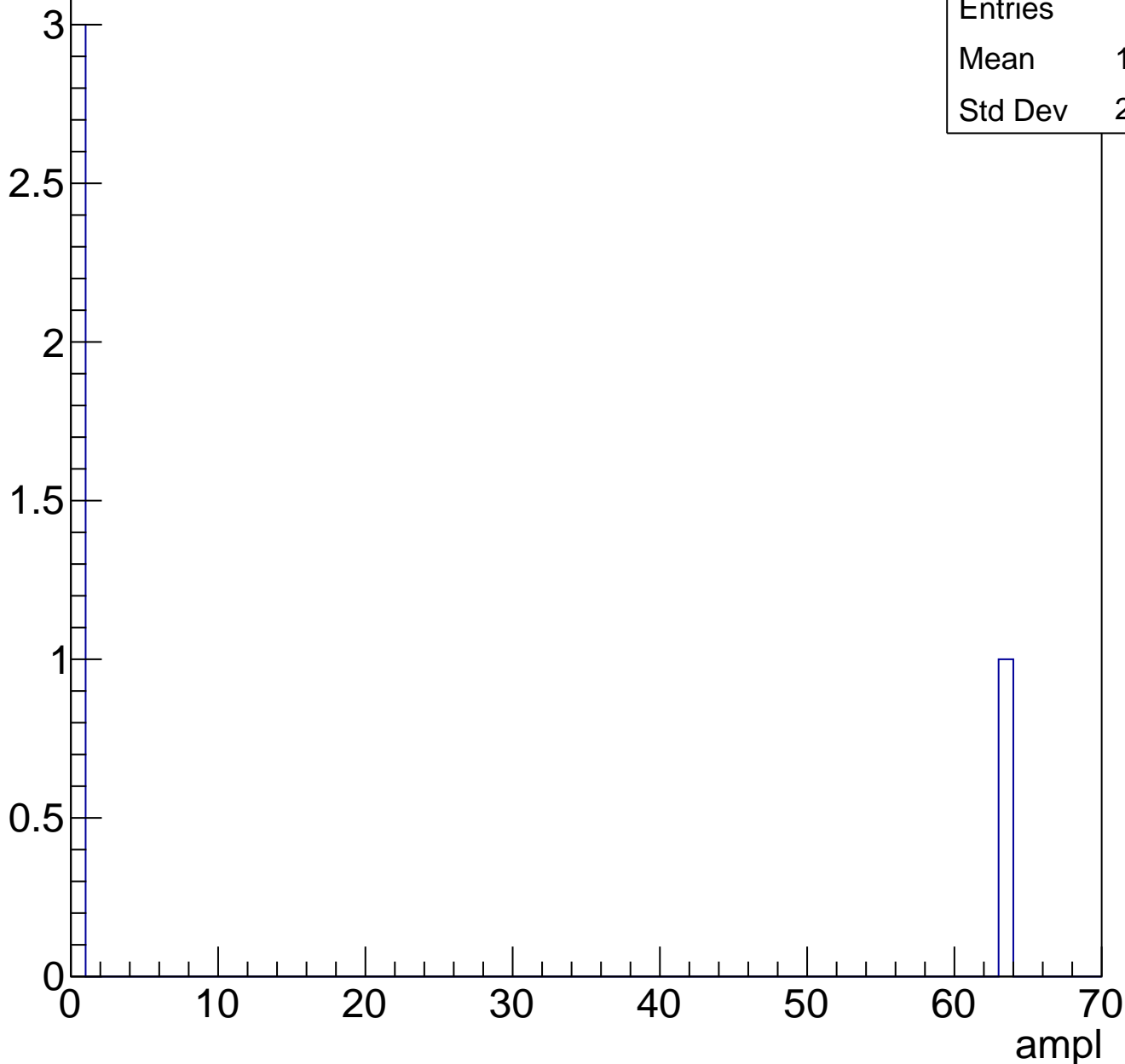
Entry



B0L102S, U5-ch61, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch62, adc0

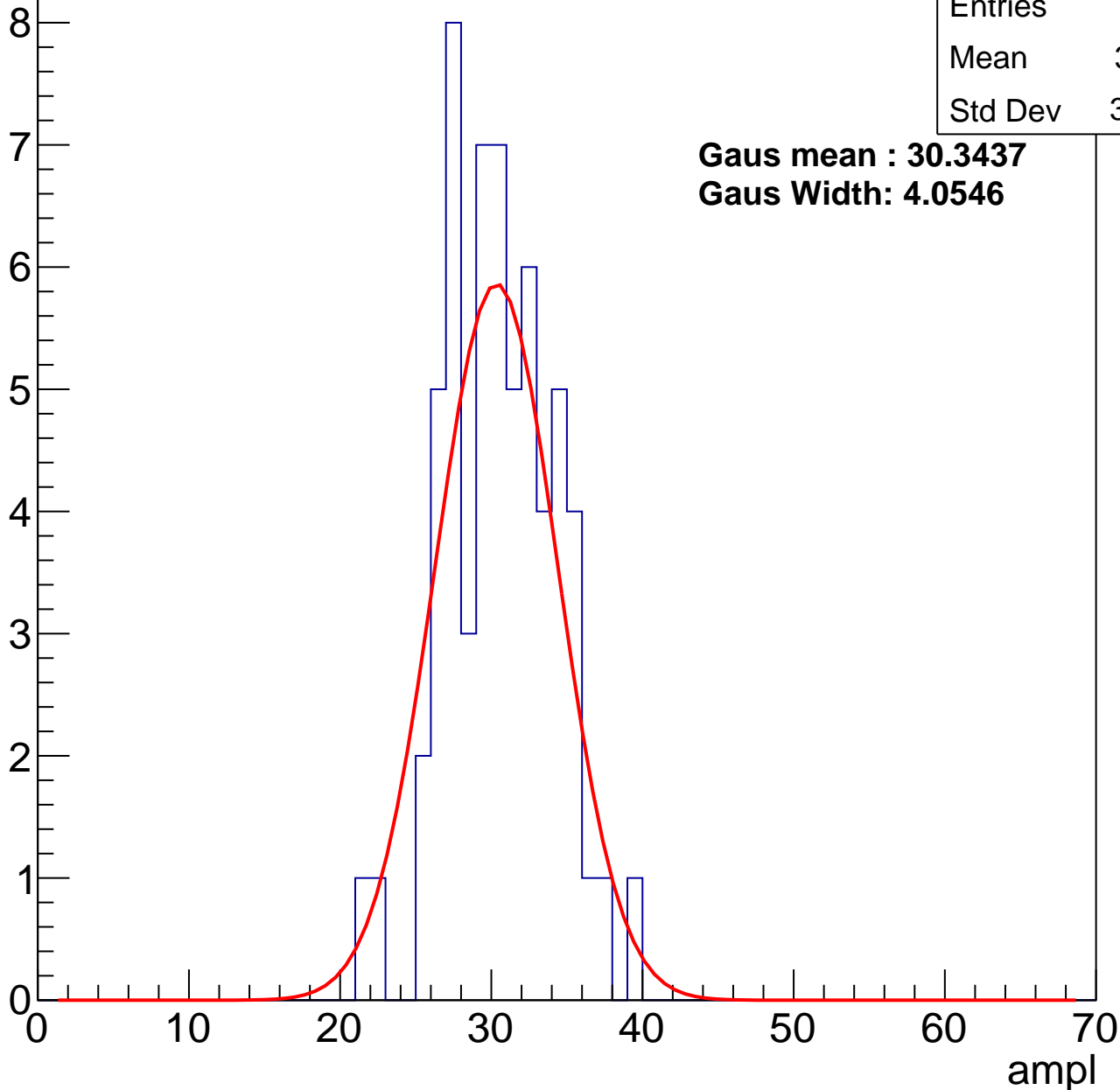
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	30.11
Std Dev	3.567

Gaus mean : 30.3437

Gaus Width: 4.0546



B0L102S, U5-ch62, adc1

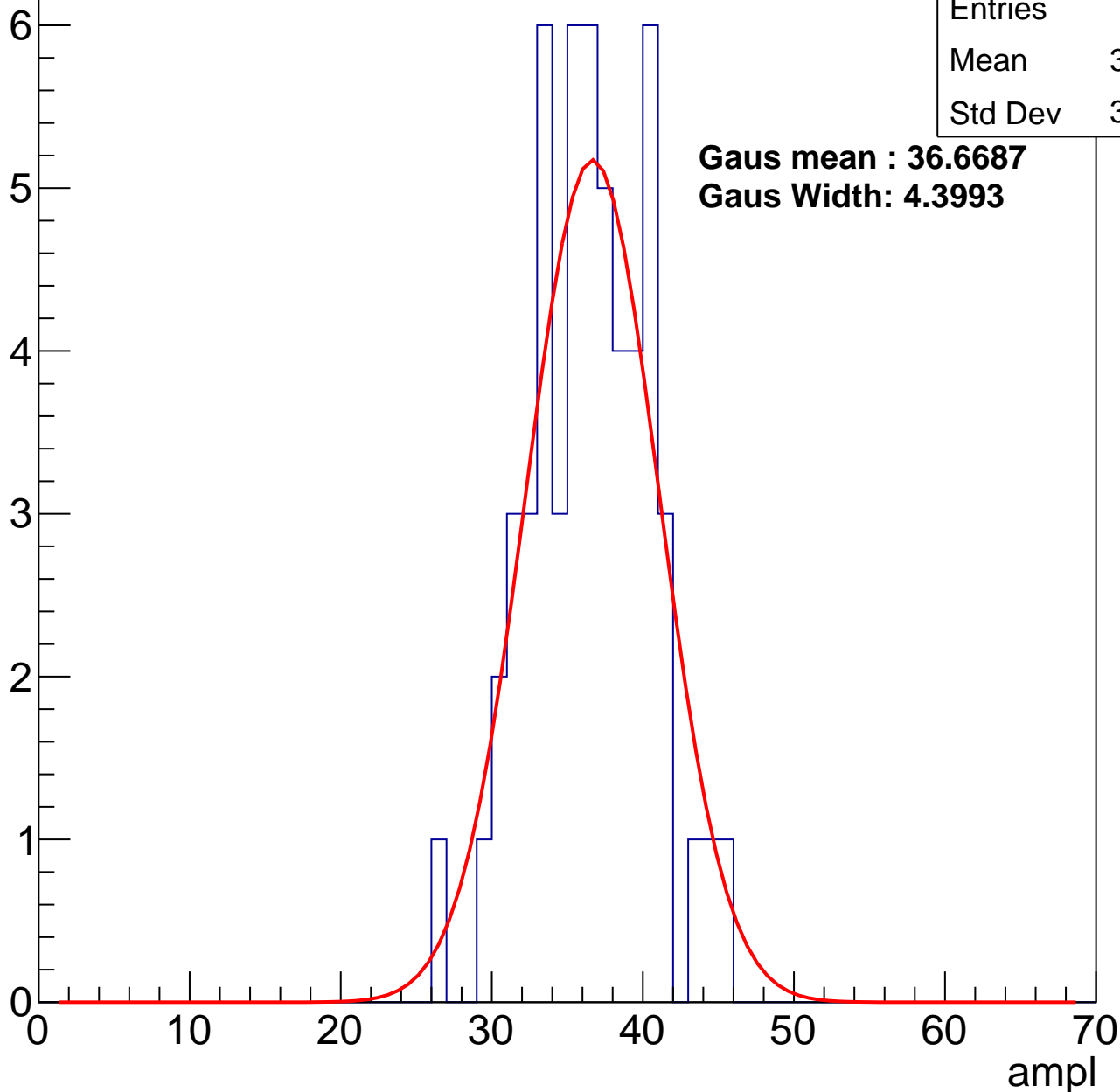
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	36.04
Std Dev	3.868

Gaus mean : 36.6687

Gaus Width: 4.3993



B0L102S, U5-ch62, adc2

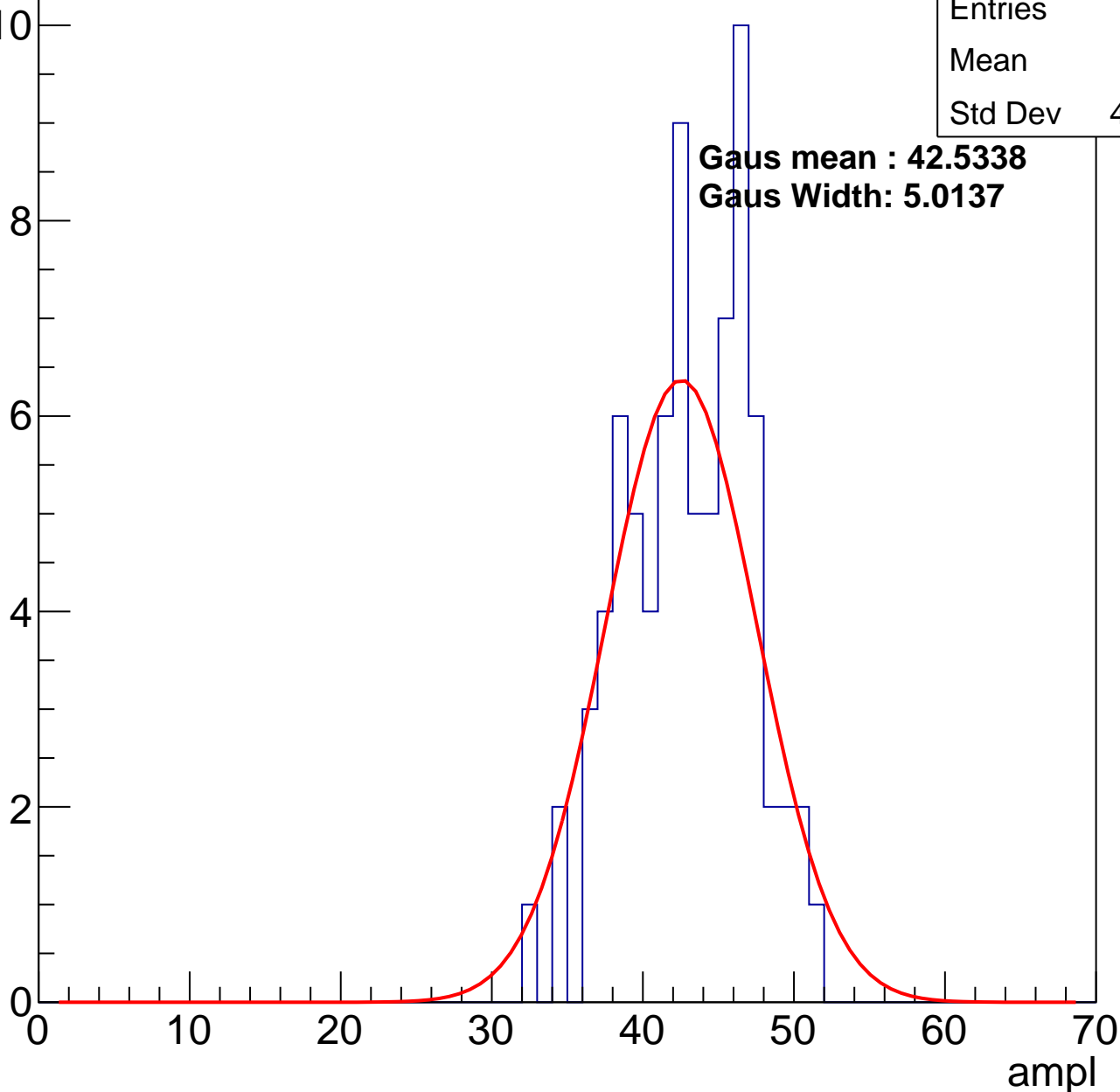
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	42.5
Std Dev	4.117

Gaus mean : 42.5338

Gaus Width: 5.0137

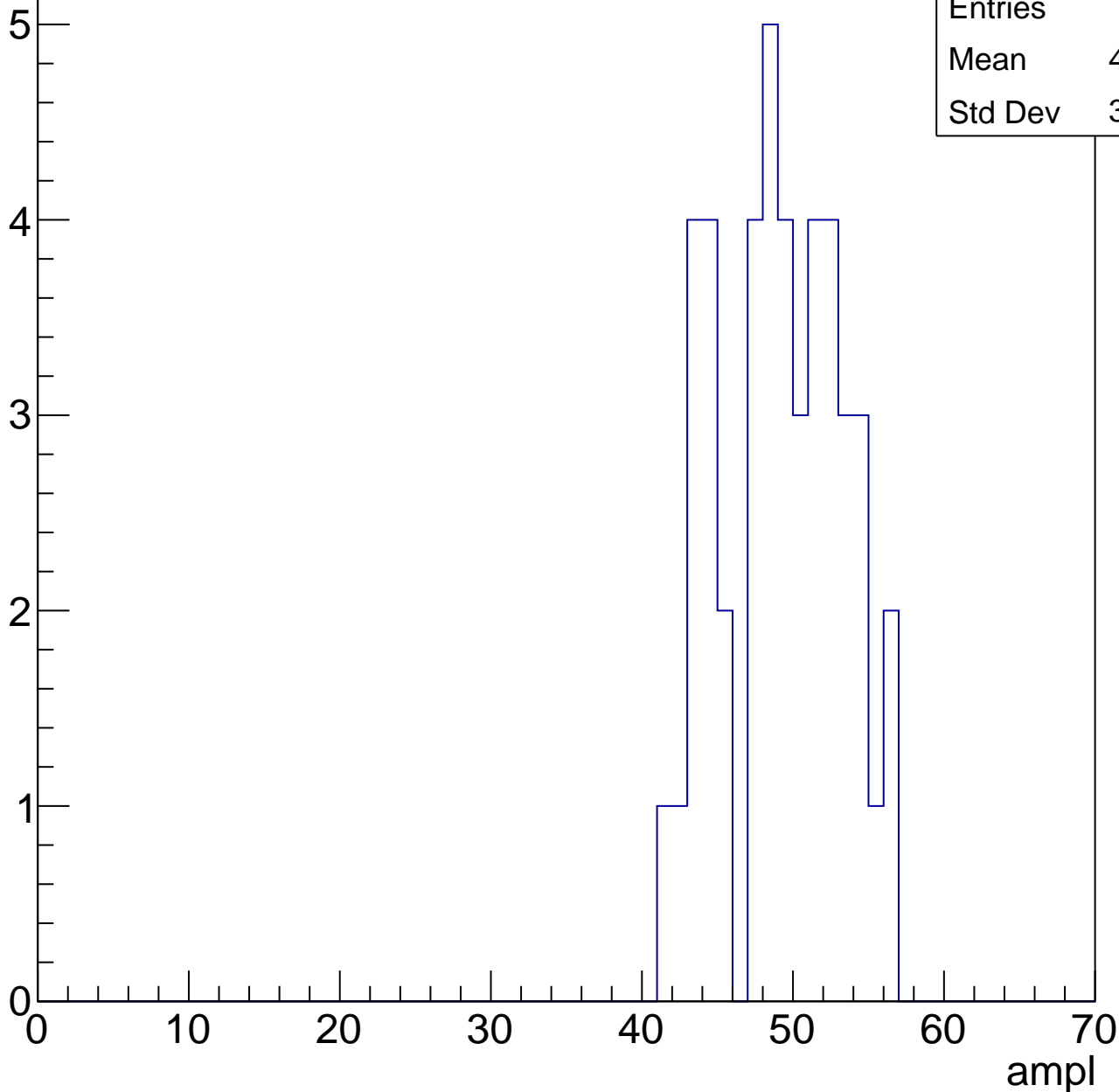


B0L102S, U5-ch62, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	45
Mean	48.78
Std Dev	3.999

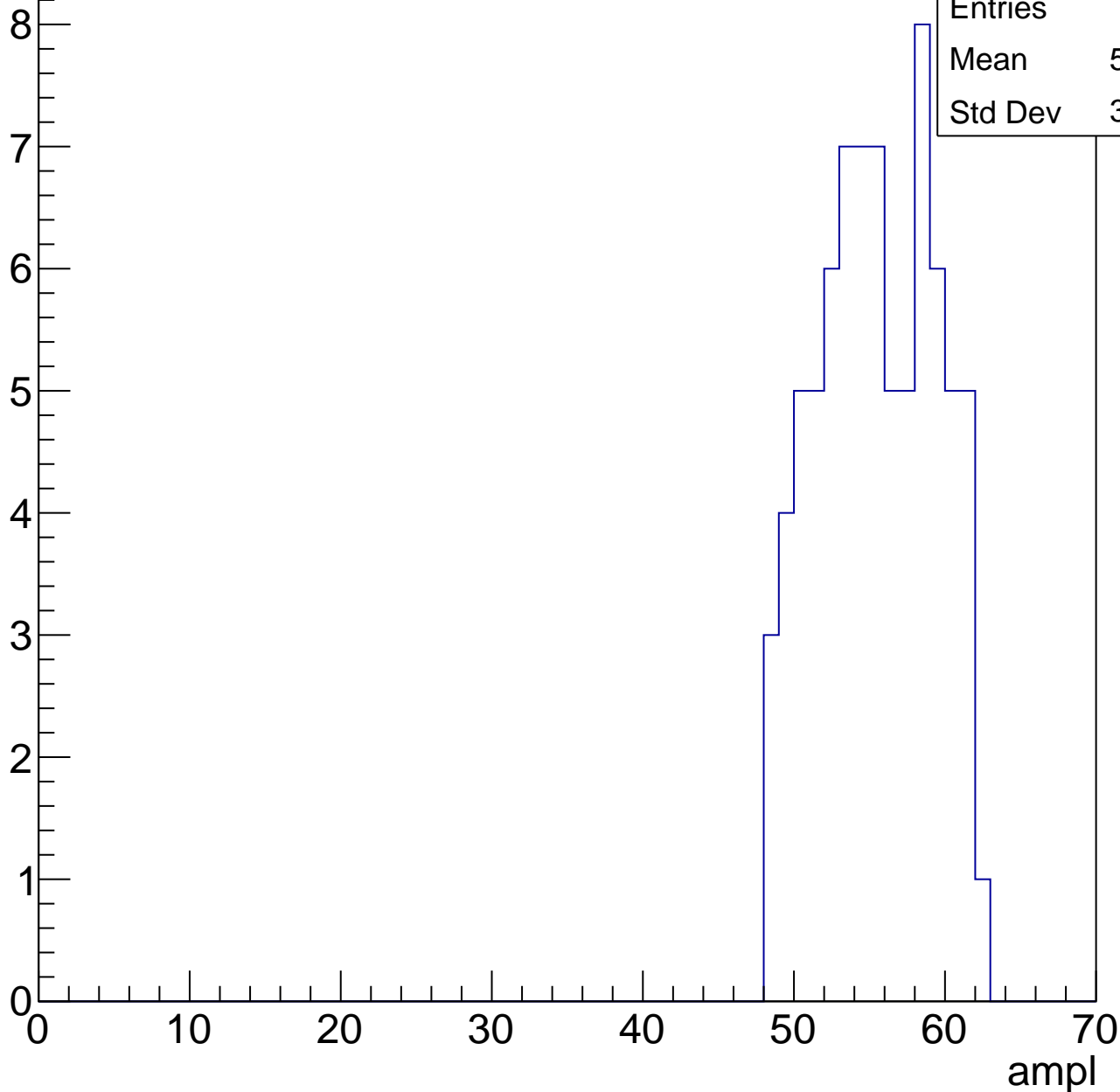


B0L102S, U5-ch62, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	54.95
Std Dev	3.785

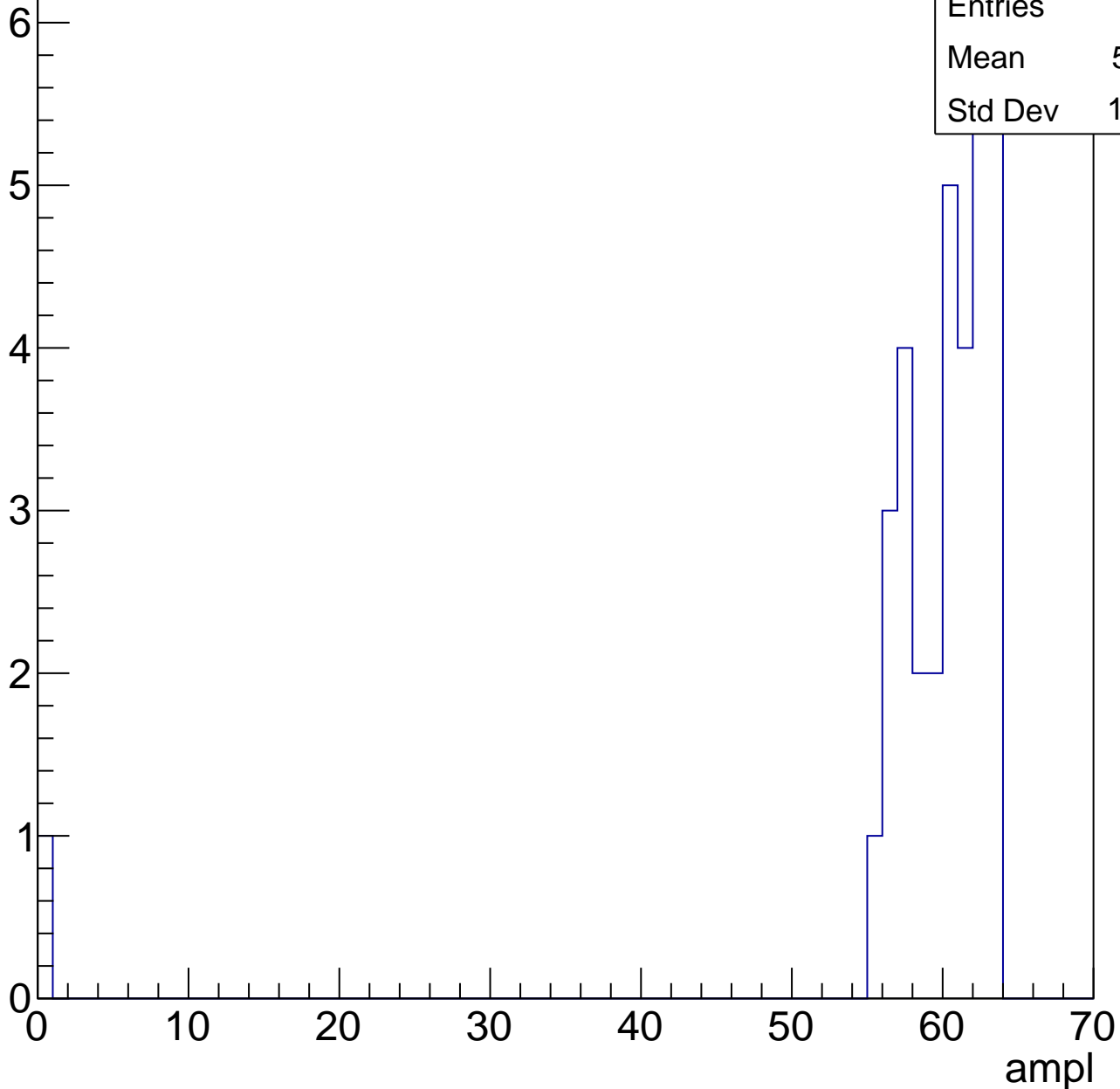


B0L102S, U5-ch62, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

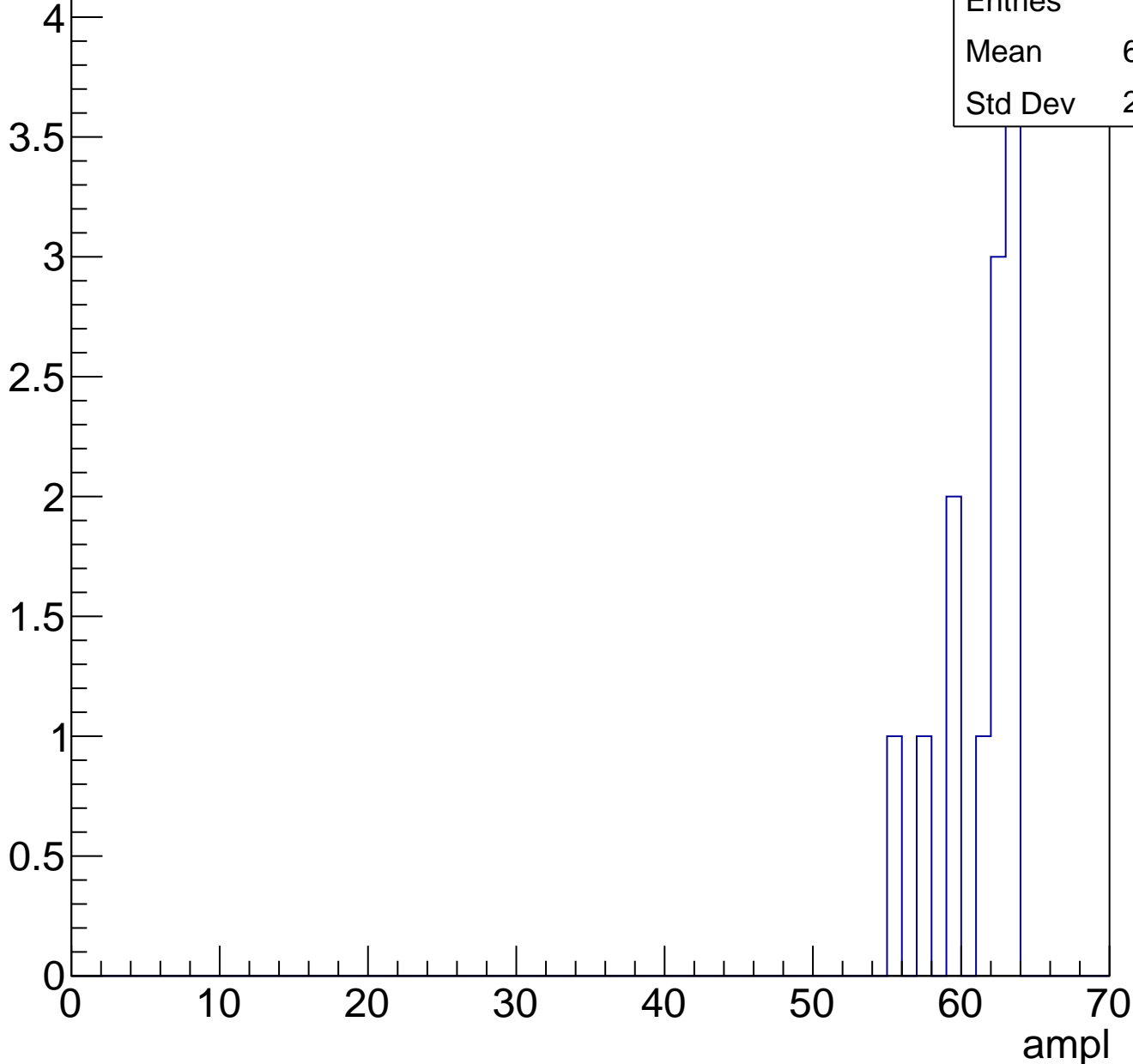
Entries	34
Mean	58.21
Std Dev	10.42



B0L102S, U5-ch62, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	12
Mean	60.75
Std Dev	2.554

B0L102S, U5-ch62, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch63, adc0

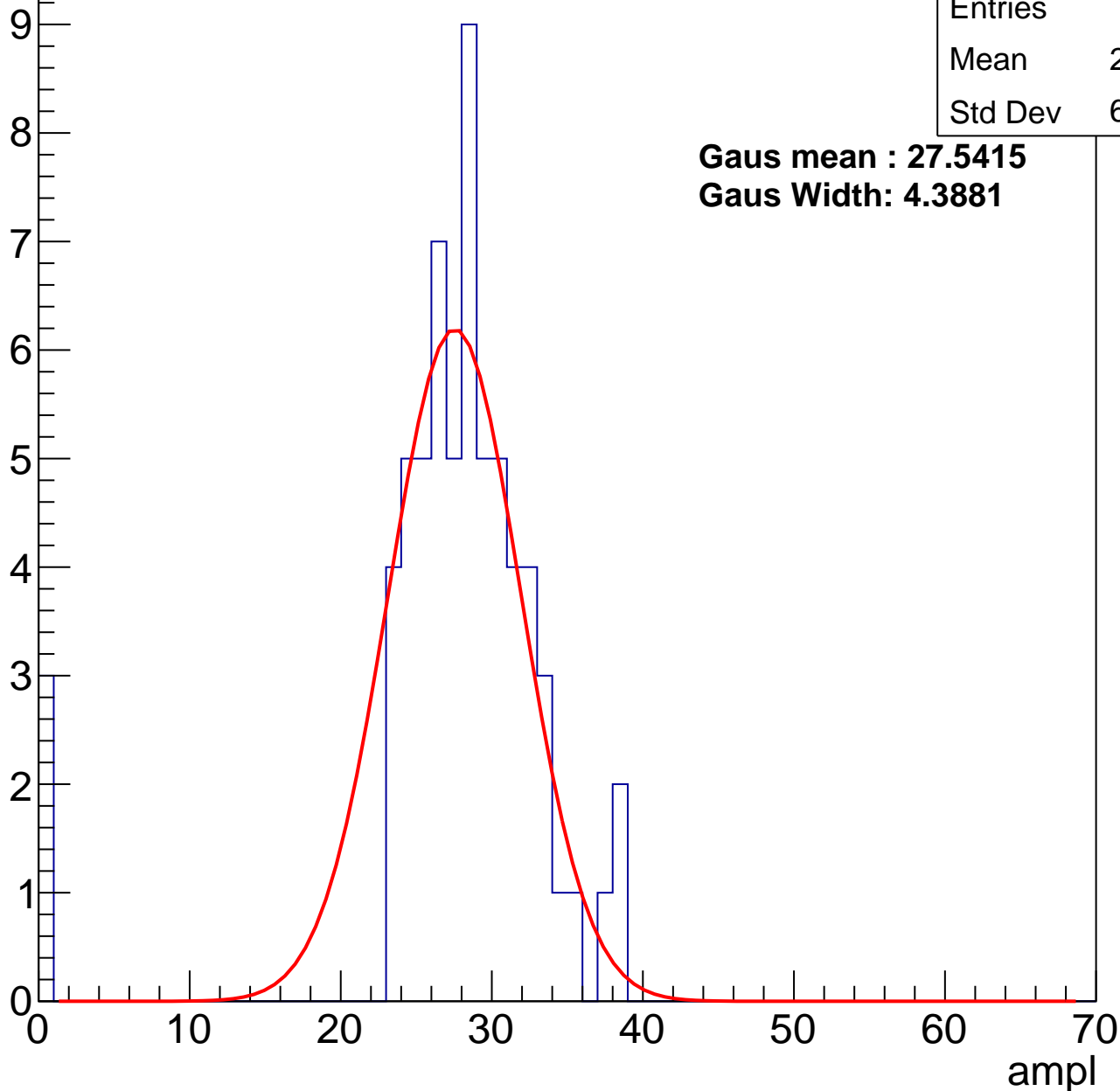
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	27.09
Std Dev	6.986

Gaus mean : 27.5415

Gaus Width: 4.3881



B0L102S, U5-ch63, adc1

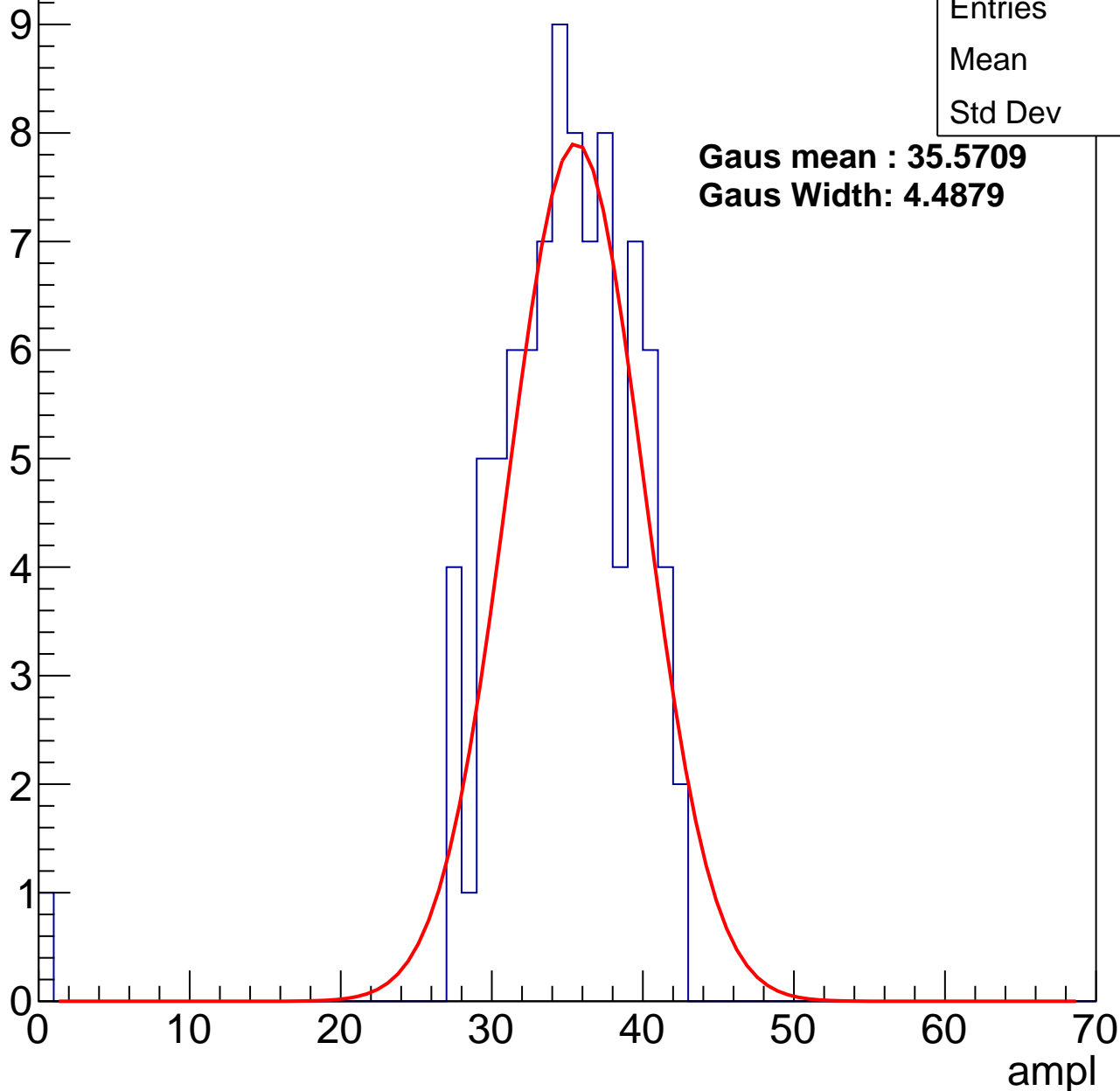
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	90
Mean	34.3
Std Dev	5.33

Gaus mean : 35.5709

Gaus Width: 4.4879



B0L102S, U5-ch63, adc2

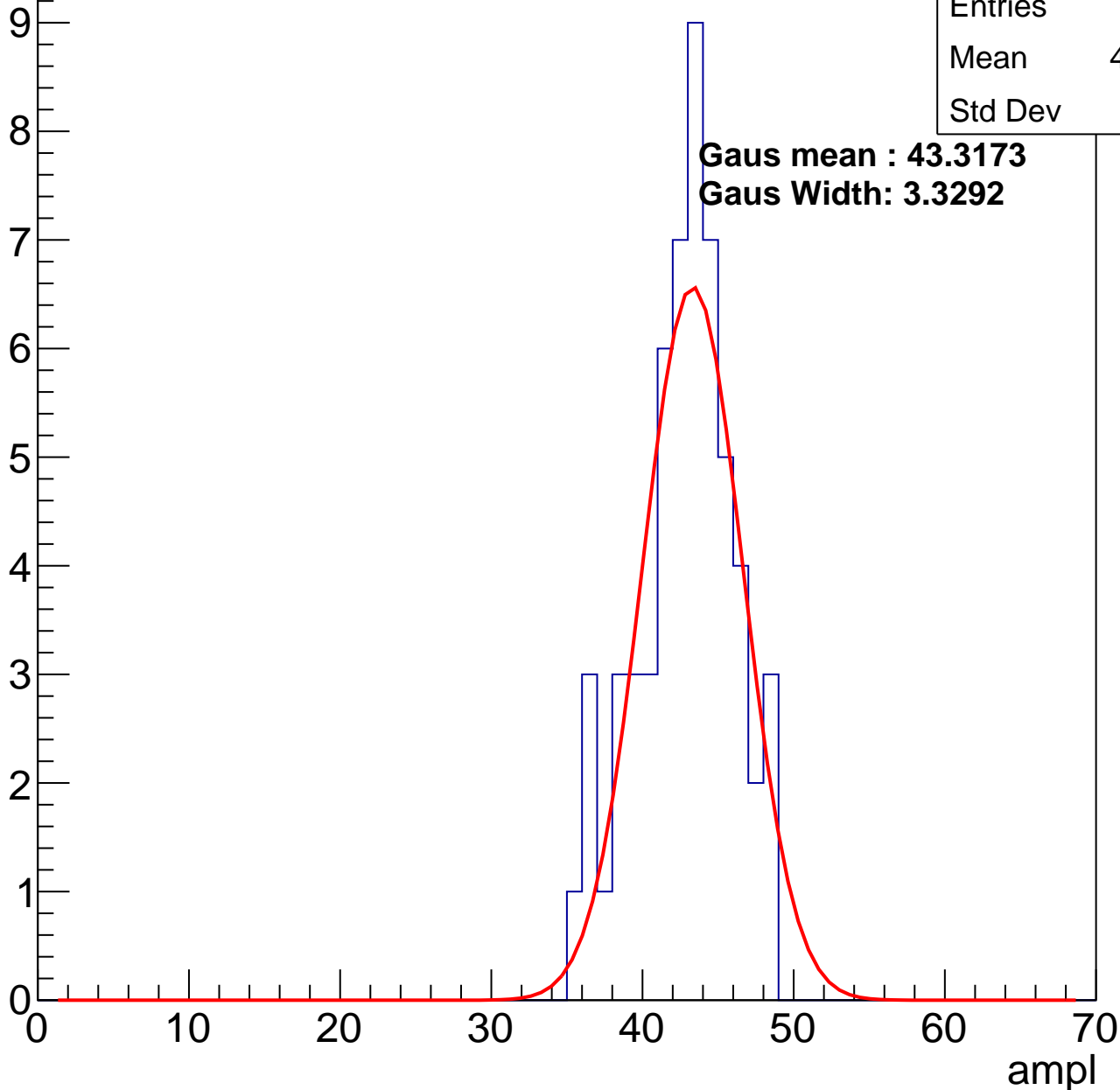
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	42.33
Std Dev	3.17

Gaus mean : 43.3173

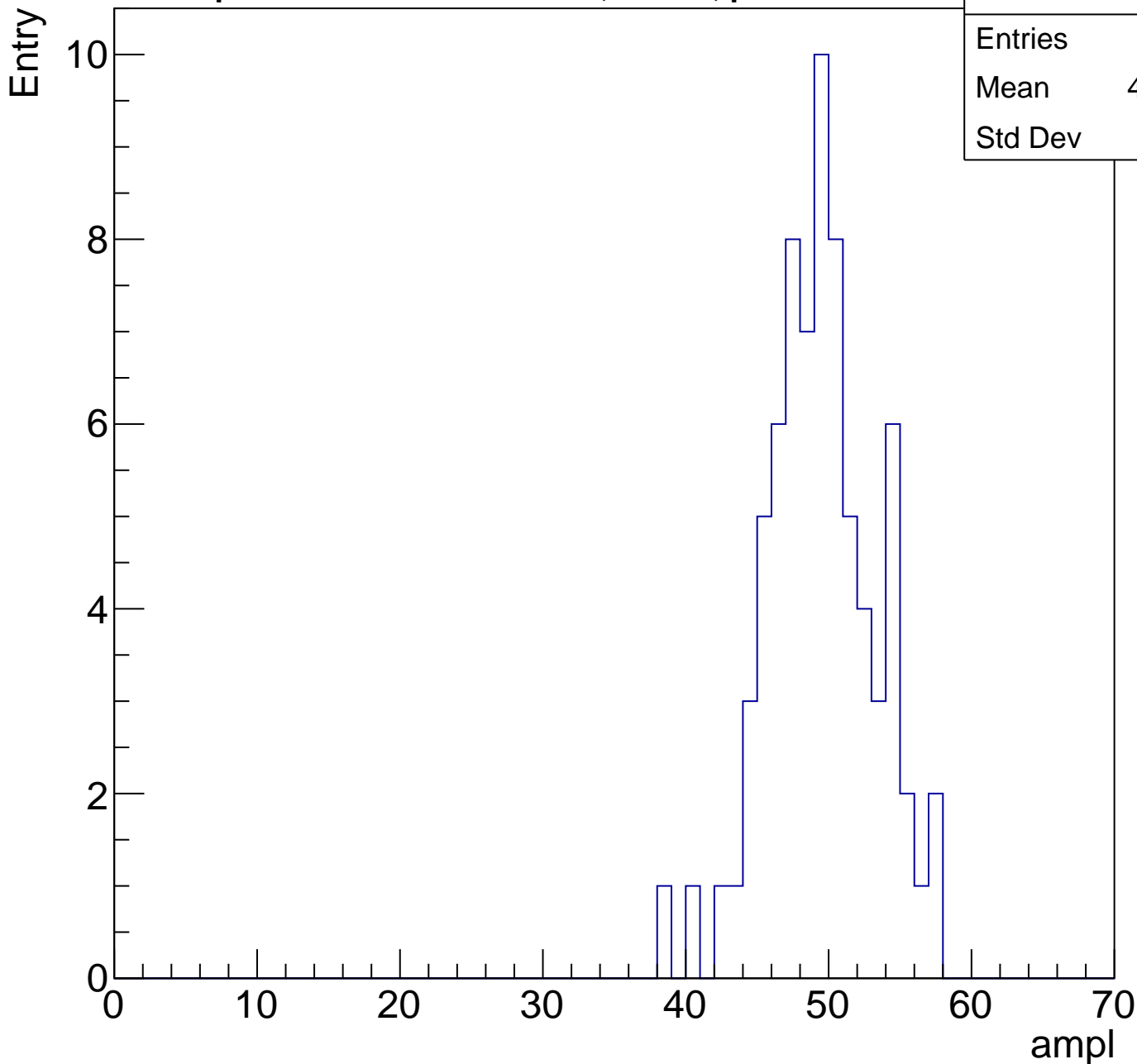
Gaus Width: 3.3292



B0L102S, U5-ch63, adc3

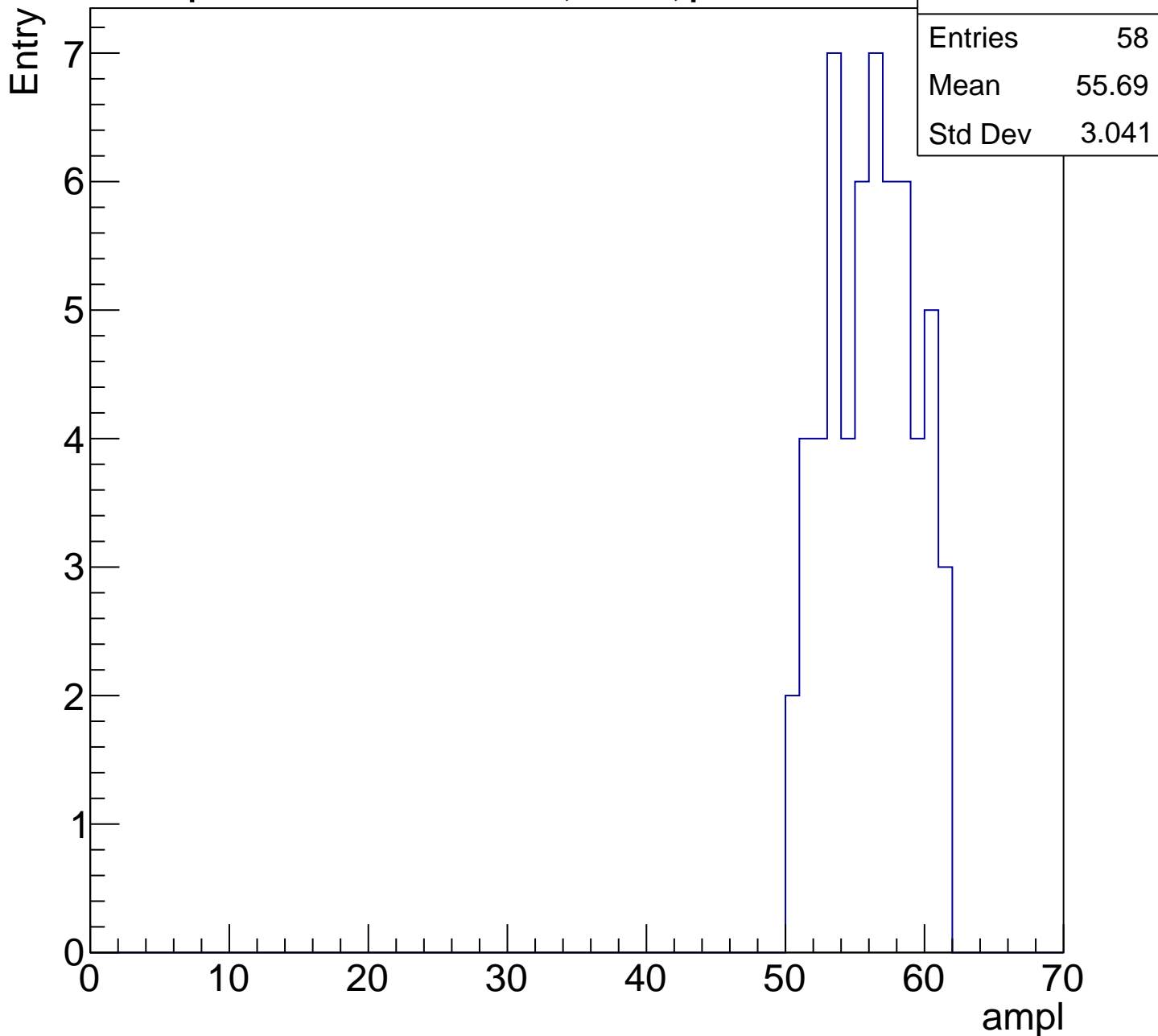
calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	48.97
Std Dev	3.76



B0L102S, U5-ch63, adc4

calib_packv5_042523_0143.root, FC#12, port B1

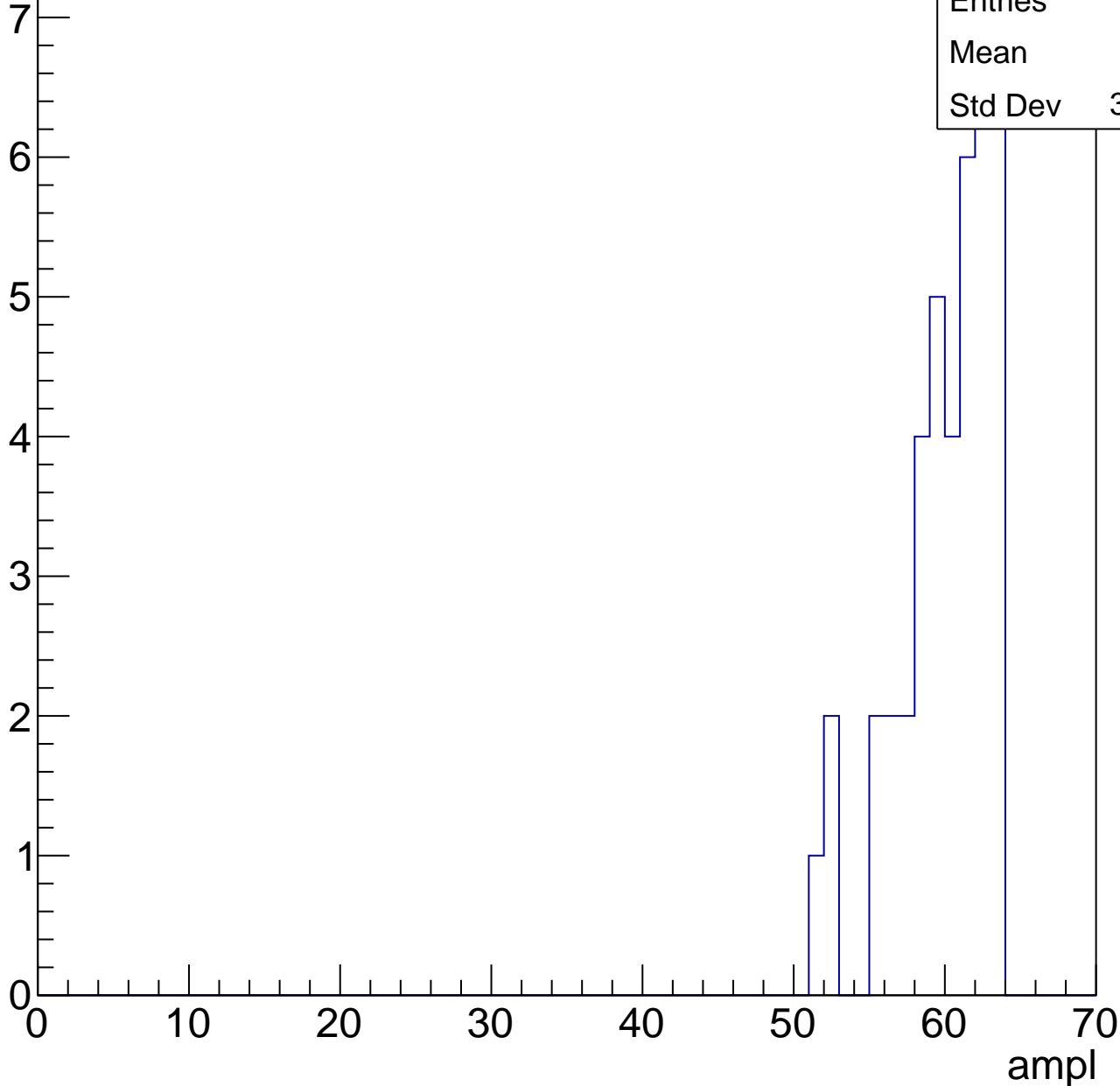


B0L102S, U5-ch63, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

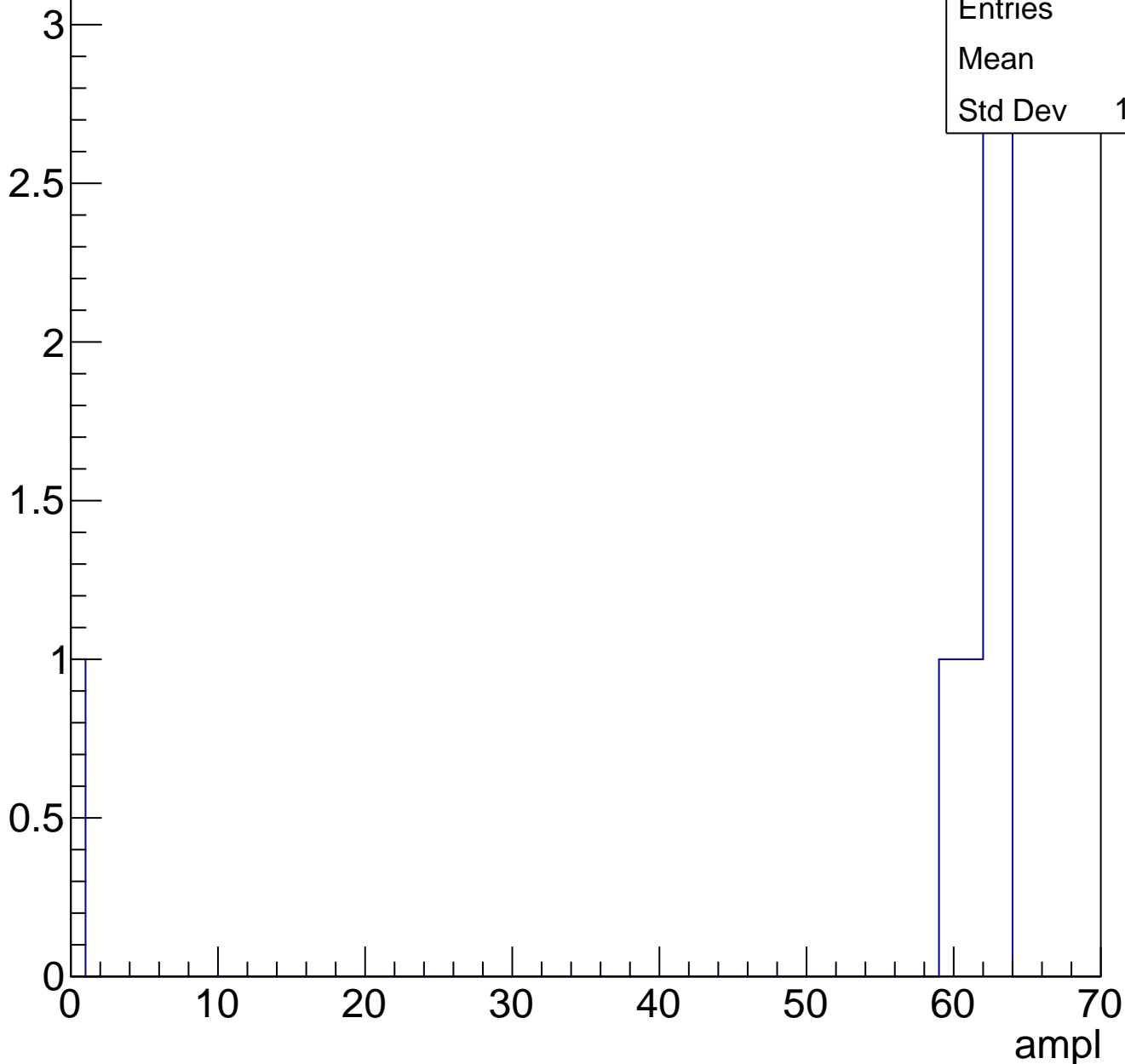
Entries	42
Mean	59.5
Std Dev	3.149



B0L102S, U5-ch63, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	10
Mean	55.5
Std Dev	18.54

B0L102S, U5-ch63, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch64, adc0

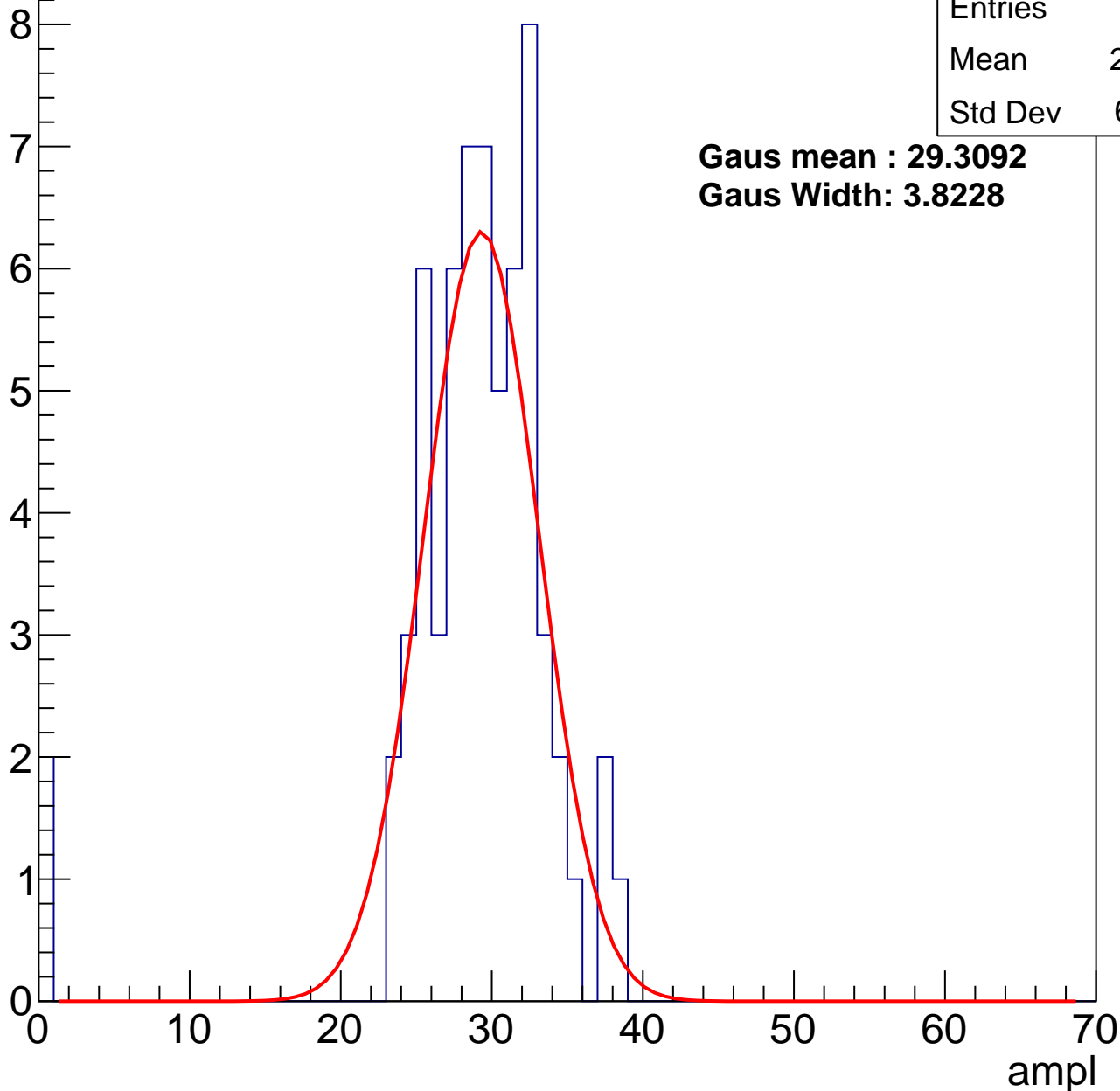
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	28.33
Std Dev	6.121

Gaus mean : 29.3092

Gaus Width: 3.8228



B0L102S, U5-ch64, adc1

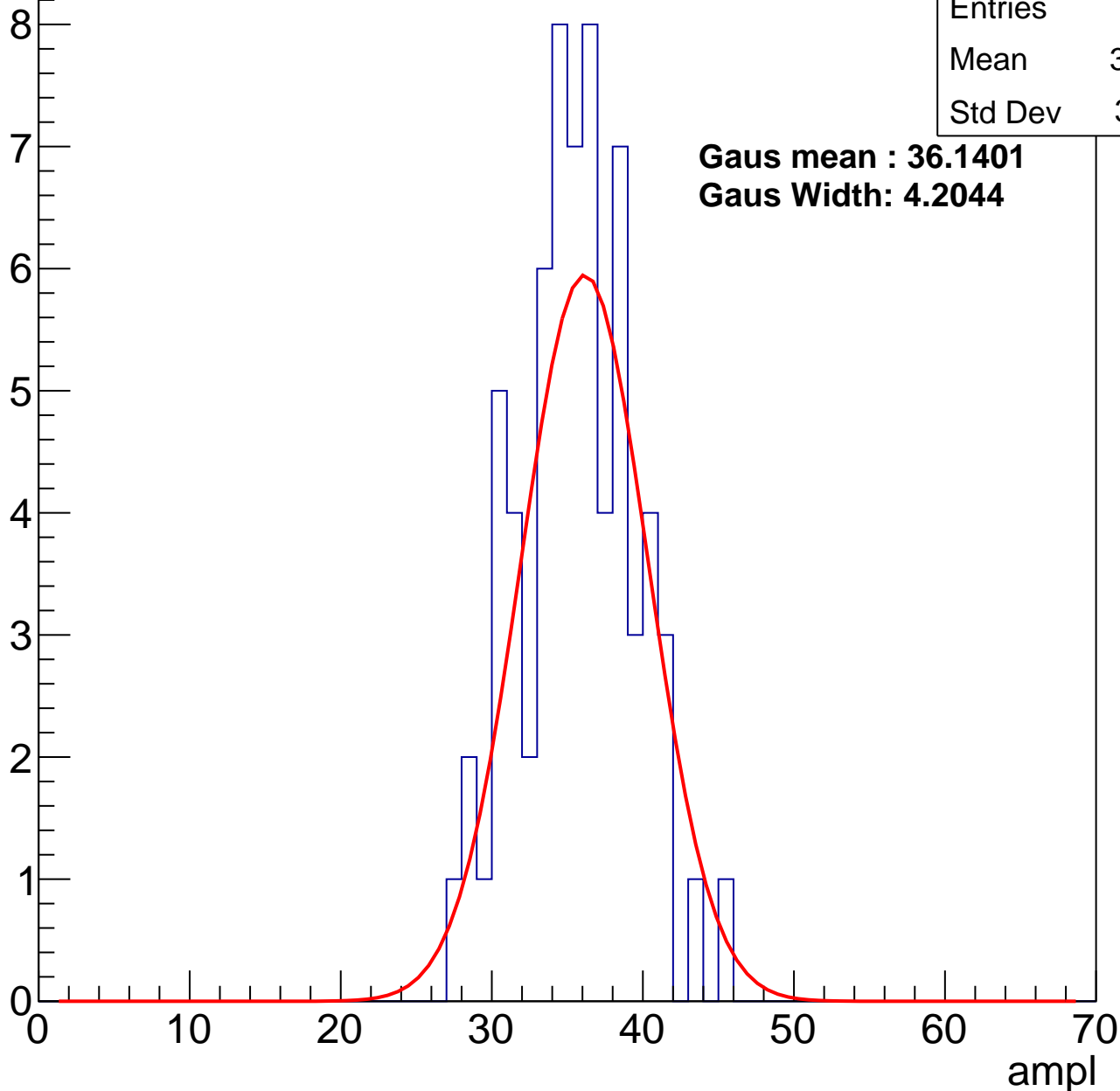
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	35.15
Std Dev	3.751

Gaus mean : 36.1401

Gaus Width: 4.2044



B0L102S, U5-ch64, adc2

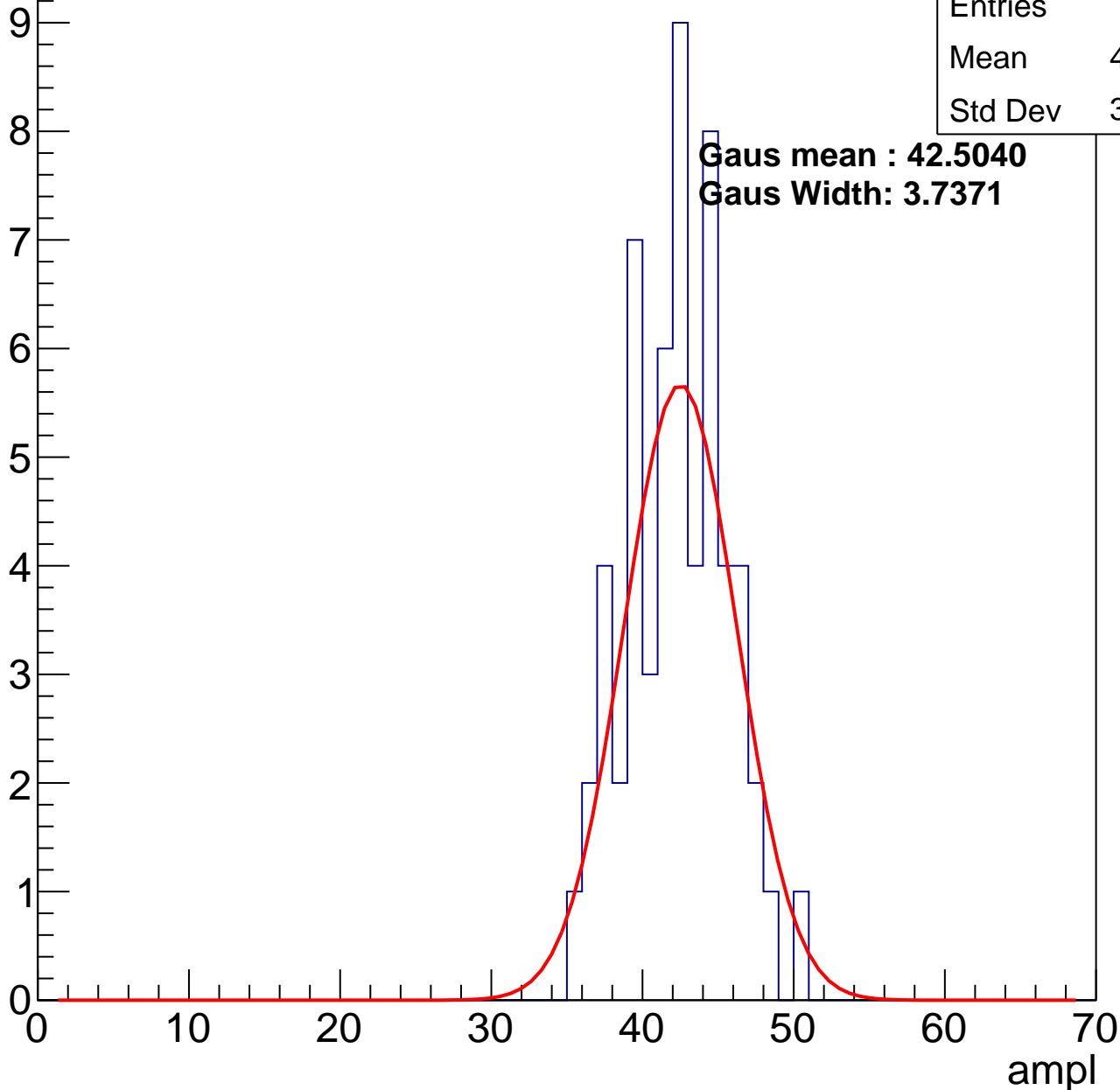
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	41.86
Std Dev	3.267

Gaus mean : 42.5040

Gaus Width: 3.7371

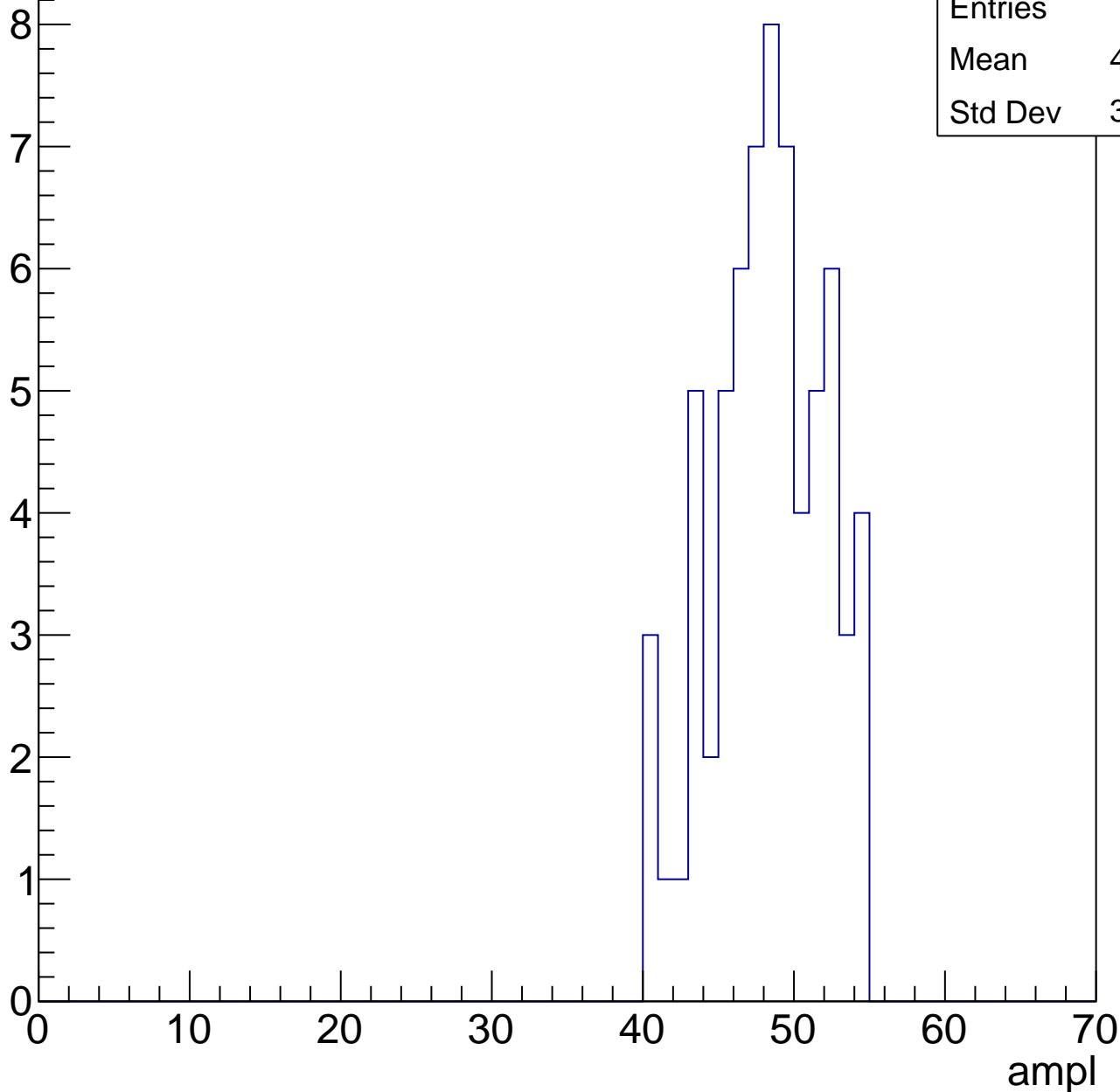


B0L102S, U5-ch64, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	47.84
Std Dev	3.647



B0L102S, U5-ch64, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	74
Mean	54.91
Std Dev	3.504

Entry

10

8

6

4

2

0

0

10

20

30

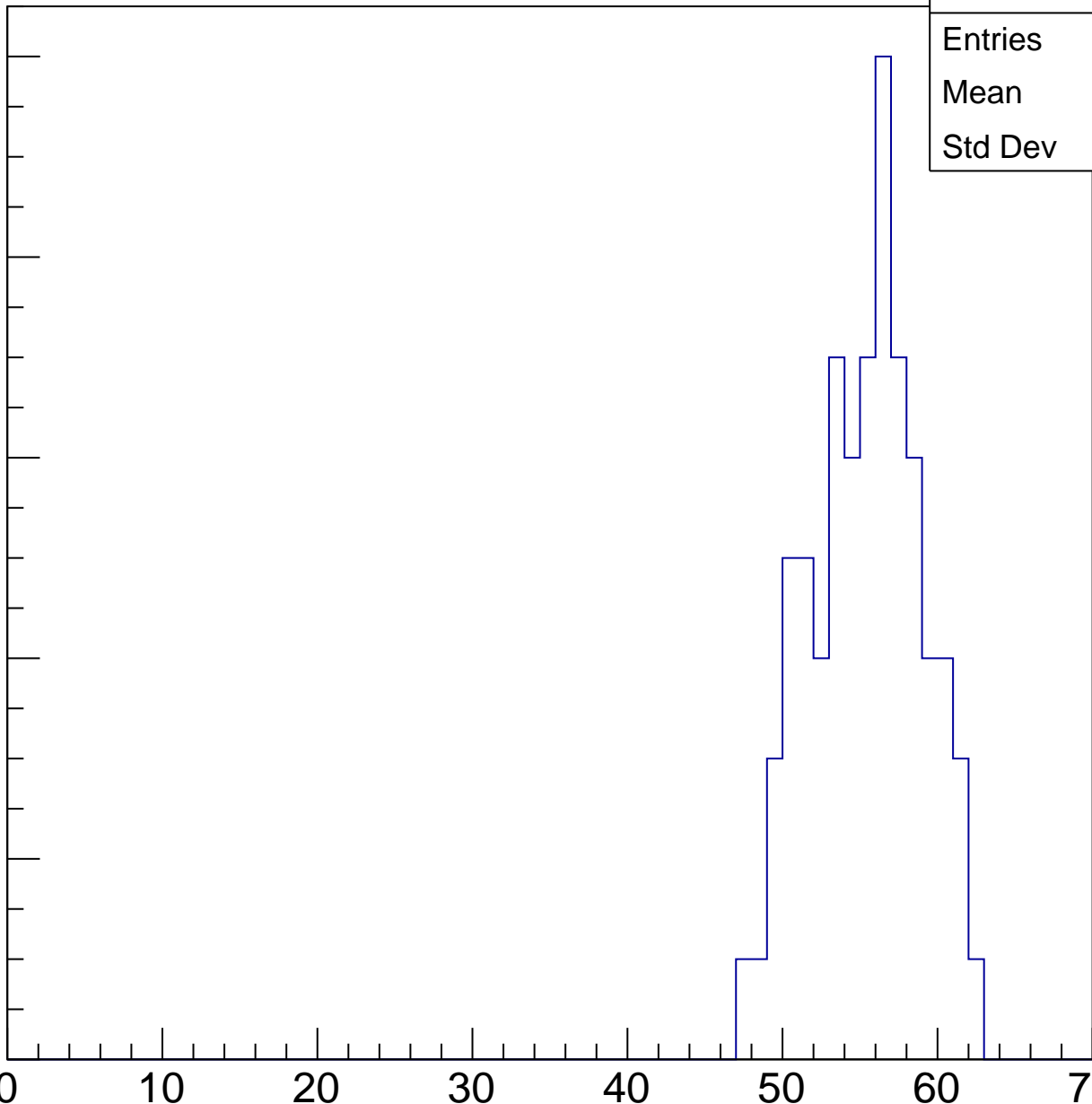
40

50

60

70

ampl

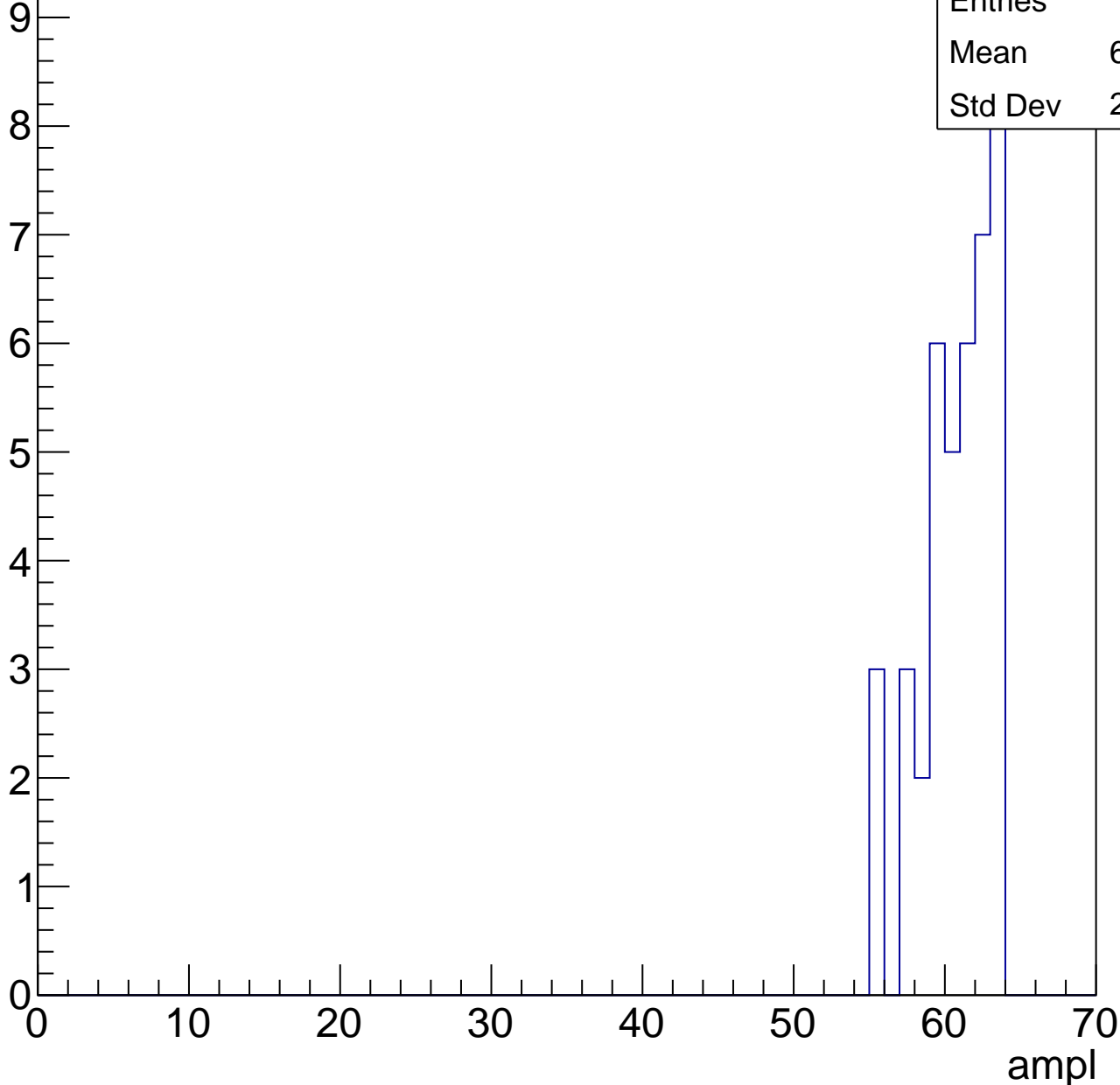


B0L102S, U5-ch64, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	60.32
Std Dev	2.352



B0L102S, U5-ch64, adc6

calib_packv5_042523_0143.root, FC#12, port B1

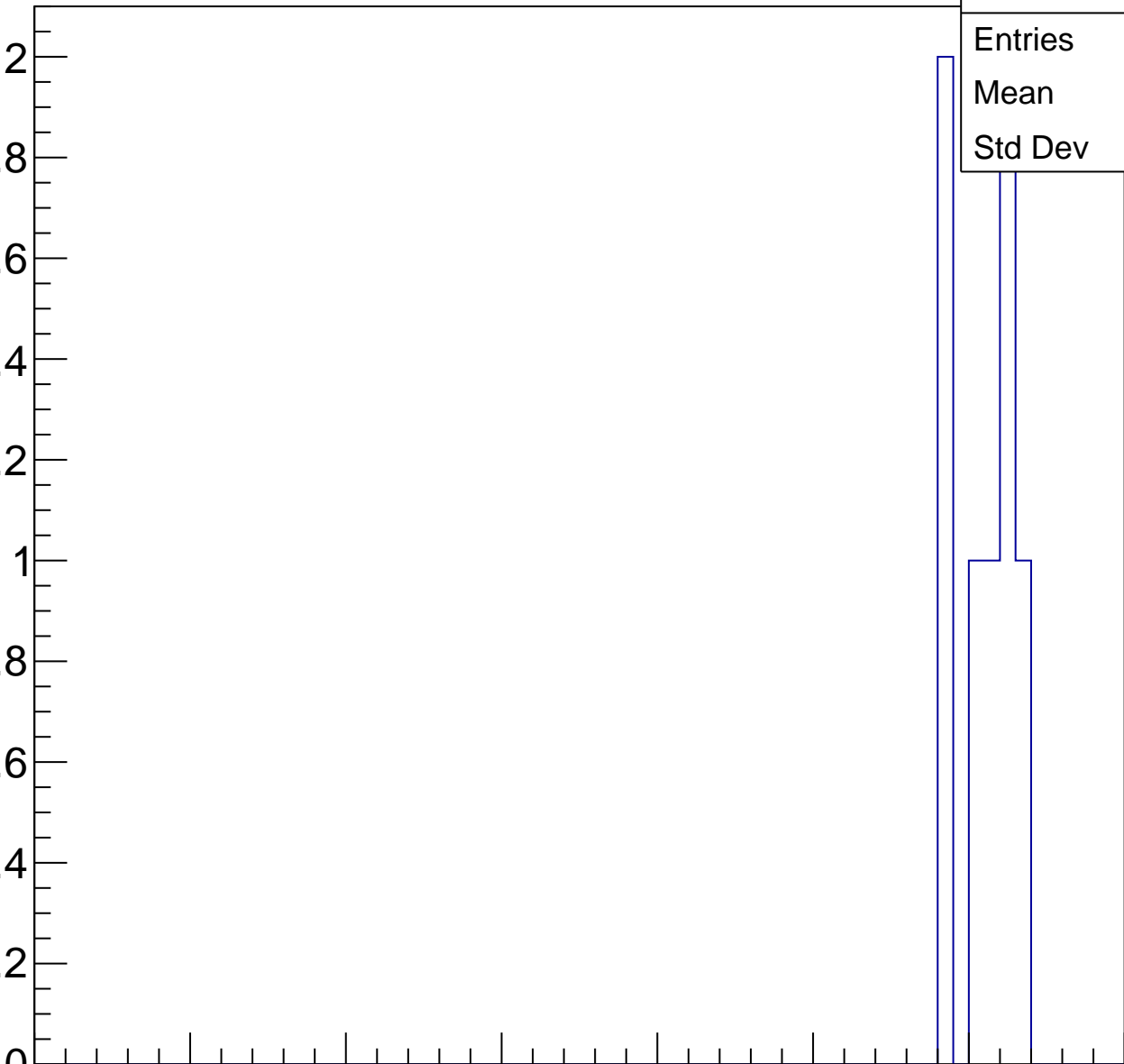
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.57
Std Dev	1.841

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch64, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L102S, U5-ch65, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	91
Mean	28.51
Std Dev	5.623

Gaus mean : 29.6853

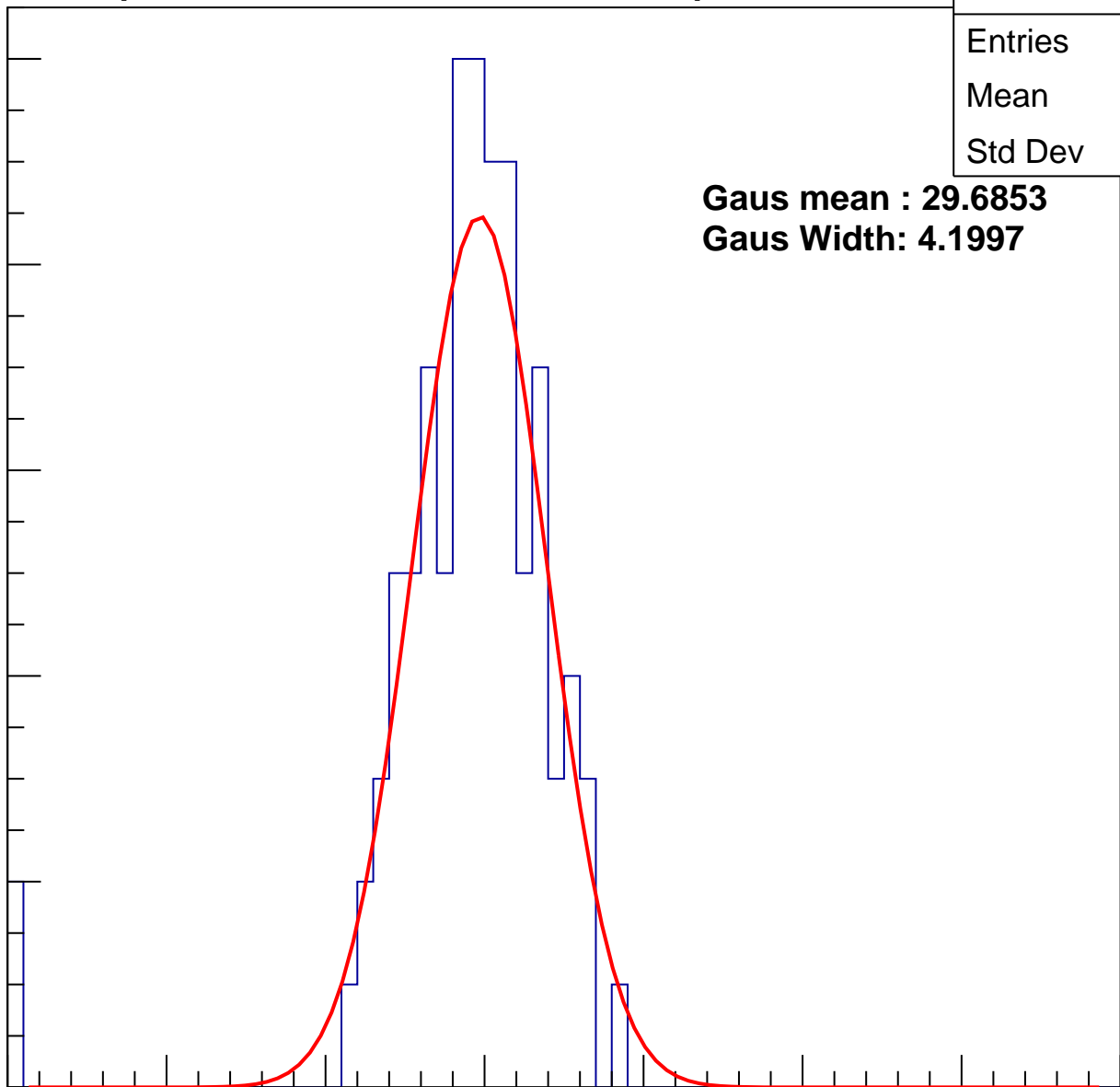
Gaus Width: 4.1997

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch65, adc1

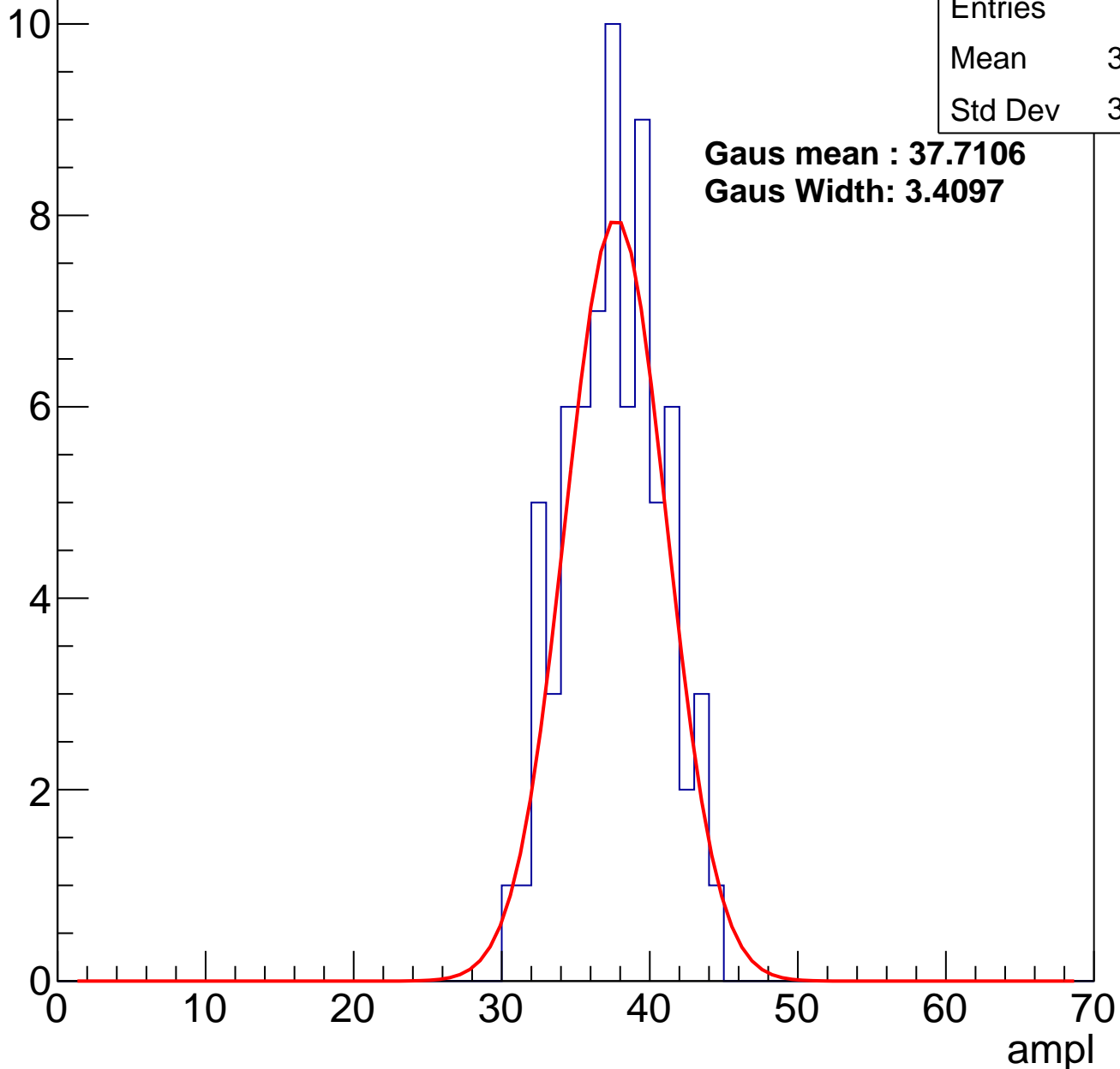
calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	37.15
Std Dev	3.209

Gaus mean : 37.7106

Gaus Width: 3.4097

Entry



B0L102S, U5-ch65, adc2

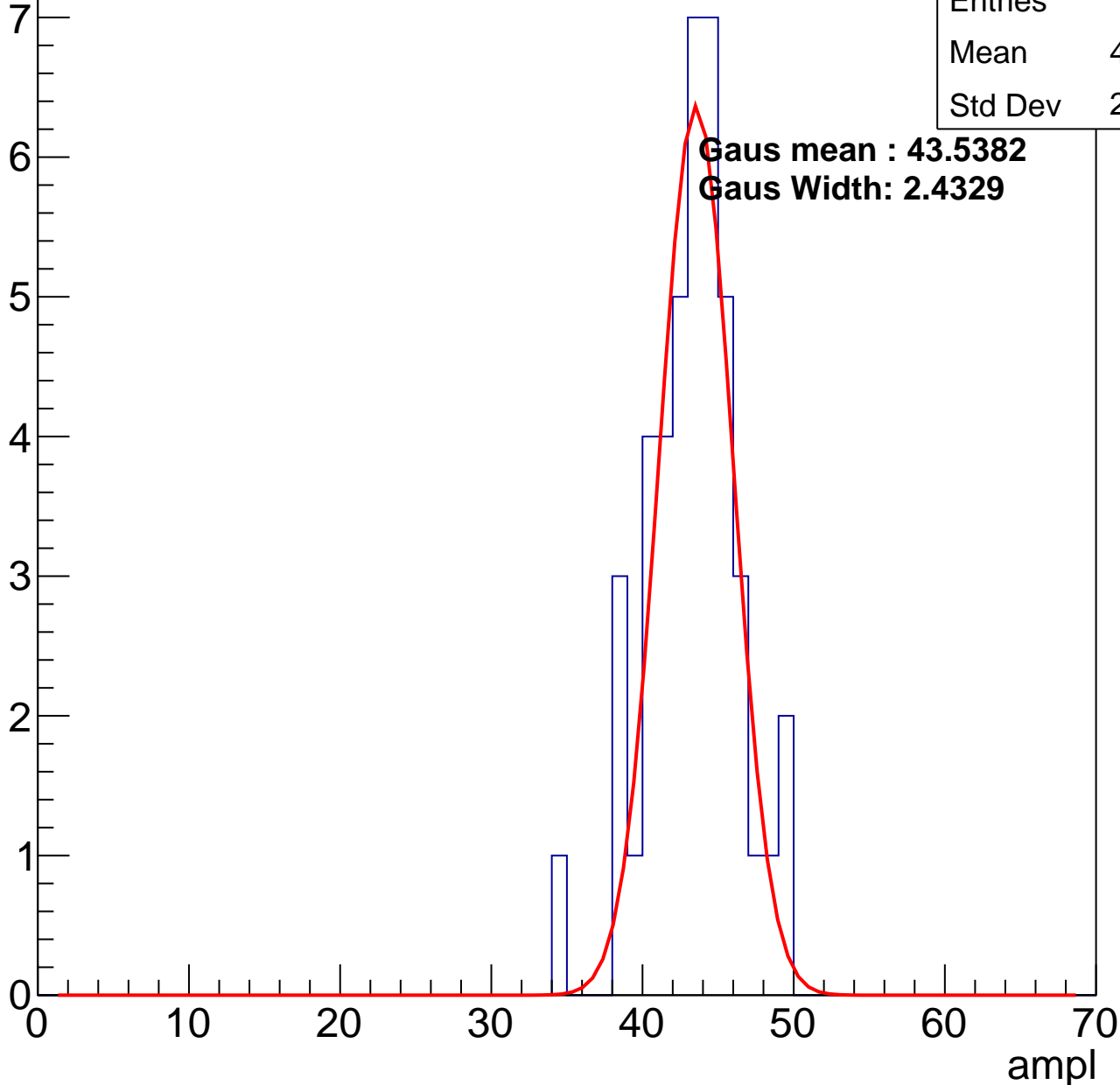
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	44
Mean	42.86
Std Dev	2.997

Gaus mean : 43.5382

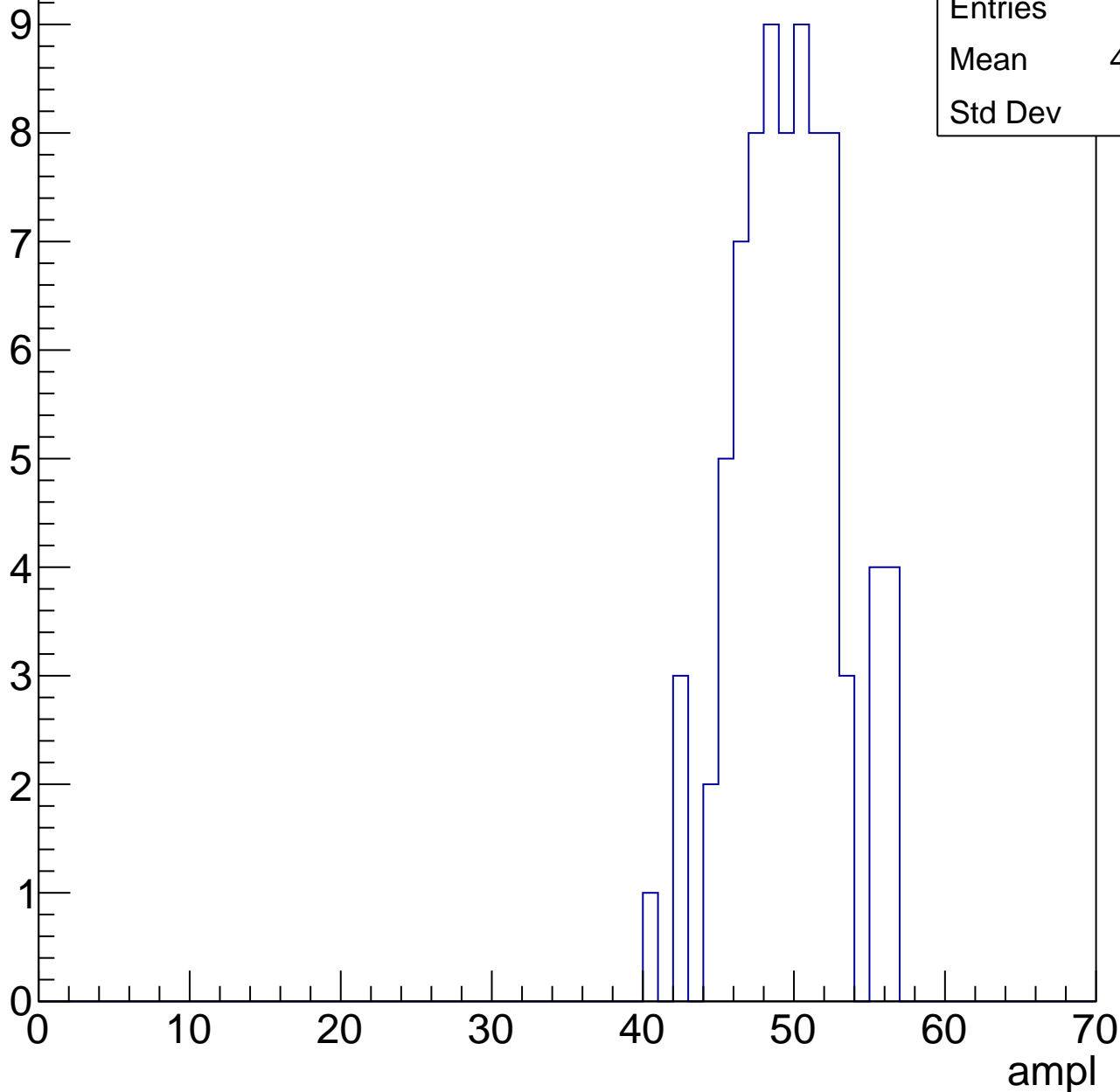
Gaus Width: 2.4329



B0L102S, U5-ch65, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



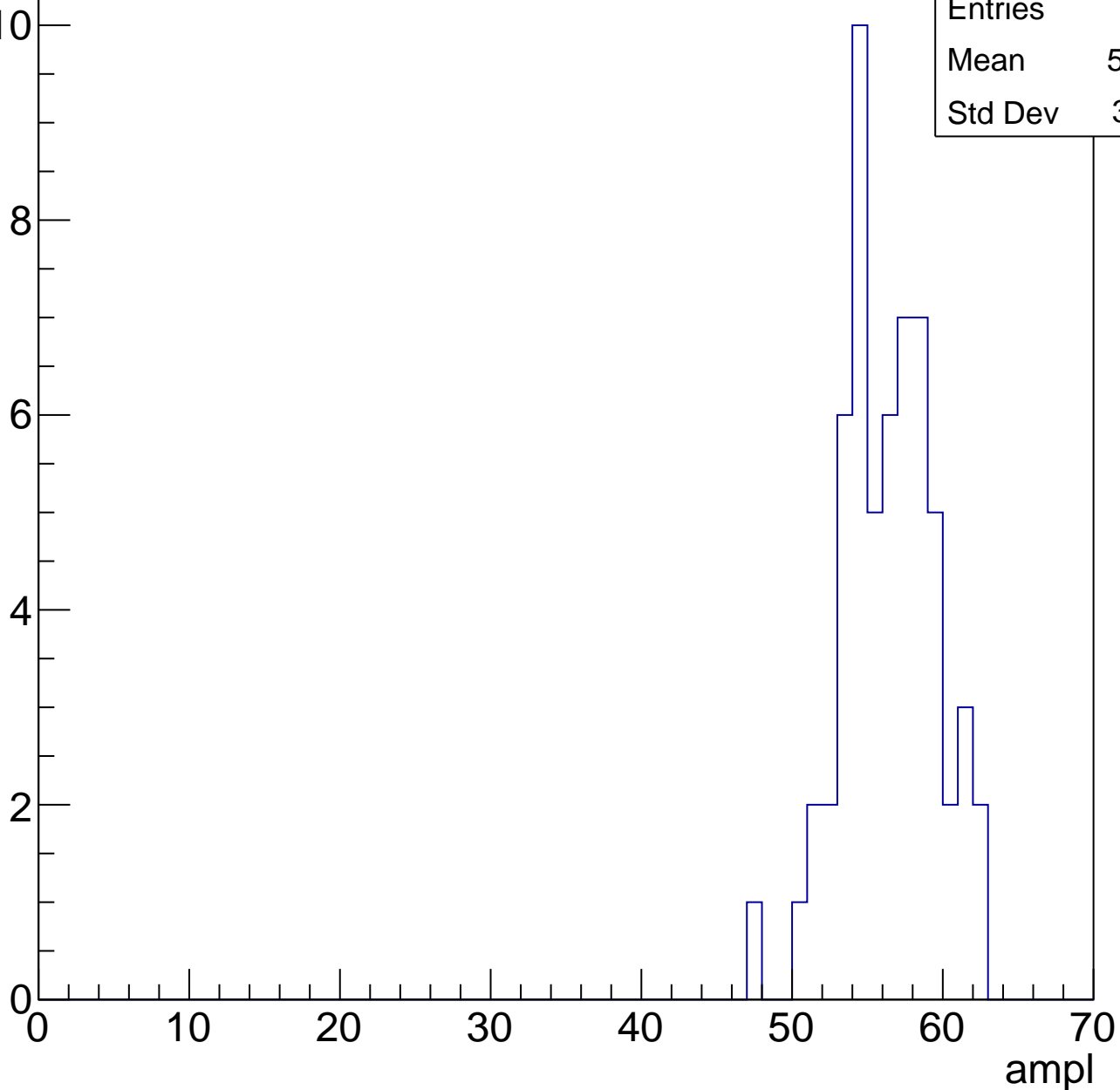
Entries	79
Mean	49.09
Std Dev	3.49

B0L102S, U5-ch65, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	55.92
Std Dev	3.071



B0L102S, U5-ch65, adc5

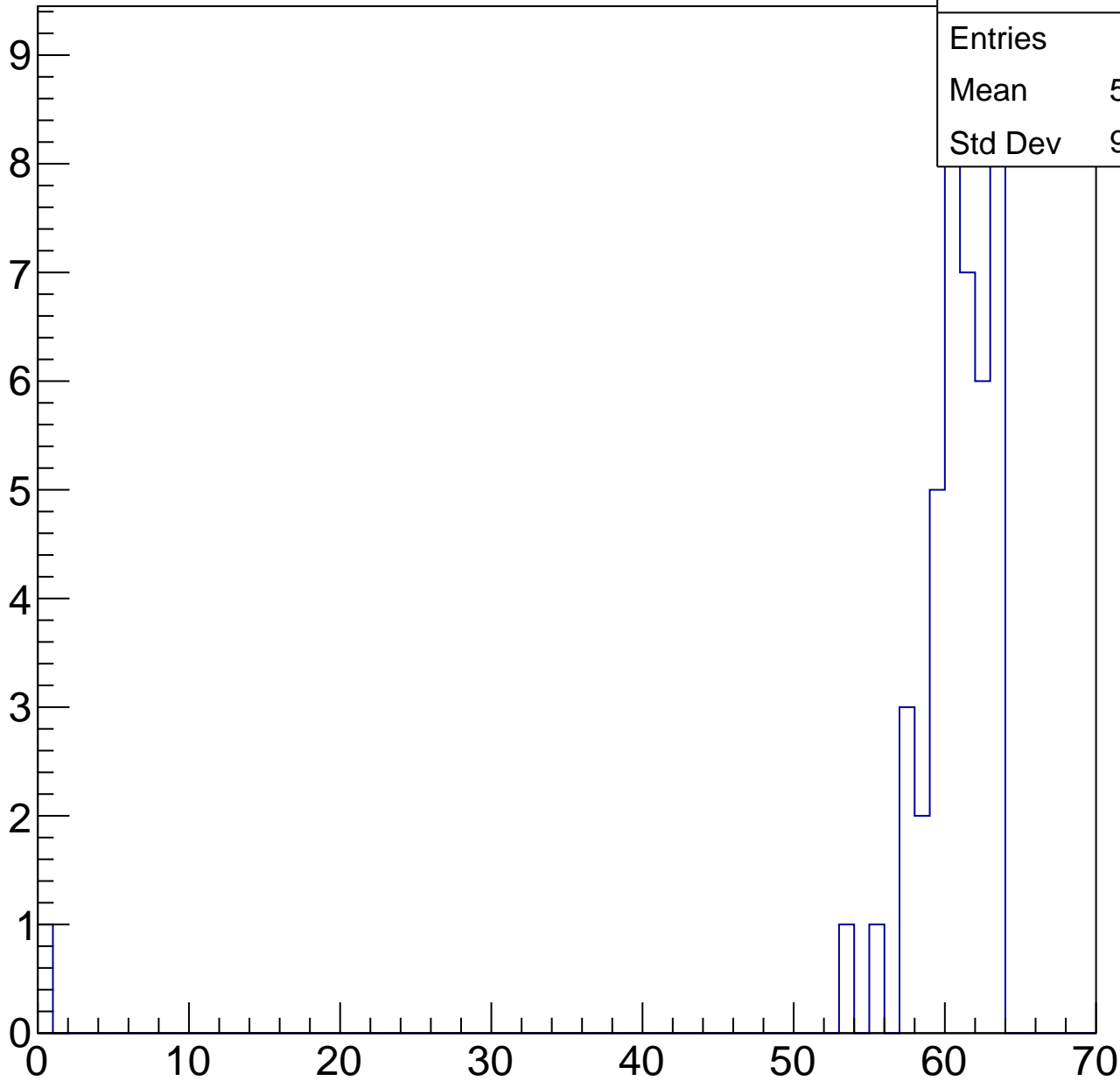
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.98
Std Dev	9.377

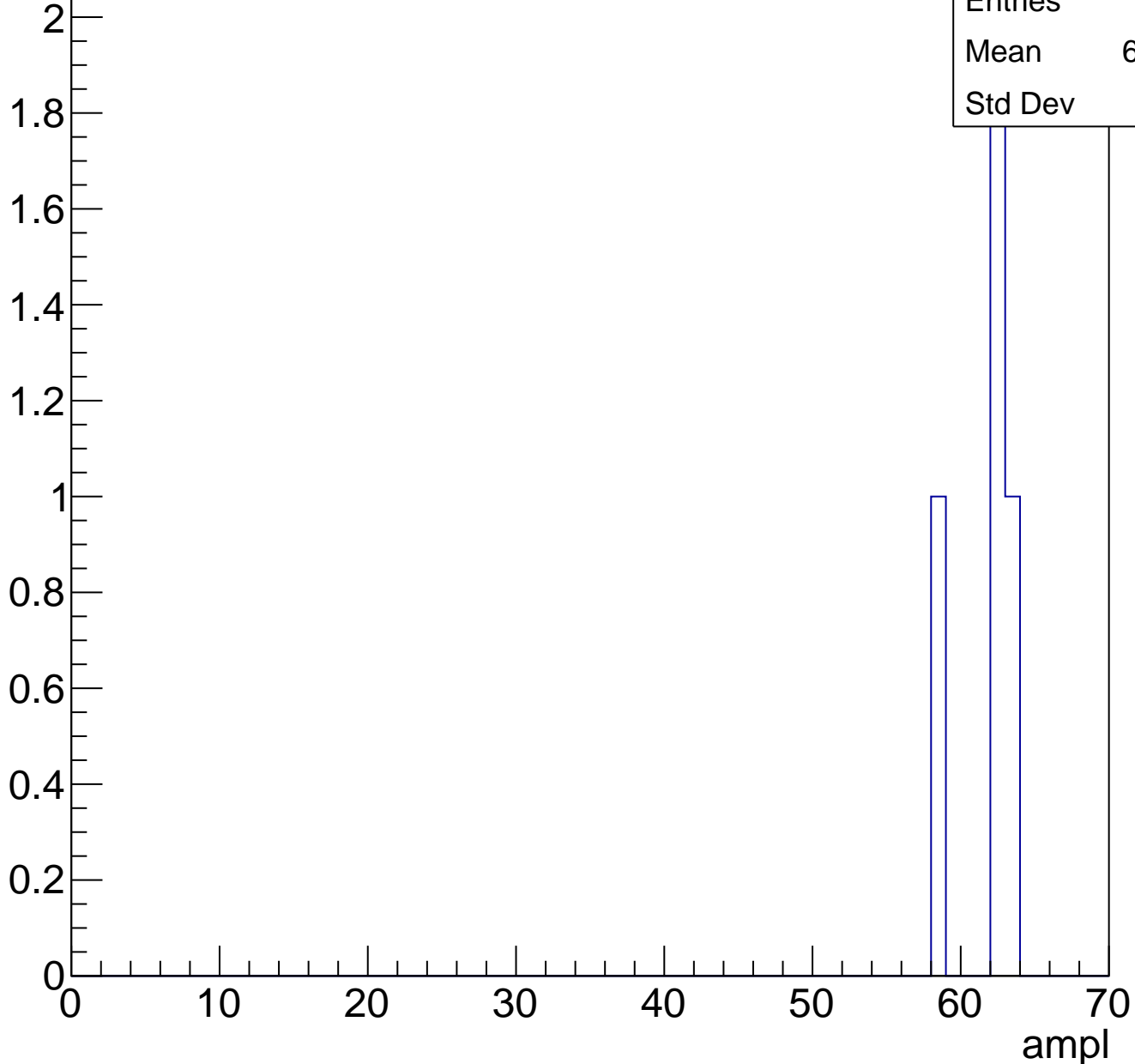
ampl



B0L102S, U5-ch65, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch65, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch66, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	73
Mean	29.6
Std Dev	5.34

Gaus mean : 30.8078

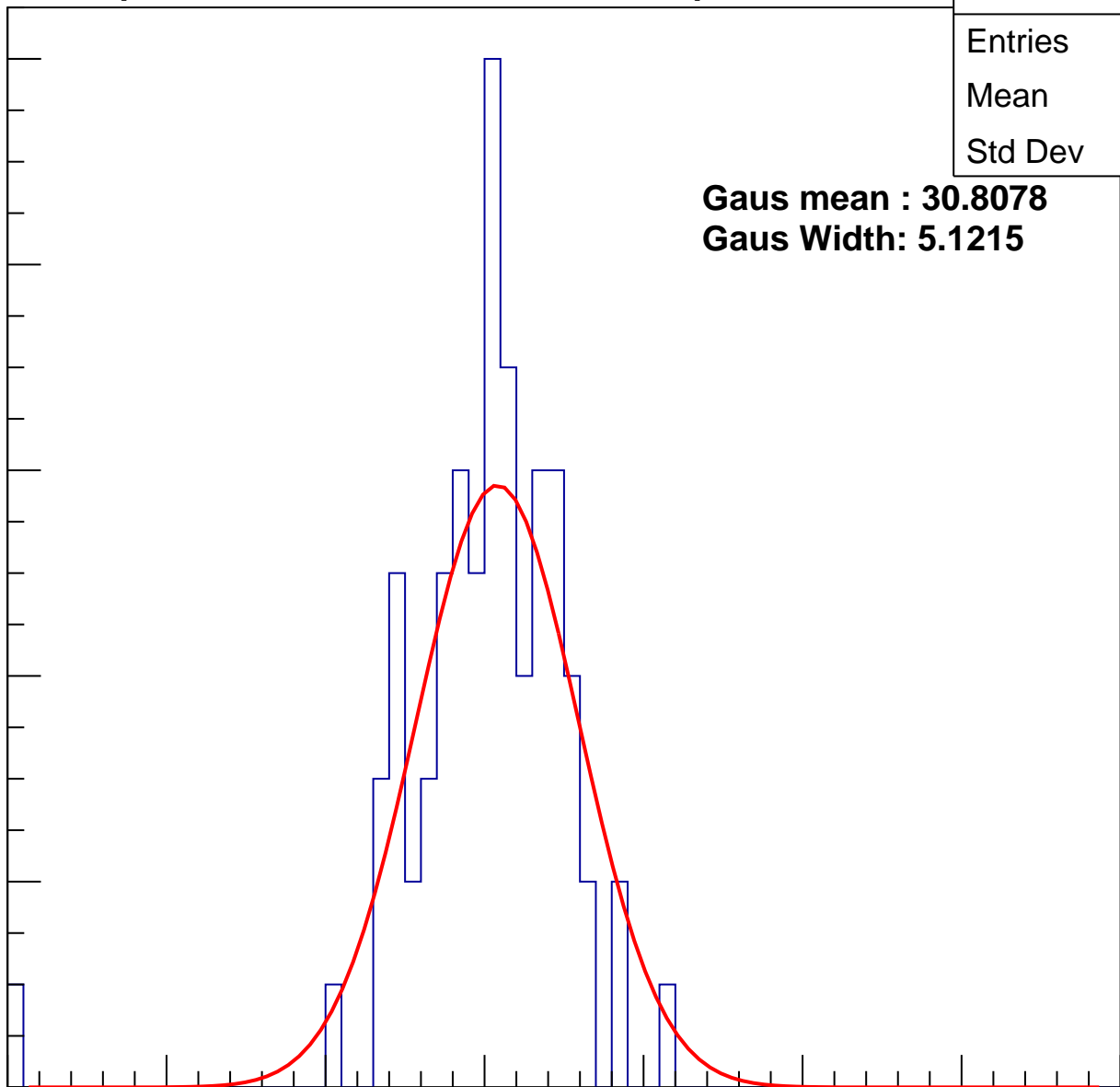
Gaus Width: 5.1215

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch66, adc1

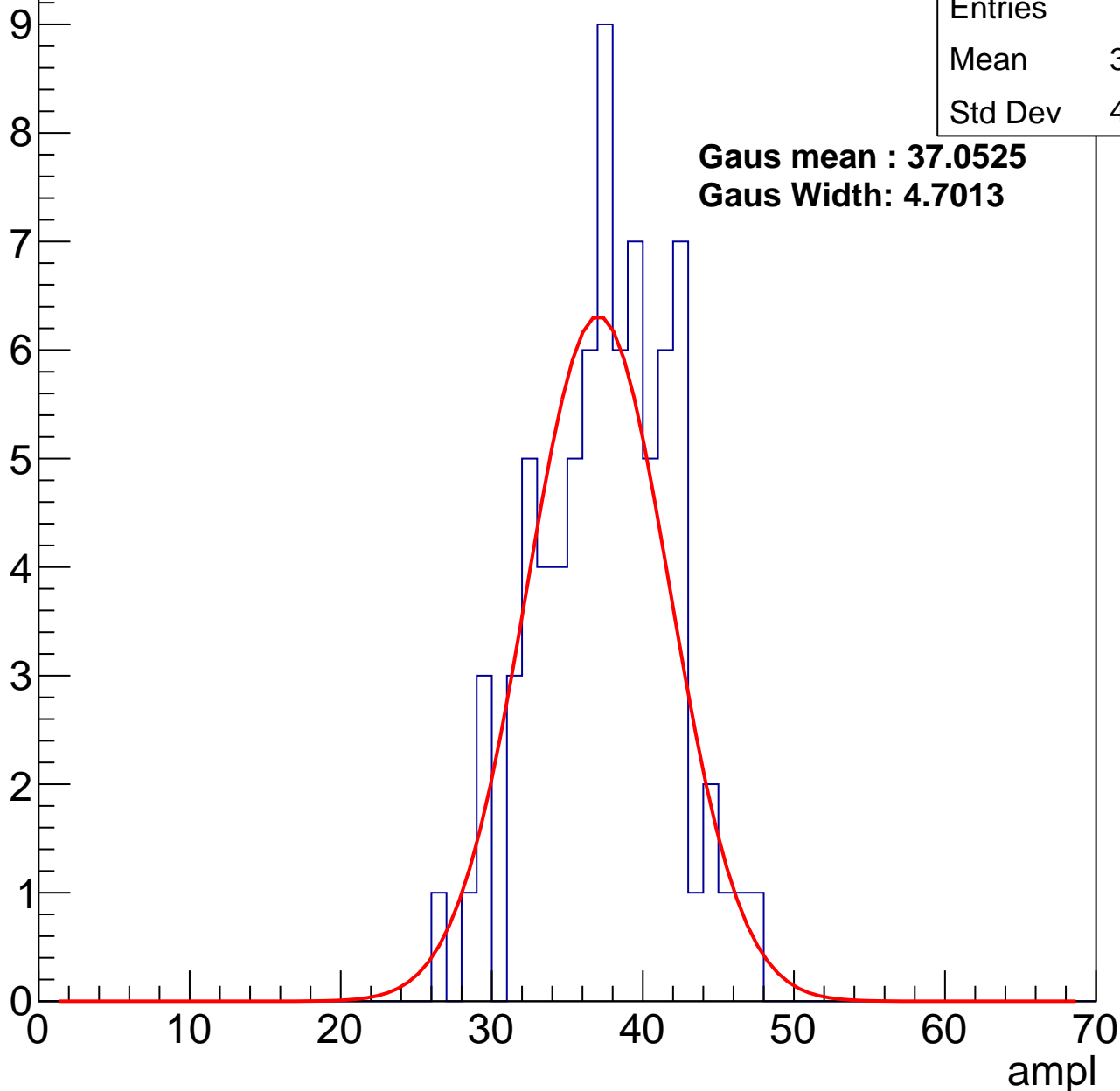
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	37.13
Std Dev	4.357

Gaus mean : 37.0525

Gaus Width: 4.7013



B0L102S, U5-ch66, adc2

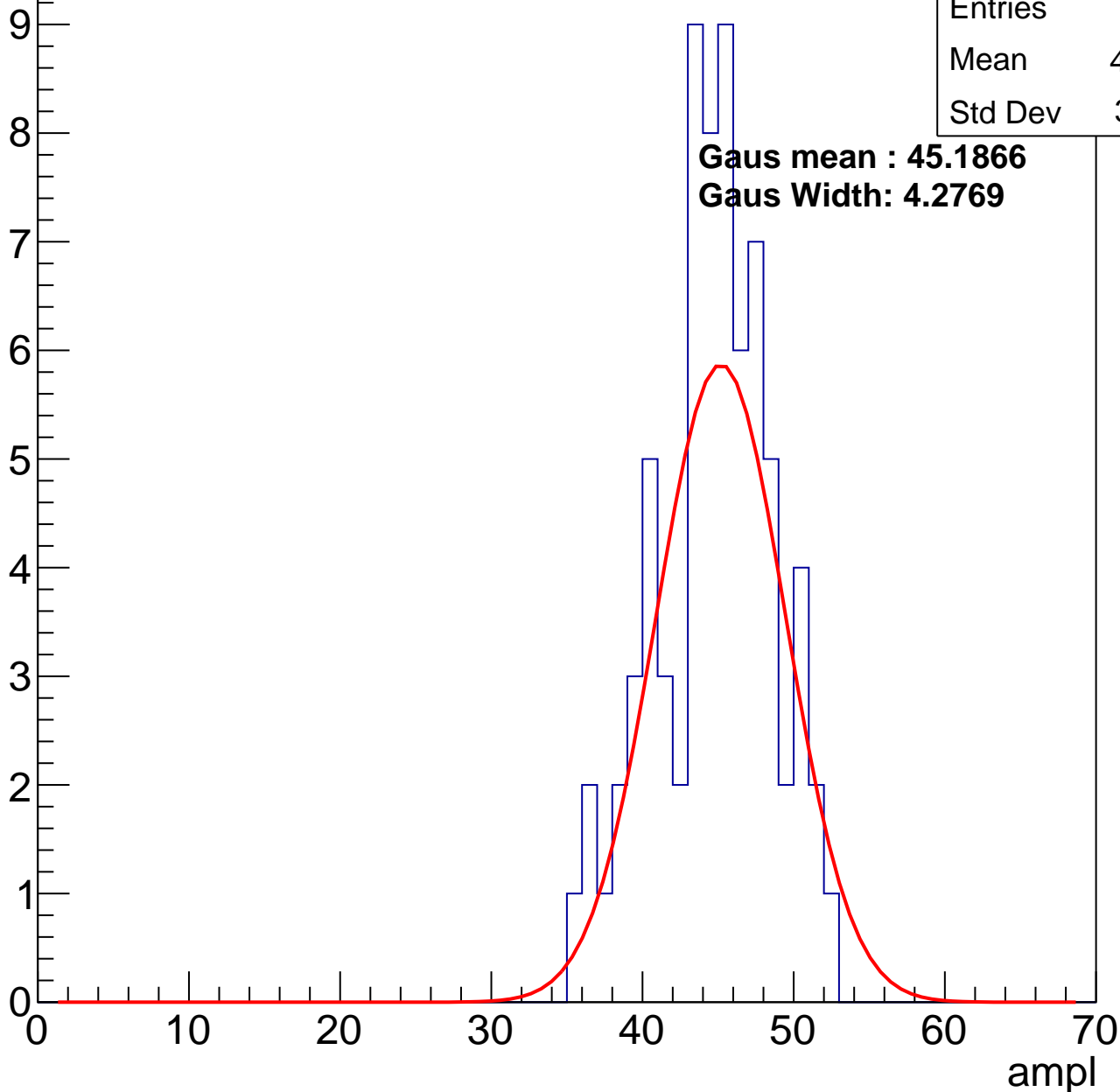
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	44.24
Std Dev	3.831

Gaus mean : 45.1866

Gaus Width: 4.2769

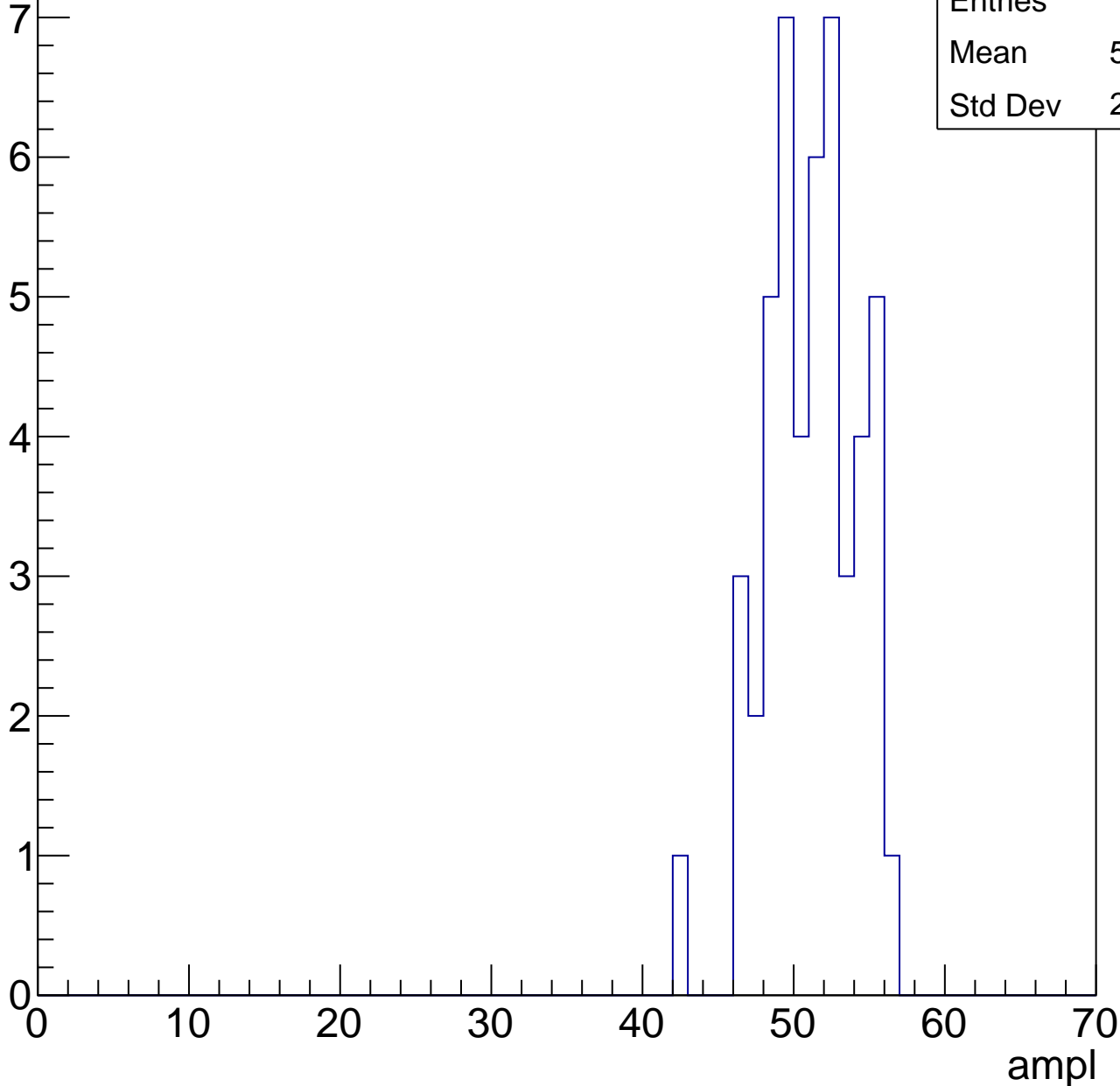


B0L102S, U5-ch66, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	50.69
Std Dev	2.959

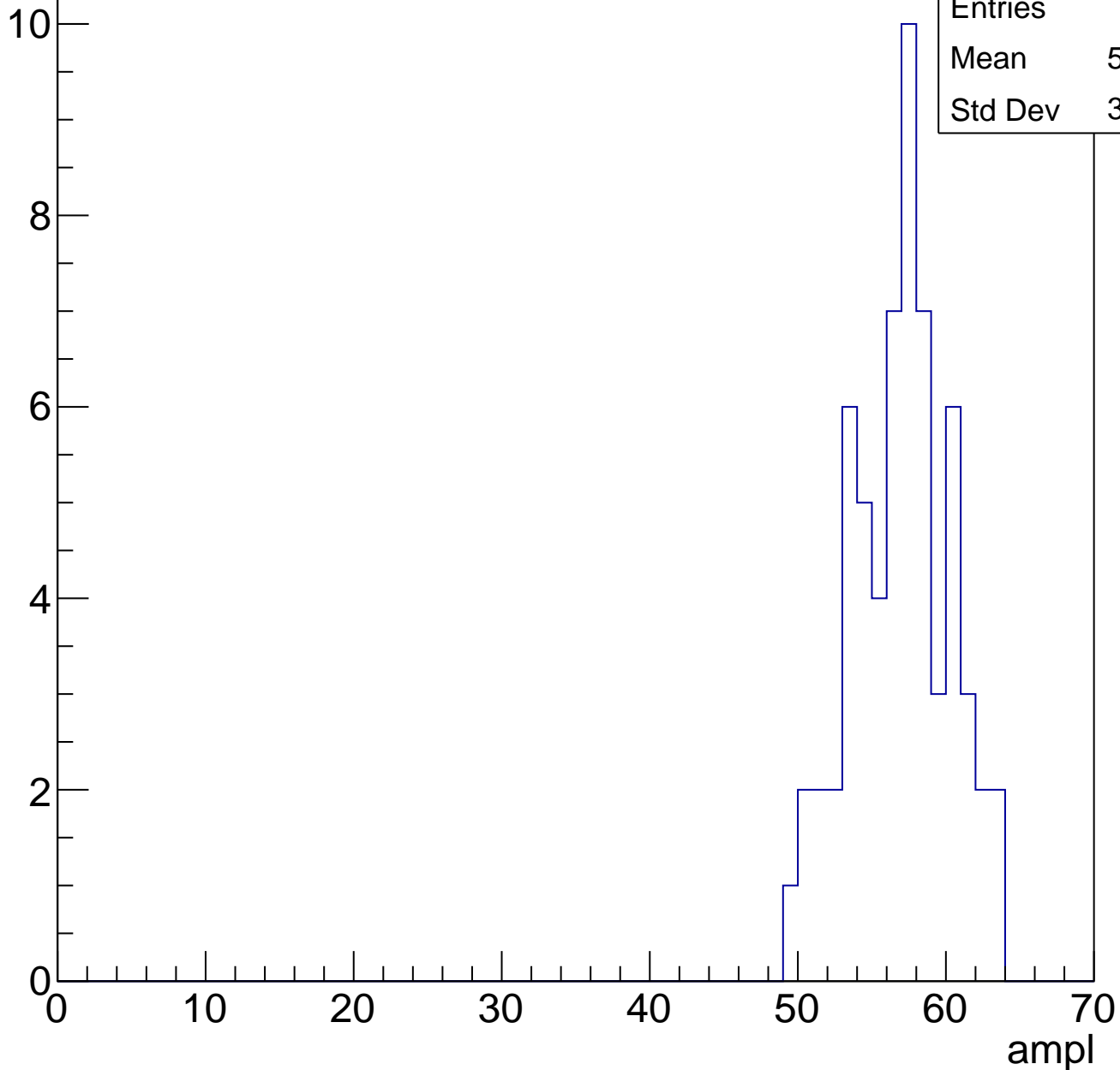


B0L102S, U5-ch66, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	62
Mean	56.47
Std Dev	3.296

Entry

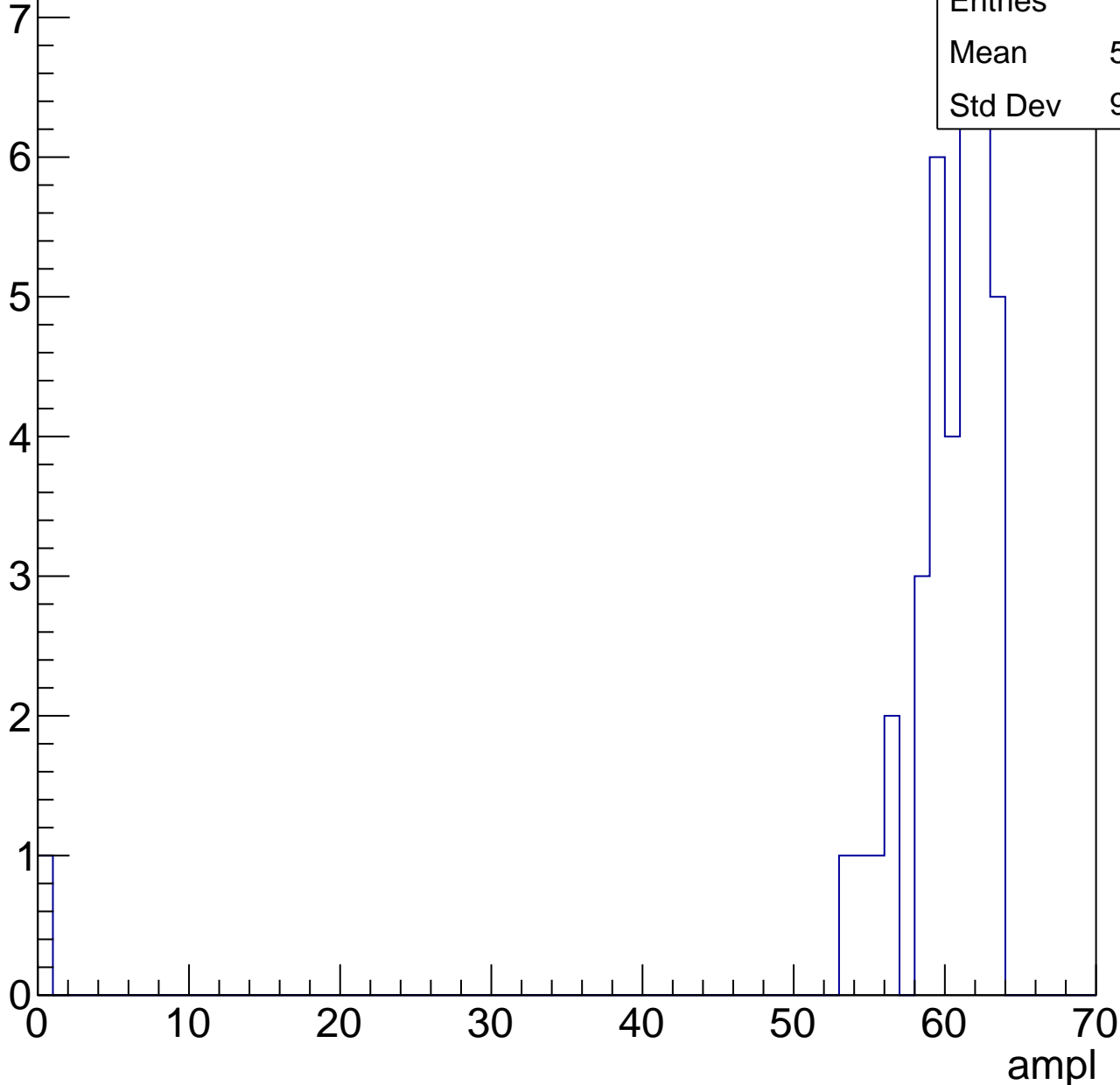


B0L102S, U5-ch66, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

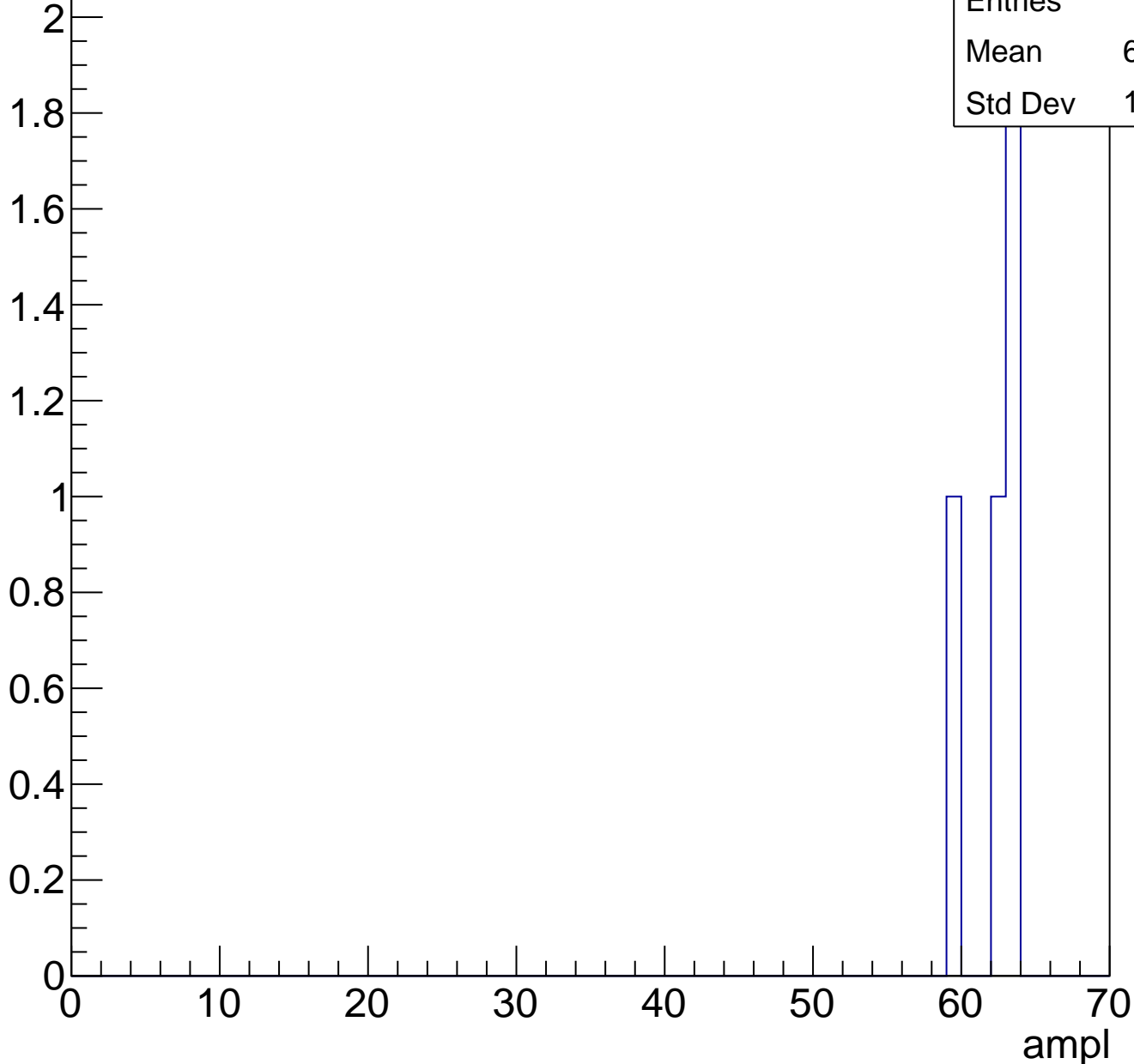
Entries	38
Mean	58.37
Std Dev	9.919



B0L102S, U5-ch66, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch66, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch67, adc0

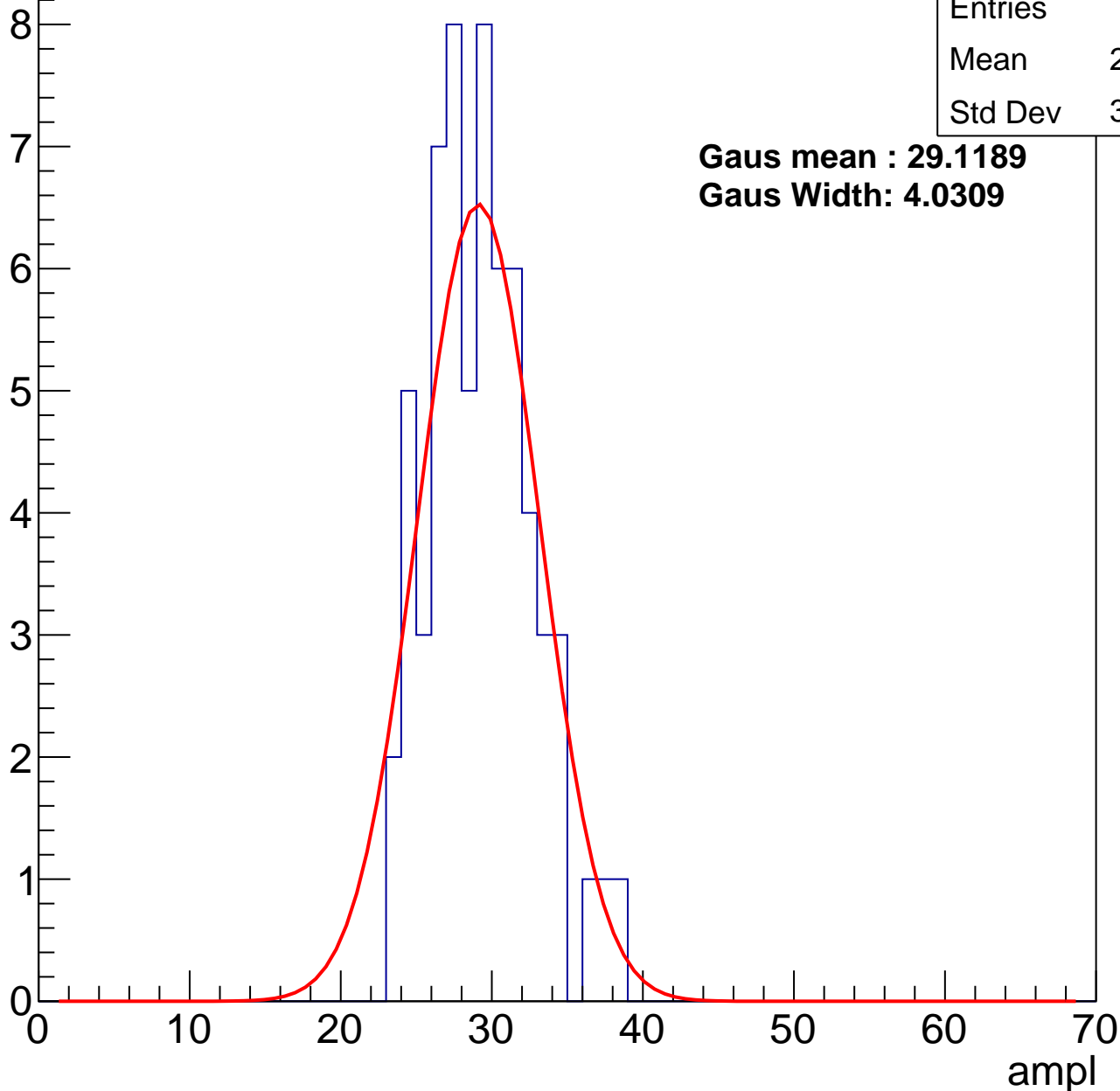
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	28.84
Std Dev	3.396

Gaus mean : 29.1189

Gaus Width: 4.0309



B0L102S, U5-ch67, adc1

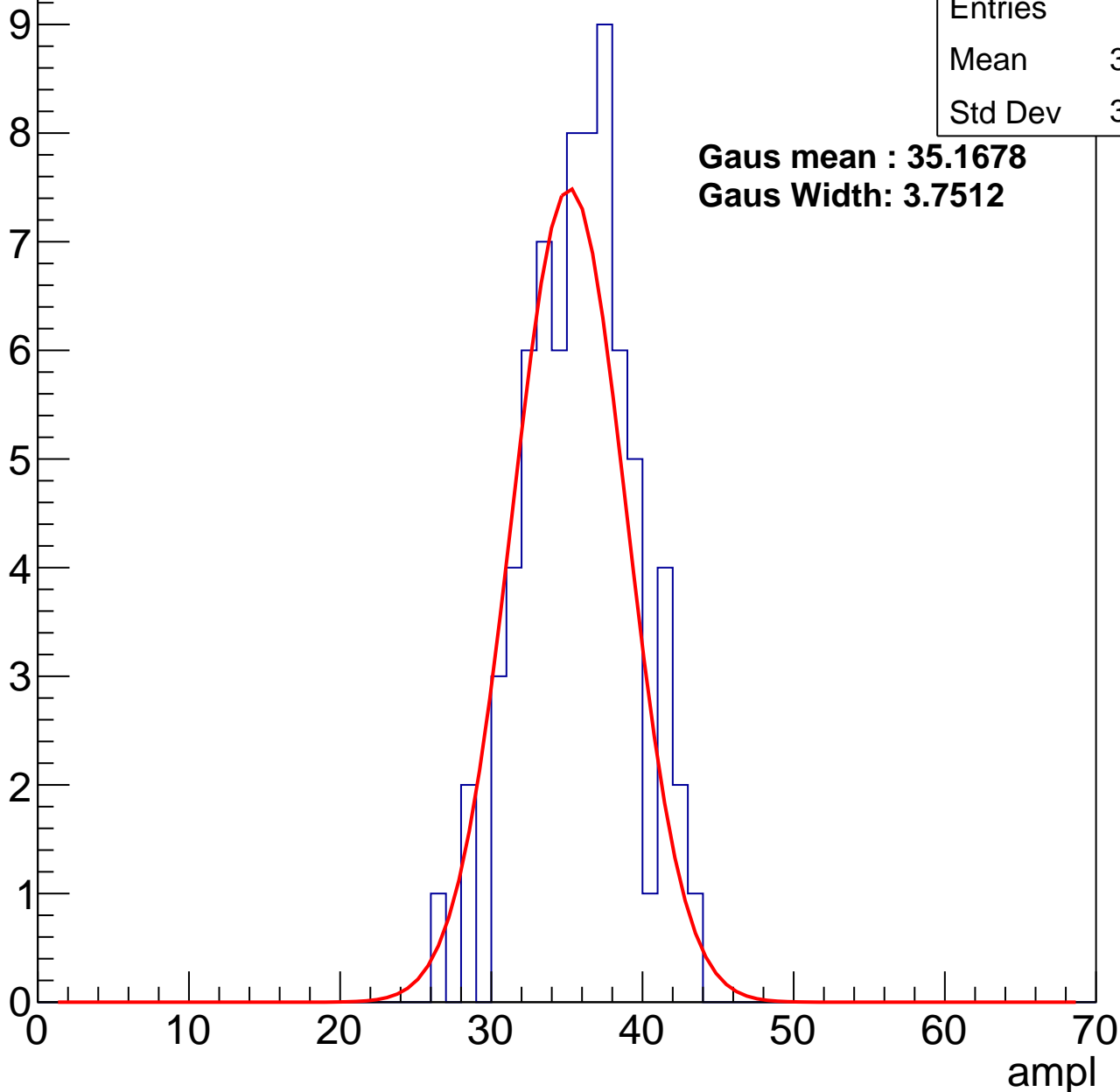
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.32
Std Dev	3.526

Gaus mean : 35.1678

Gaus Width: 3.7512



B0L102S, U5-ch67, adc2

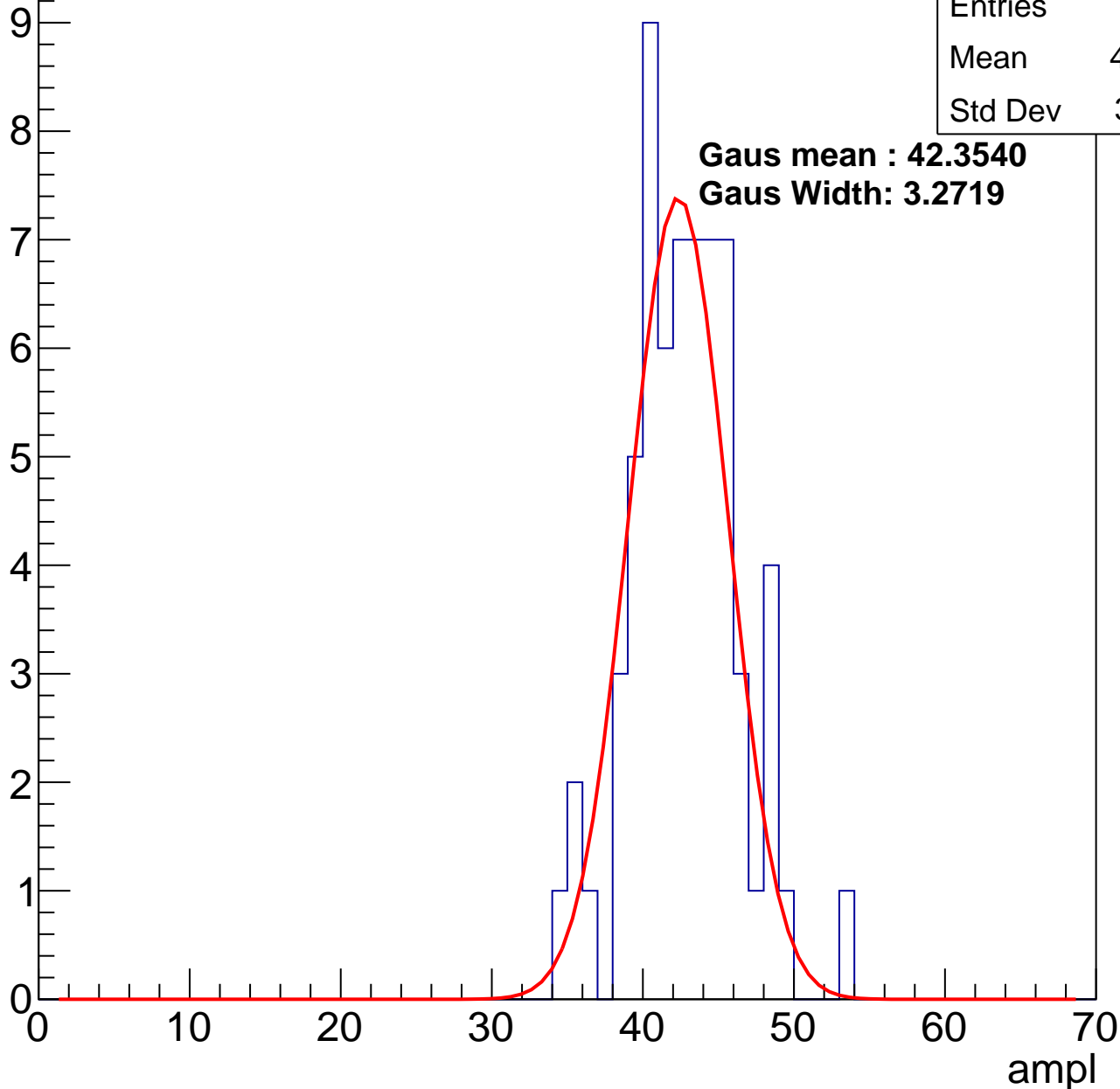
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.34
Std Dev	3.531

Gaus mean : 42.3540

Gaus Width: 3.2719

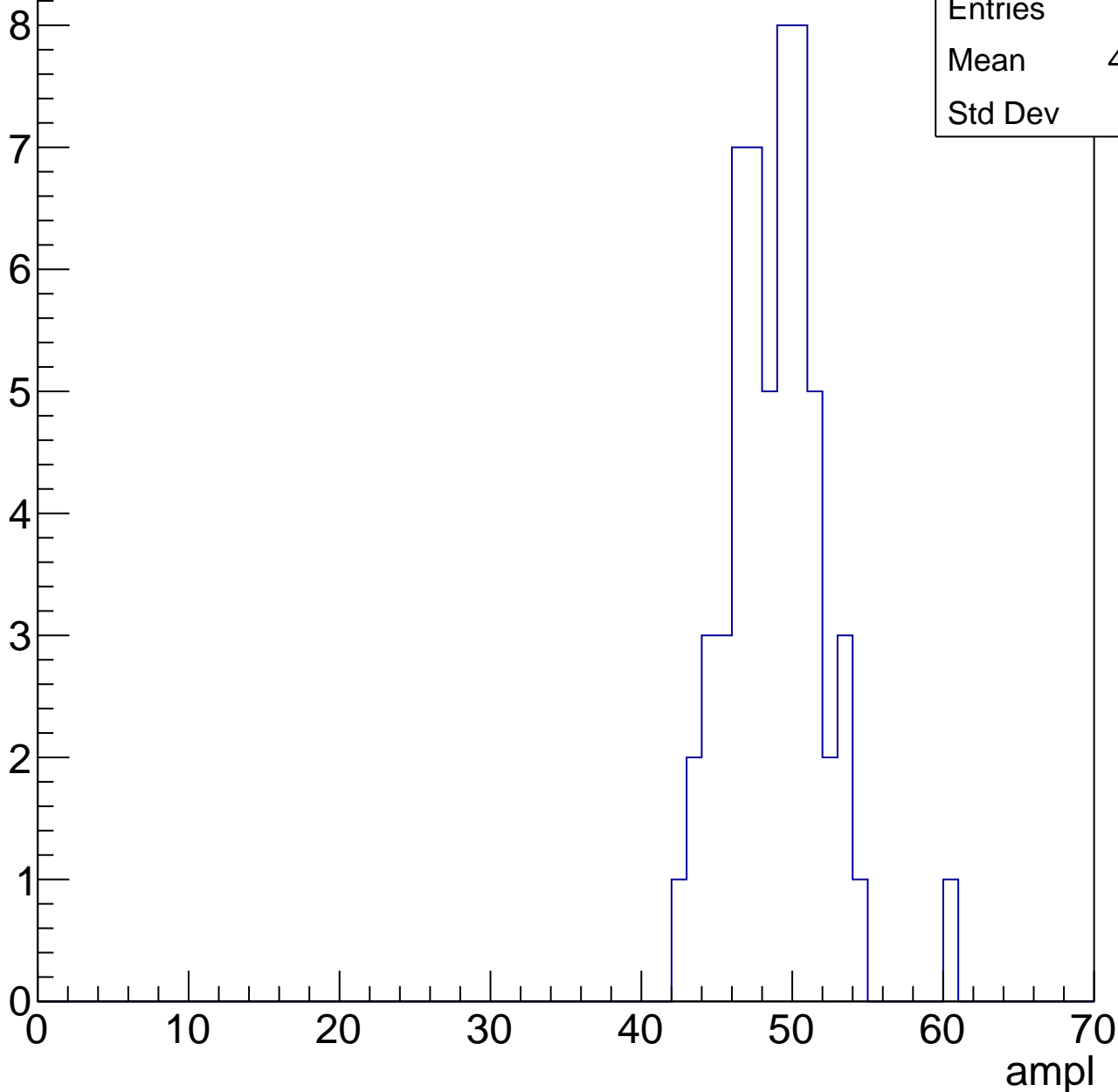


B0L102S, U5-ch67, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

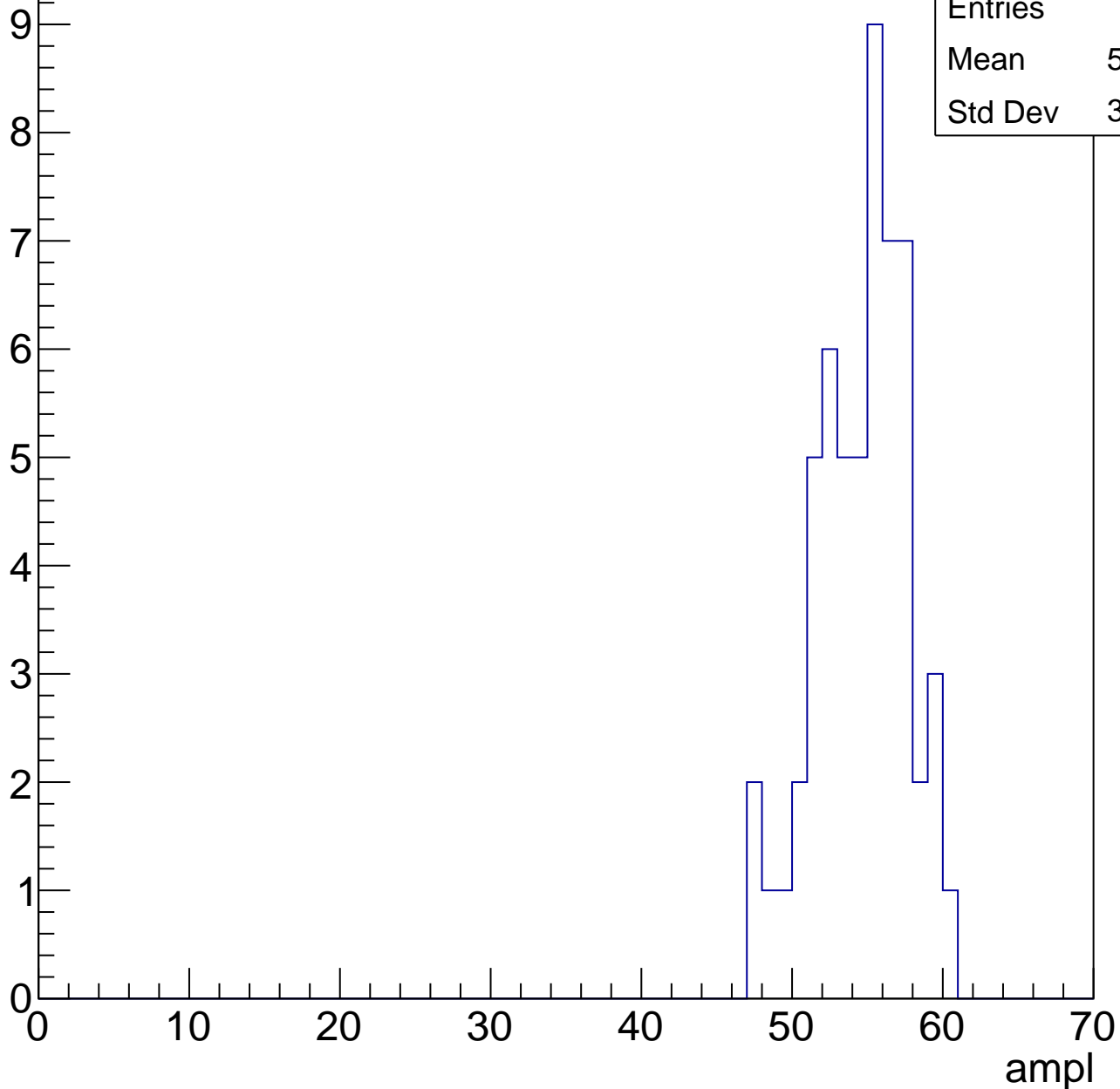
Entries	56
Mean	48.39
Std Dev	3.16



B0L102S, U5-ch67, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



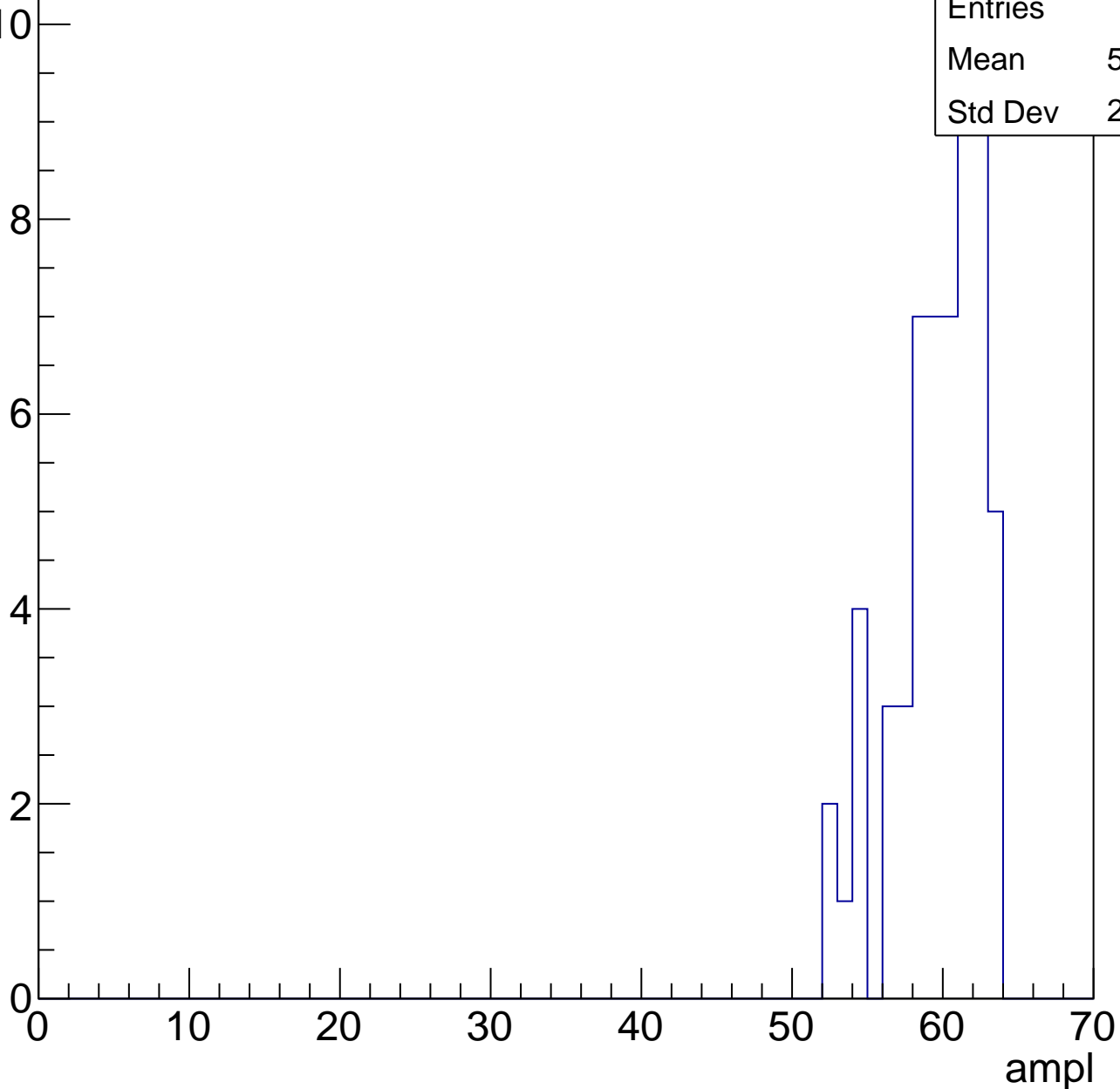
Entries	56
Mean	54.14
Std Dev	3.008

B0L102S, U5-ch67, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	59.22
Std Dev	2.907

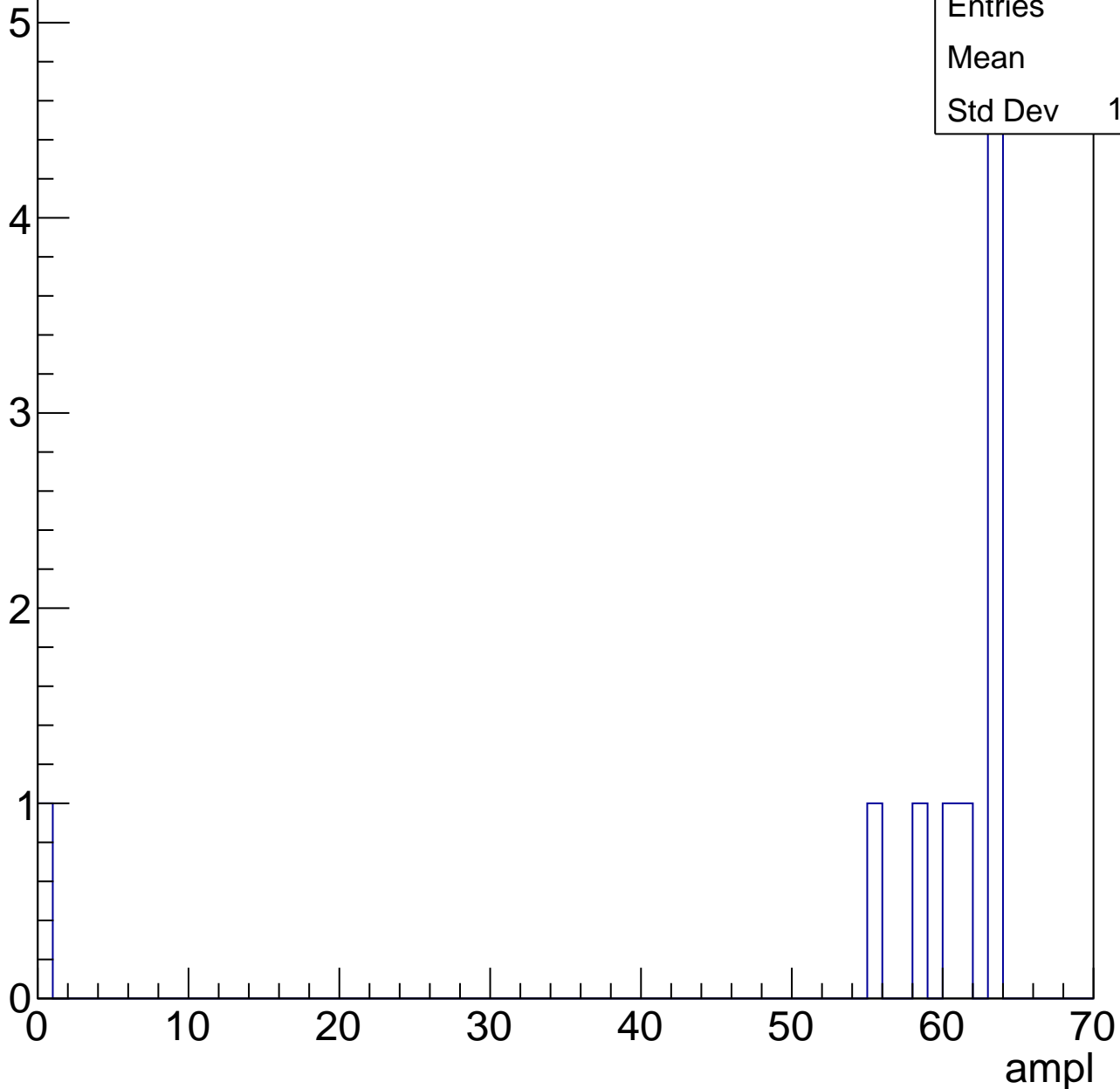


B0L102S, U5-ch67, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	10
Mean	54.9
Std Dev	18.48



B0L102S, U5-ch67, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch68, adc0

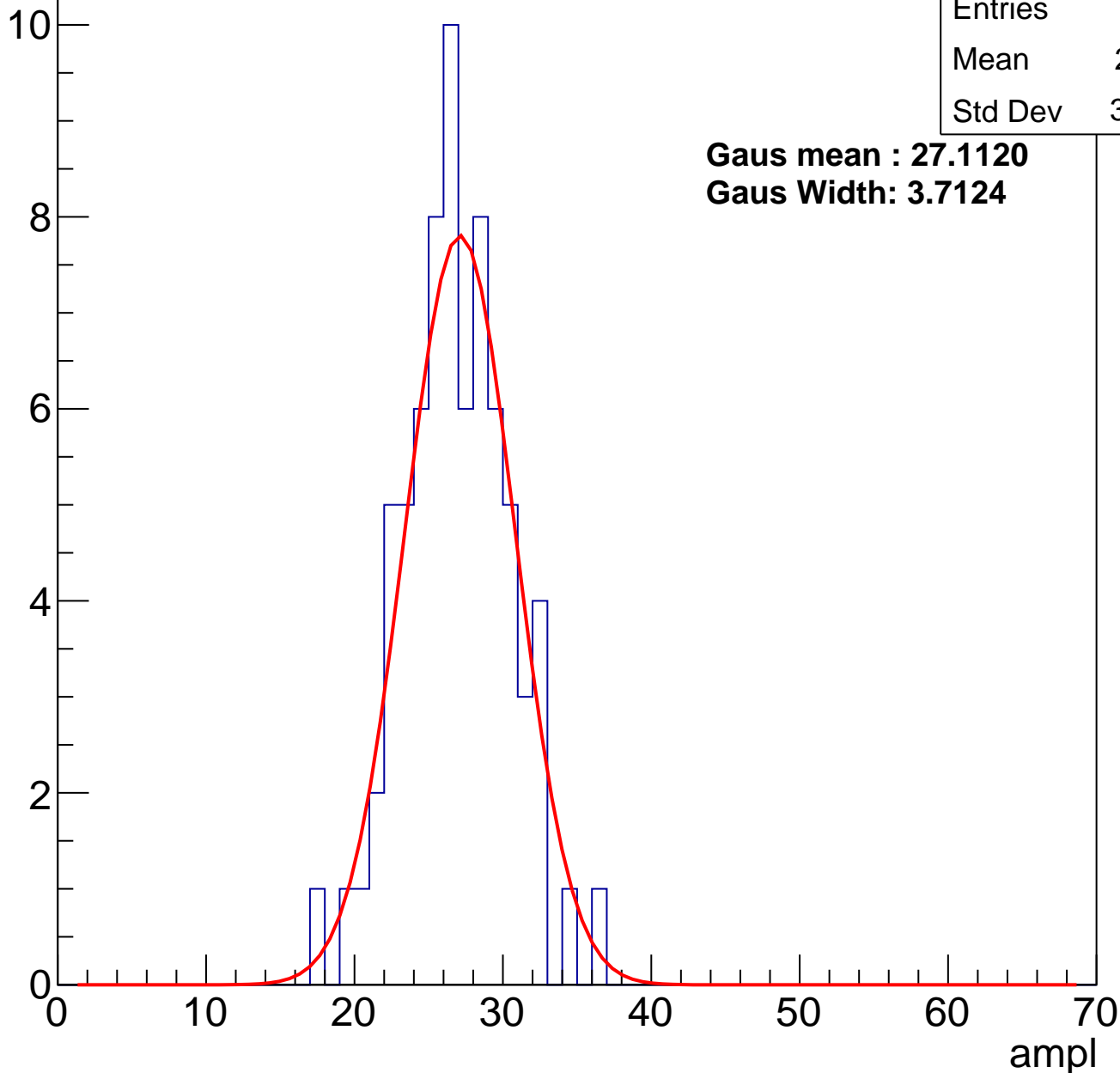
calib_packv5_042523_0143.root, FC#12, port B1

Entries	73
Mean	26.41
Std Dev	3.542

Gaus mean : 27.1120

Gaus Width: 3.7124

Entry



B0L102S, U5-ch68, adc1

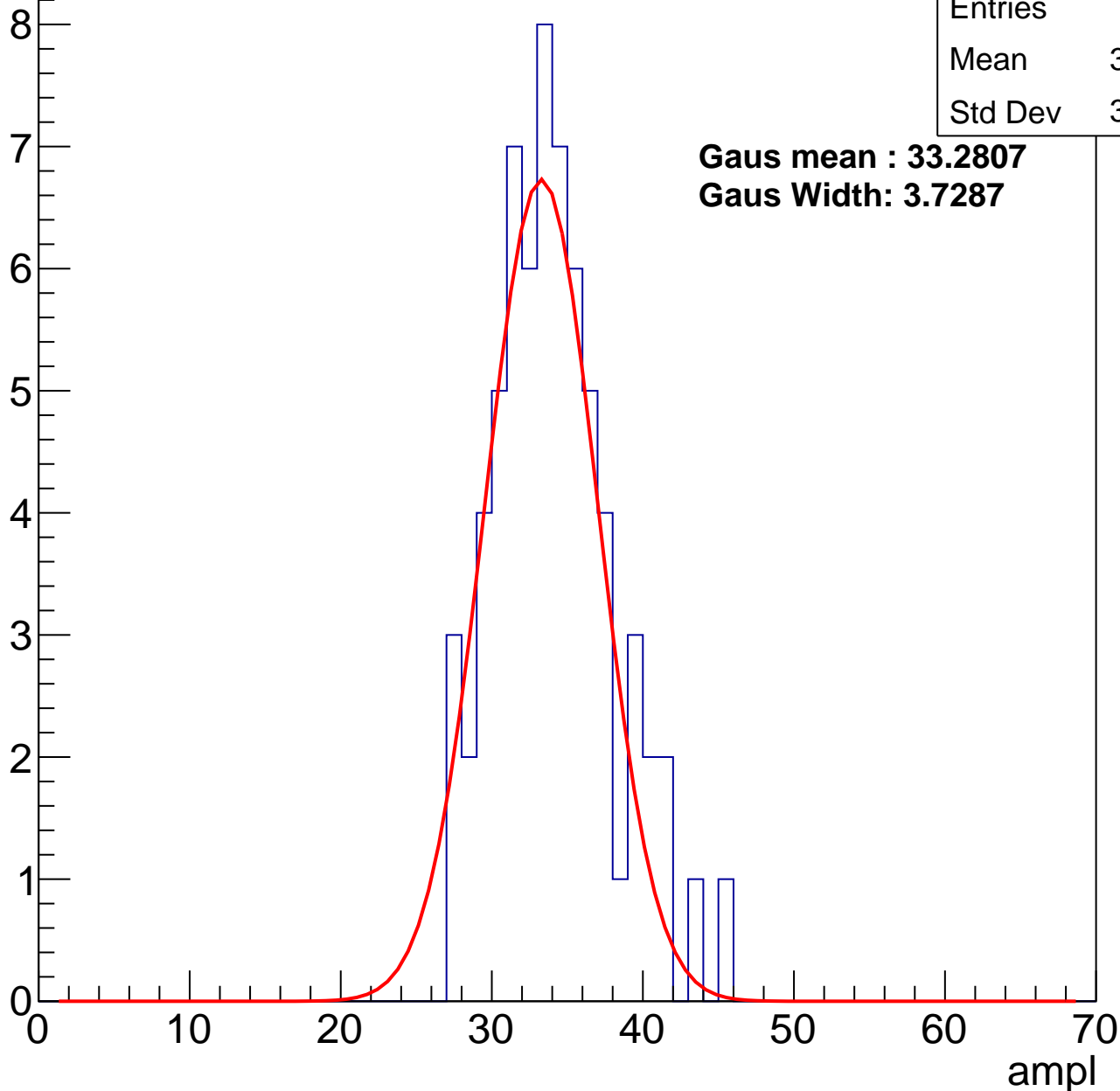
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	33.69
Std Dev	3.887

Gaus mean : 33.2807

Gaus Width: 3.7287



B0L102S, U5-ch68, adc2

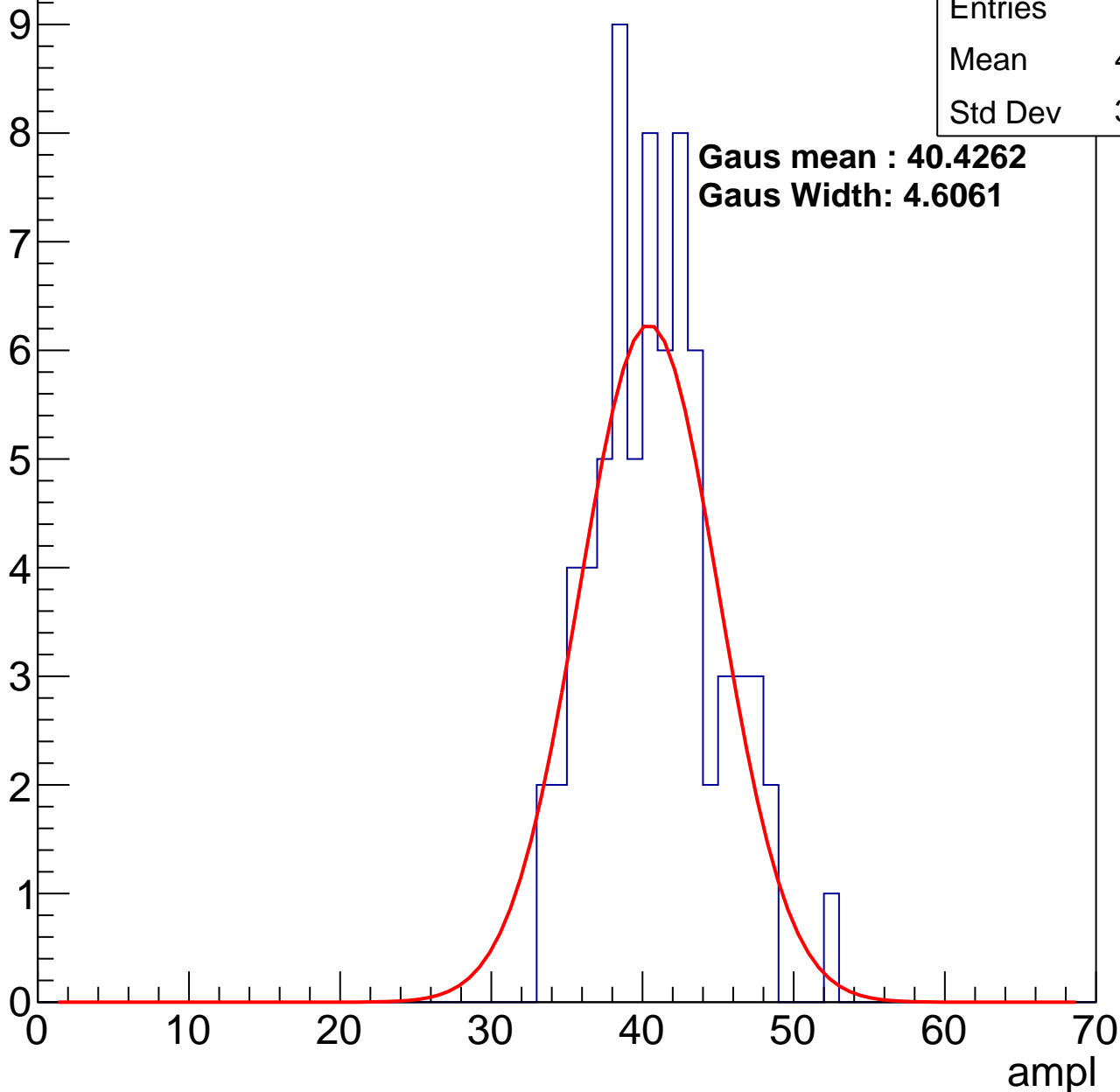
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	40.41
Std Dev	3.941

Gaus mean : 40.4262

Gaus Width: 4.6061

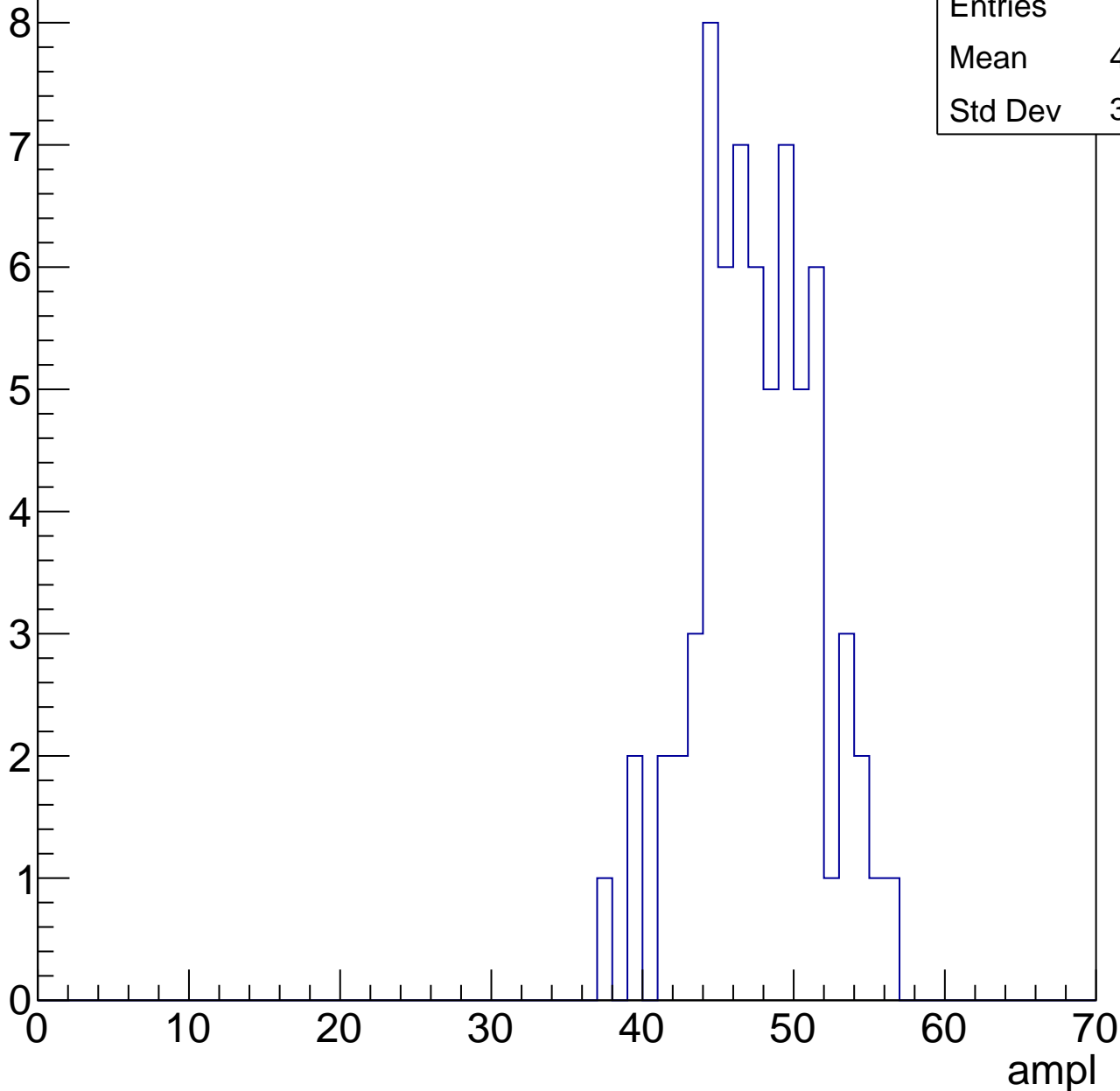


B0L102S, U5-ch68, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	47.13
Std Dev	3.929

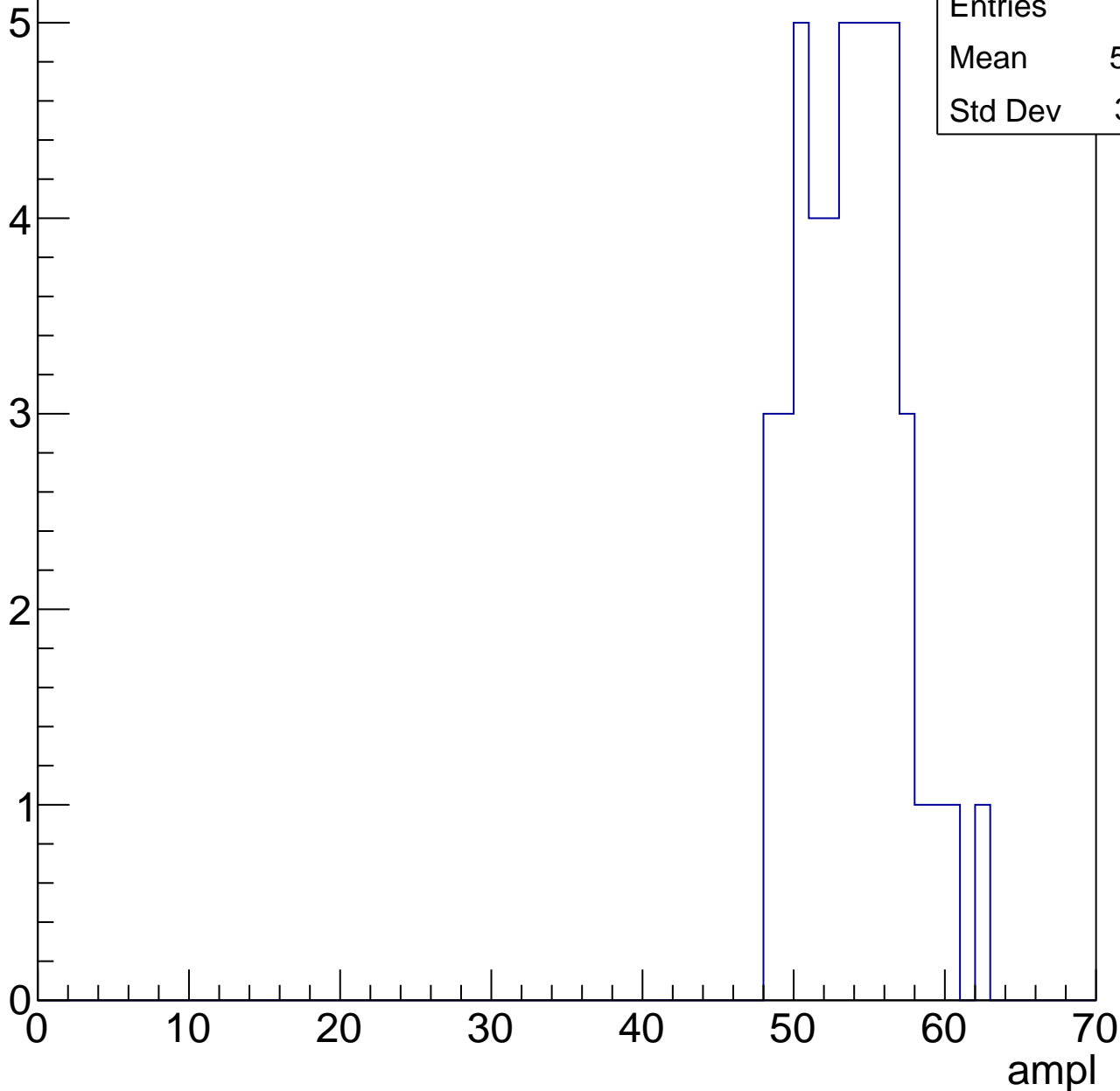


B0L102S, U5-ch68, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	53.33
Std Dev	3.271

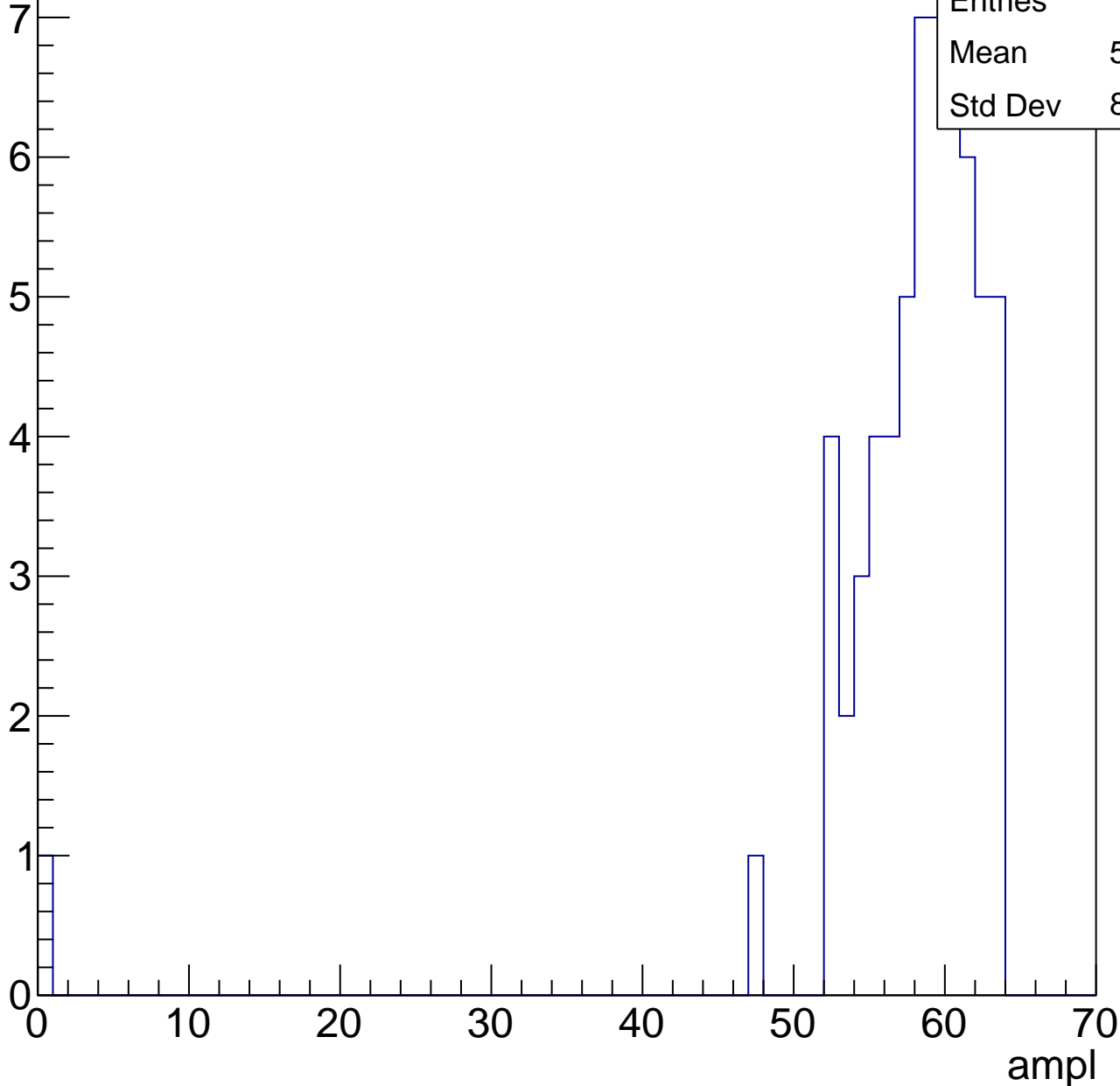


B0L102S, U5-ch68, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	57.08
Std Dev	8.125

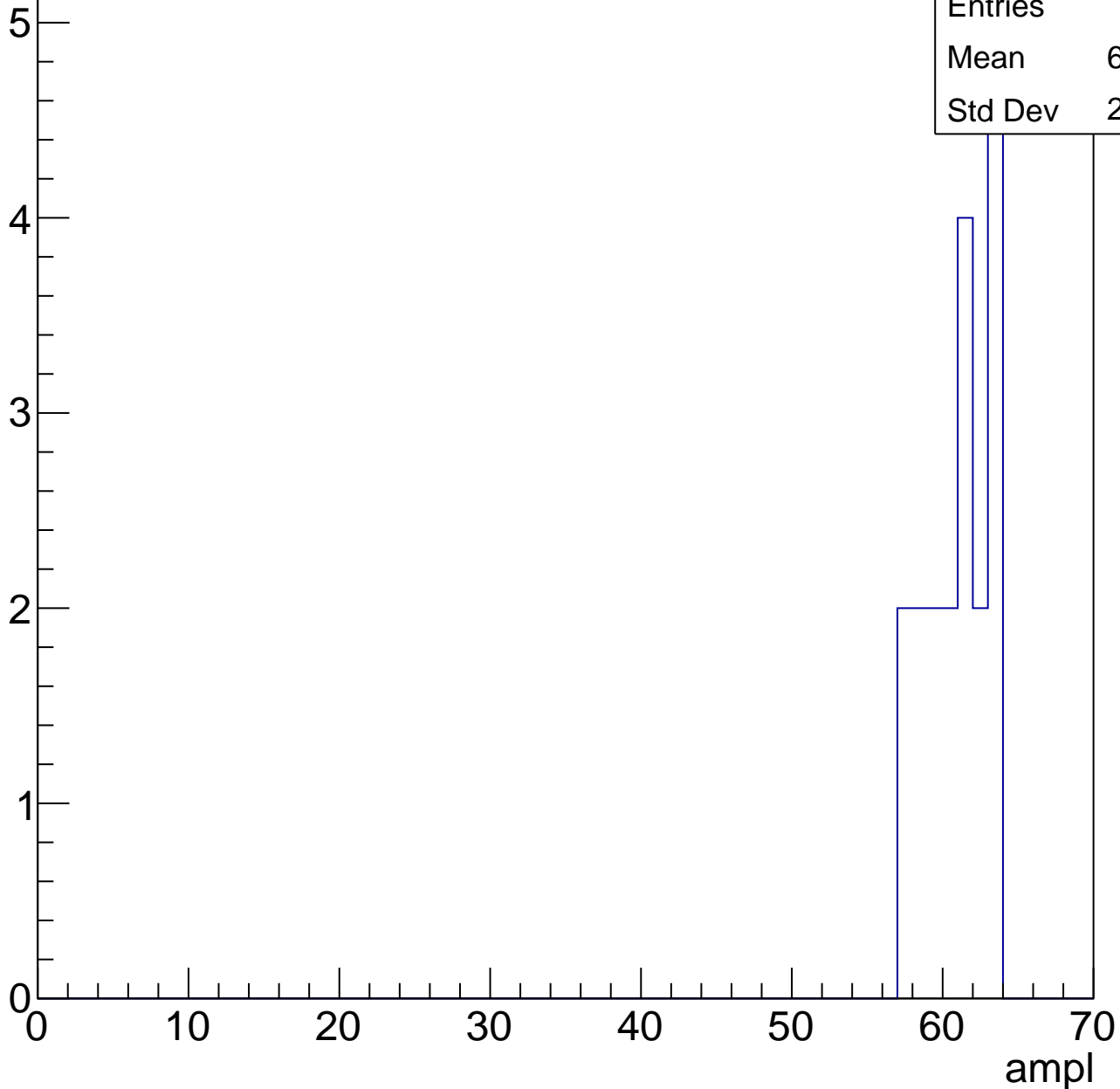


B0L102S, U5-ch68, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	19
Mean	60.58
Std Dev	2.034



B0L102S, U5-ch68, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



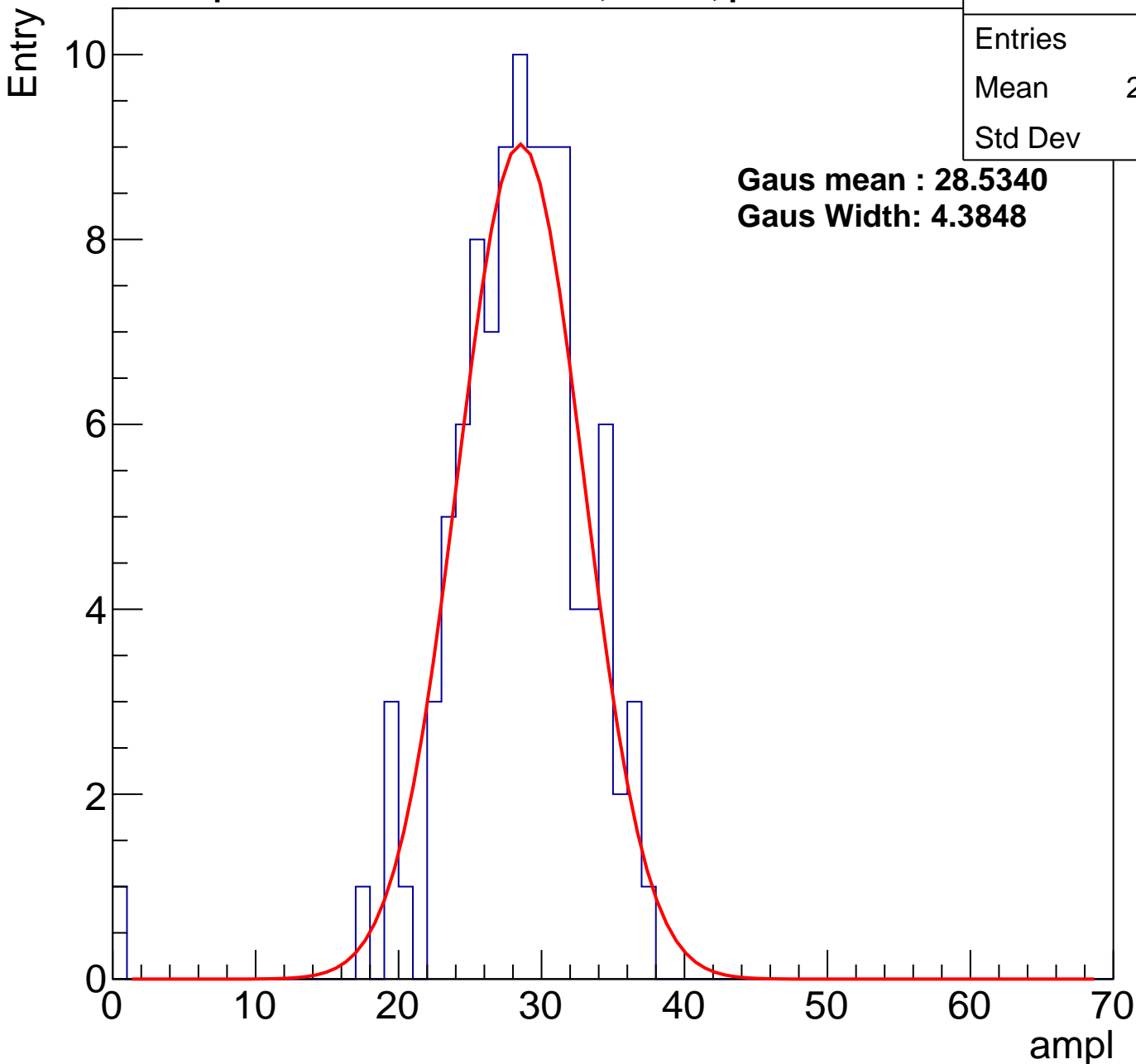
B0L102S, U5-ch69, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	101
Mean	27.85
Std Dev	5

Gaus mean : 28.5340

Gaus Width: 4.3848



B0L102S, U5-ch69, adc1

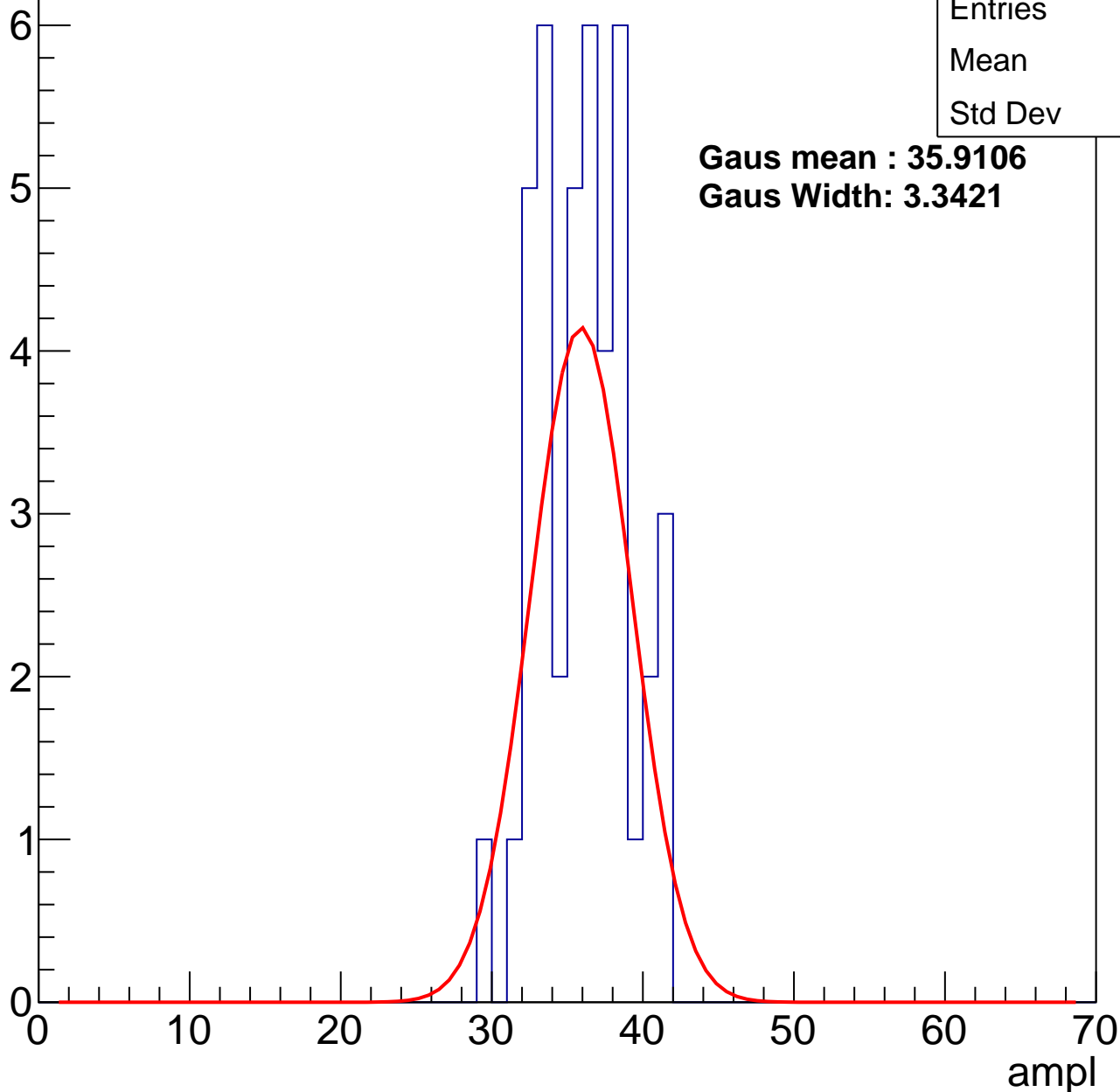
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	35.6
Std Dev	2.92

Gaus mean : 35.9106

Gaus Width: 3.3421



B0L102S, U5-ch69, adc2

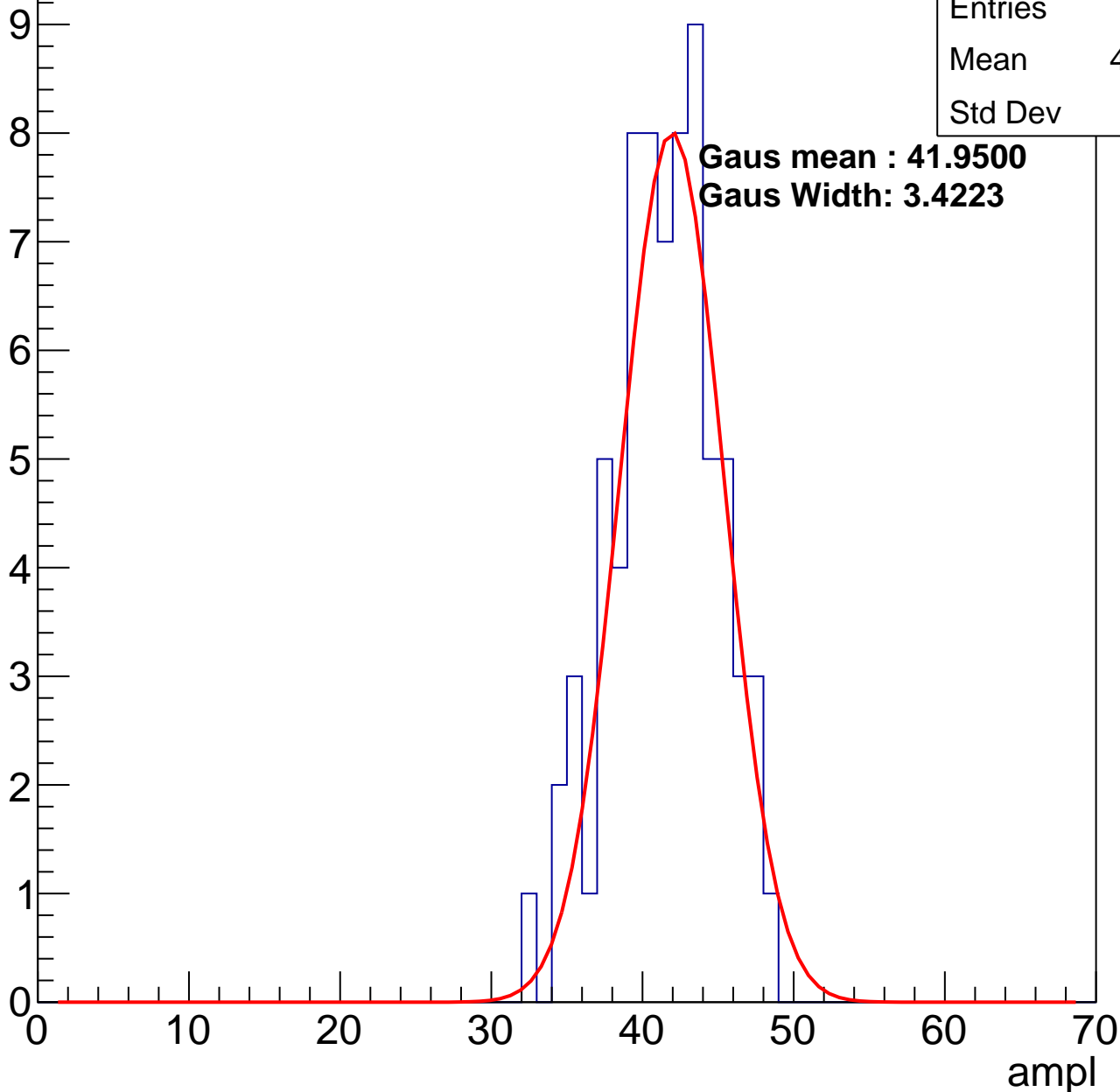
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	40.99
Std Dev	3.45

Gaus mean : 41.9500

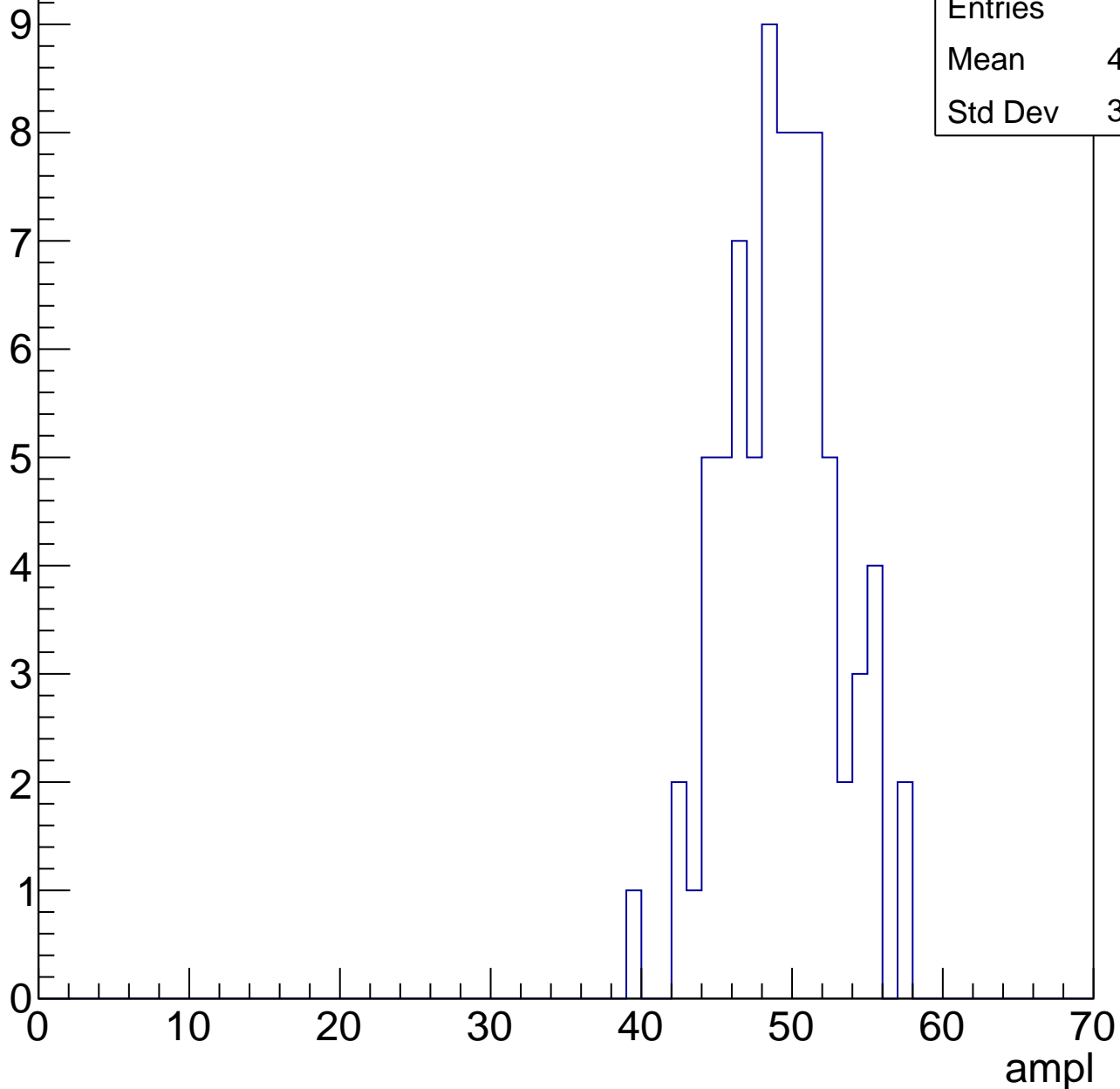
Gaus Width: 3.4223



B0L102S, U5-ch69, adc3

calib_packv5_042523_0143.root, FC#12, port B1

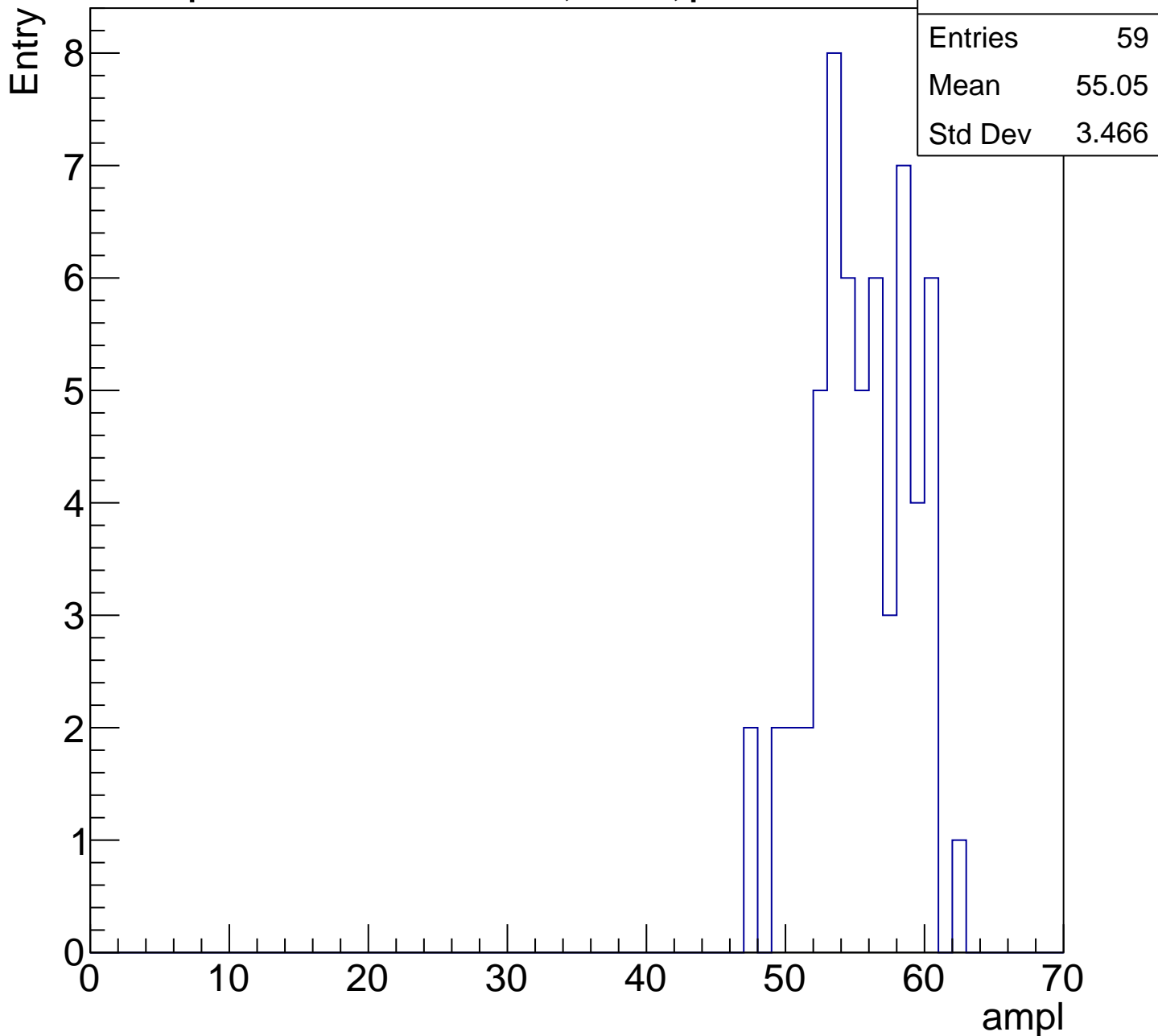
Entry



Entries	75
Mean	48.83
Std Dev	3.638

B0L102S, U5-ch69, adc4

calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch69, adc5

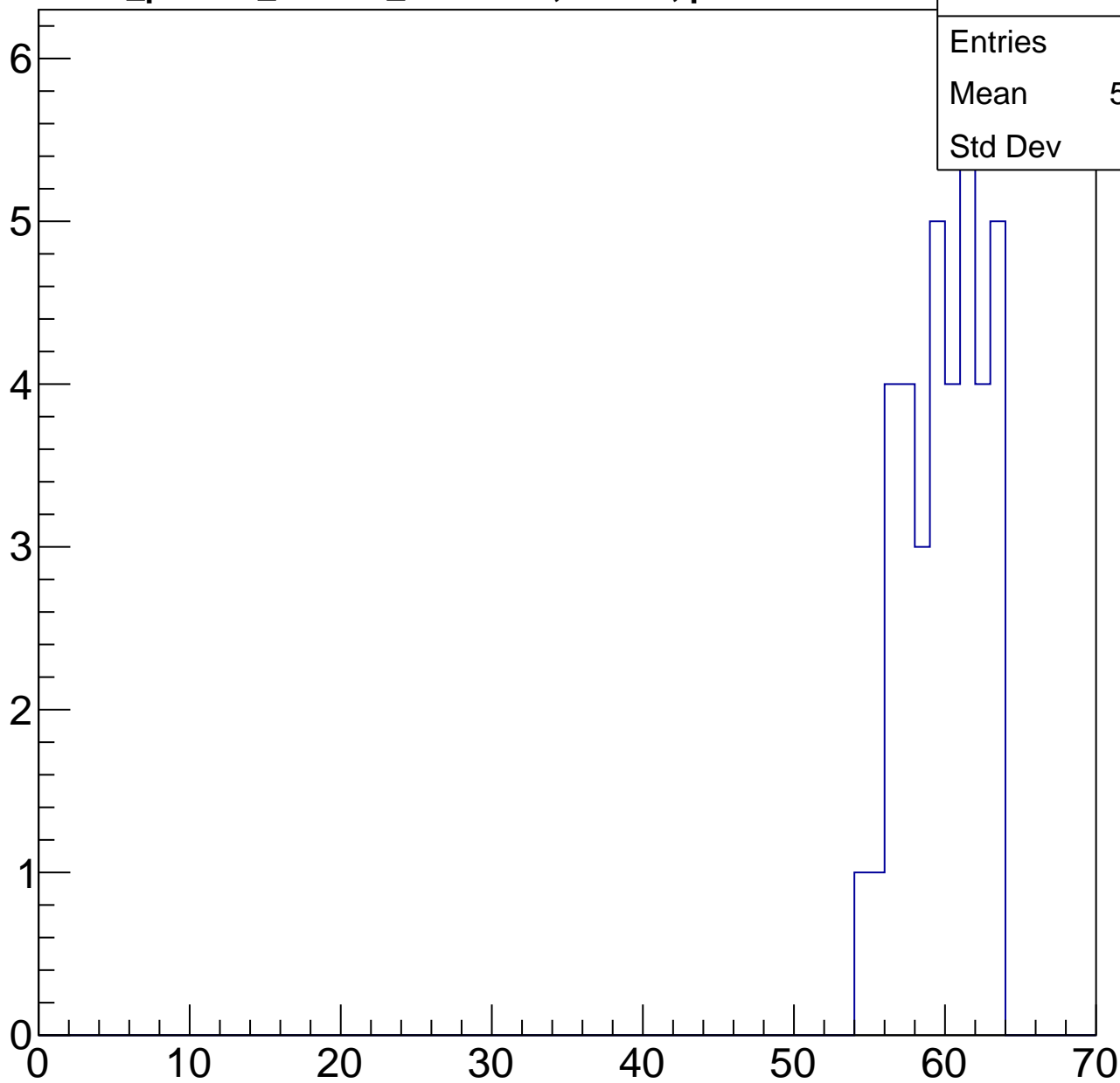
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

Entries	37
Mean	59.43
Std Dev	2.51

ampl

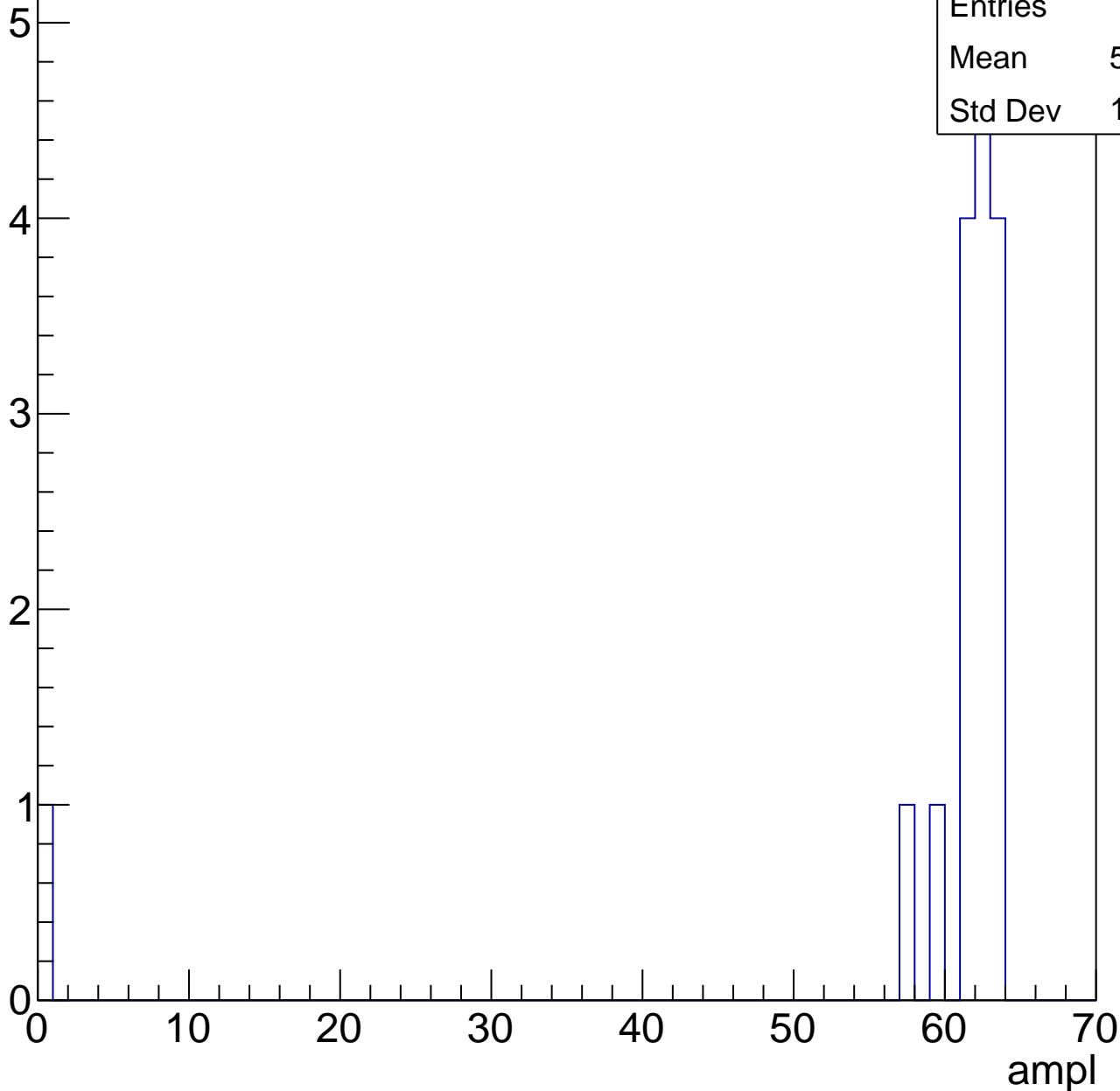


B0L102S, U5-ch69, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

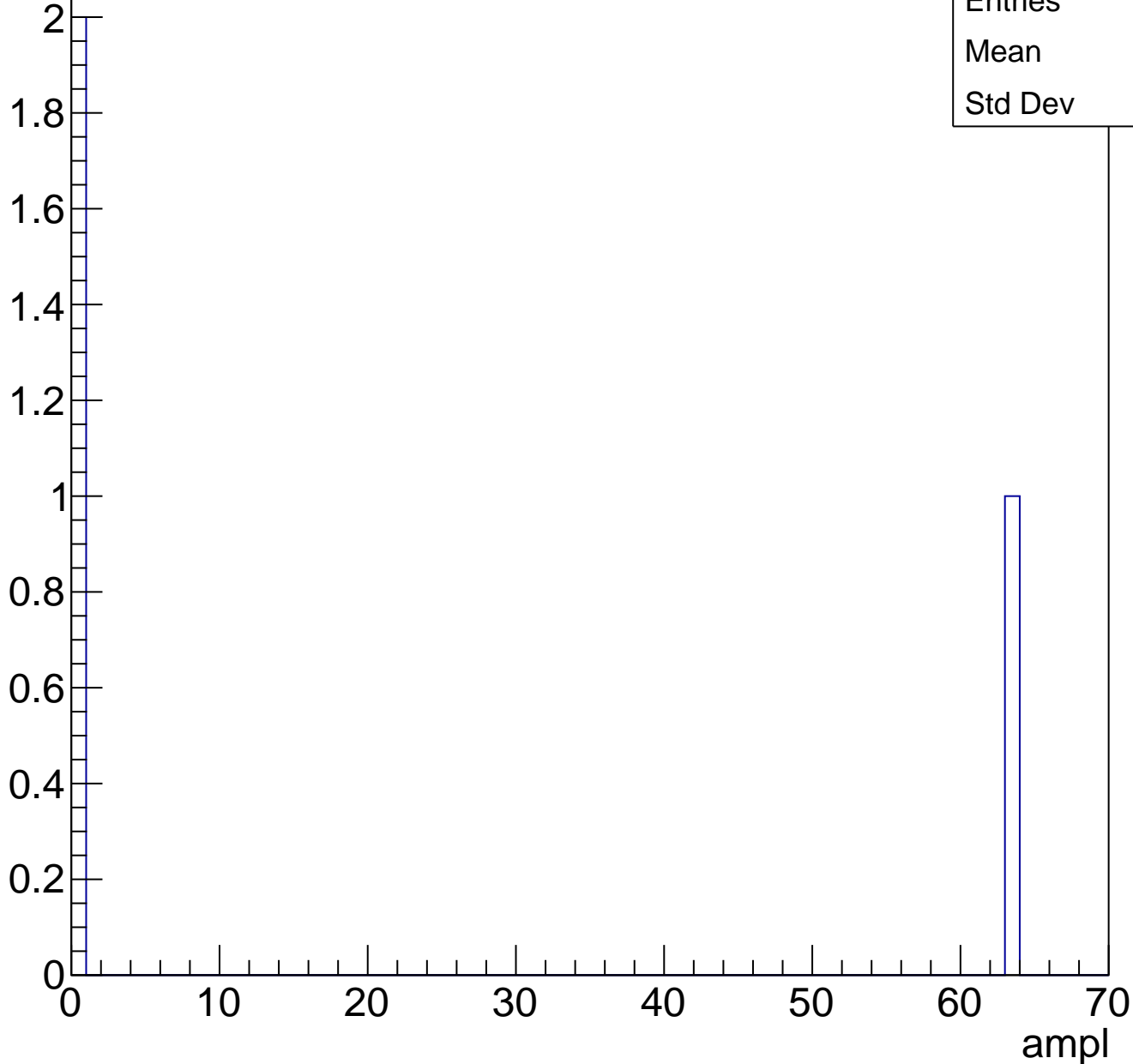
Entries	16
Mean	57.62
Std Dev	14.96



B0L102S, U5-ch69, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch70, adc0

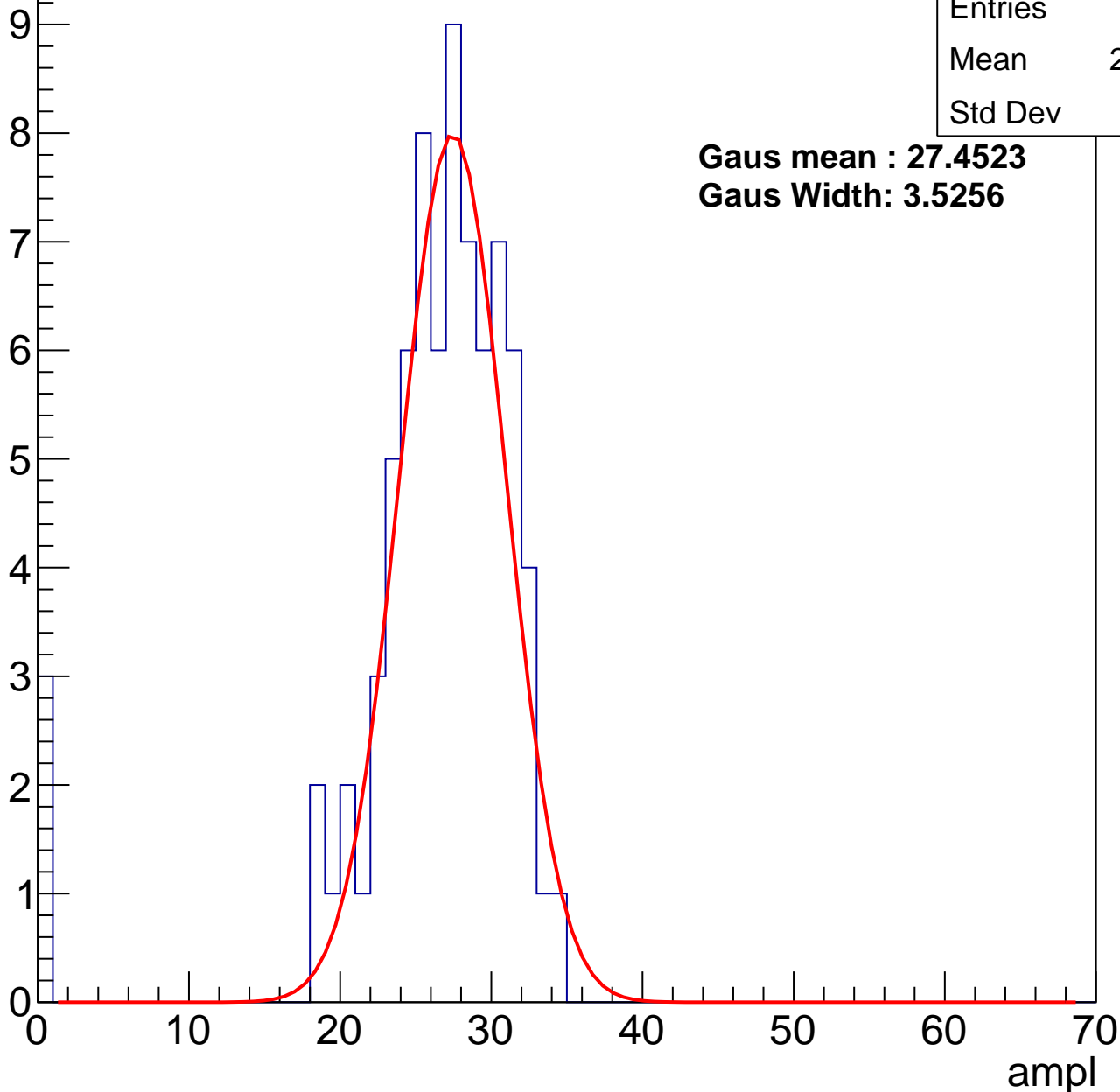
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	25.65
Std Dev	6.23

Gaus mean : 27.4523

Gaus Width: 3.5256



B0L102S, U5-ch70, adc1

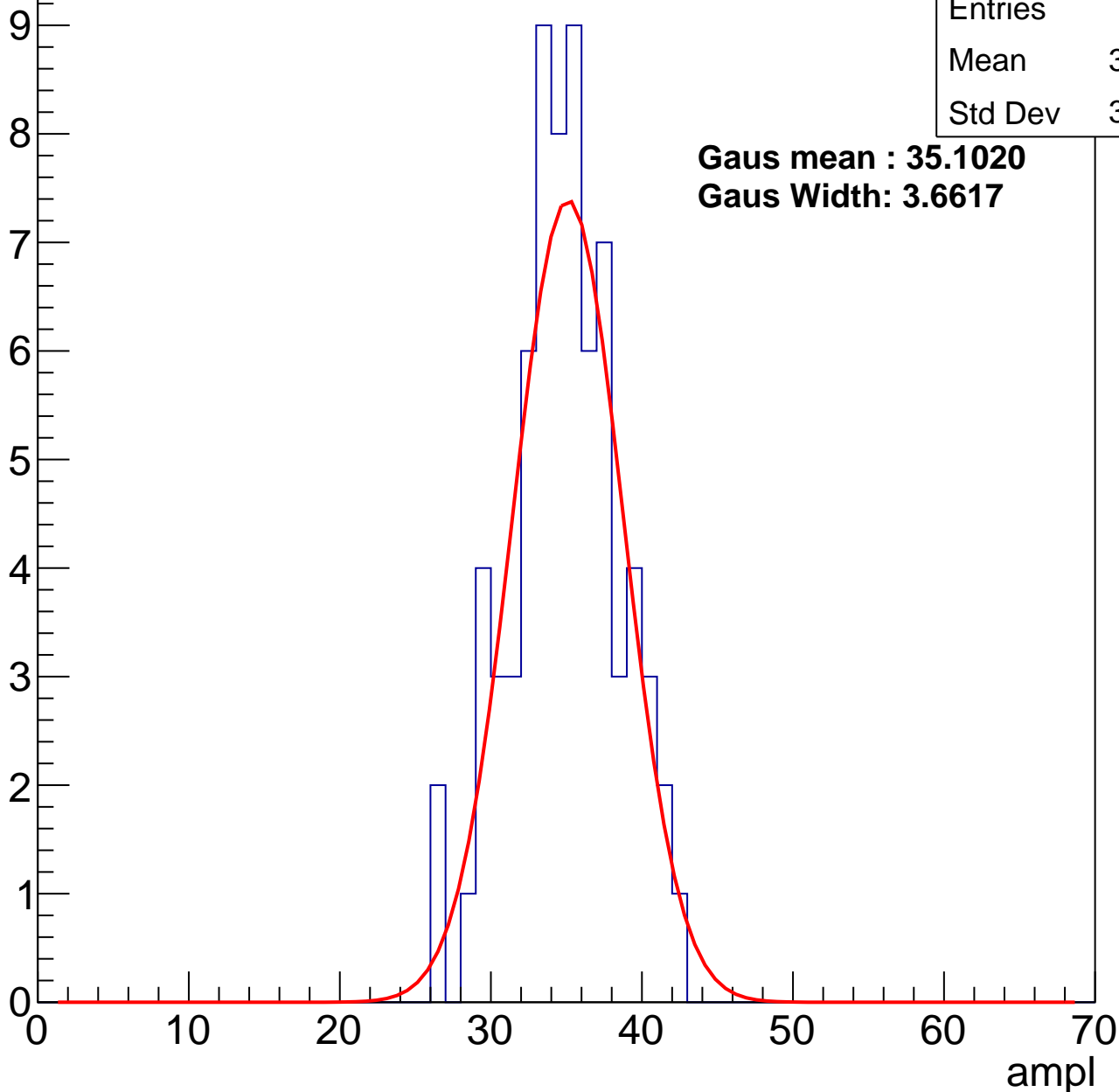
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	34.42
Std Dev	3.507

Gaus mean : 35.1020

Gaus Width: 3.6617



B0L102S, U5-ch70, adc2

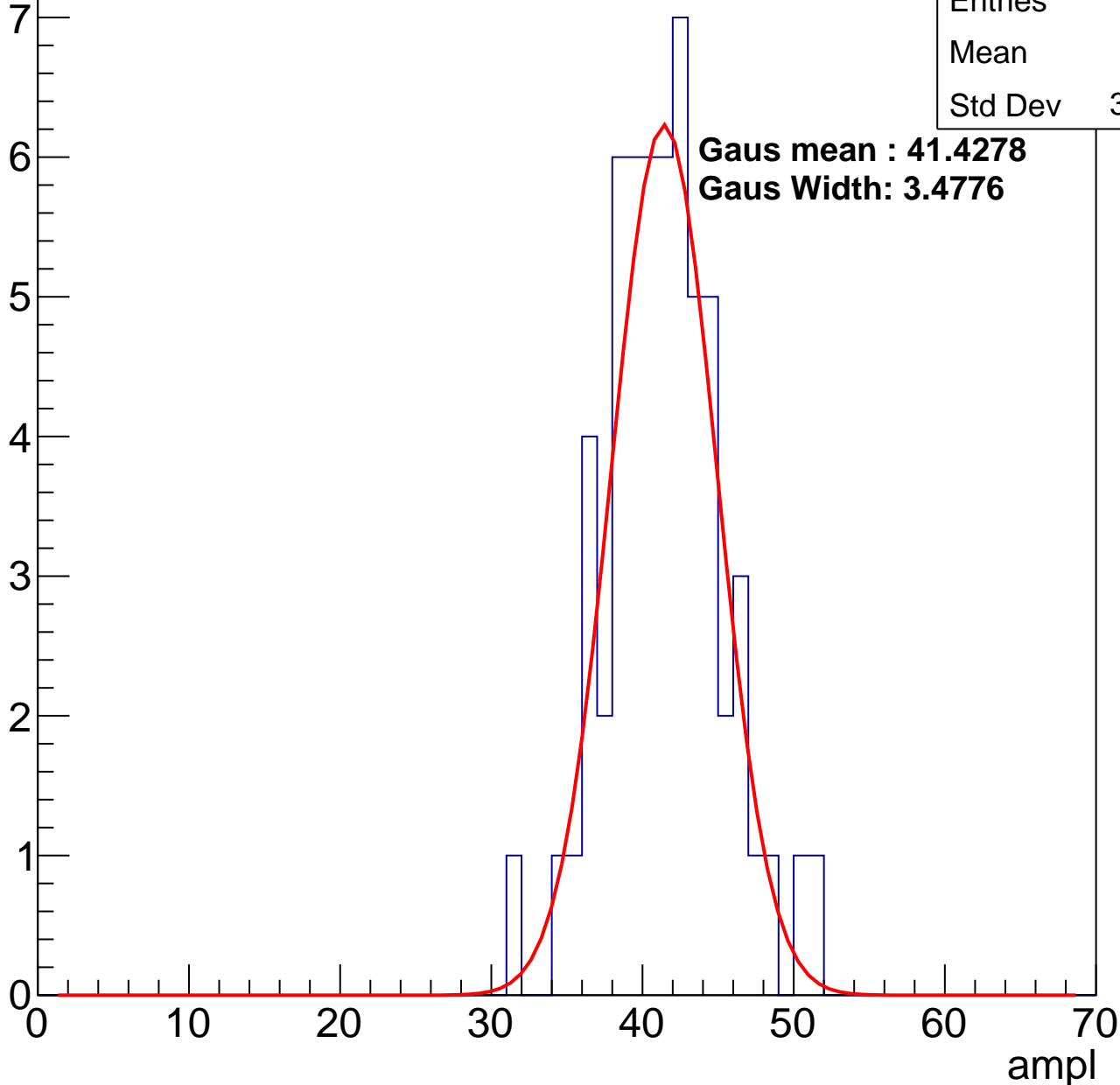
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	41
Std Dev	3.787

Gaus mean : 41.4278

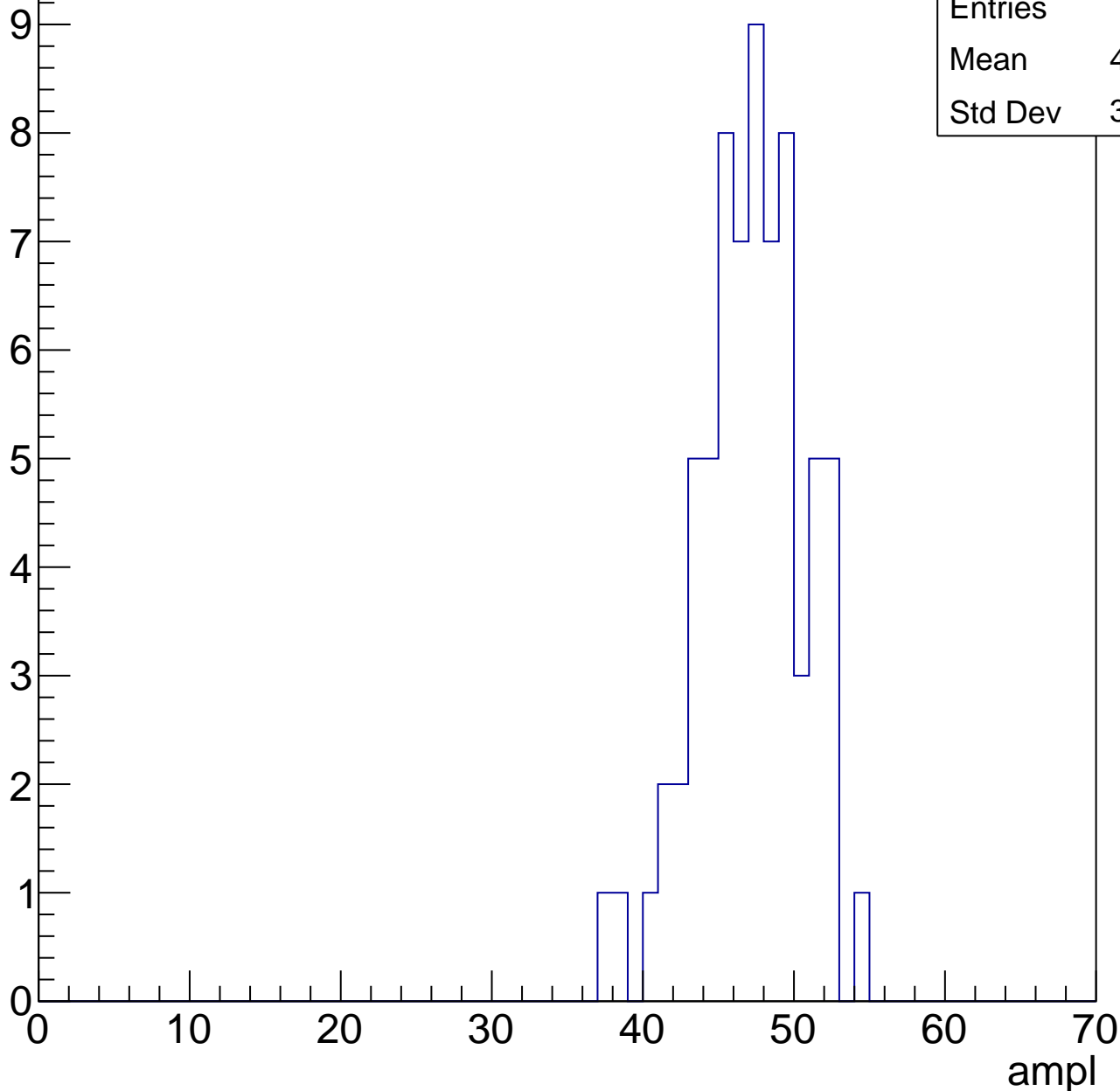
Gaus Width: 3.4776



B0L102S, U5-ch70, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

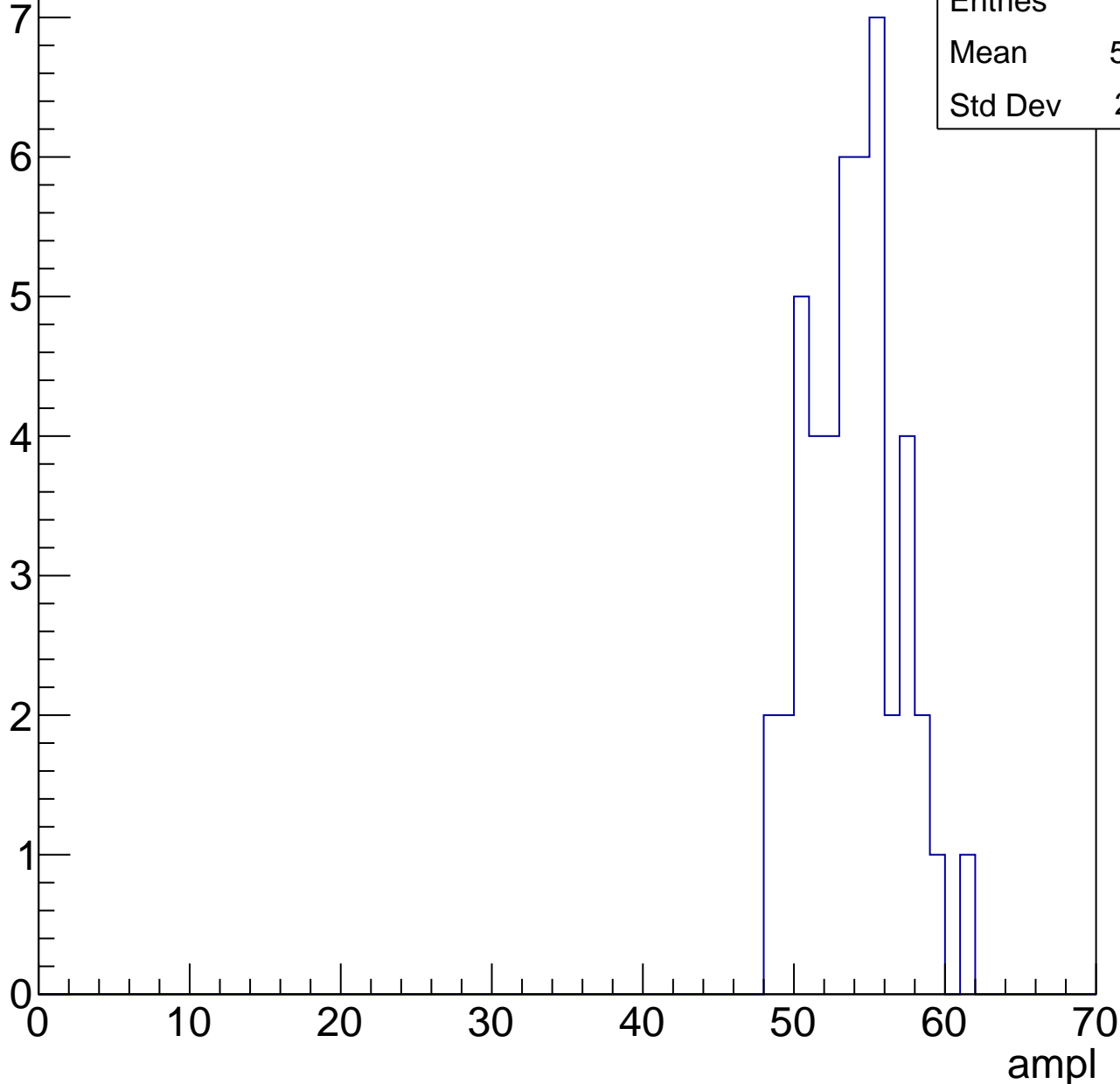


Entries	70
Mean	46.69
Std Dev	3.442

B0L102S, U5-ch70, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

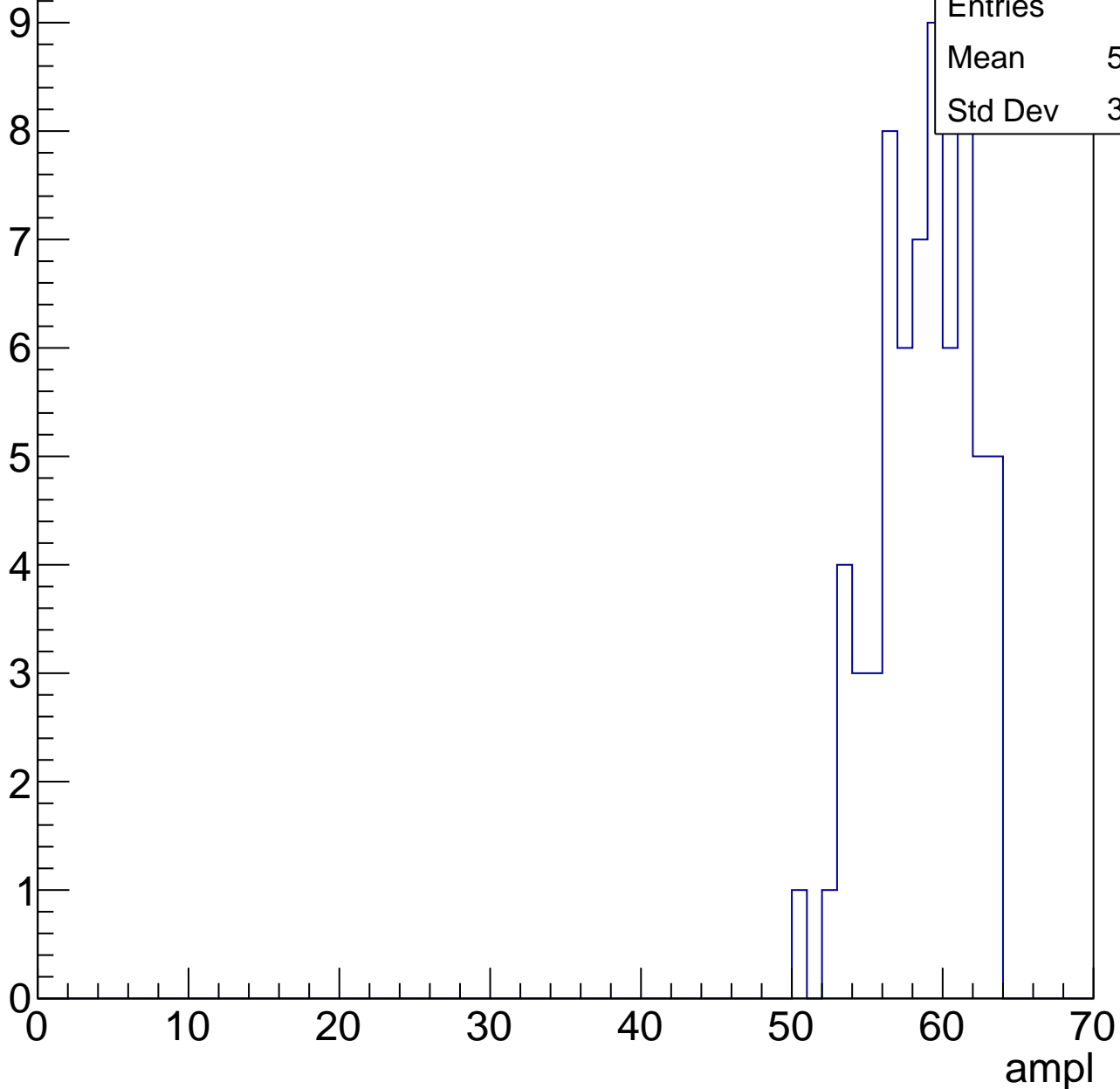


Entries	46
Mean	53.46
Std Dev	2.961

B0L102S, U5-ch70, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

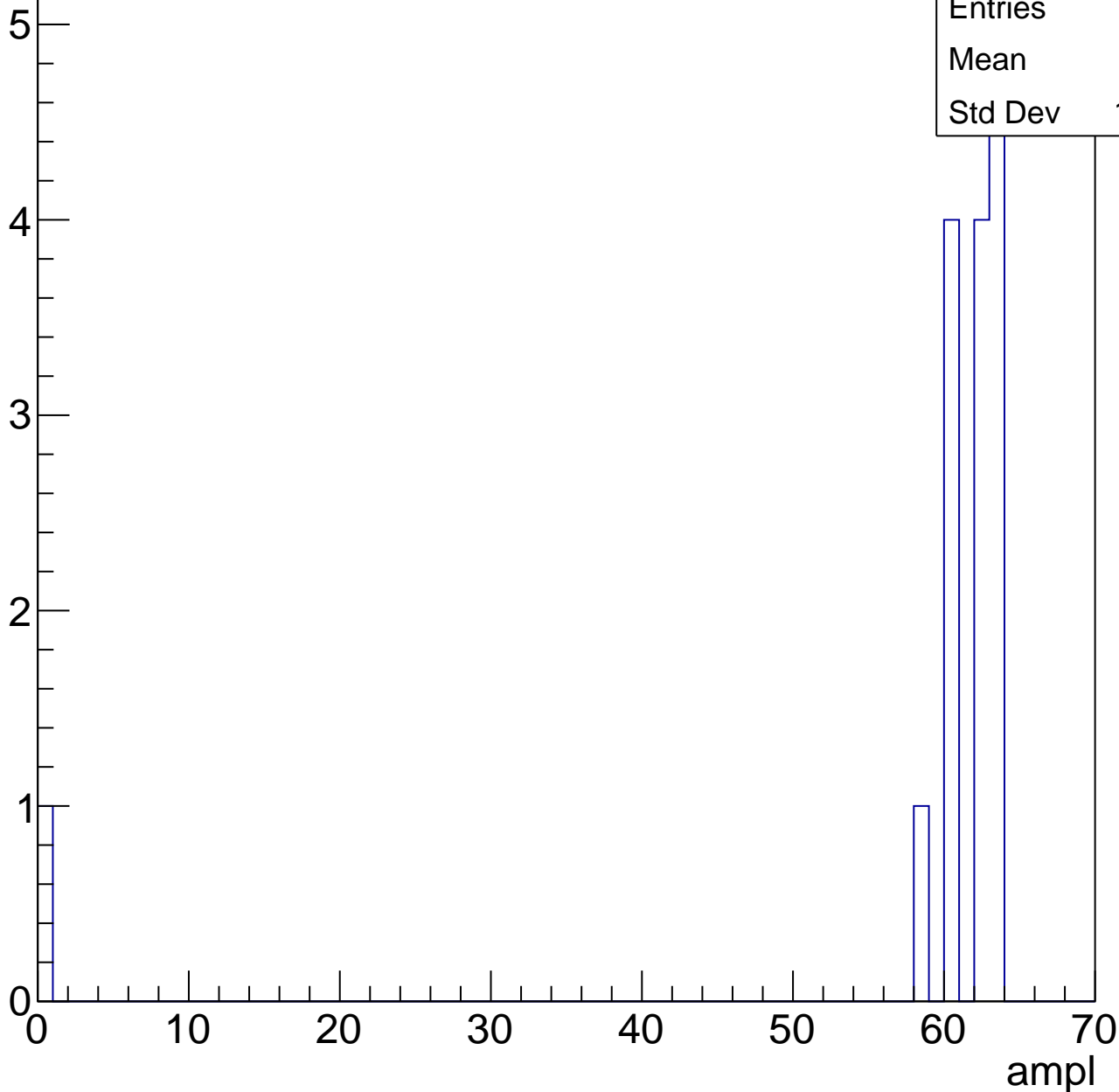


B0L102S, U5-ch70, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.4
Std Dev	15.41



B0L102S, U5-ch70, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch71, adc0

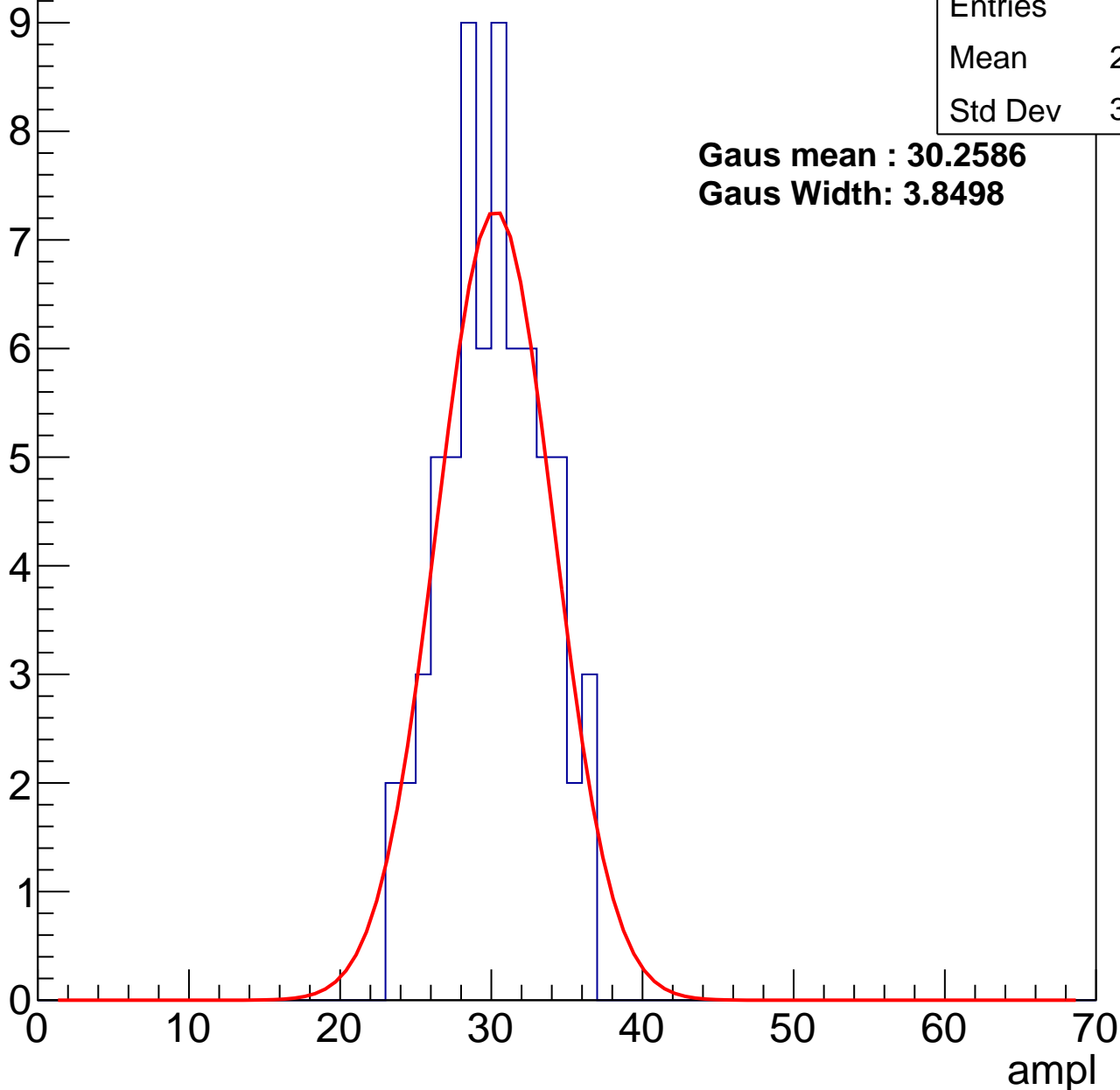
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	29.72
Std Dev	3.253

Gaus mean : 30.2586

Gaus Width: 3.8498



B0L102S, U5-ch71, adc1

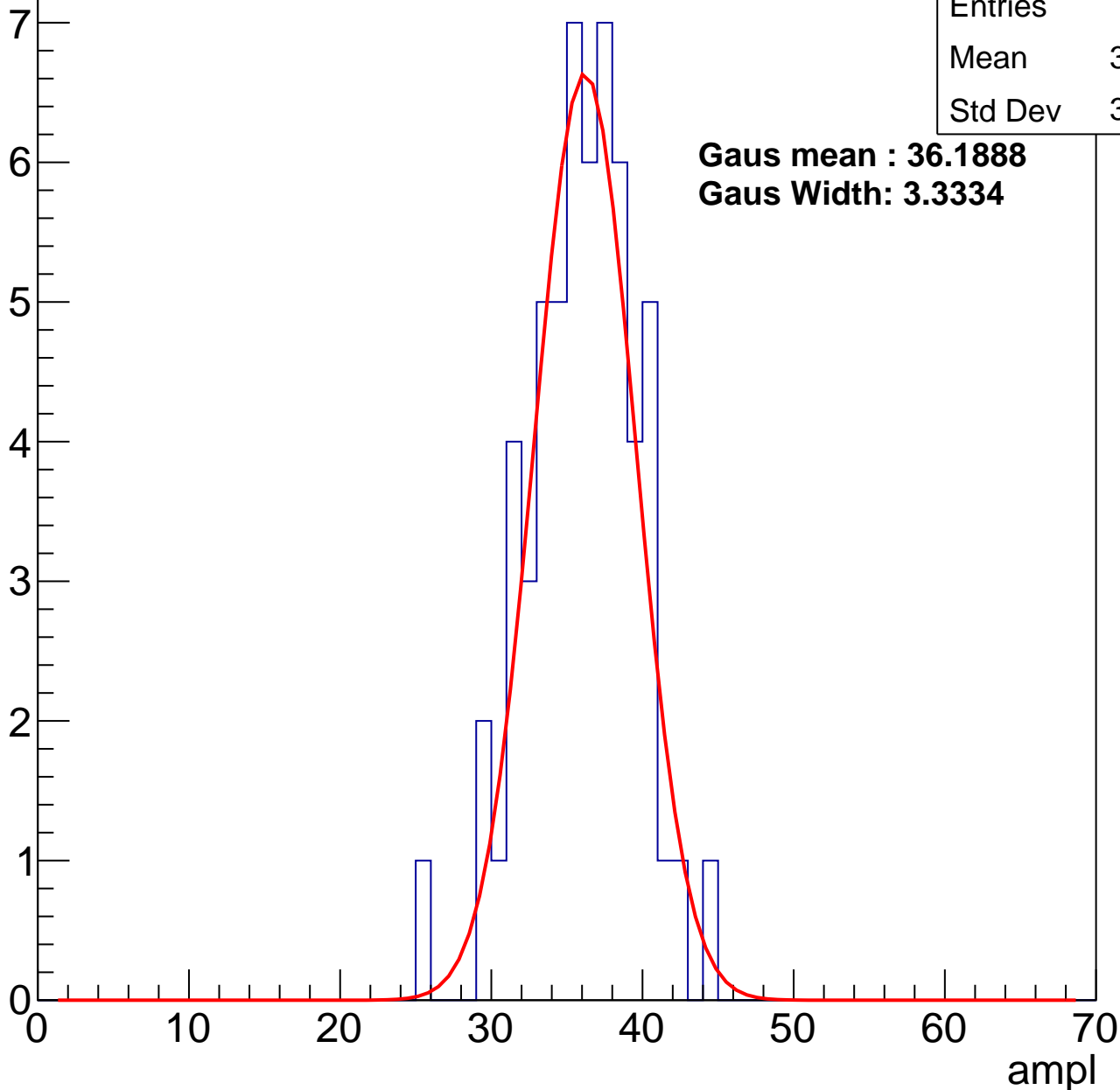
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	35.58
Std Dev	3.533

Gaus mean : 36.1888

Gaus Width: 3.3334



B0L102S, U5-ch71, adc2

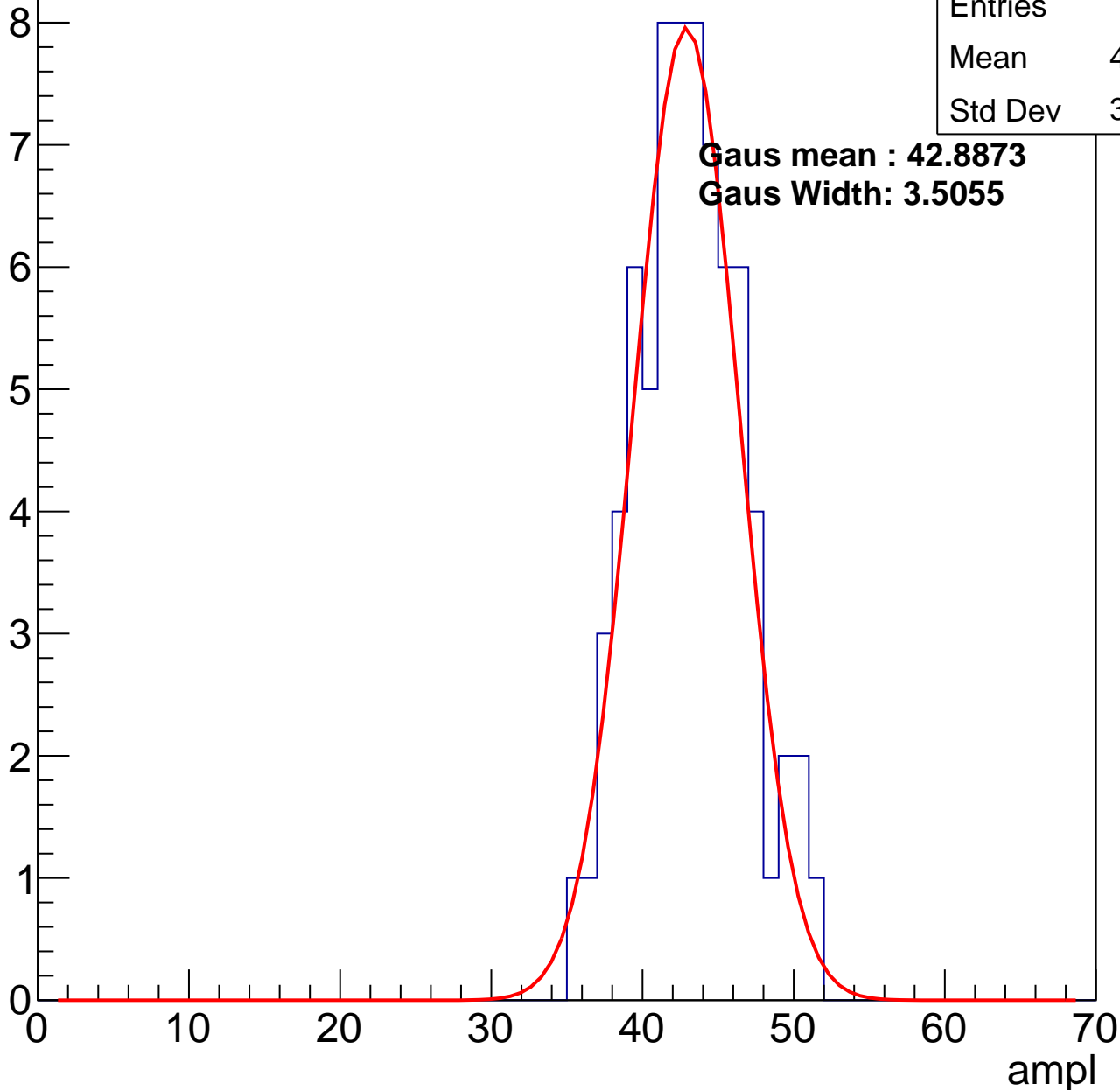
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	42.67
Std Dev	3.515

Gaus mean : 42.8873

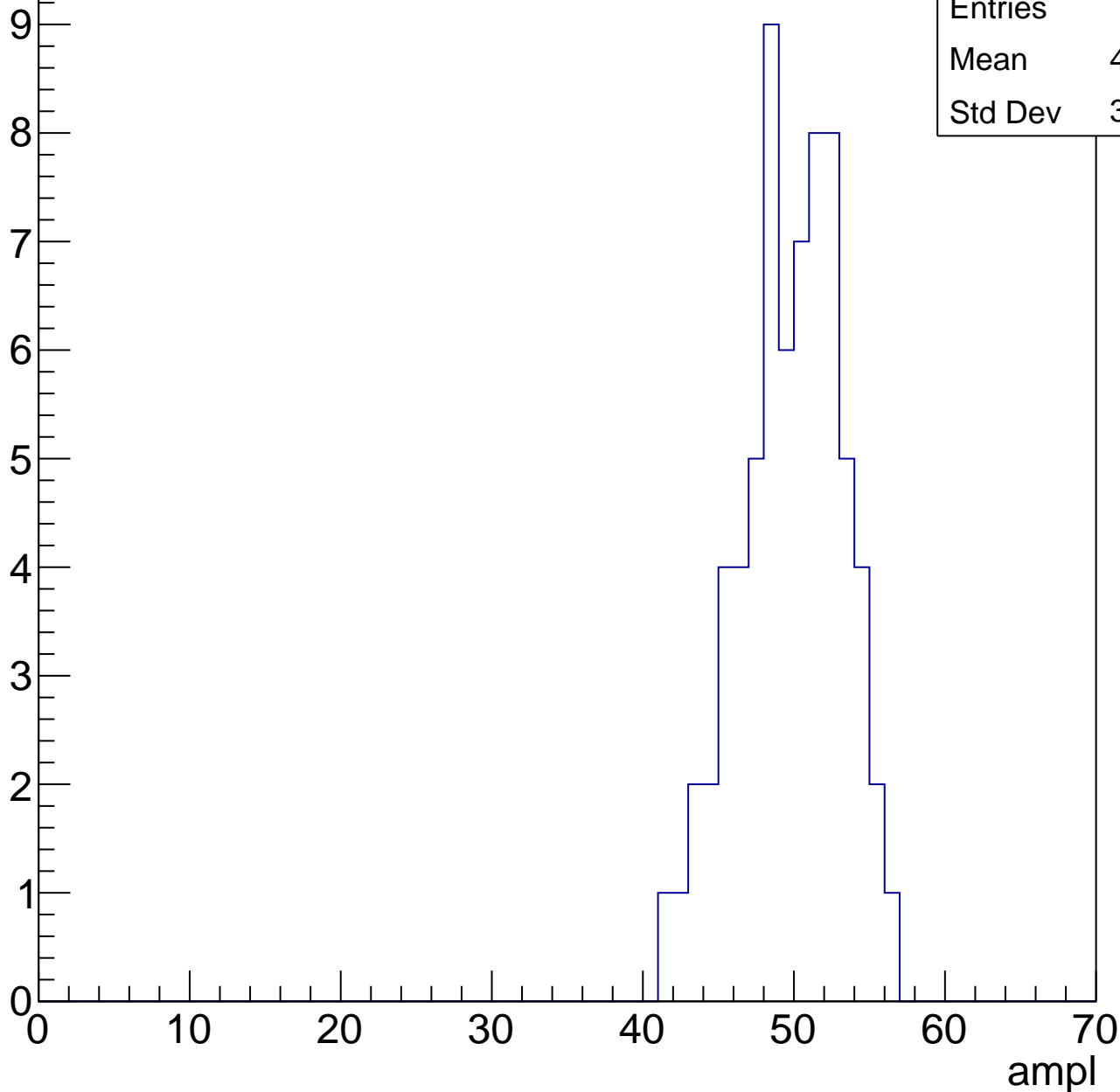
Gaus Width: 3.5055



B0L102S, U5-ch71, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

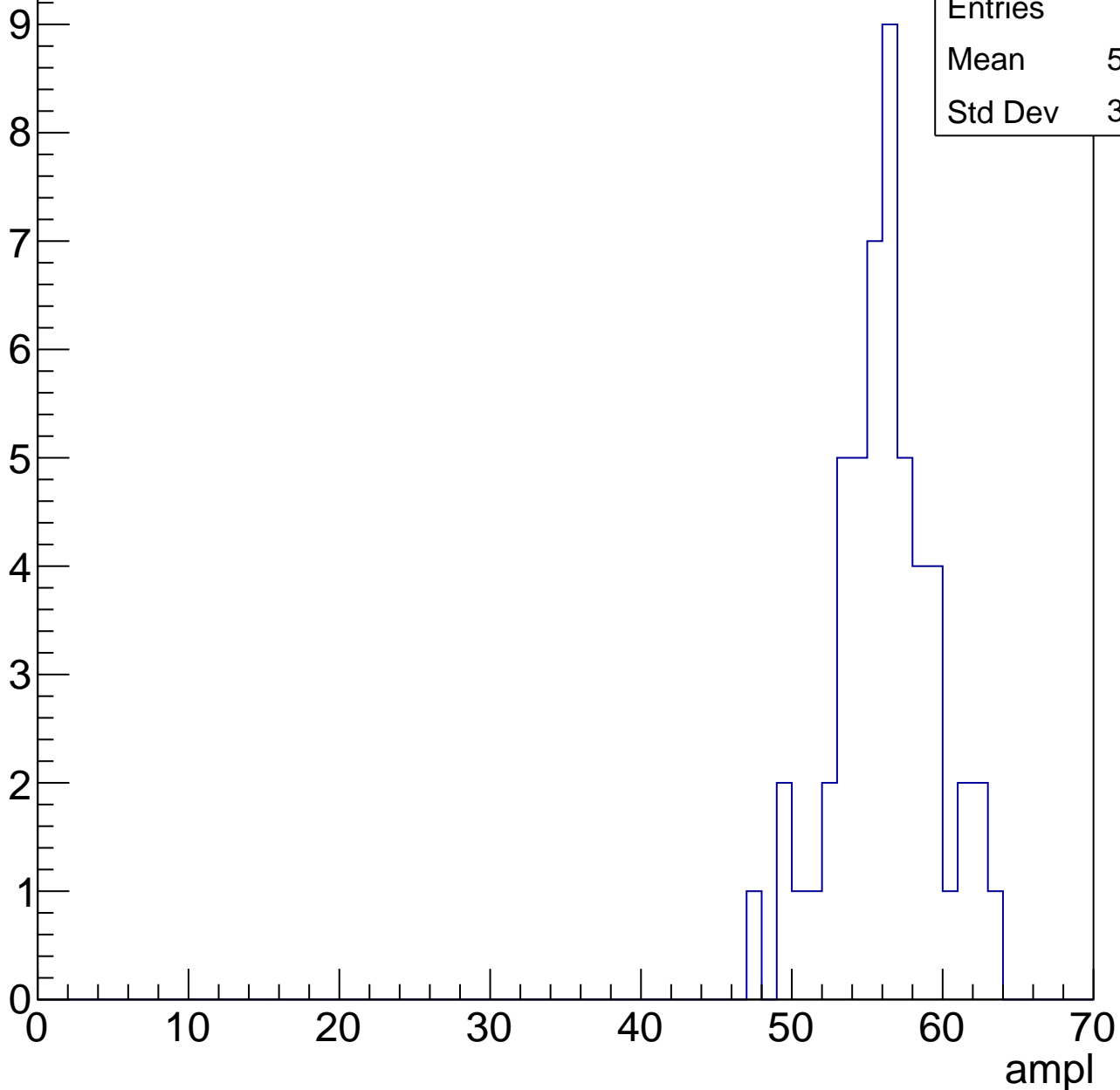


B0L102S, U5-ch71, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55.69
Std Dev	3.349



B0L102S, U5-ch71, adc5

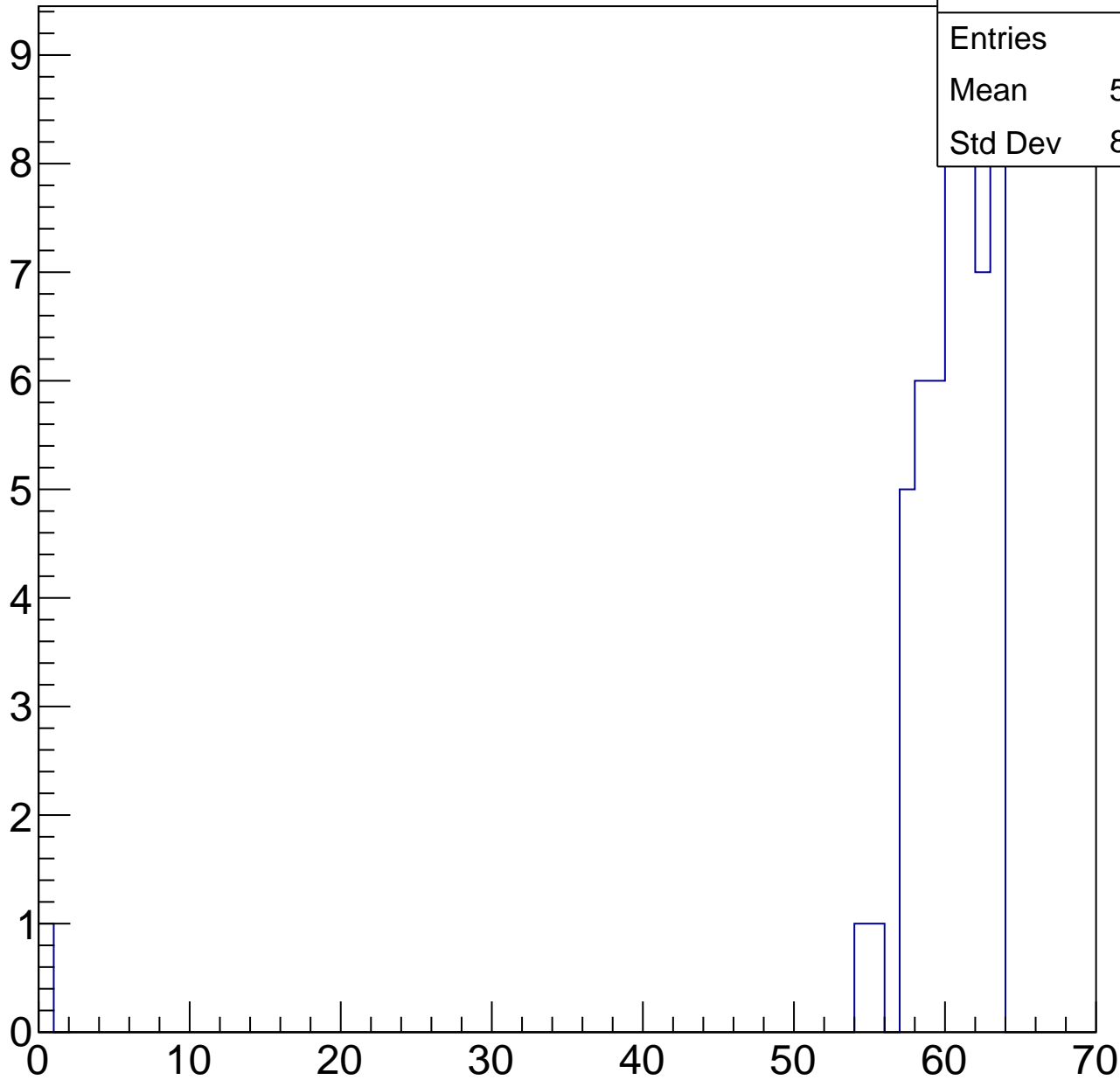
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.94
Std Dev	8.543

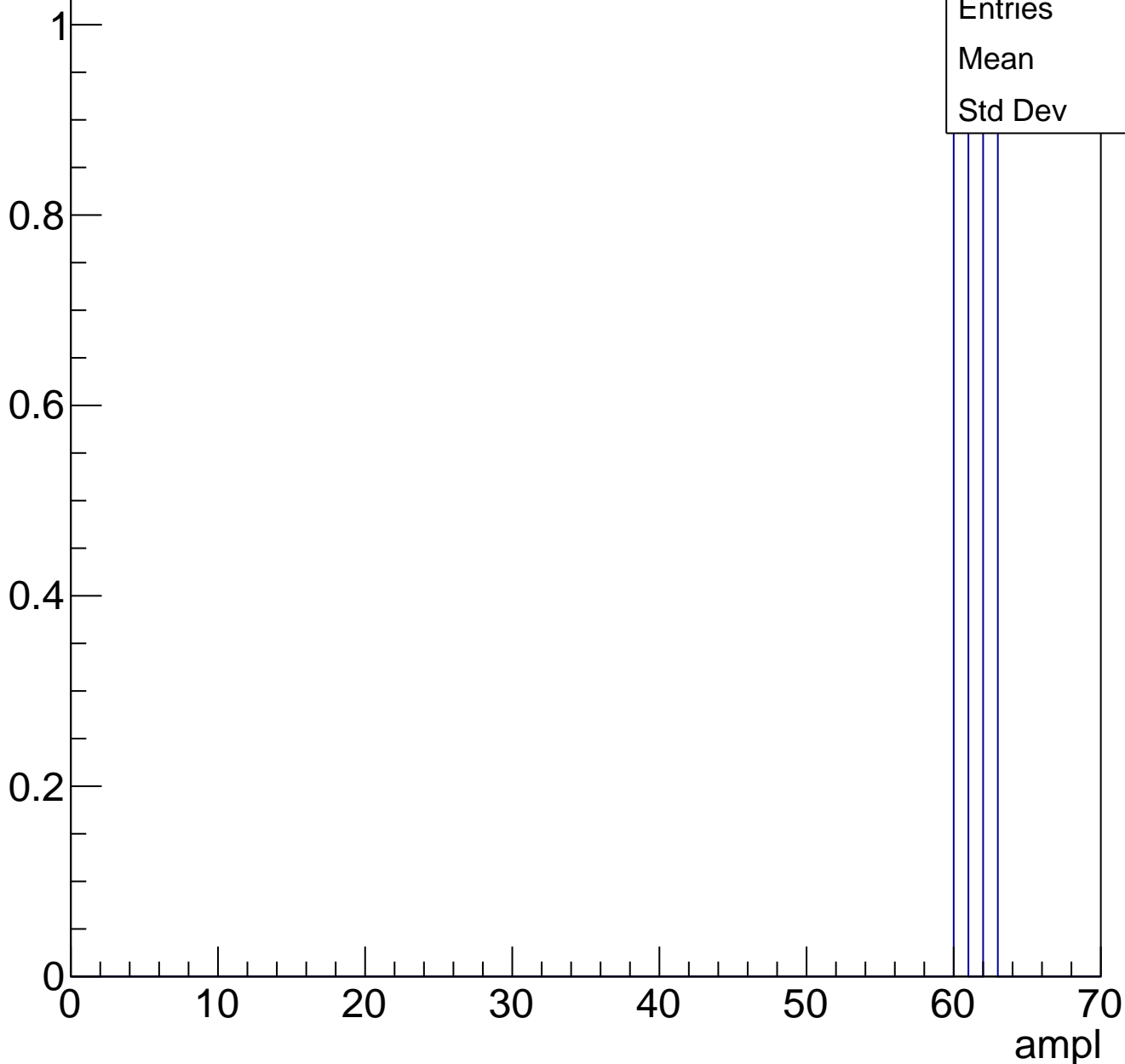
ampl



B0L102S, U5-ch71, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch71, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	11
Std Dev	11

B0L102S, U5-ch72, adc0

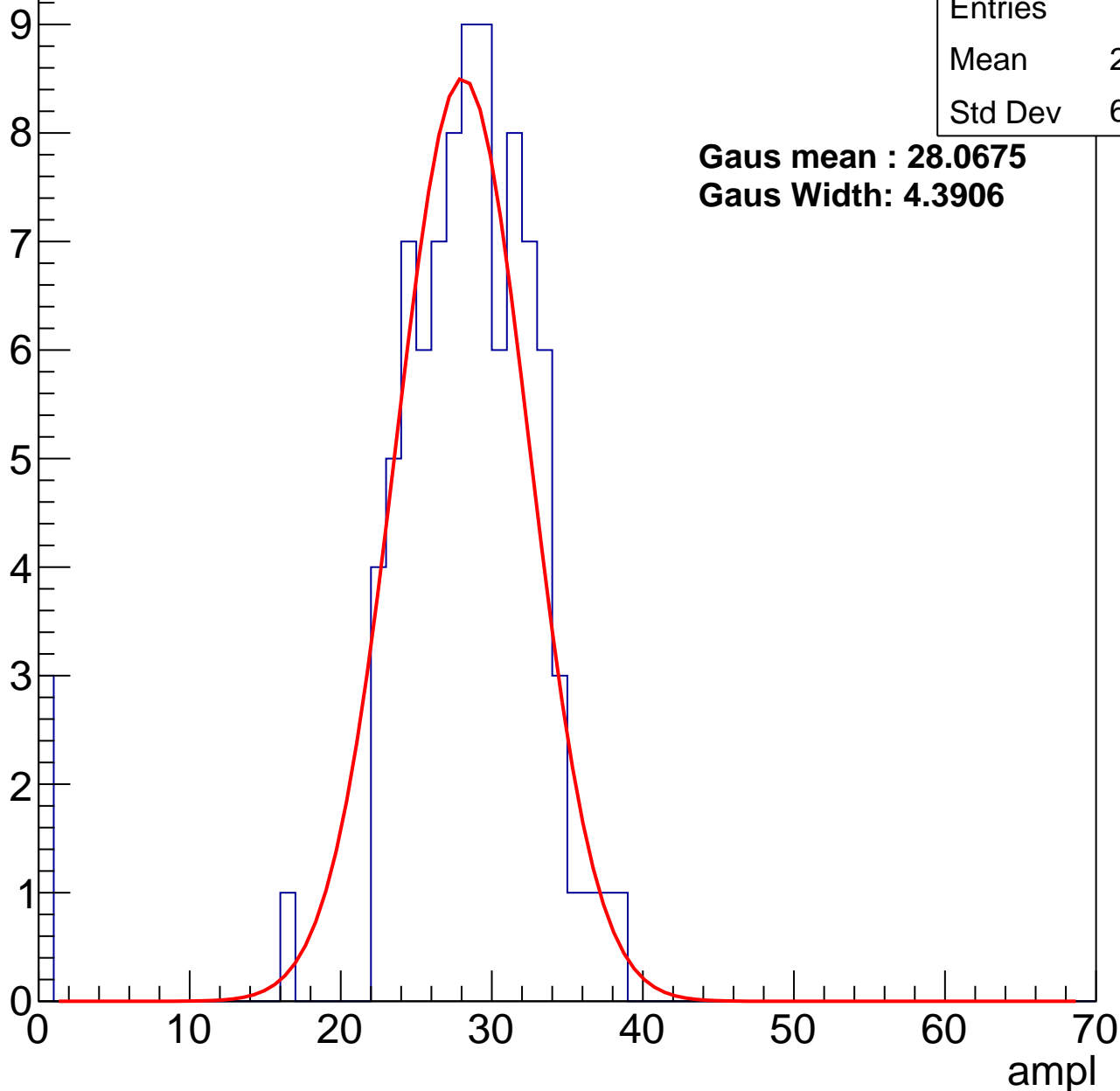
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	93
Mean	27.38
Std Dev	6.307

Gaus mean : 28.0675

Gaus Width: 4.3906



B0L102S, U5-ch72, adc1

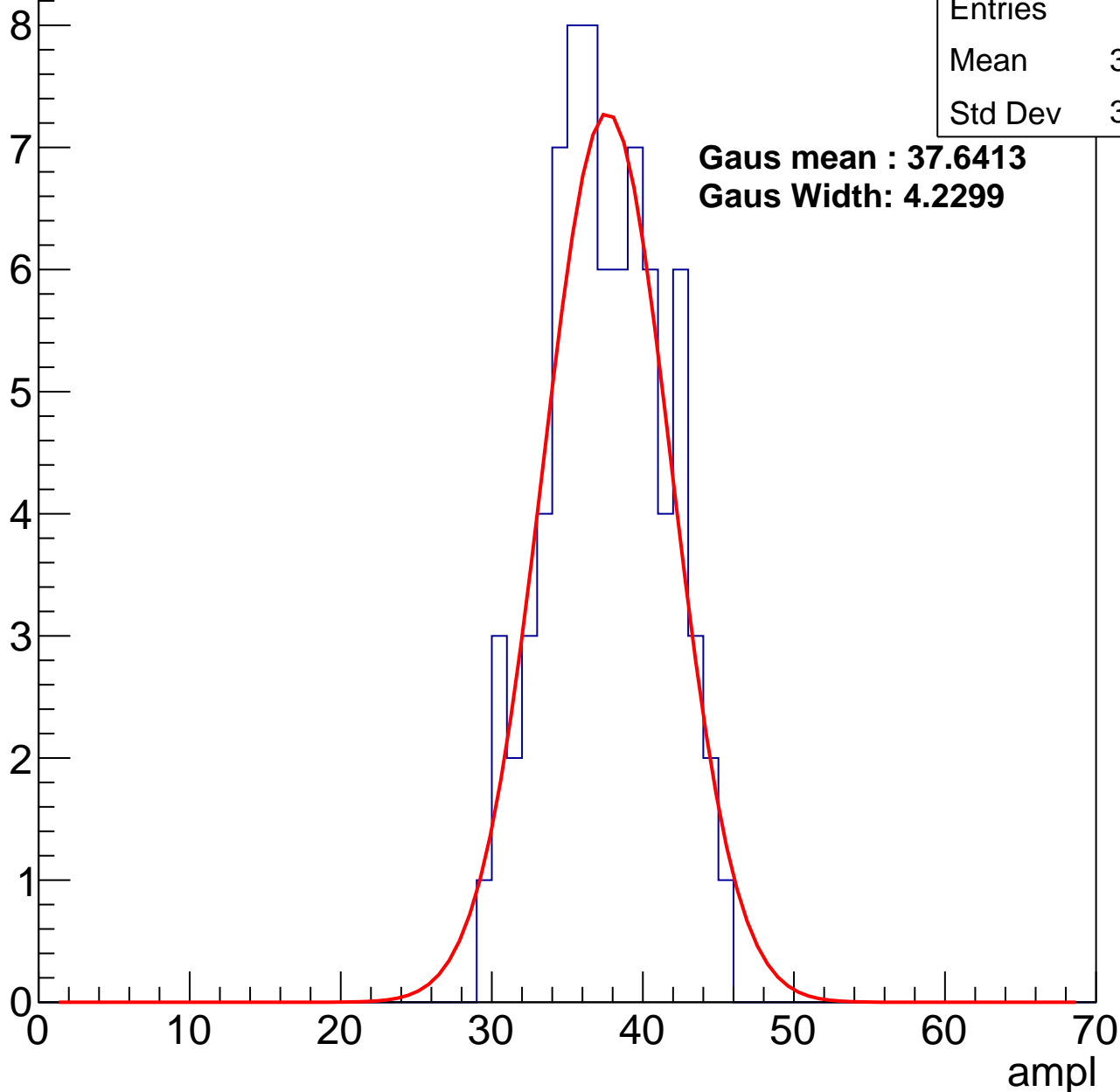
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	37.09
Std Dev	3.773

Gaus mean : 37.6413

Gaus Width: 4.2299



B0L102S, U5-ch72, adc2

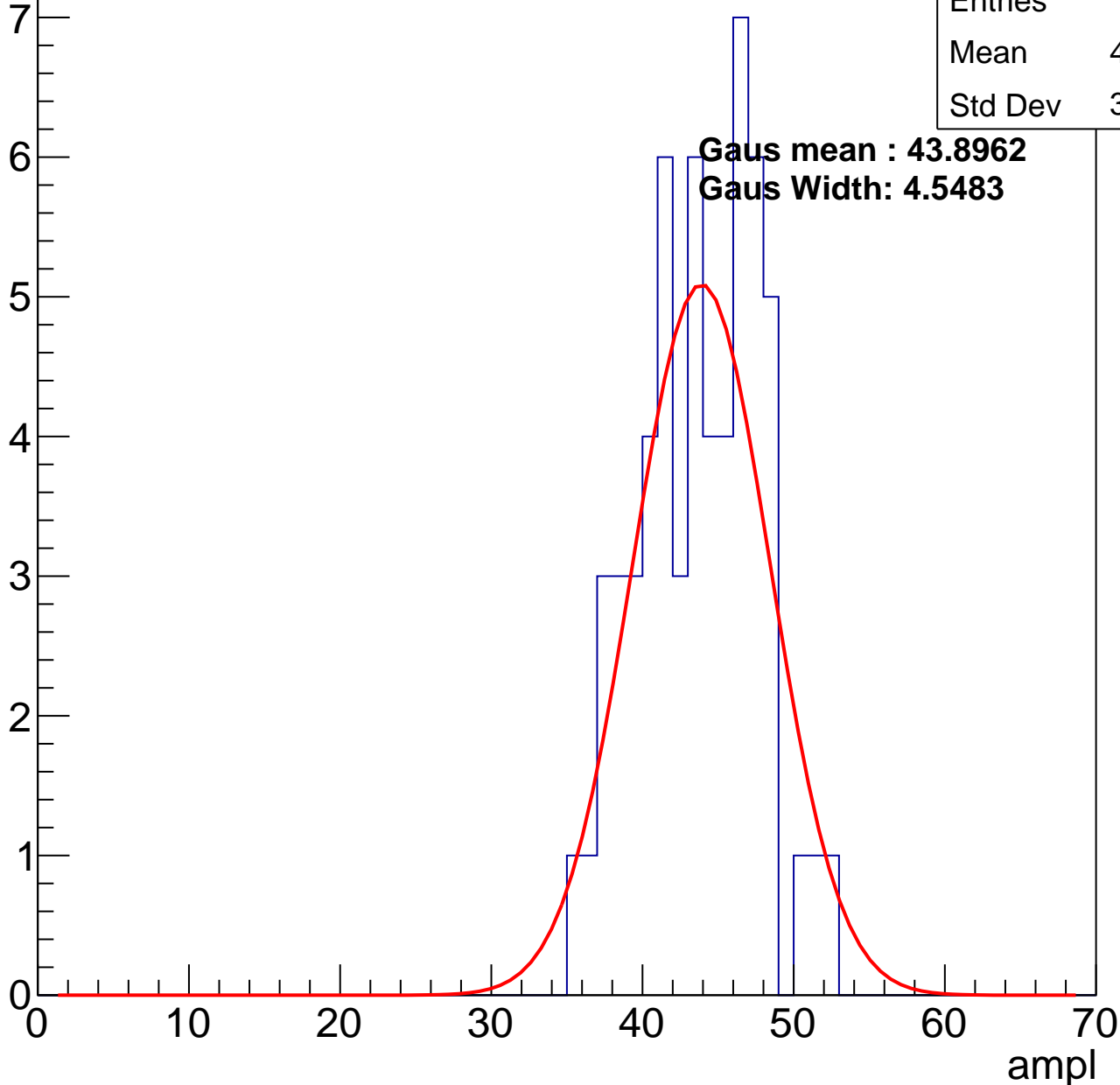
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	43.32
Std Dev	3.903

Gaus mean : 43.8962

Gaus Width: 4.5483

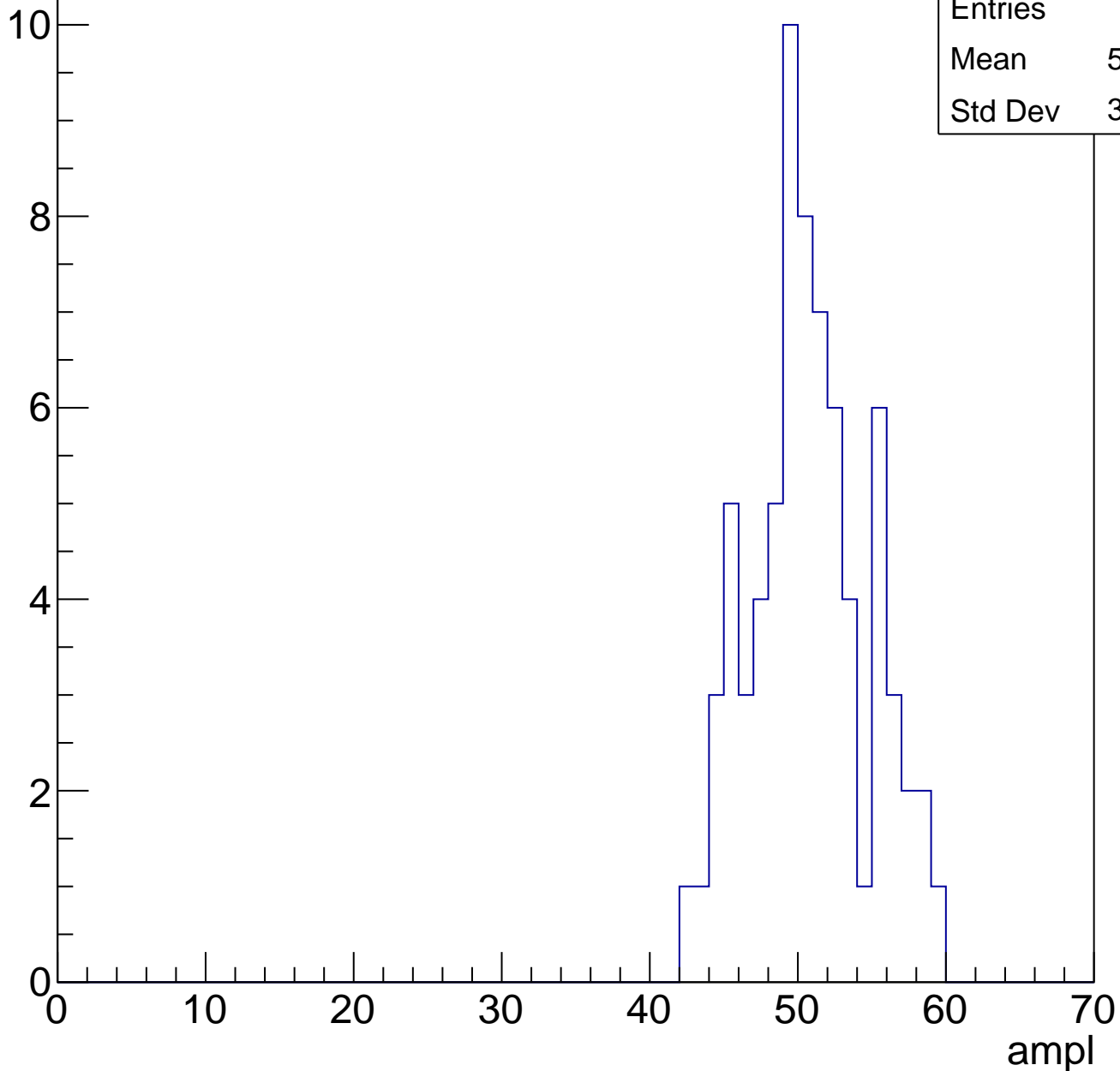


B0L102S, U5-ch72, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	50.28
Std Dev	3.913

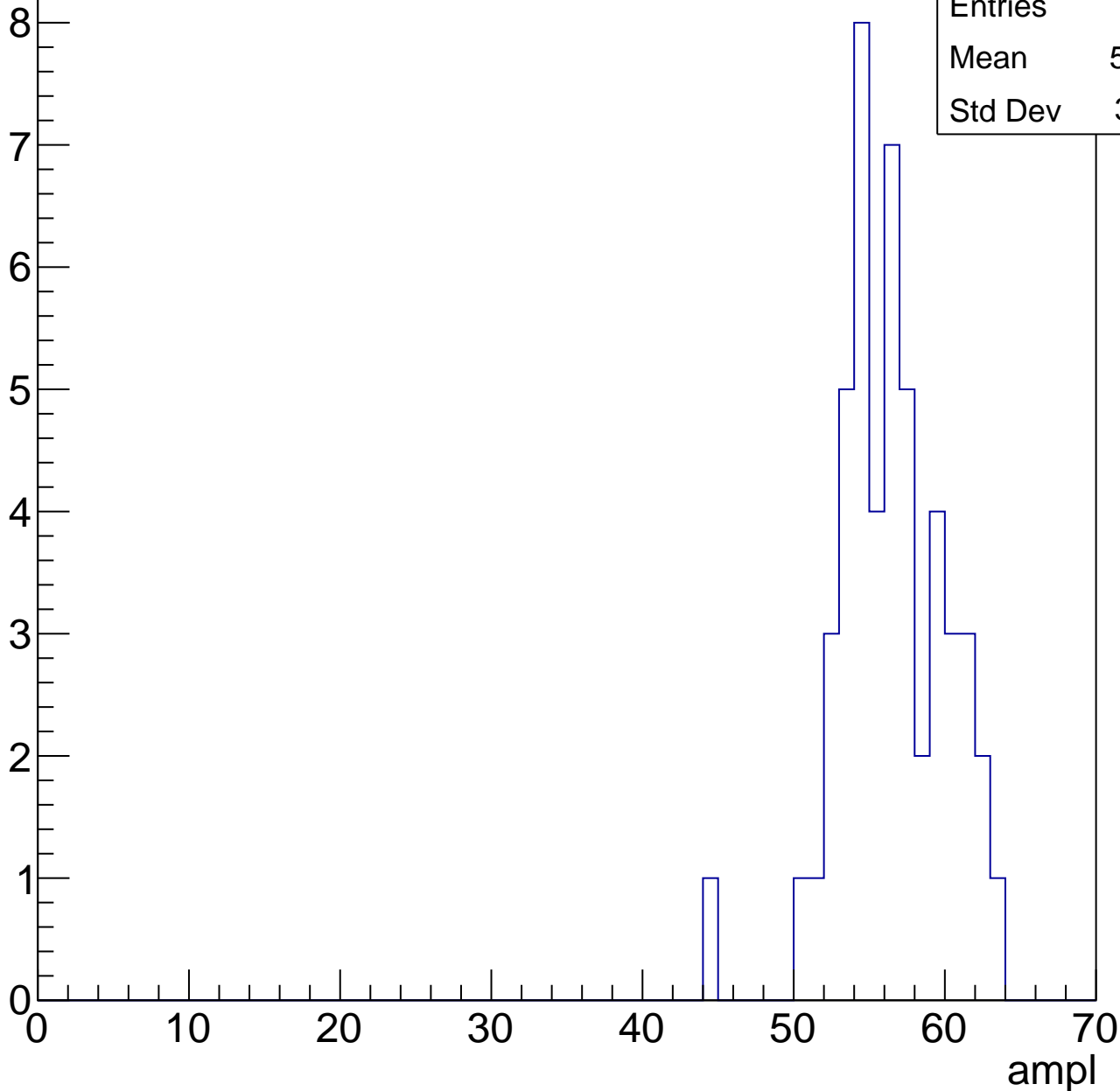


B0L102S, U5-ch72, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

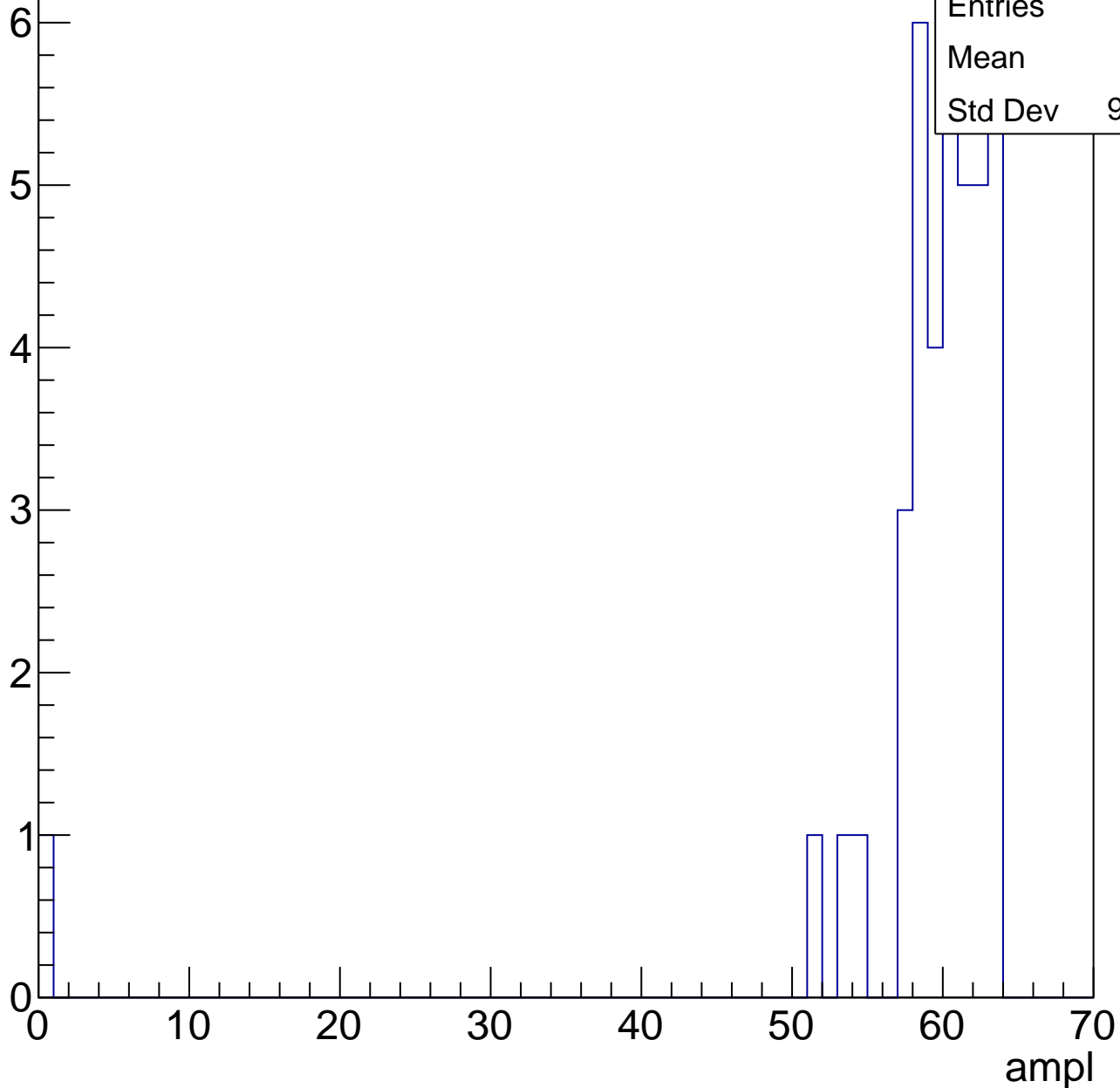
Entries	50
Mean	55.94
Std Dev	3.541



B0L102S, U5-ch72, adc5

calib_packv5_042523_0143.root, FC#12, port B1

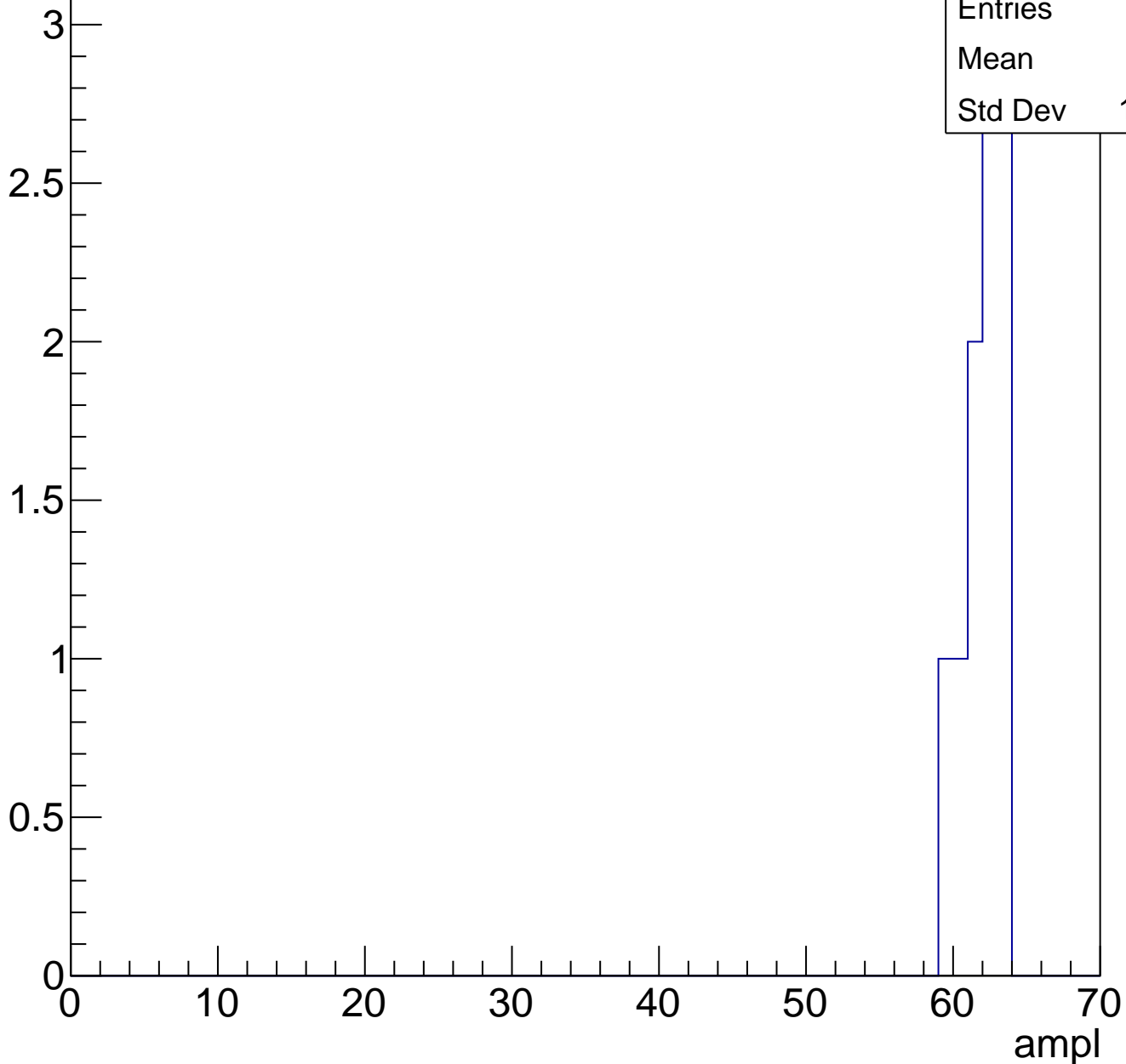
Entry



B0L102S, U5-ch72, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch72, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch73, adc0

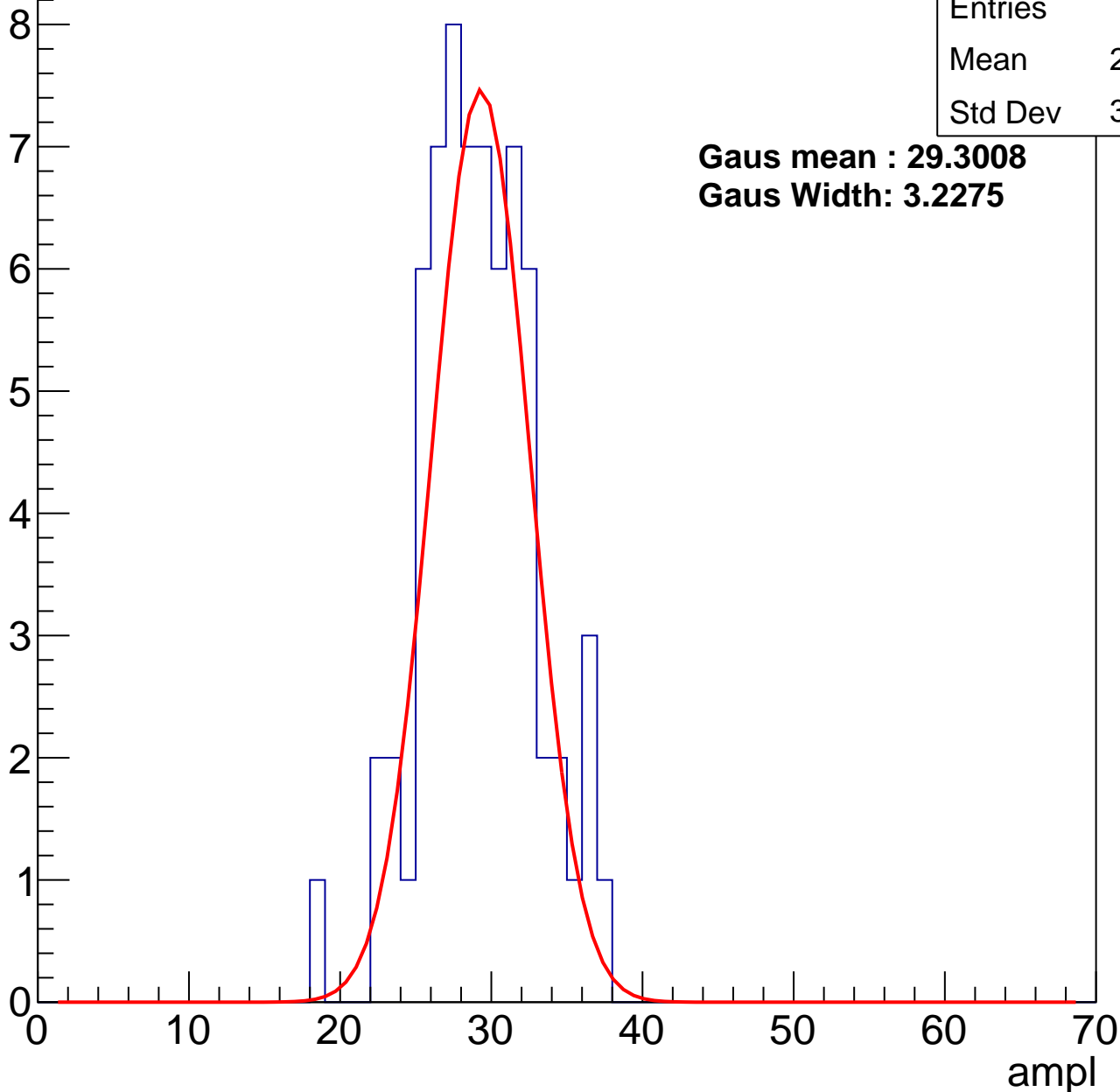
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.72
Std Dev	3.663

Gaus mean : 29.3008

Gaus Width: 3.2275



B0L102S, U5-ch73, adc1

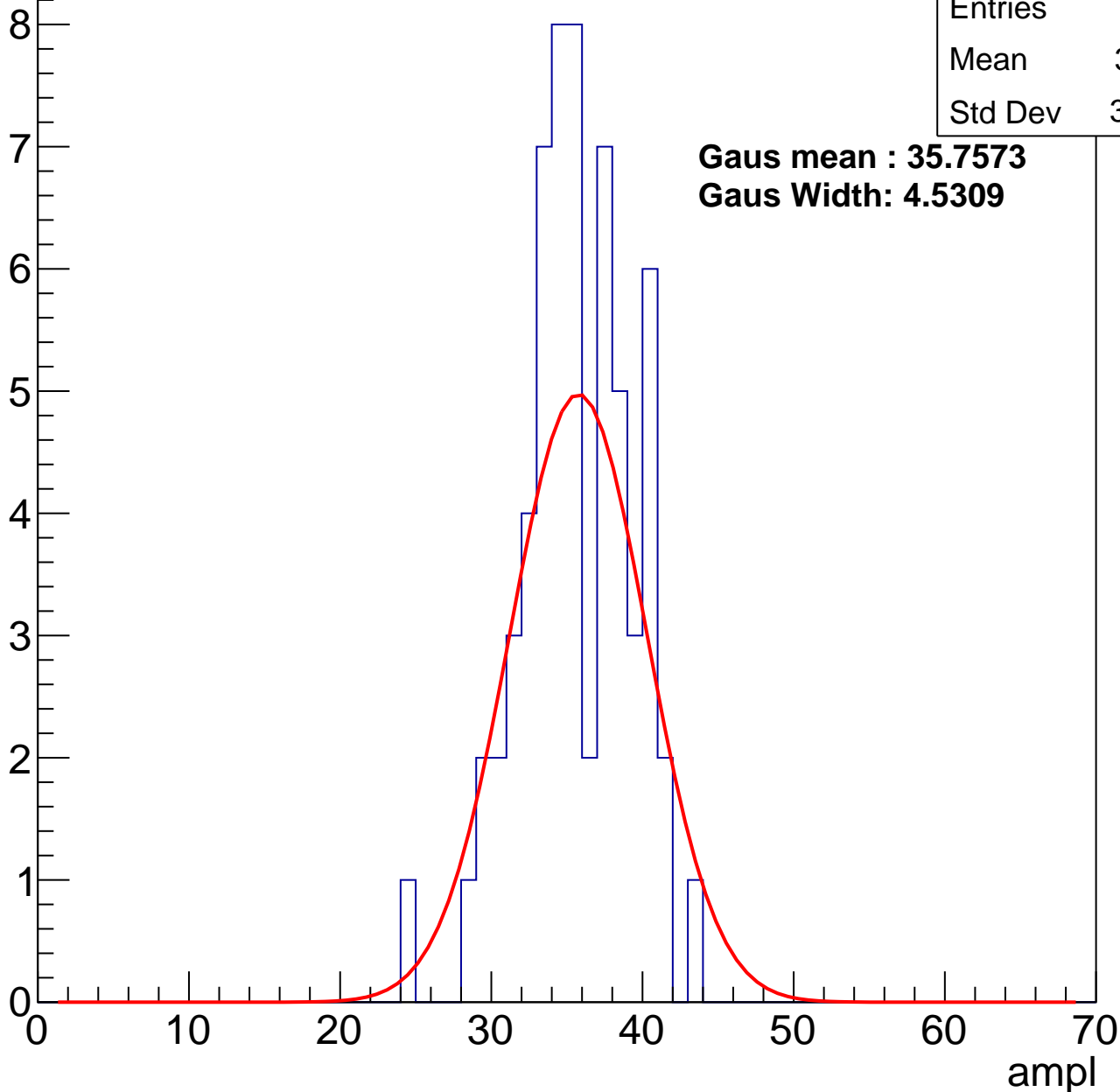
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	35.11
Std Dev	3.637

Gaus mean : 35.7573

Gaus Width: 4.5309



B0L102S, U5-ch73, adc2

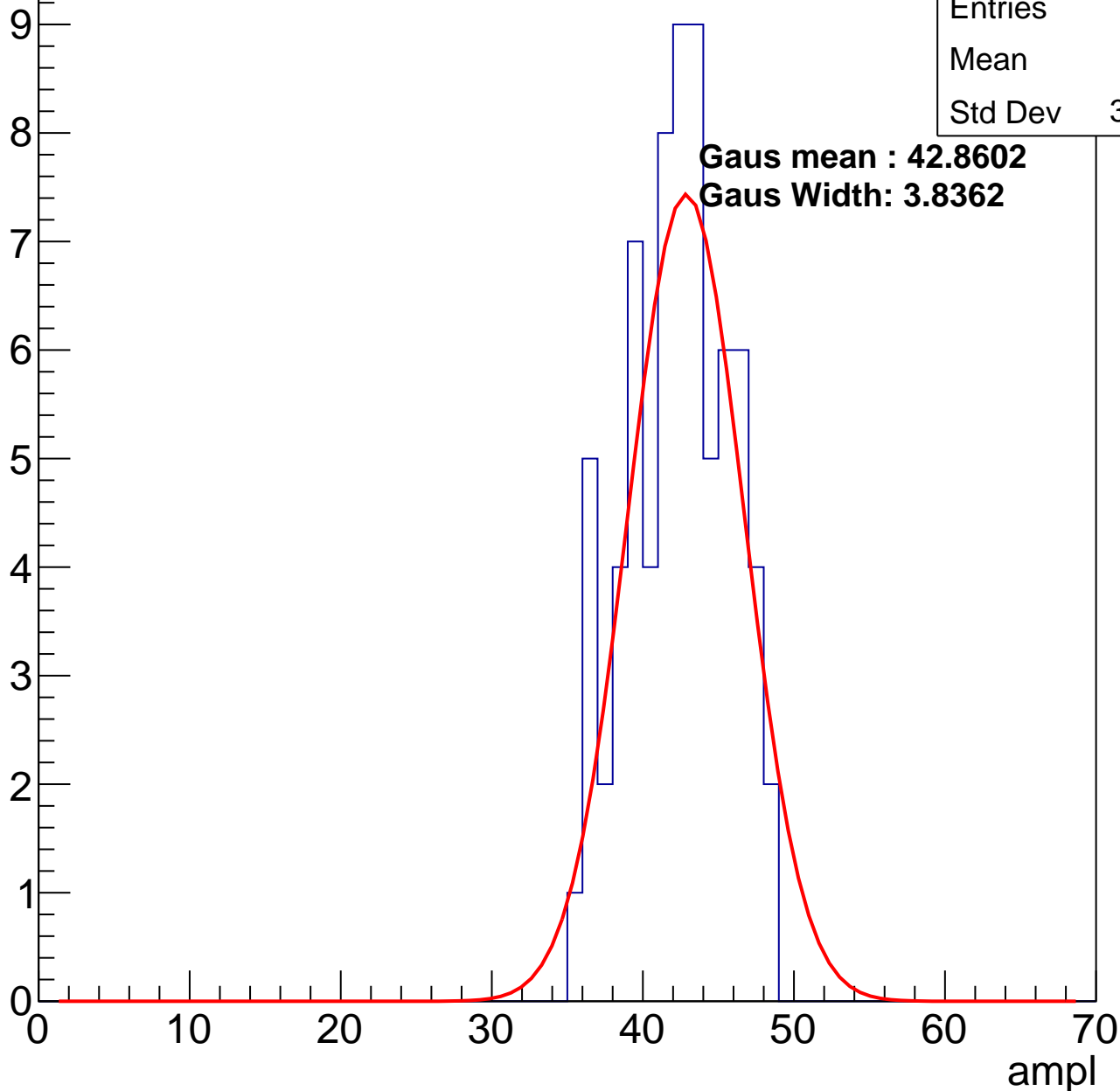
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	41.9
Std Dev	3.292

Gaus mean : 42.8602

Gaus Width: 3.8362

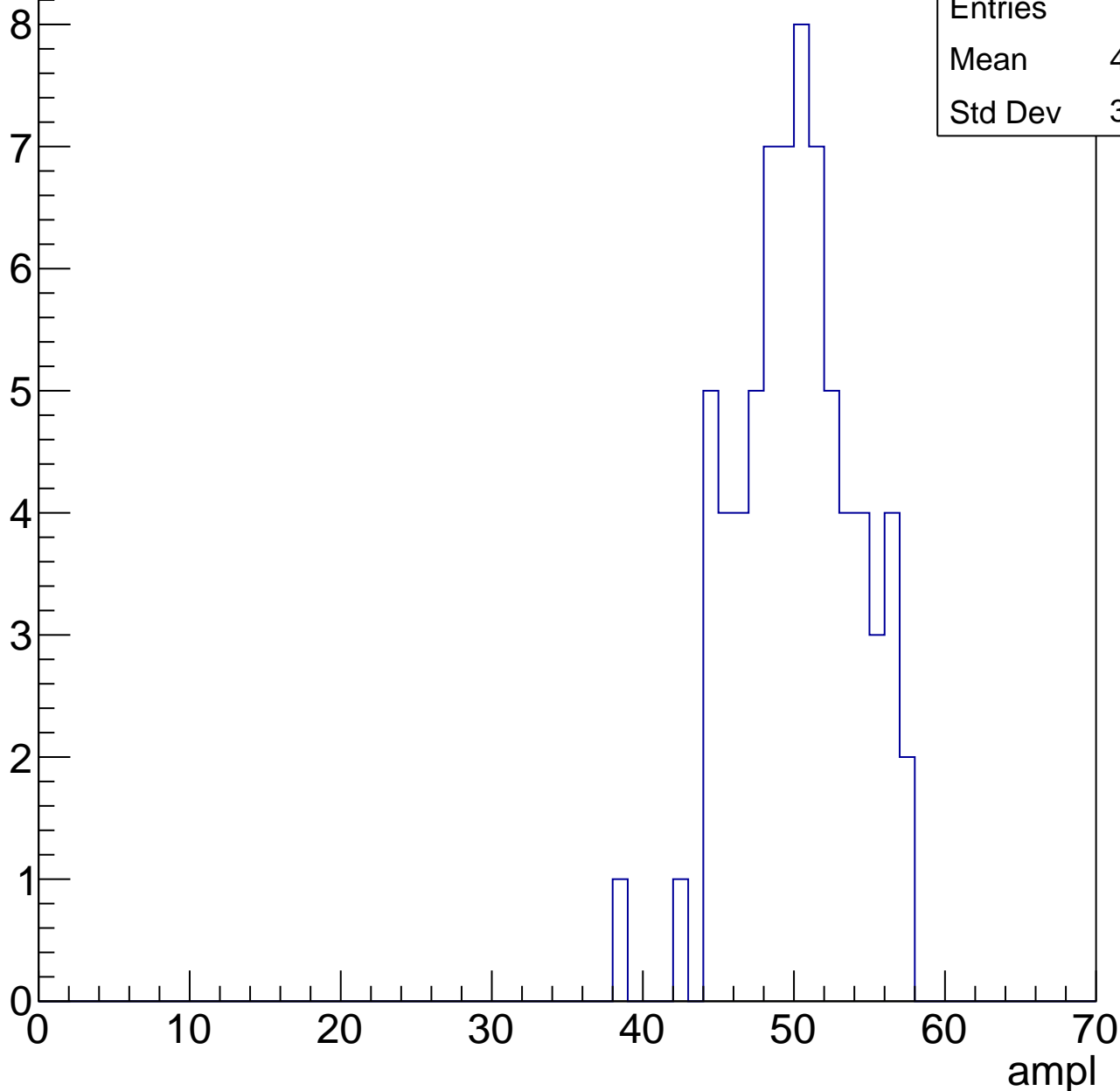


B0L102S, U5-ch73, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	49.66
Std Dev	3.878

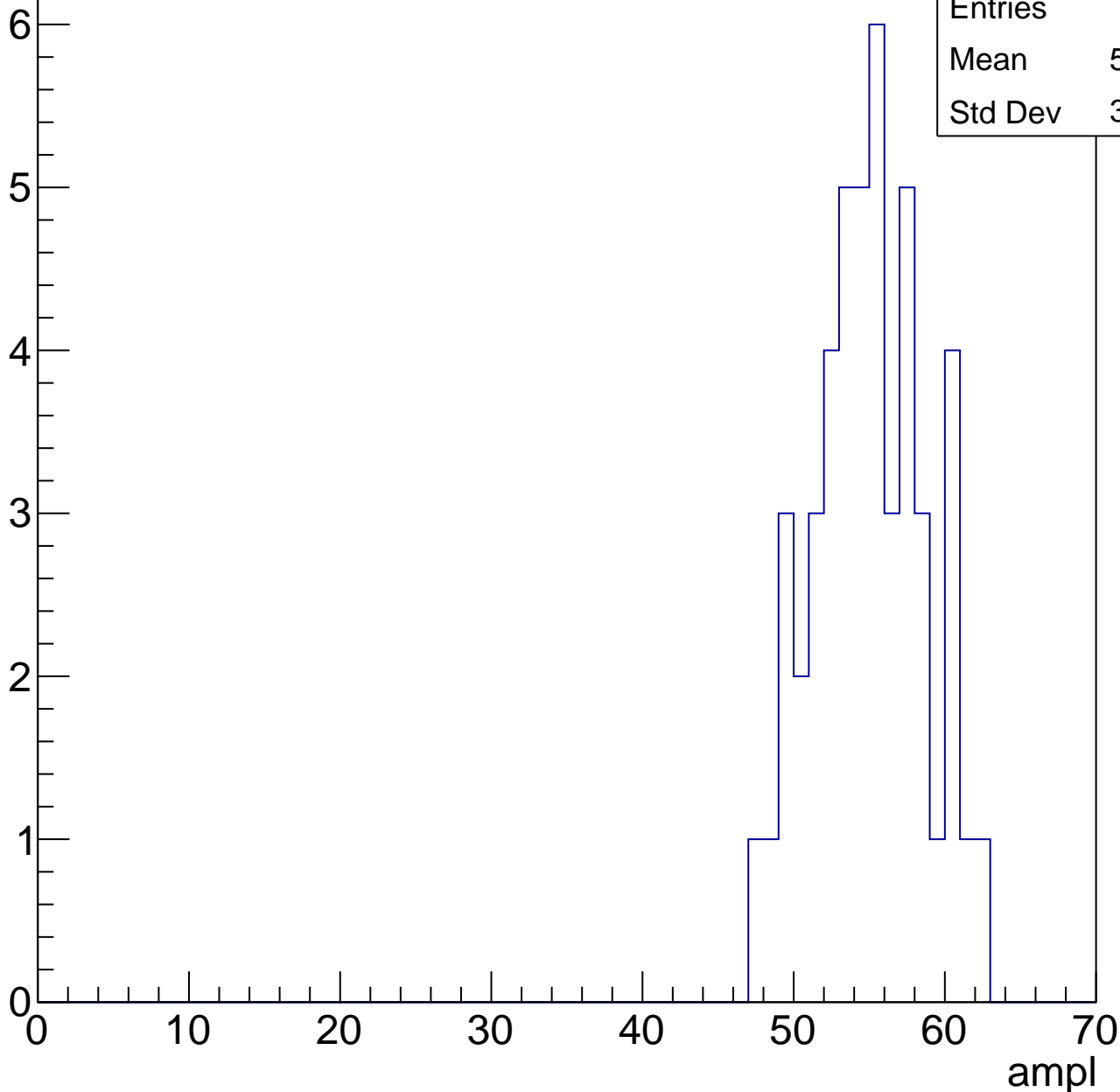


B0L102S, U5-ch73, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	54.52
Std Dev	3.594



B0L102S, U5-ch73, adc5

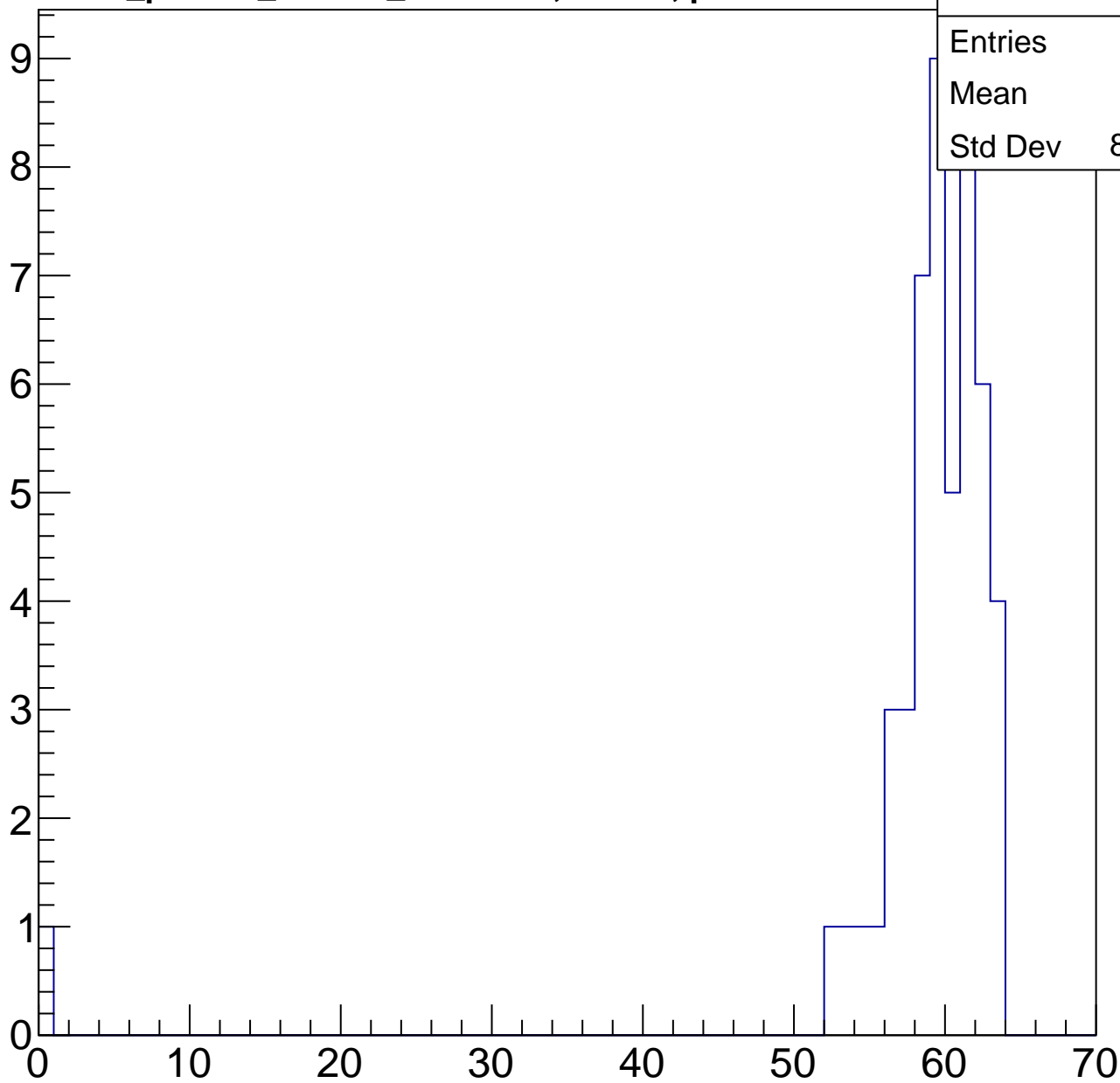
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.1
Std Dev	8.598

ampl

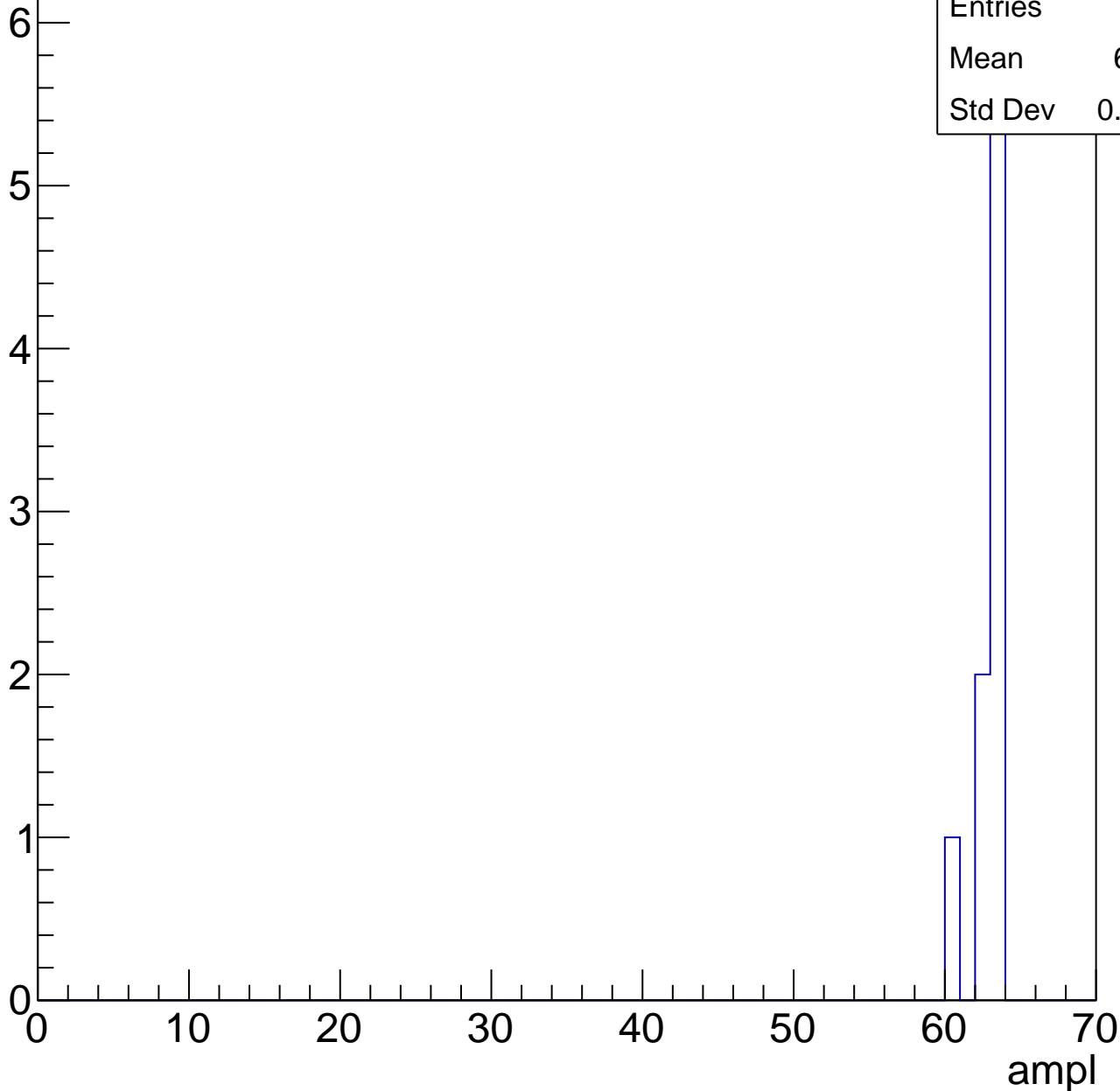


B0L102S, U5-ch73, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

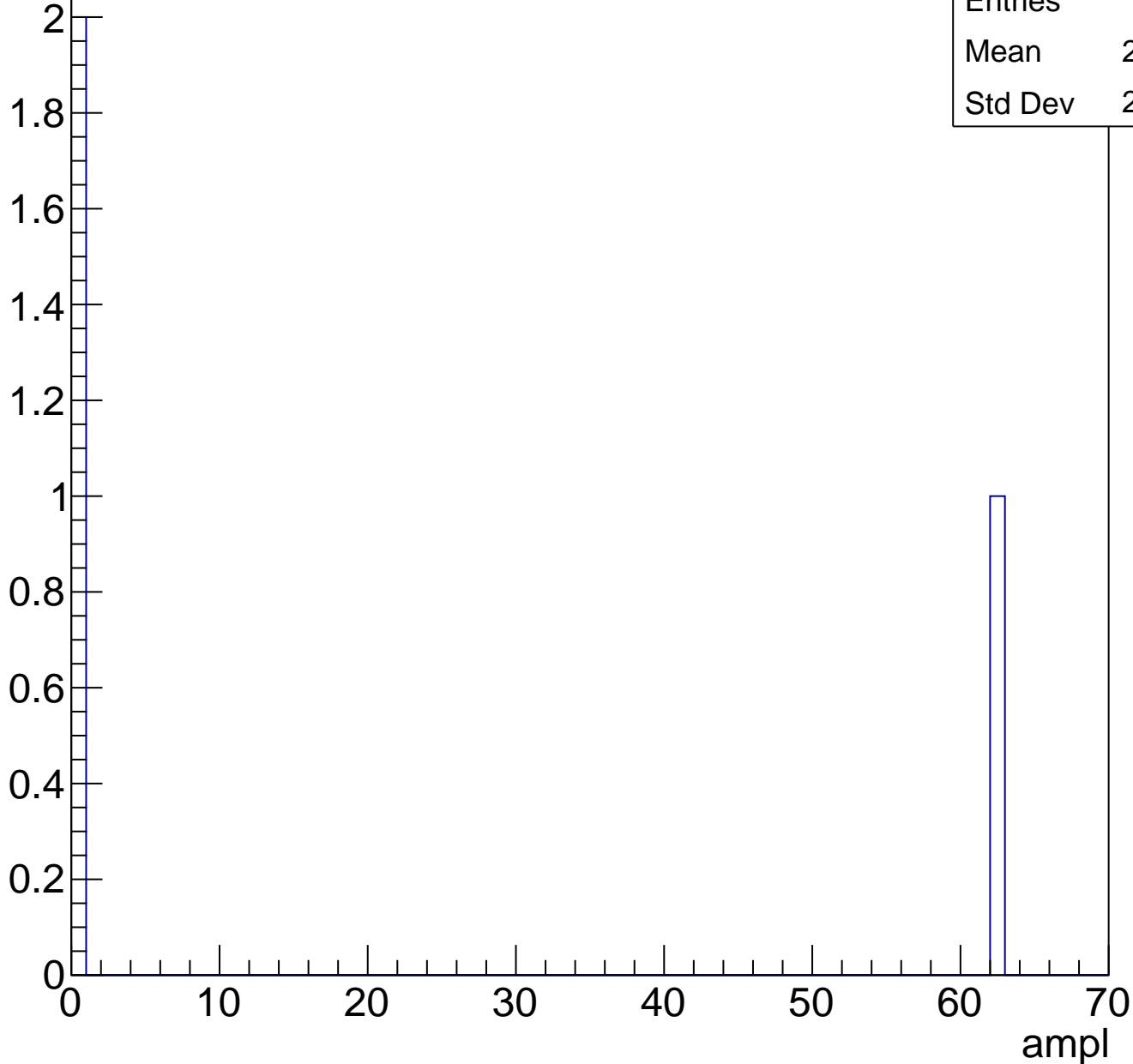
Entries	9
Mean	62.44
Std Dev	0.9558



B0L102S, U5-ch73, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch74, adc0

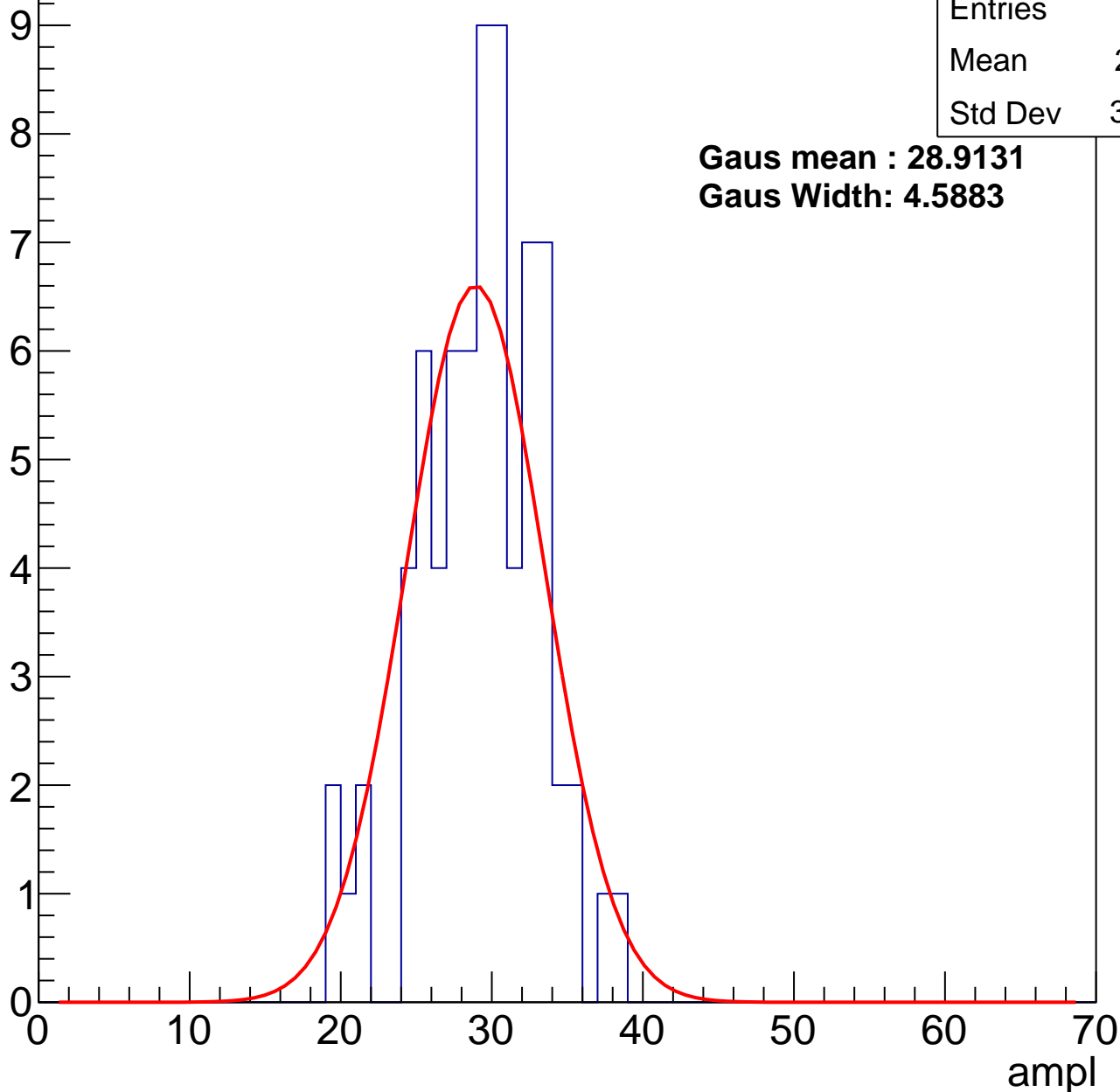
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	28.81
Std Dev	3.944

Gaus mean : 28.9131

Gaus Width: 4.5883



B0L102S, U5-ch74, adc1

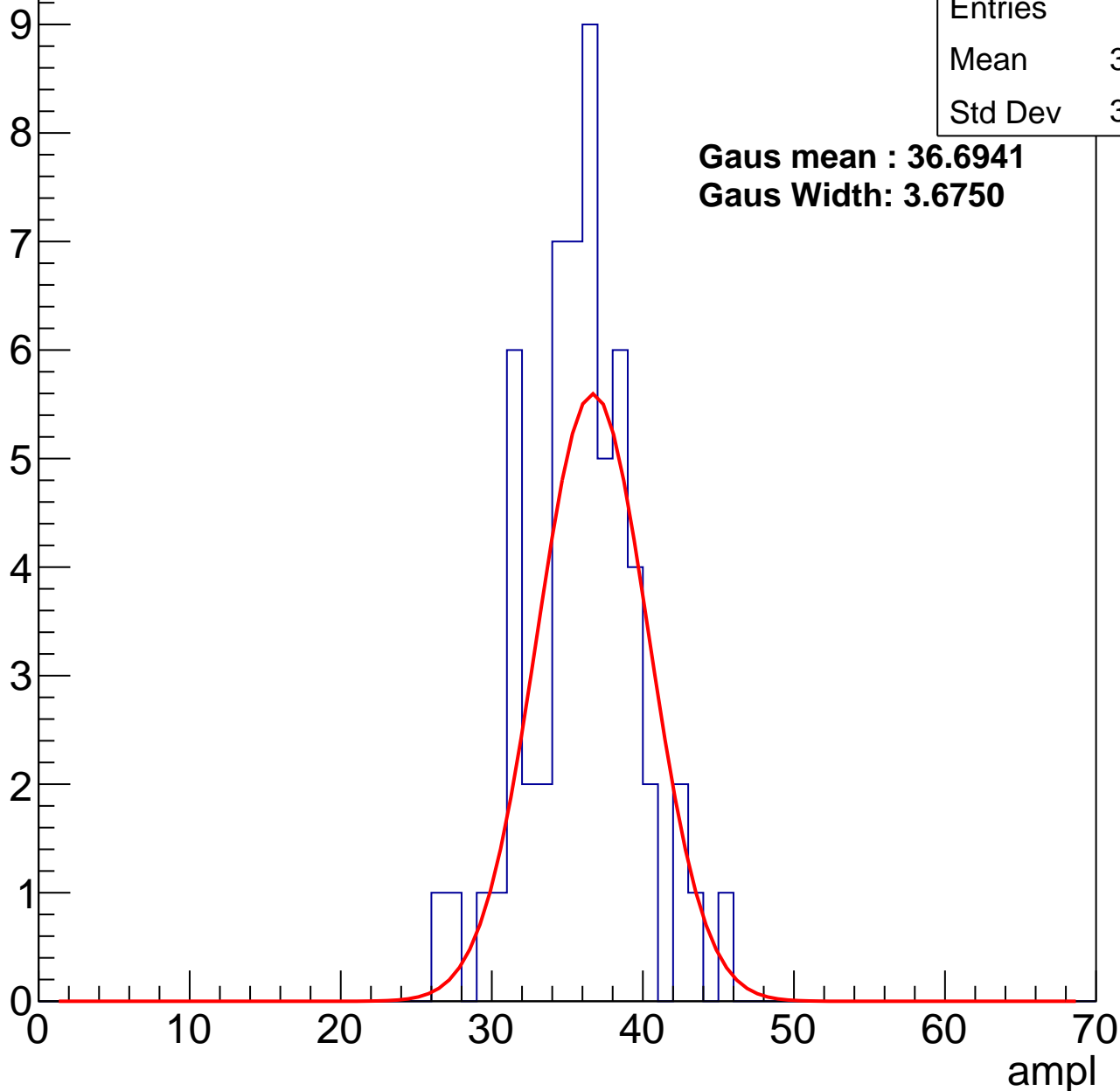
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	35.45
Std Dev	3.673

Gaus mean : 36.6941

Gaus Width: 3.6750



B0L102S, U5-ch74, adc2

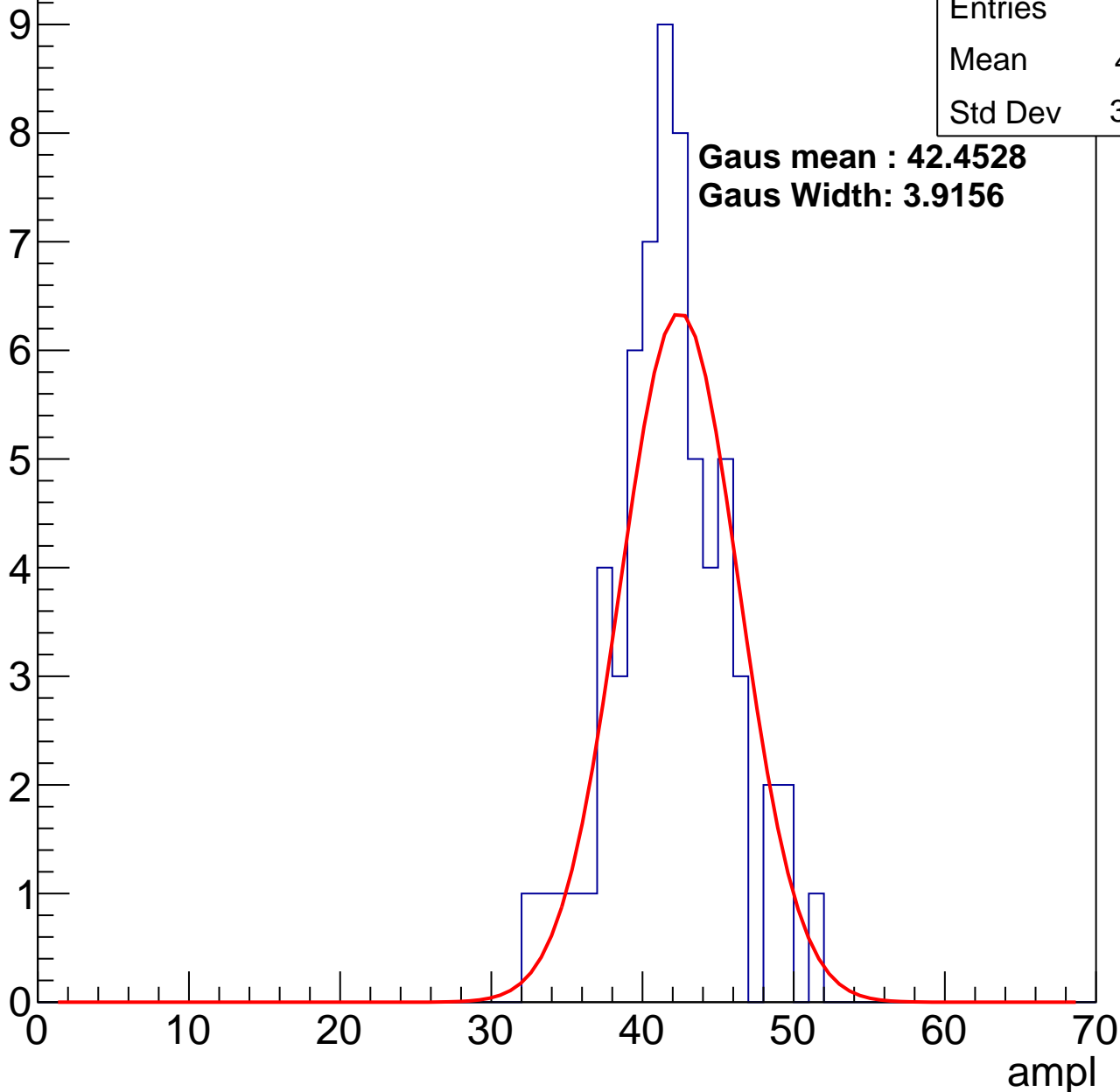
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.41
Std Dev	3.774

Gaus mean : 42.4528

Gaus Width: 3.9156

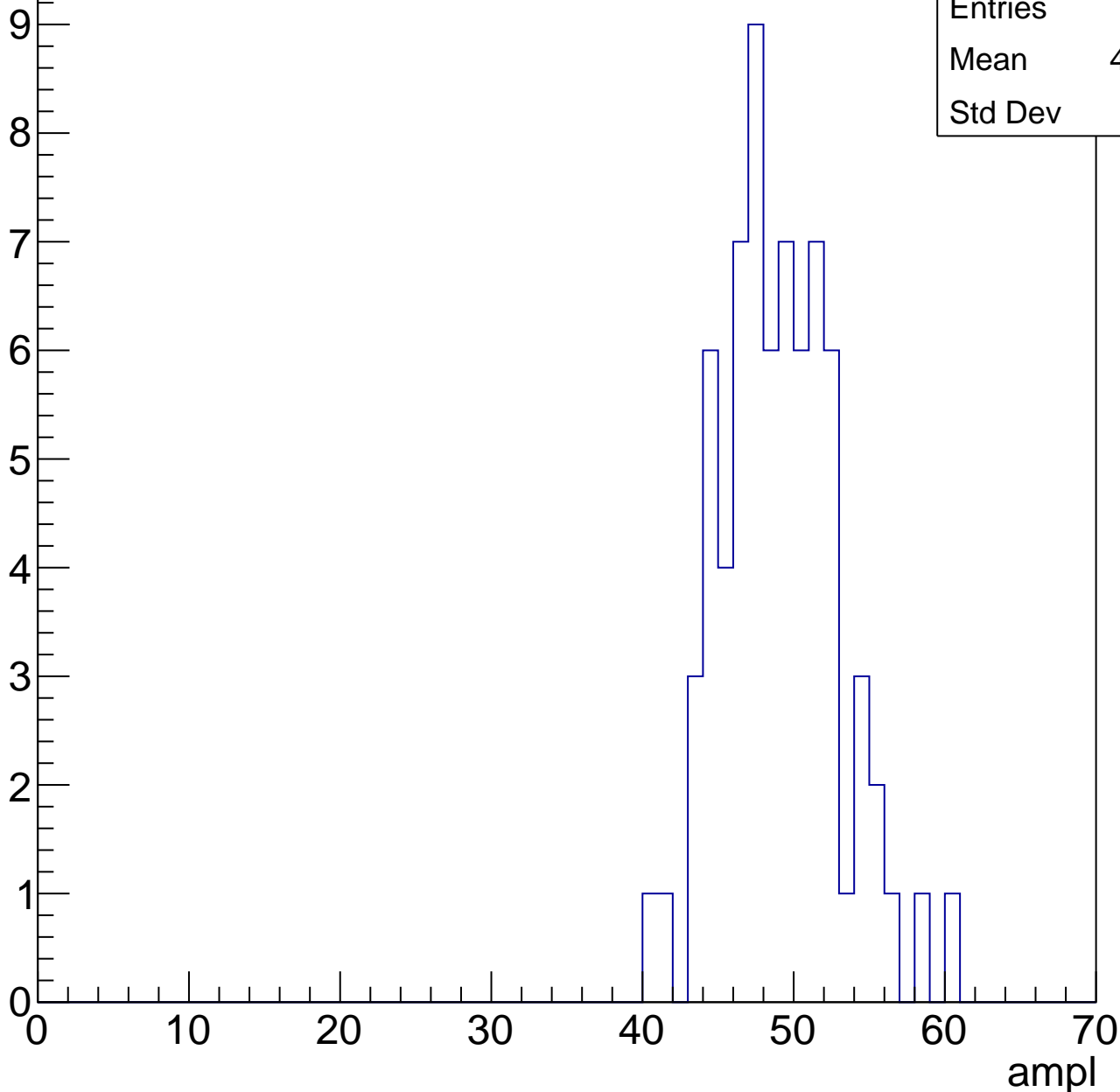


B0L102S, U5-ch74, adc3

calib_packv5_042523_0143.root, FC#12, port B1

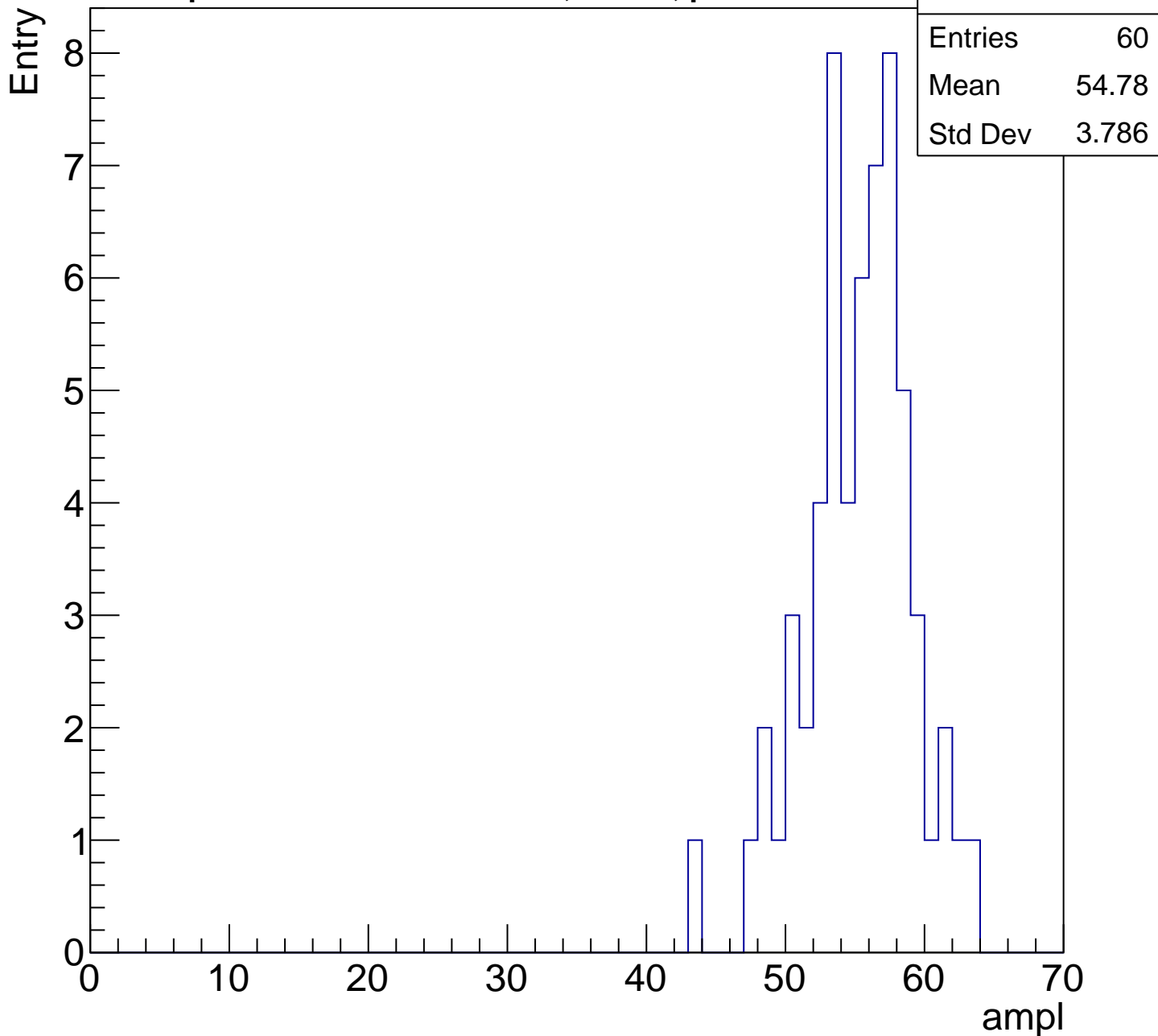
Entry

Entries	72
Mean	48.58
Std Dev	3.84



B0L102S, U5-ch74, adc4

calib_packv5_042523_0143.root, FC#12, port B1

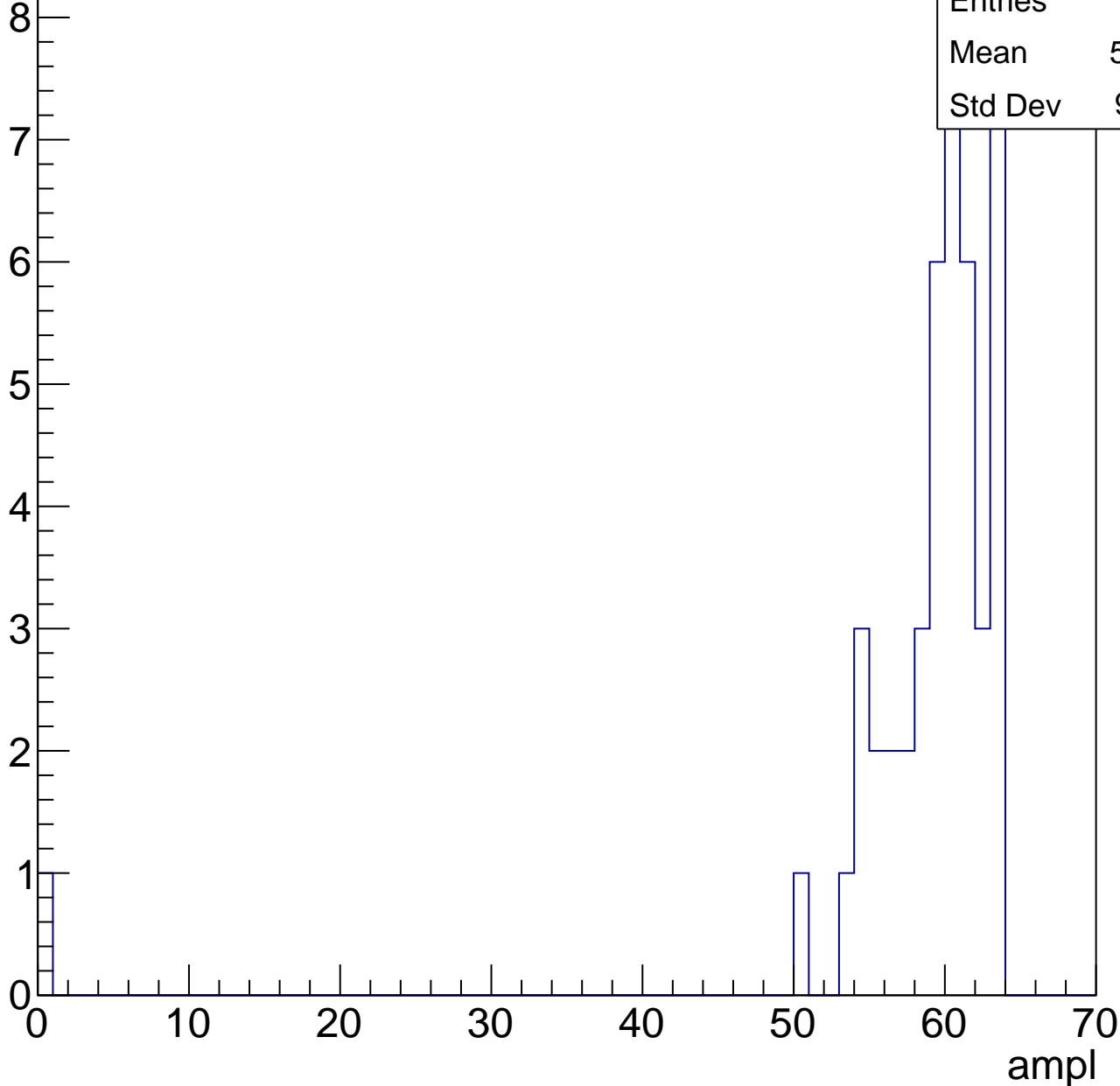


B0L102S, U5-ch74, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	57.93
Std Dev	9.171

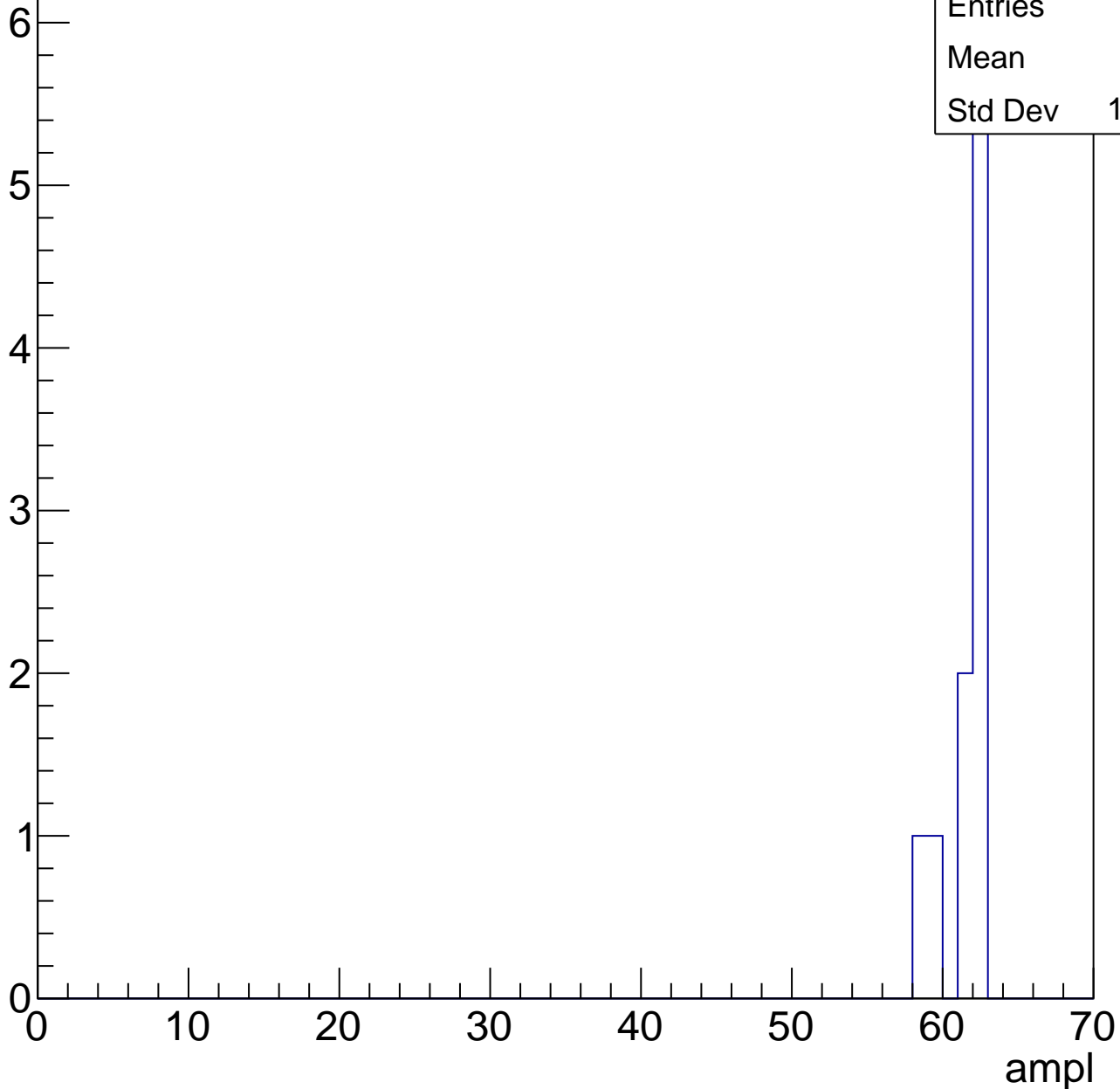


B0L102S, U5-ch74, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	10
Mean	61.1
Std Dev	1.375



B0L102S, U5-ch74, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch75, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	28.79
Std Dev	6.726

Gaus mean : 30.2843

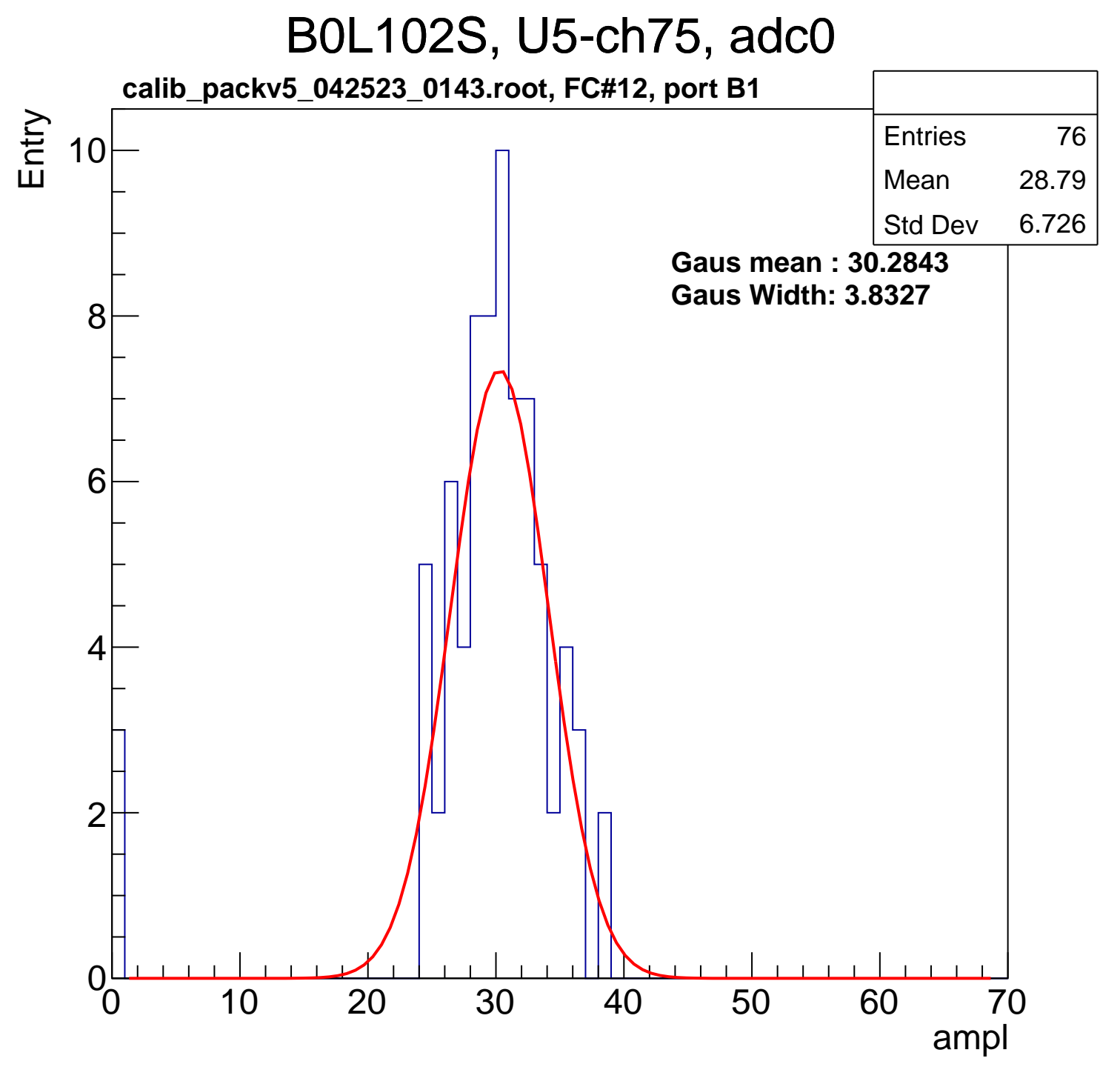
Gaus Width: 3.8327

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch75, adc1

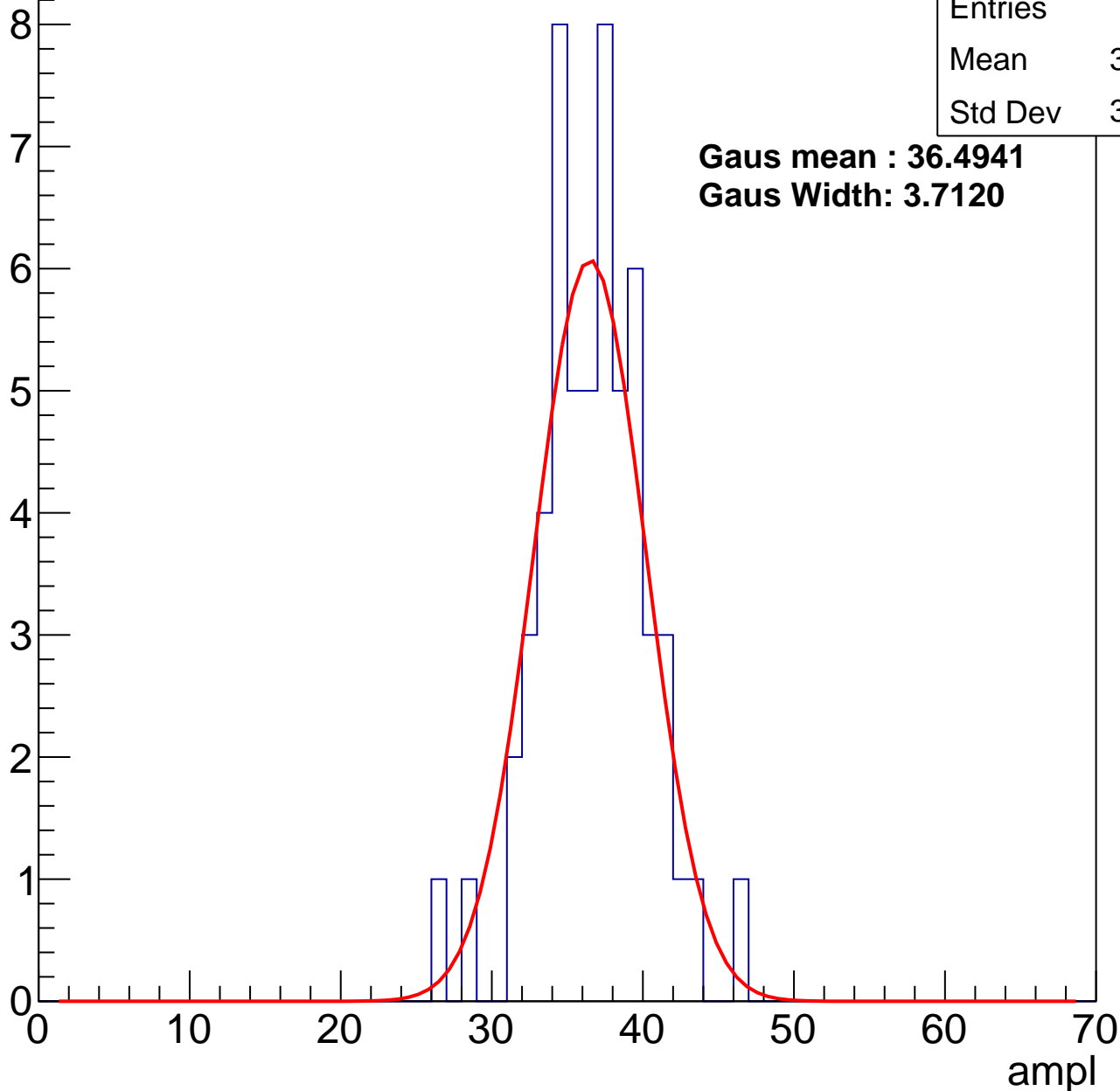
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	36.23
Std Dev	3.559

Gaus mean : 36.4941

Gaus Width: 3.7120



B0L102S, U5-ch75, adc2

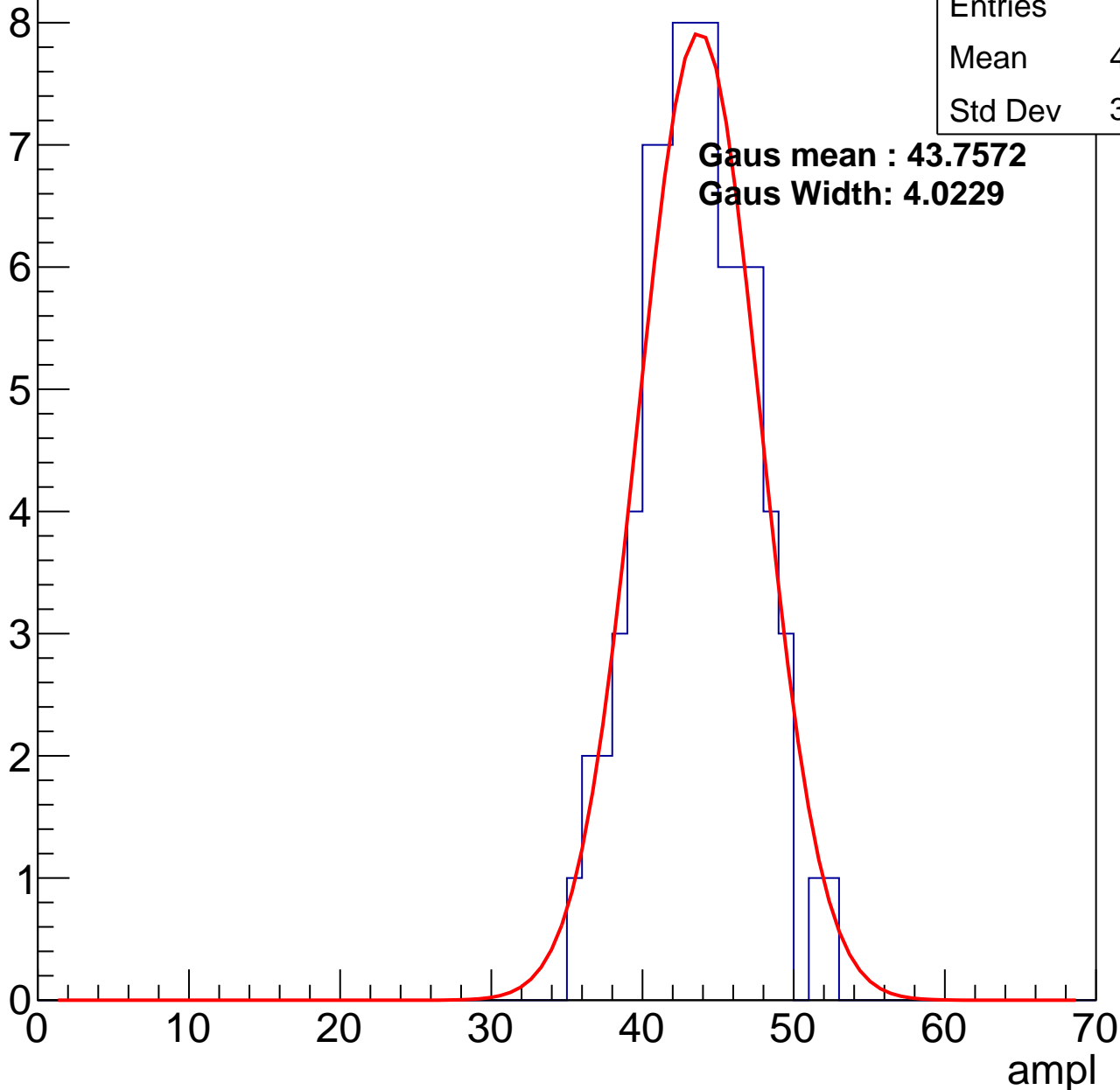
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	43.12
Std Dev	3.614

Gaus mean : 43.7572

Gaus Width: 4.0229

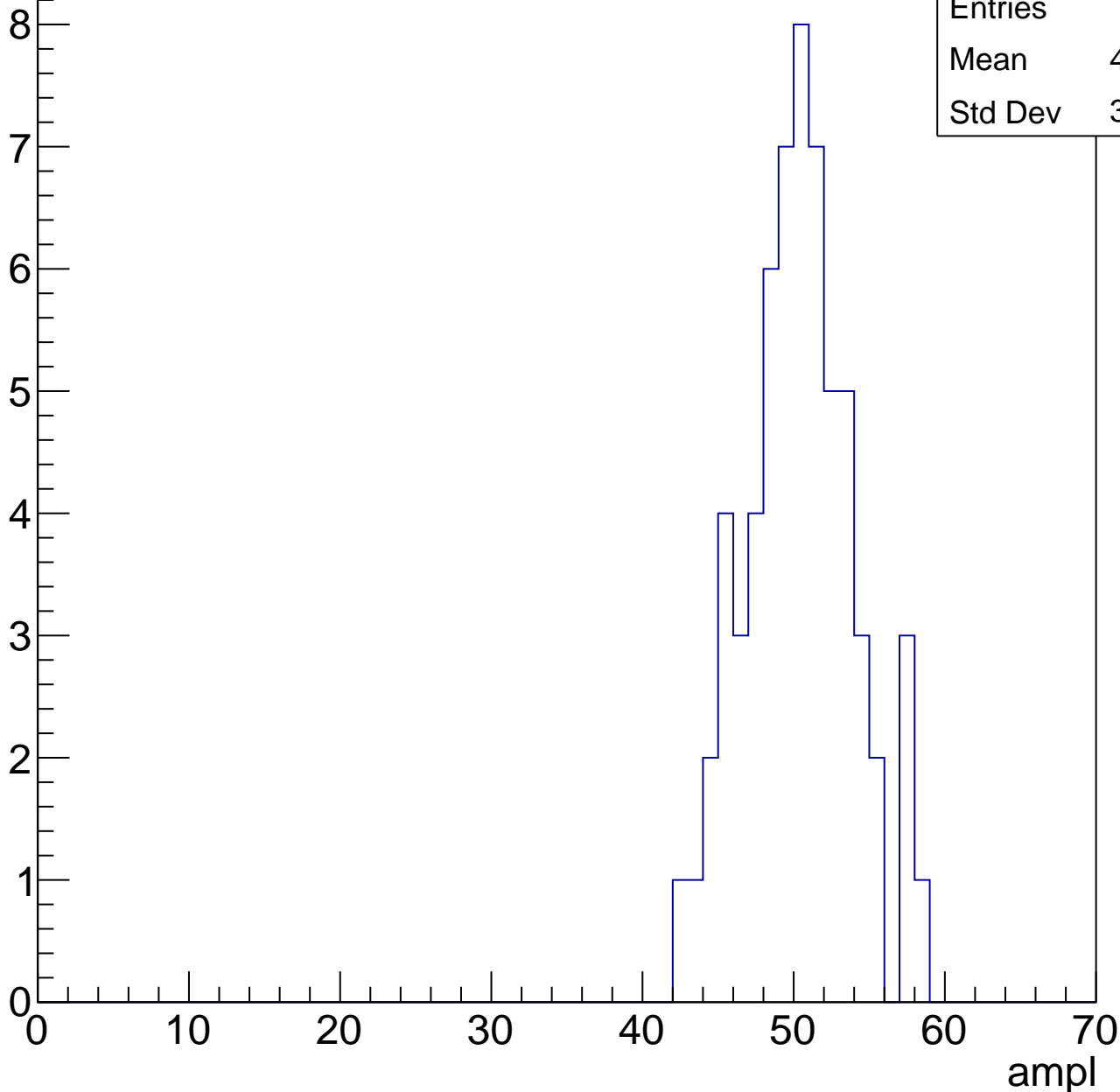


B0L102S, U5-ch75, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

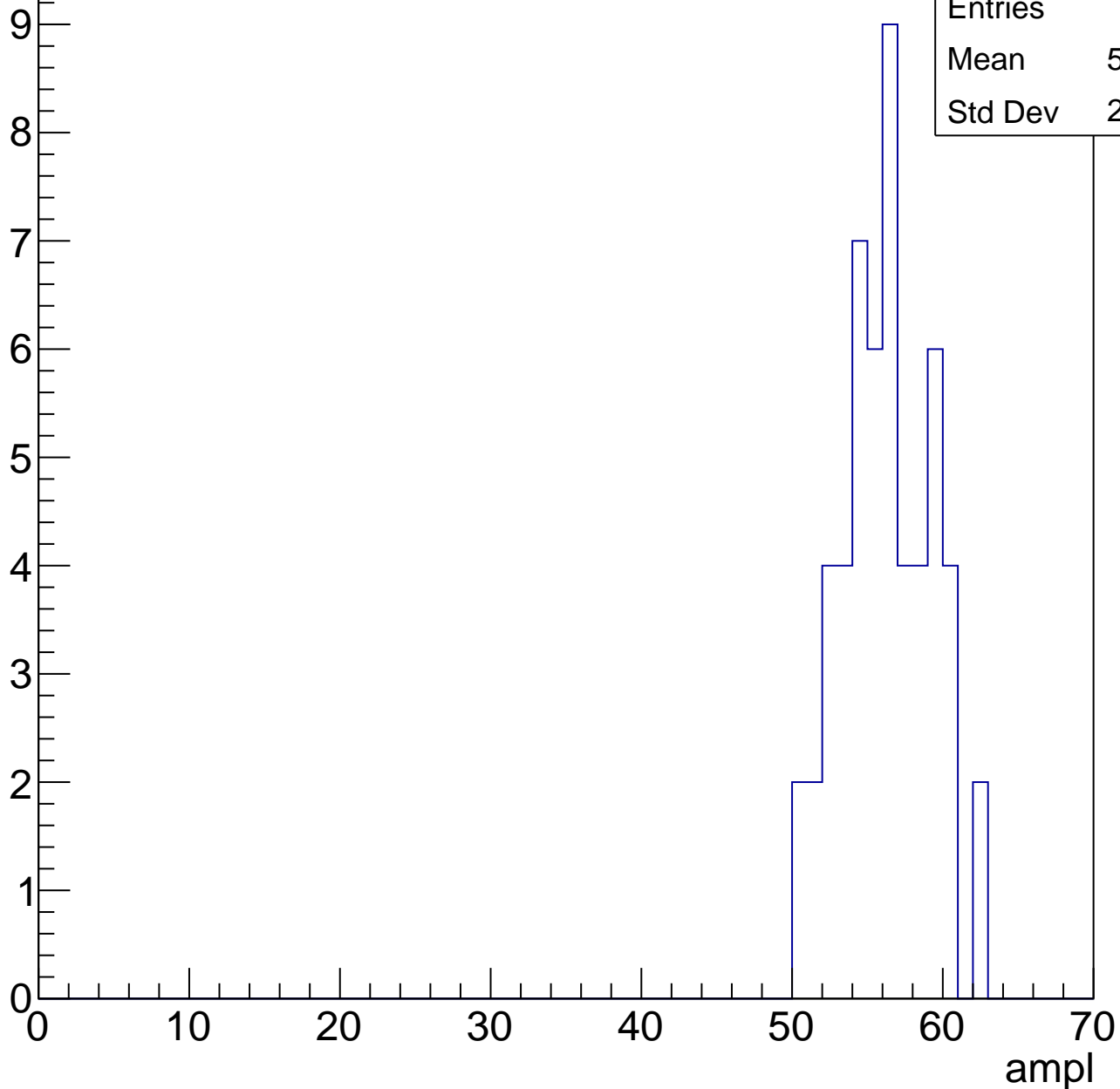
Entries	62
Mean	49.89
Std Dev	3.547



B0L102S, U5-ch75, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	54
Mean	55.78
Std Dev	2.929

B0L102S, U5-ch75, adc5

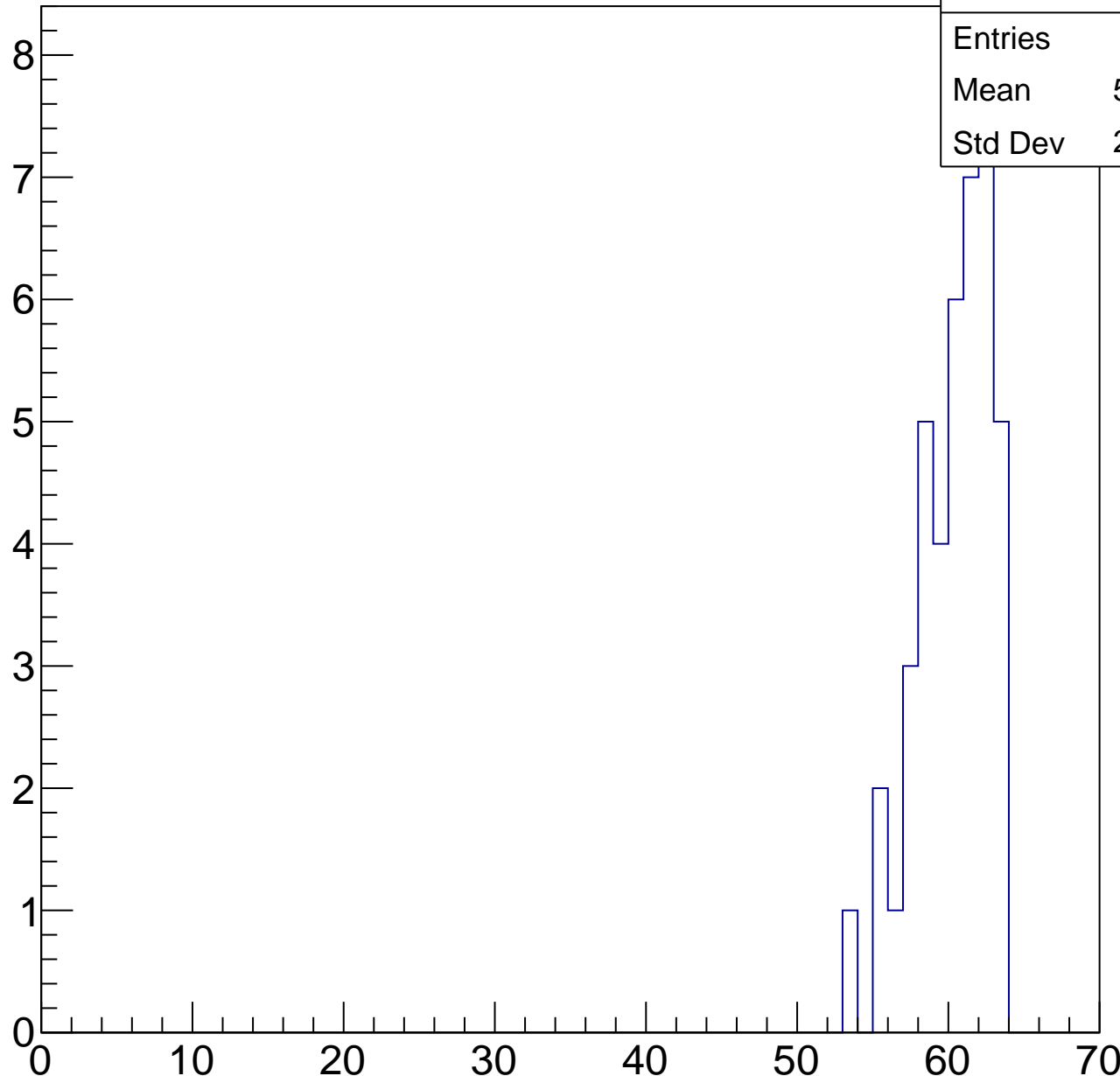
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.86
Std Dev	2.436

ampl

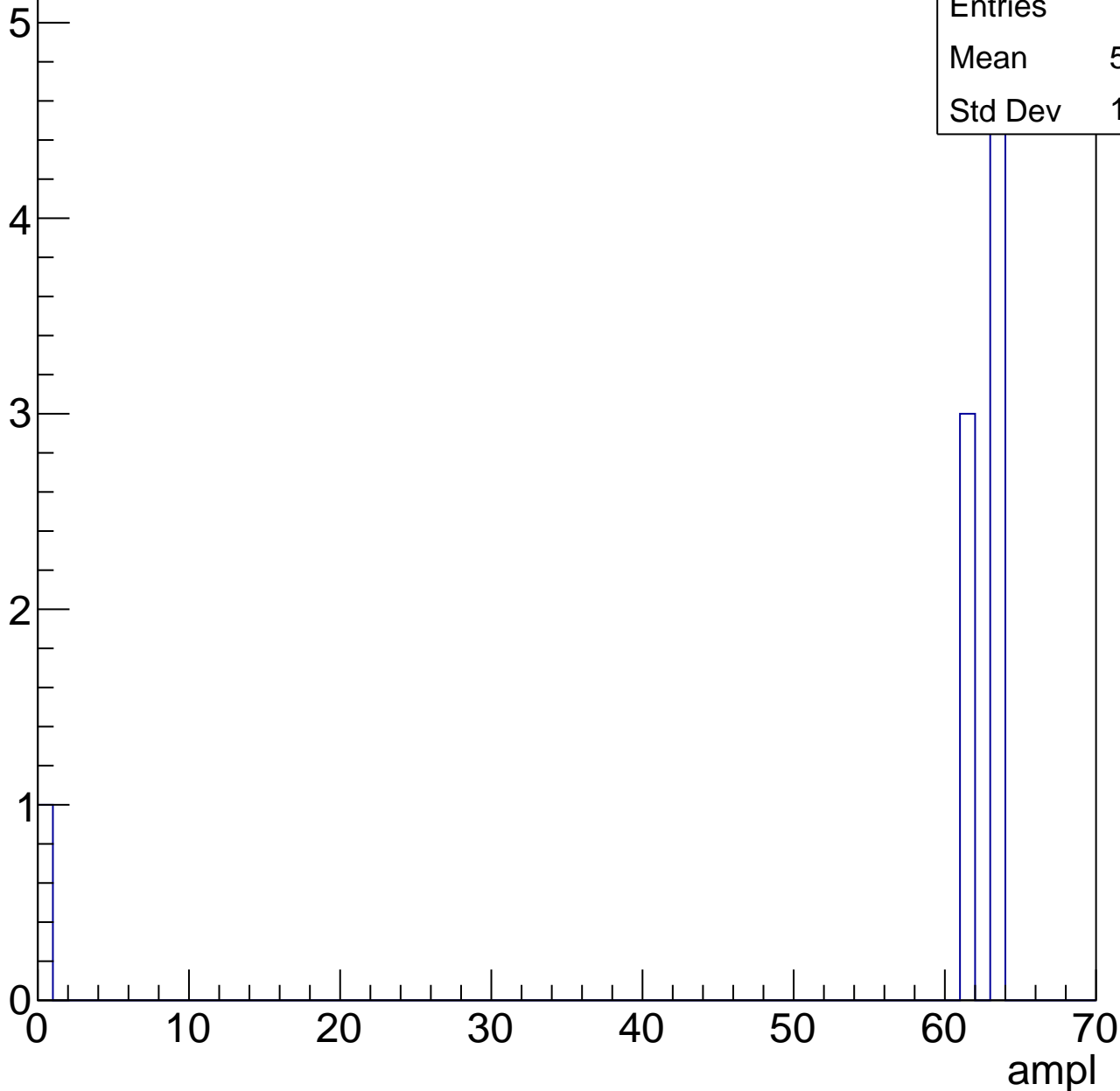


B0L102S, U5-ch75, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	9
Mean	55.33
Std Dev	19.58



B0L102S, U5-ch75, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	10
Std Dev	10

B0L102S, U5-ch76, adc0

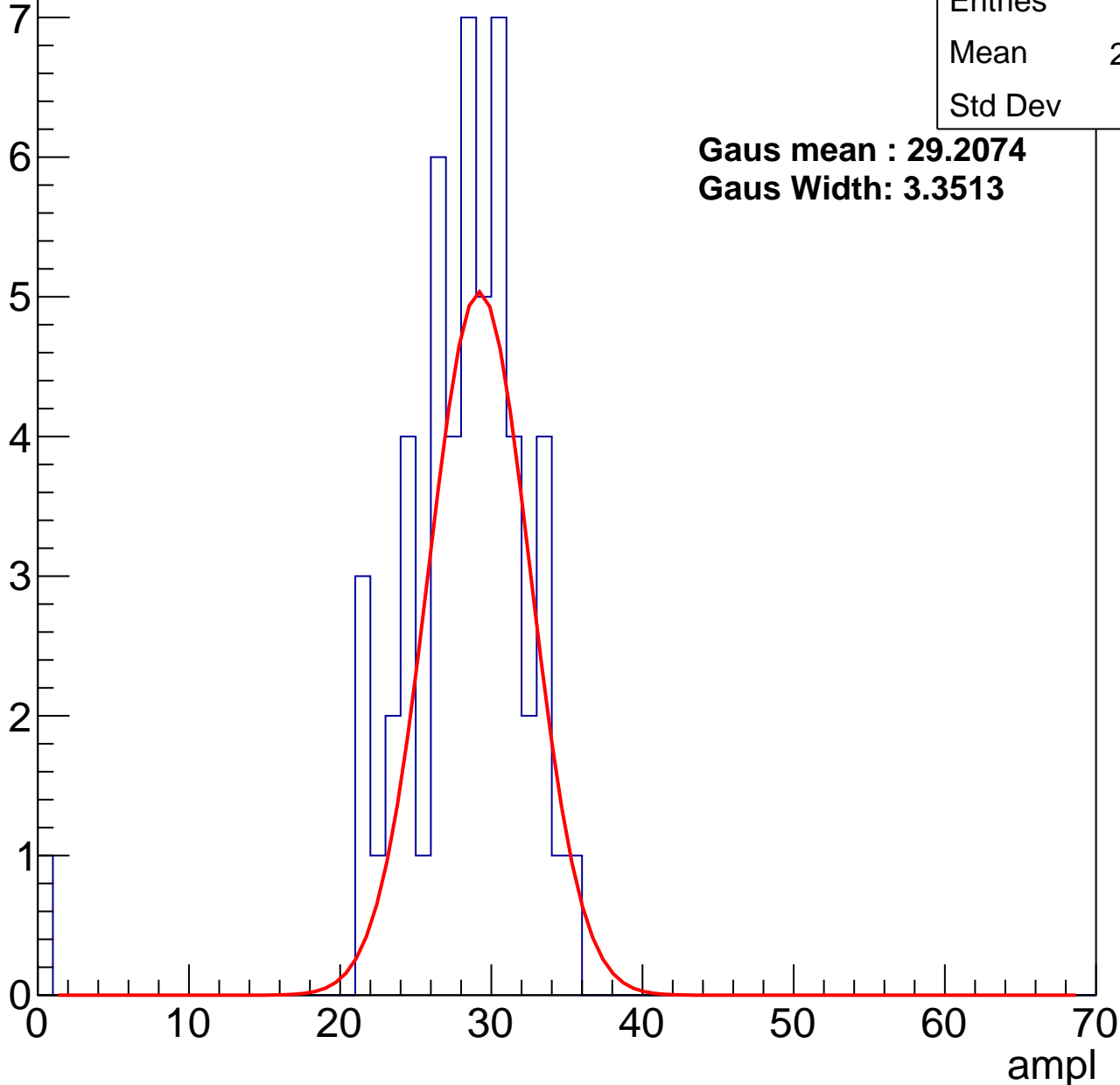
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	27.47
Std Dev	5.12

Gaus mean : 29.2074

Gaus Width: 3.3513



B0L102S, U5-ch76, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	86
Mean	34.23
Std Dev	5.306

Gaus mean : 35.1334

Gaus Width: 4.2362

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L102S, U5-ch76, adc2

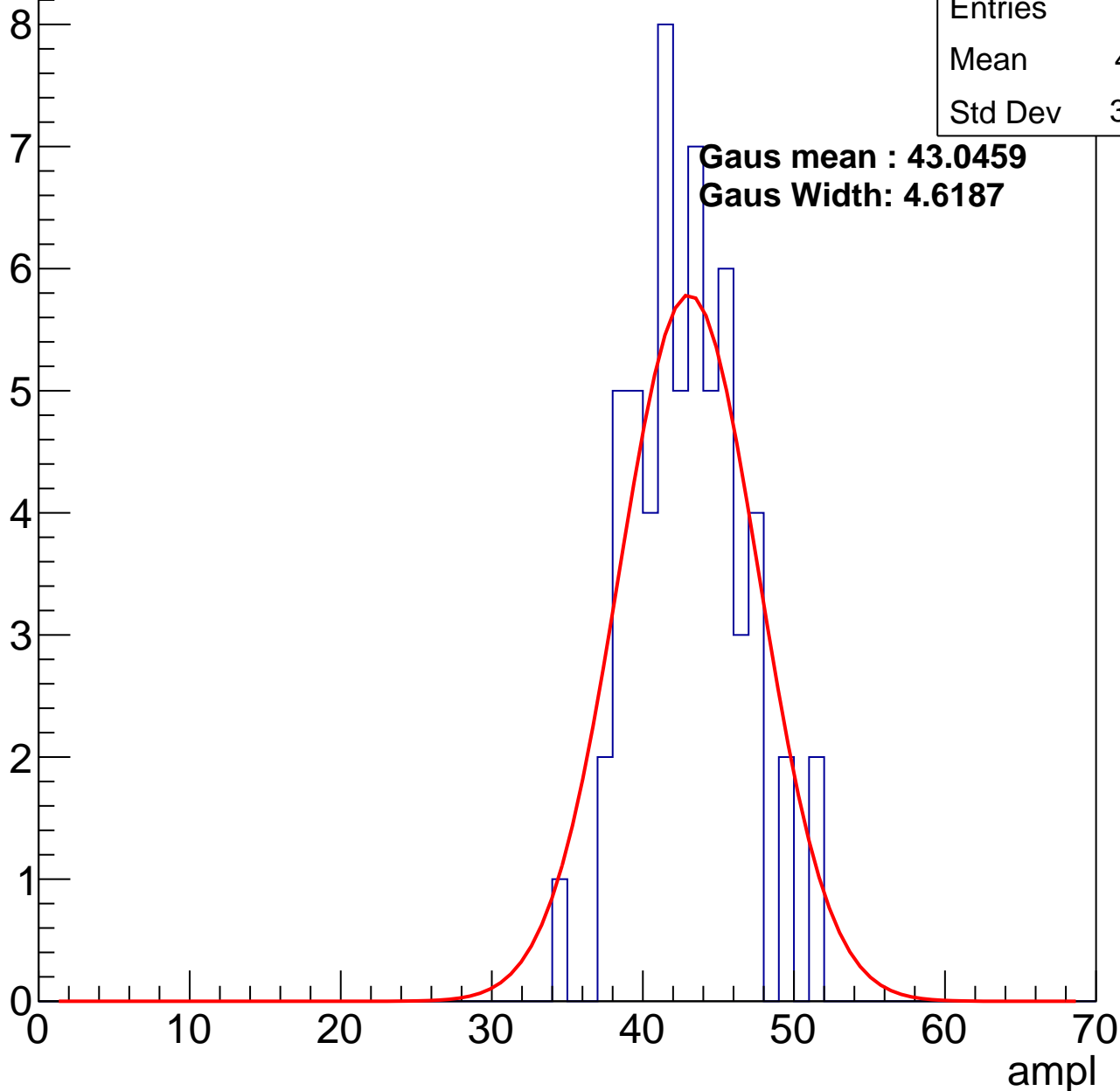
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	42.51
Std Dev	3.534

Gaus mean : 43.0459

Gaus Width: 4.6187

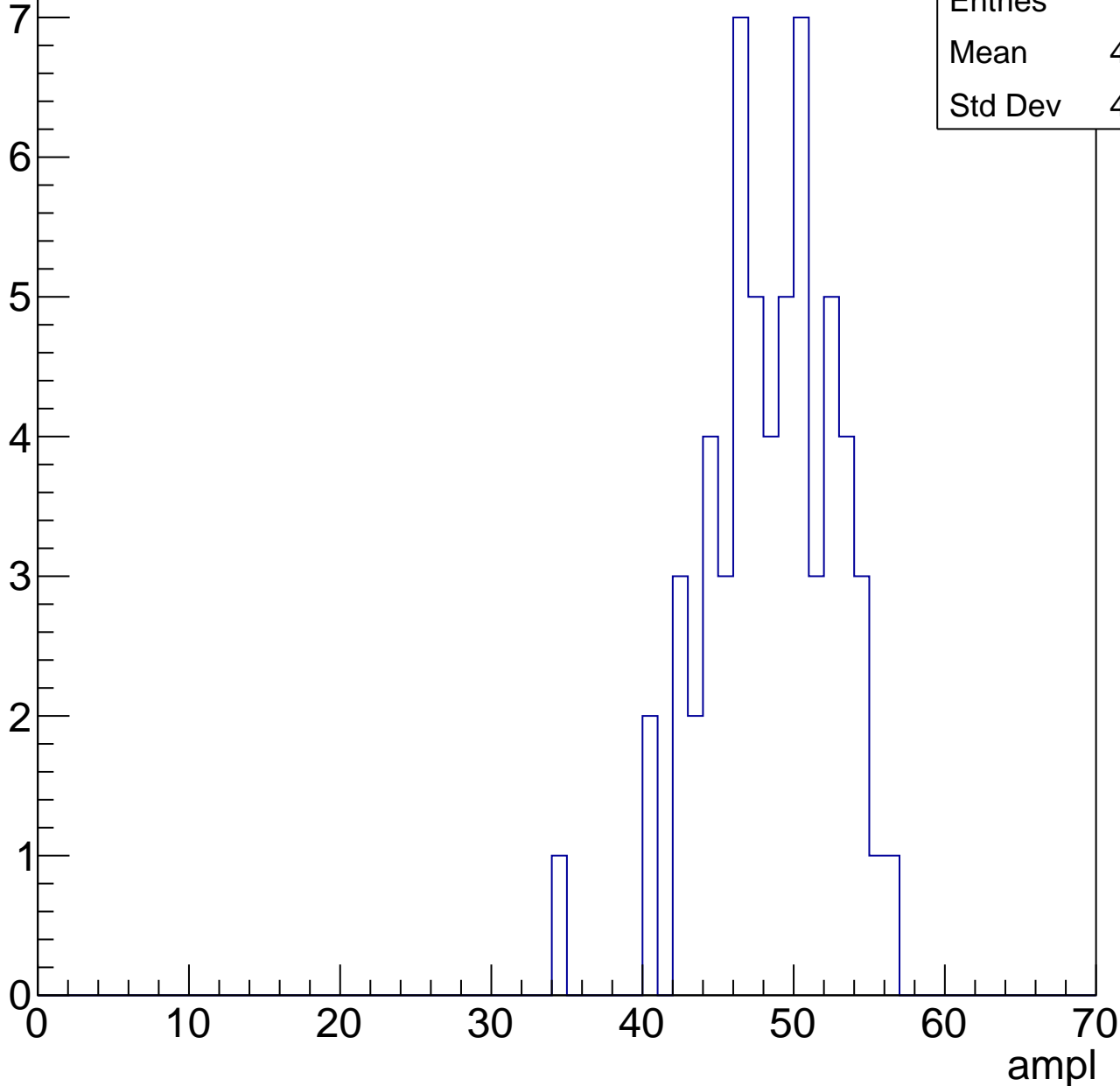


B0L102S, U5-ch76, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	47.98
Std Dev	4.217

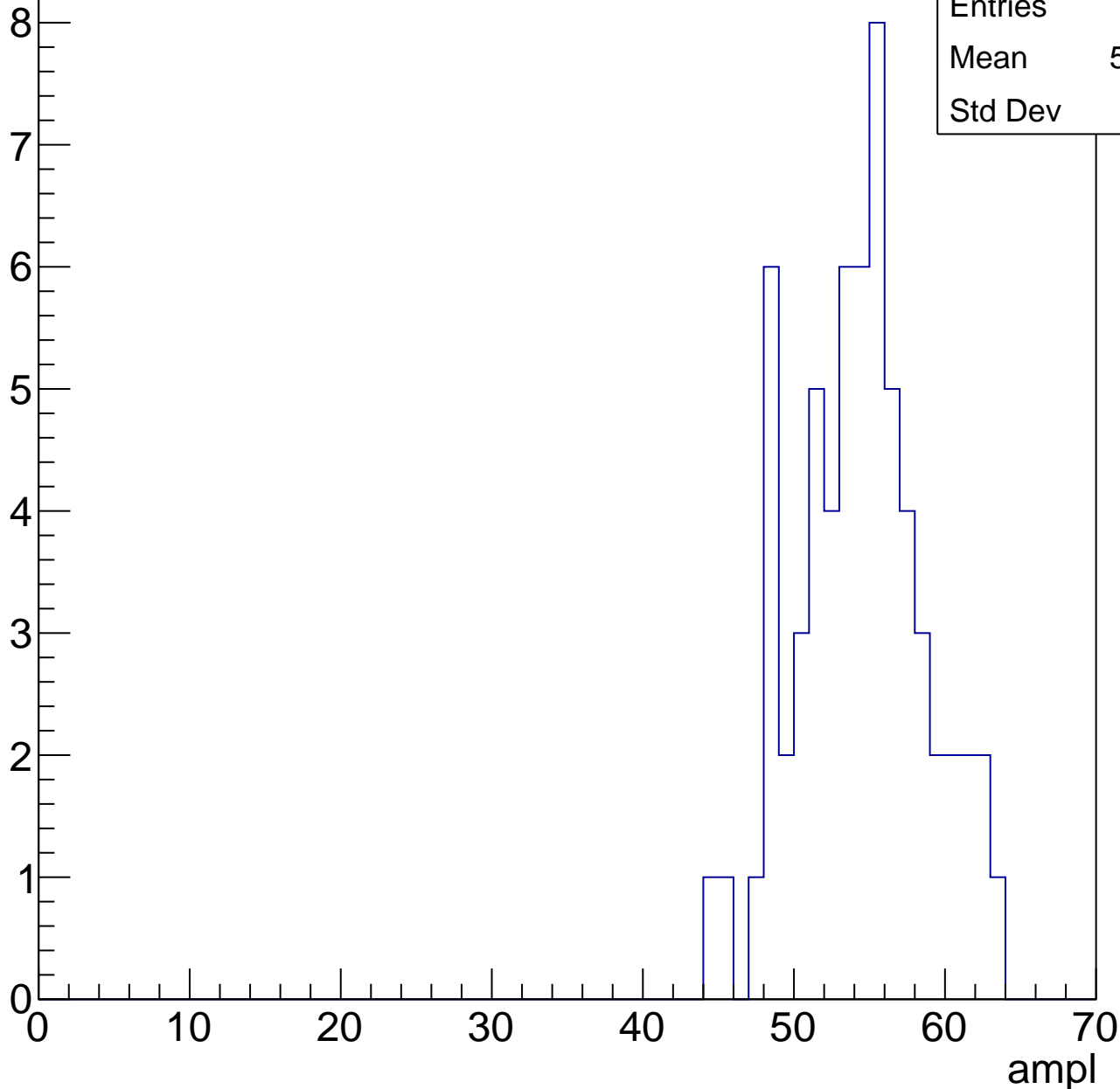


B0L102S, U5-ch76, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	53.84
Std Dev	4.24

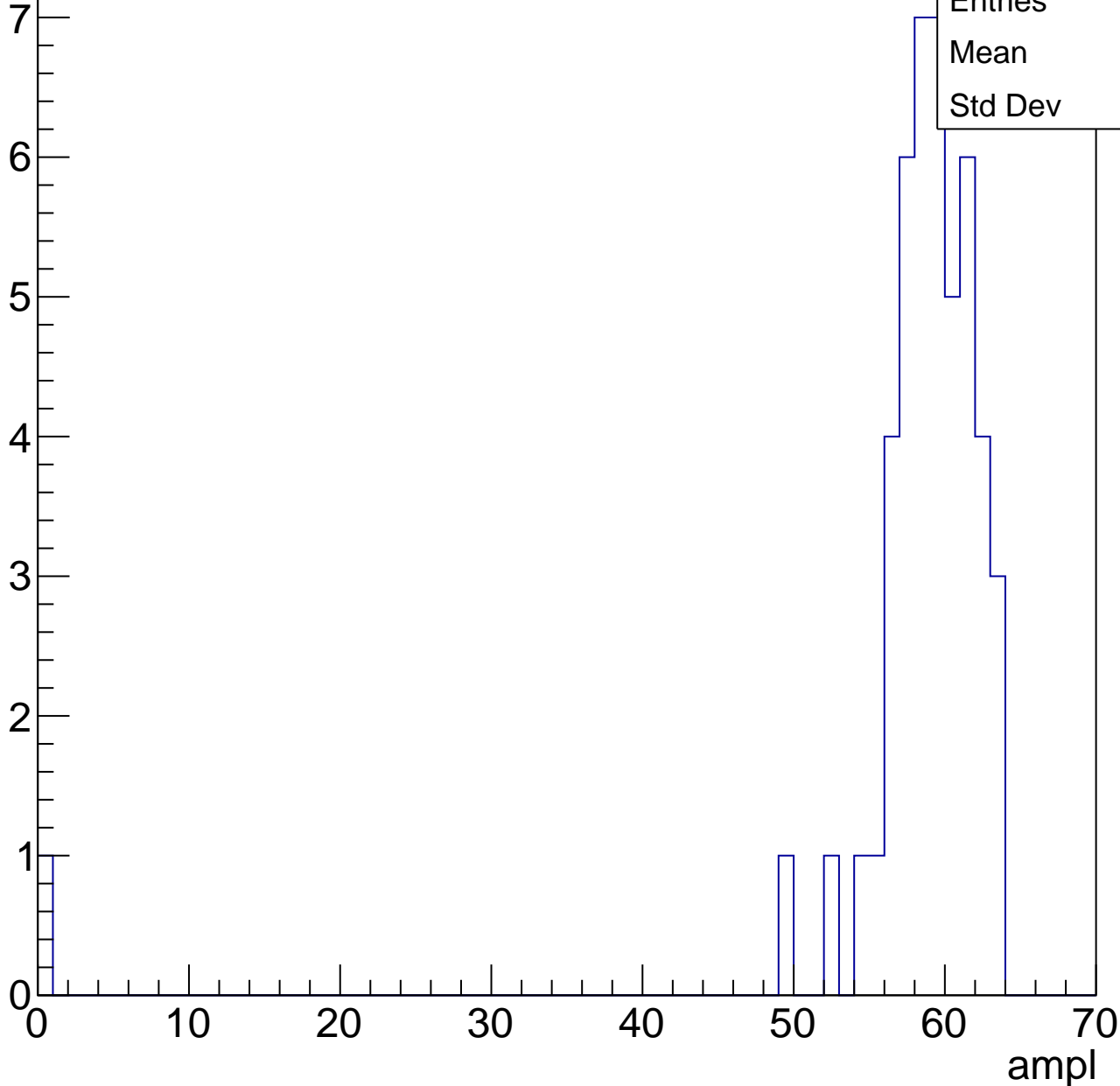


B0L102S, U5-ch76, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	57.4
Std Dev	8.91

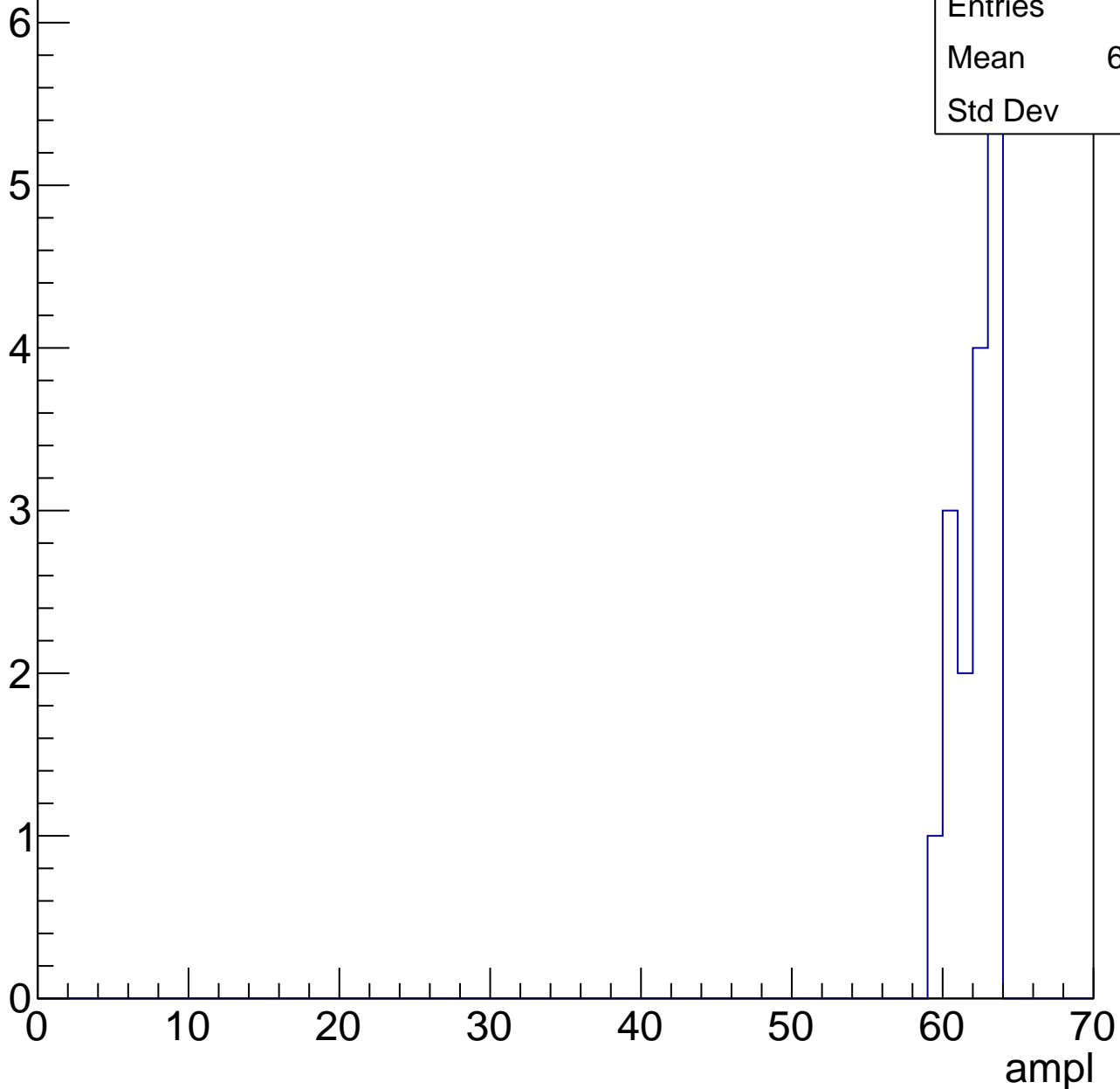


B0L102S, U5-ch76, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	16
Mean	61.69
Std Dev	1.31



B0L102S, U5-ch76, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch77, adc0

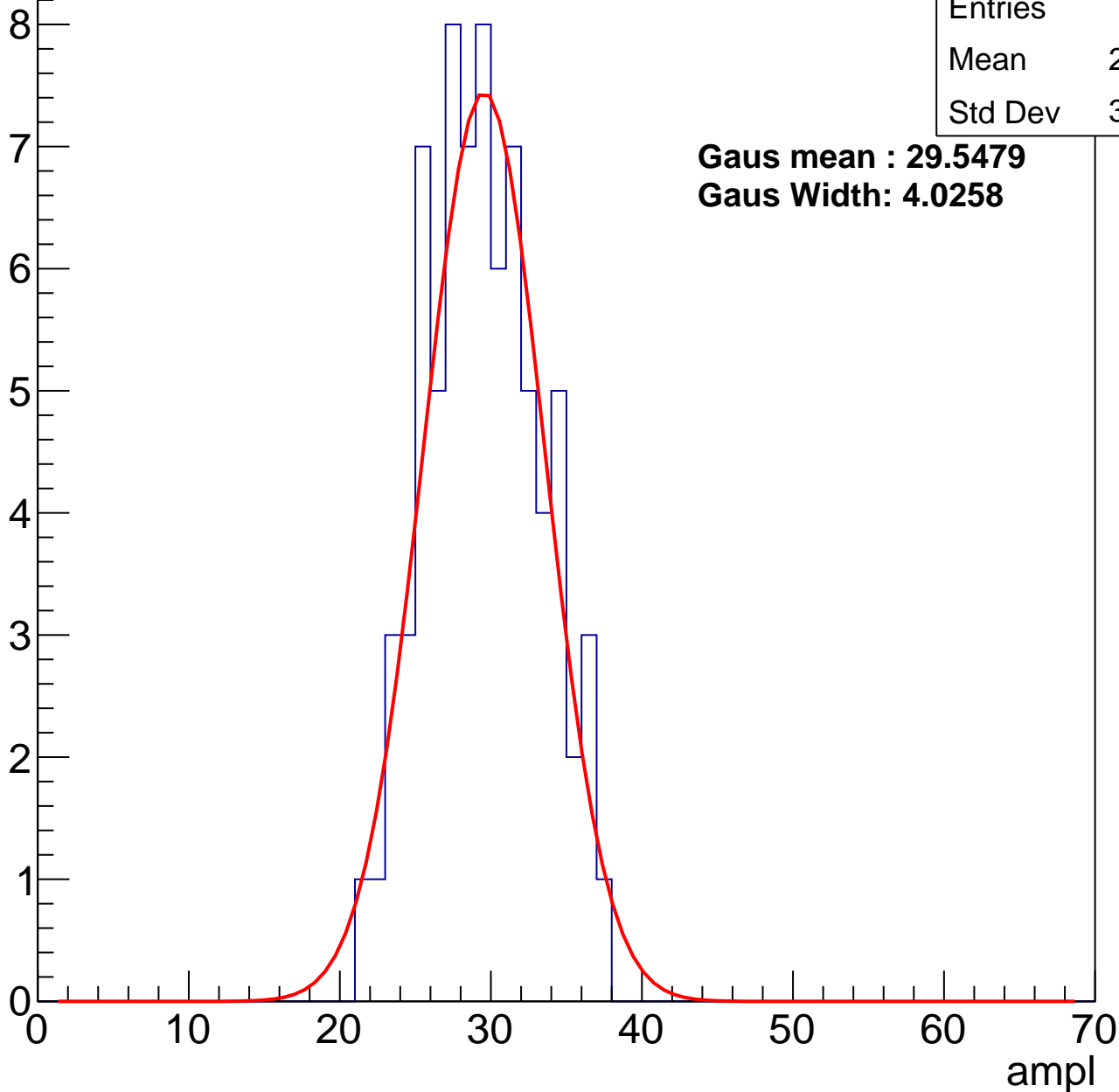
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	29.04
Std Dev	3.704

Gaus mean : 29.5479

Gaus Width: 4.0258



B0L102S, U5-ch77, adc1

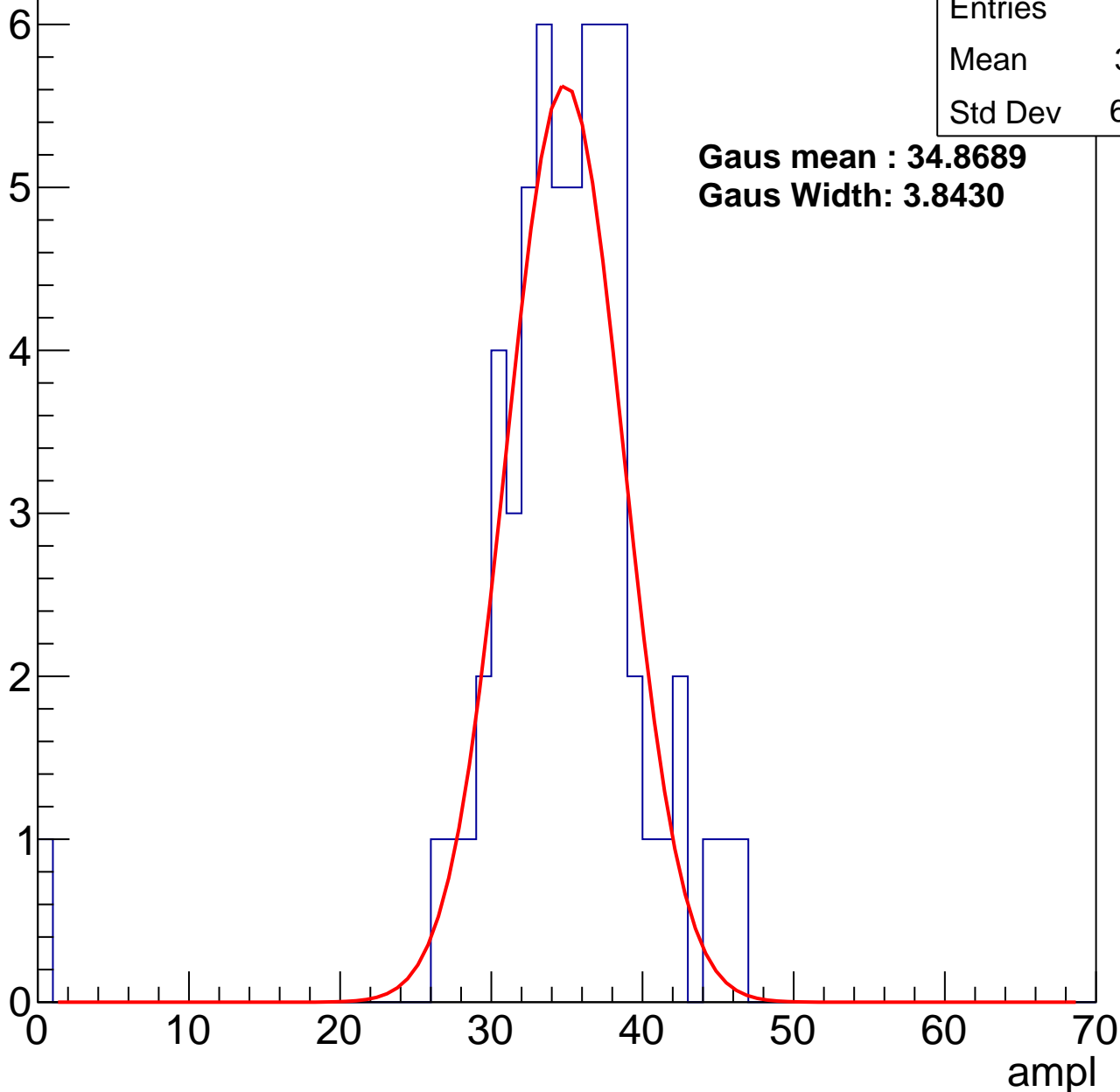
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	34.41
Std Dev	6.099

Gaus mean : 34.8689

Gaus Width: 3.8430



B0L102S, U5-ch77, adc2

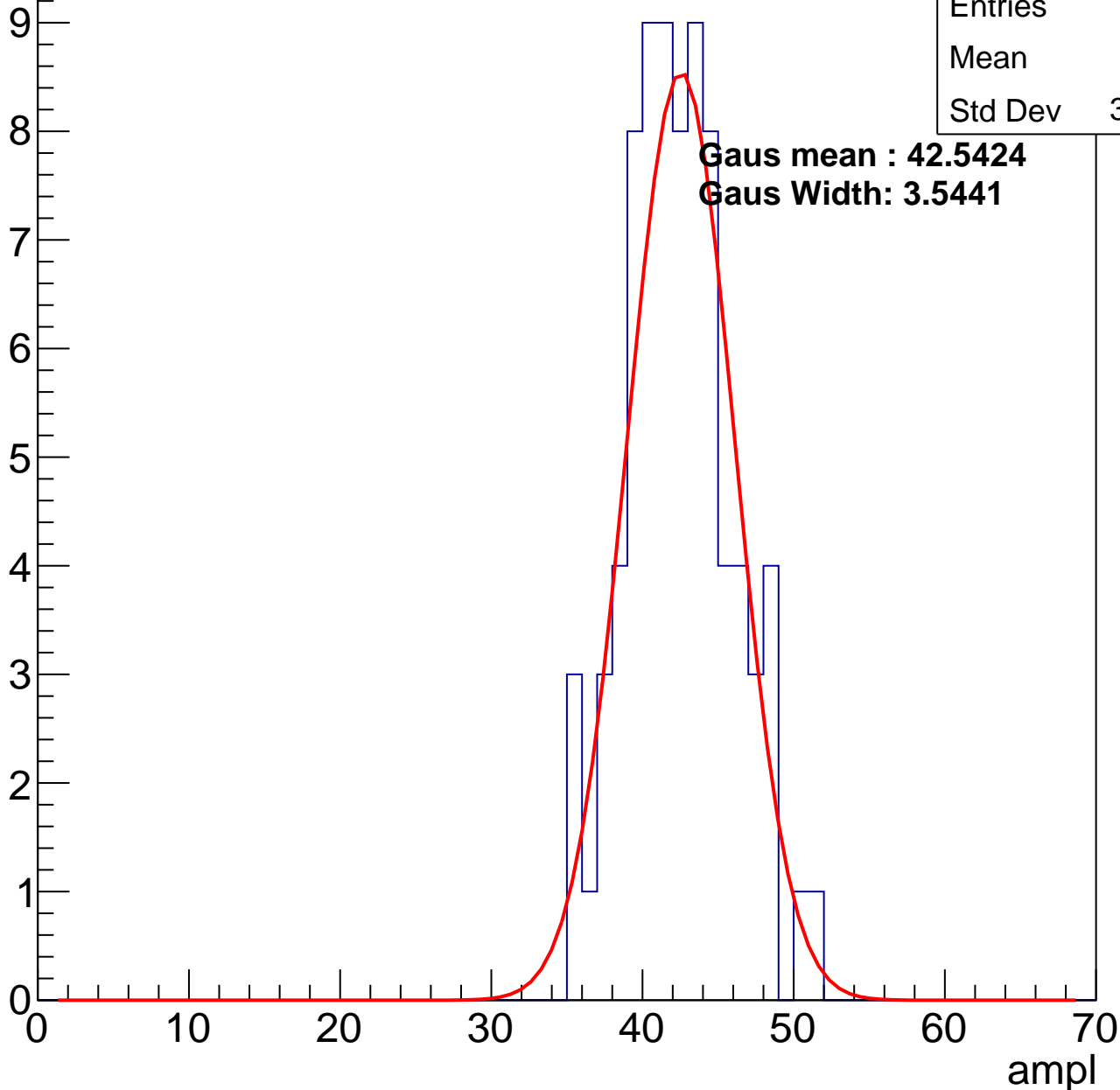
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	42
Std Dev	3.457

Gaus mean : 42.5424

Gaus Width: 3.5441

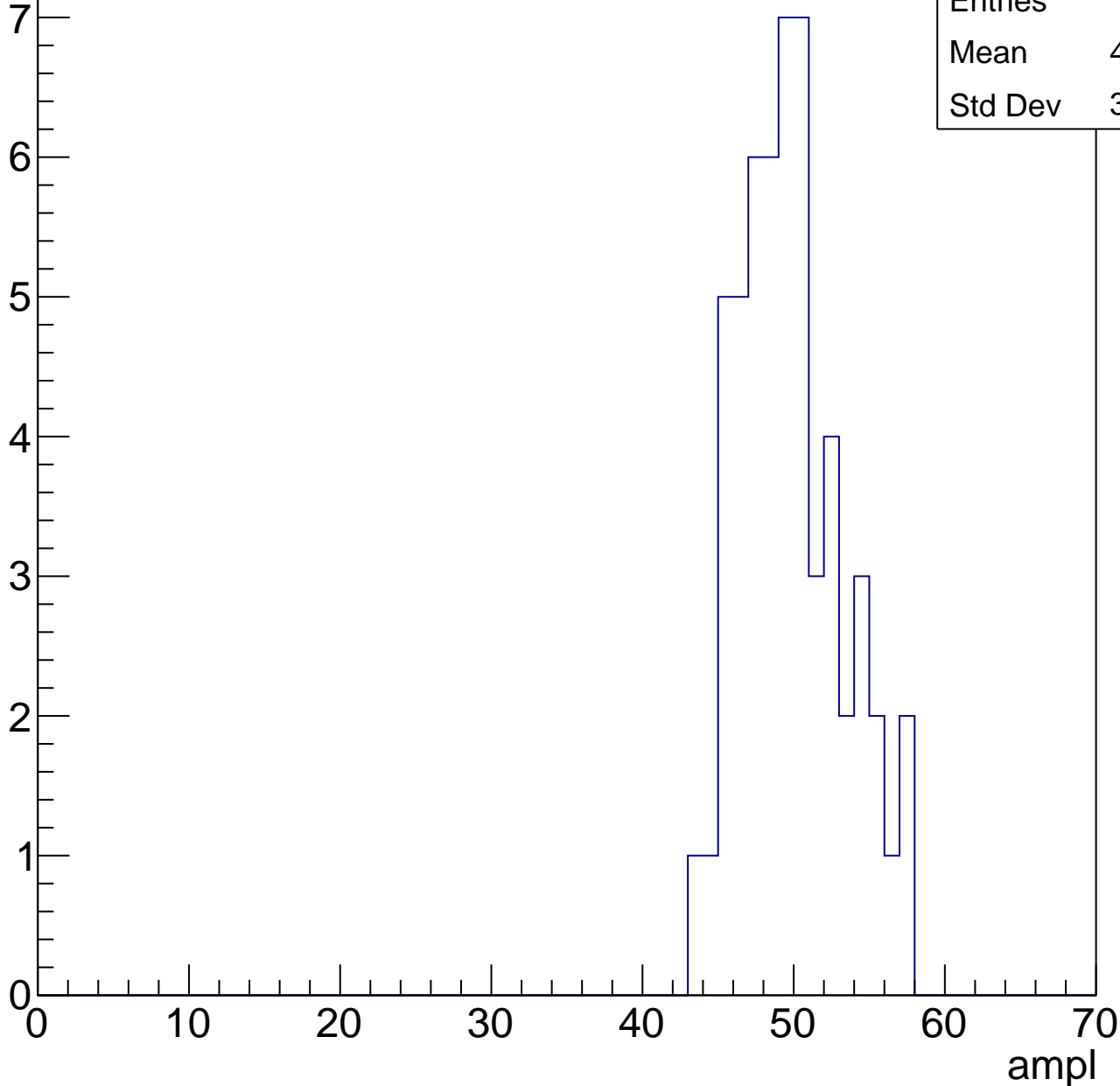


B0L102S, U5-ch77, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

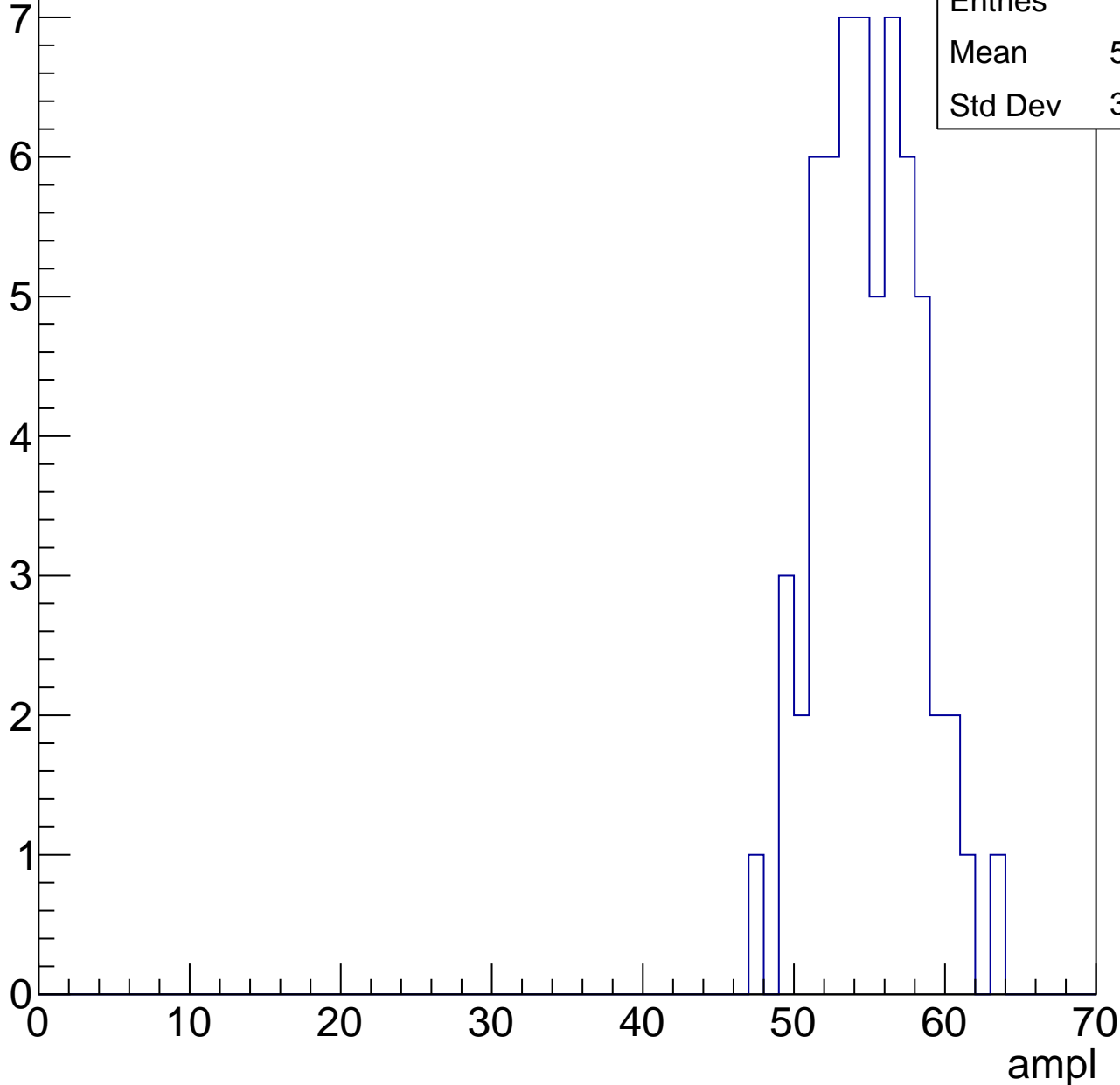
Entries	55
Mean	49.35
Std Dev	3.359



B0L102S, U5-ch77, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

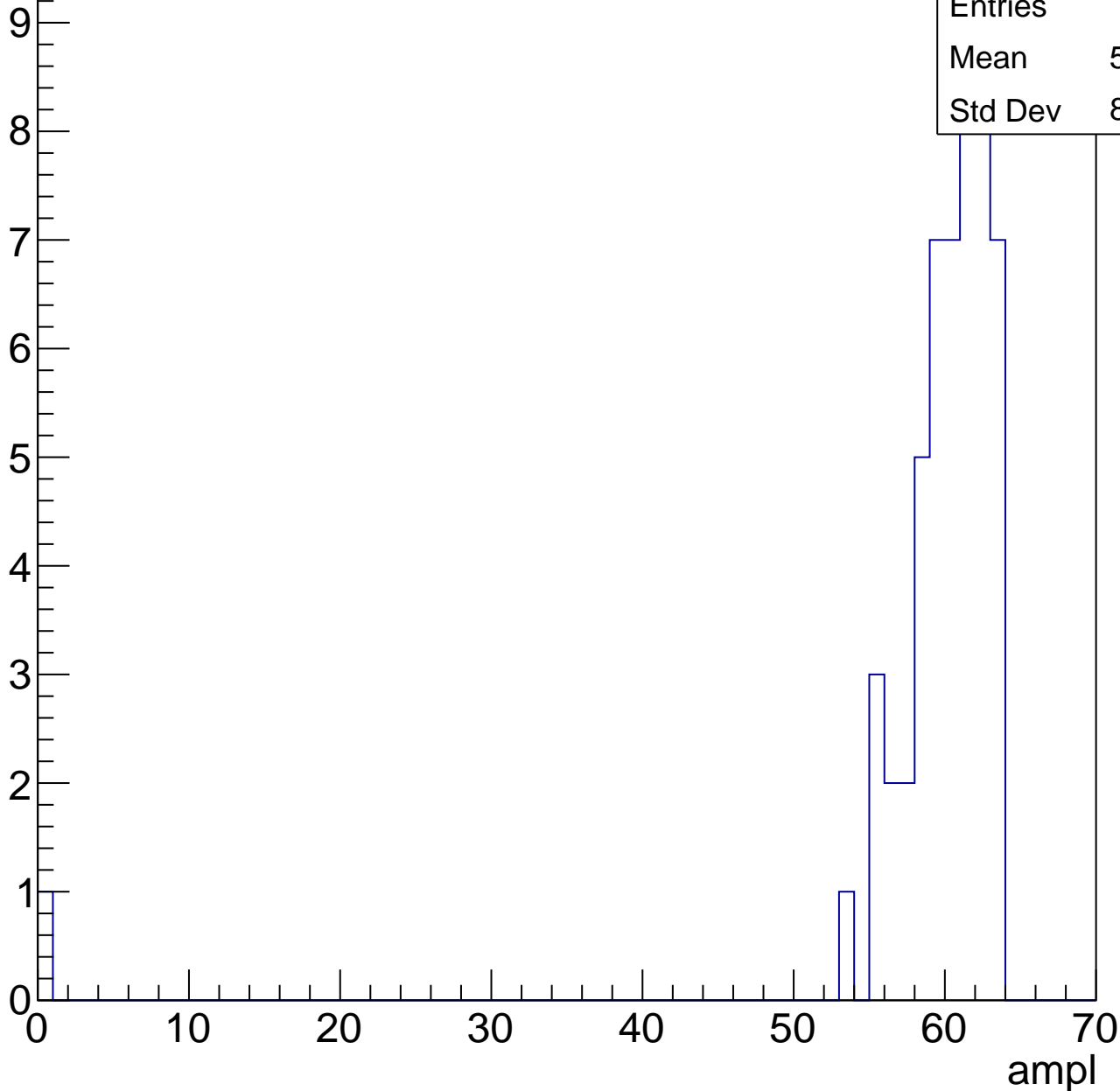


B0L102S, U5-ch77, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	58.73
Std Dev	8.576



B0L102S, U5-ch77, adc6

calib_packv5_042523_0143.root, FC#12, port B1

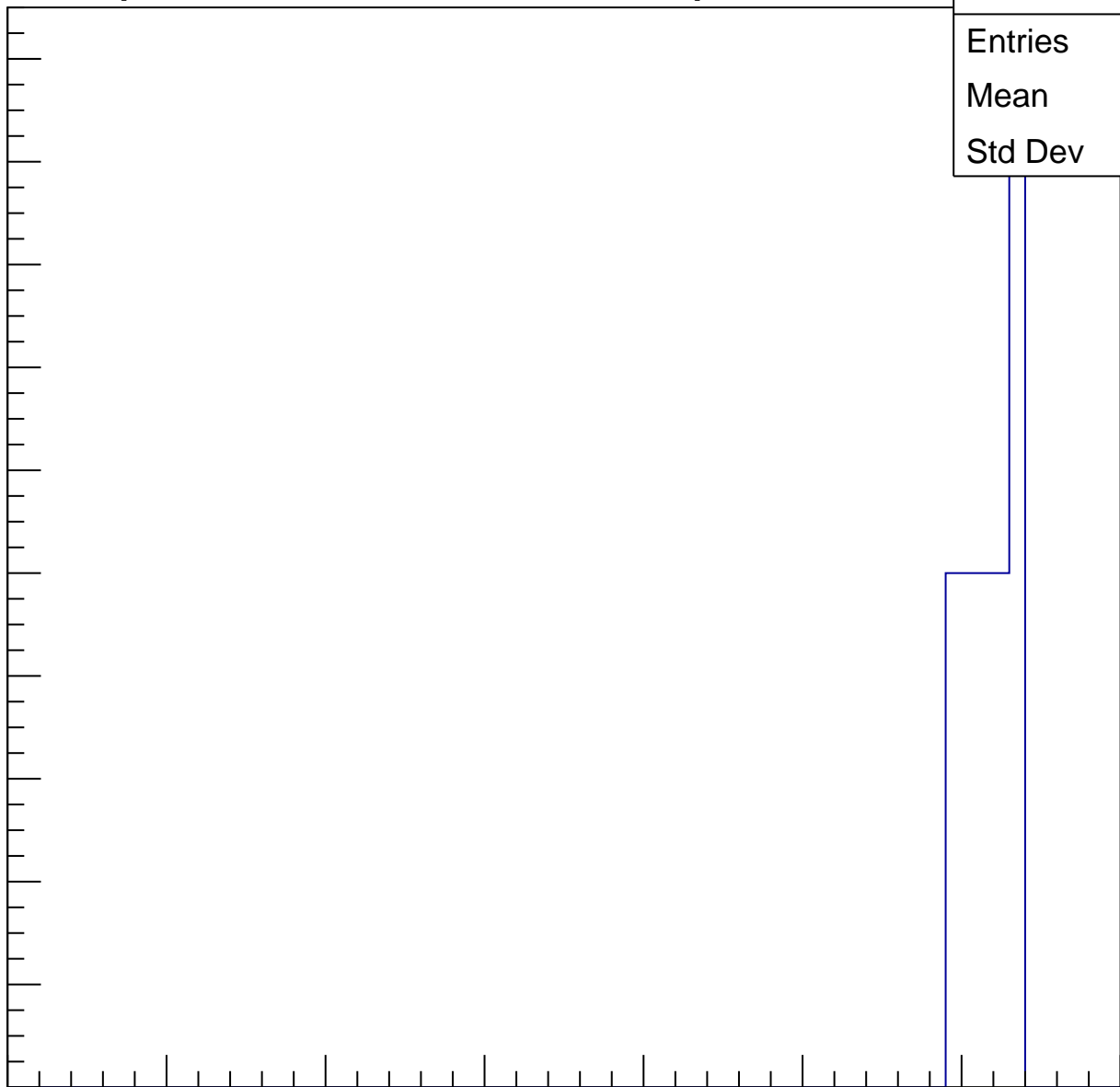
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.491

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch77, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch78, adc0

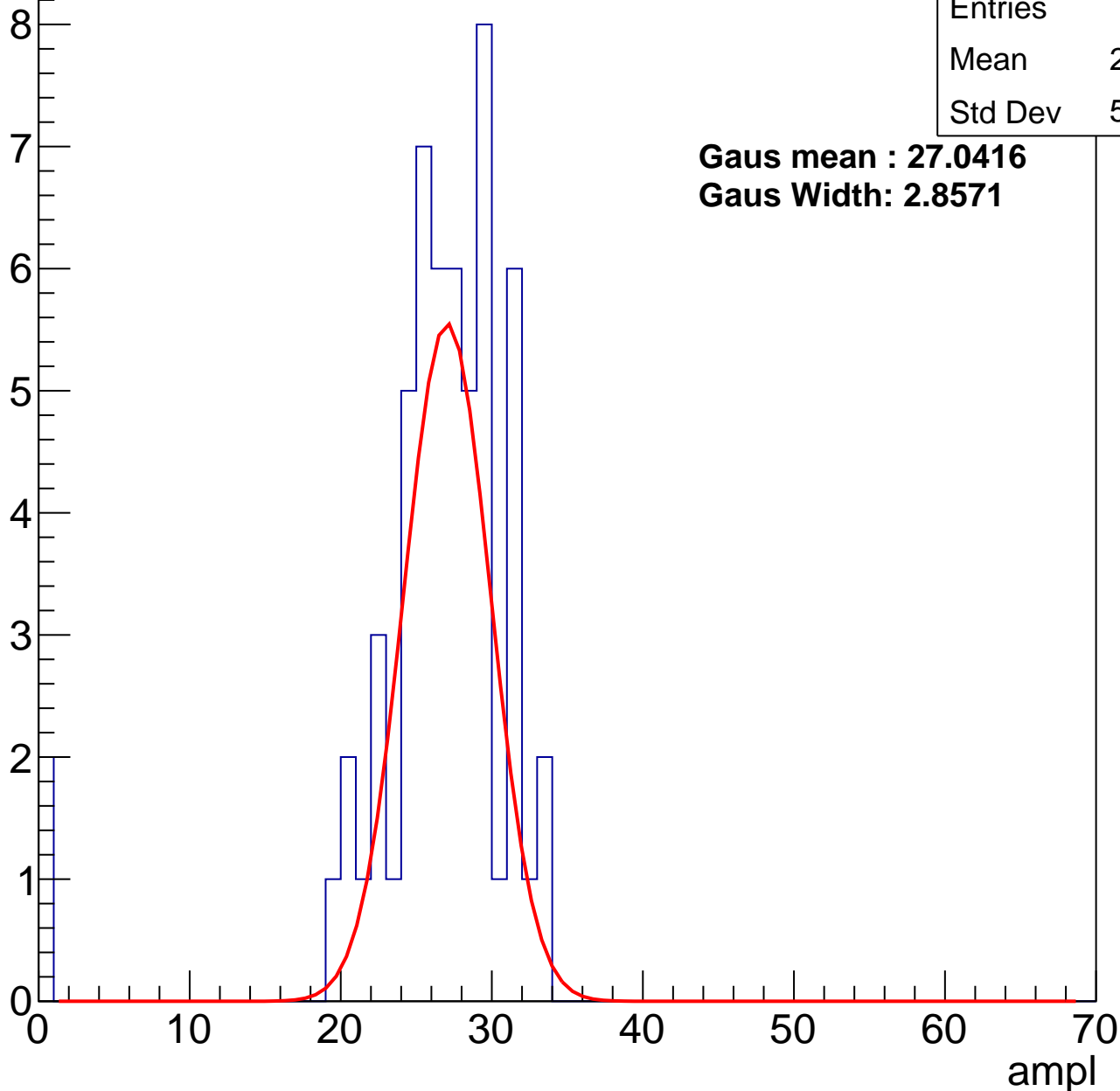
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	25.75
Std Dev	5.889

Gaus mean : 27.0416

Gaus Width: 2.8571



B0L102S, U5-ch78, adc1

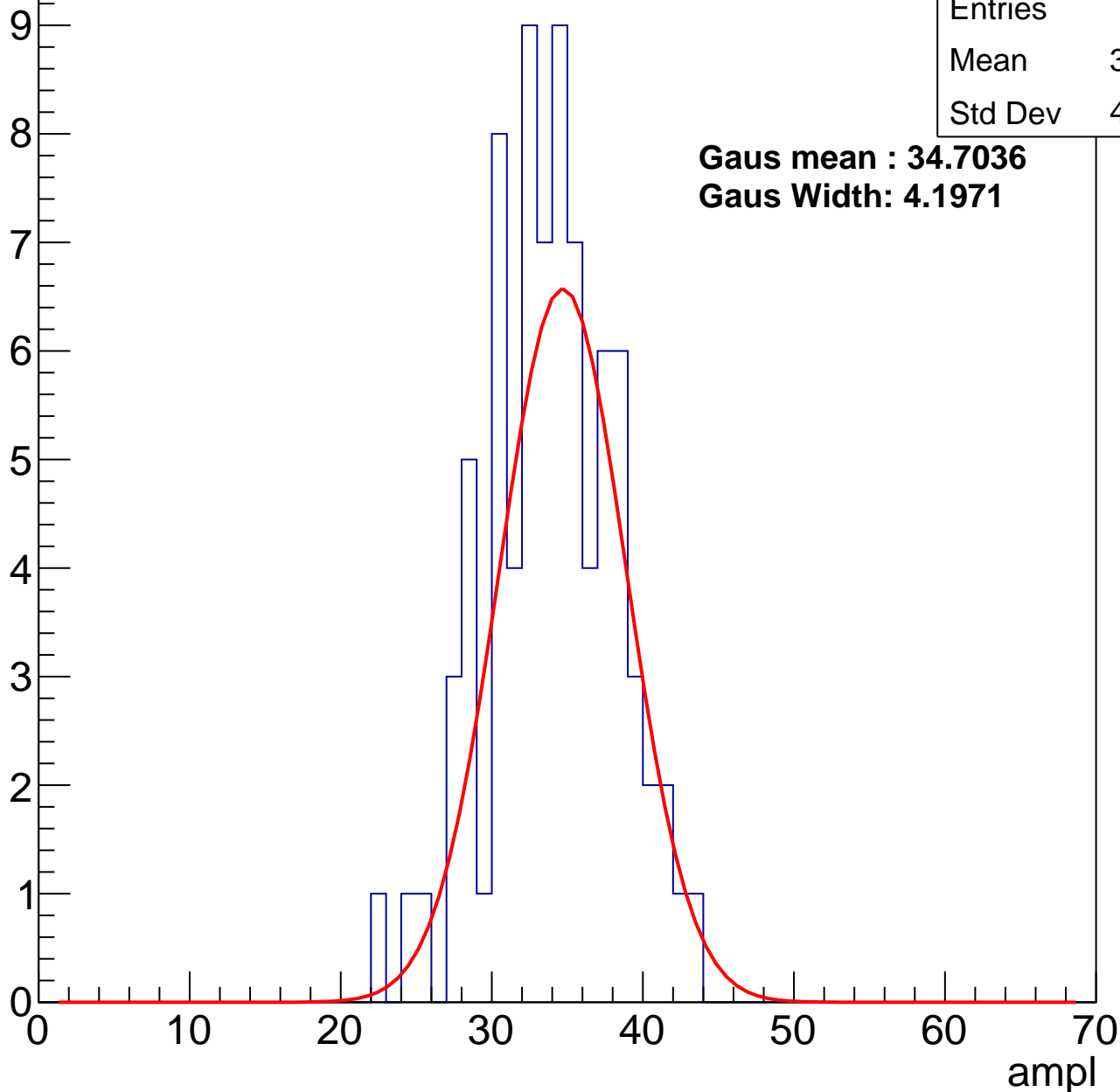
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	33.49
Std Dev	4.196

Gaus mean : 34.7036

Gaus Width: 4.1971



B0L102S, U5-ch78, adc2

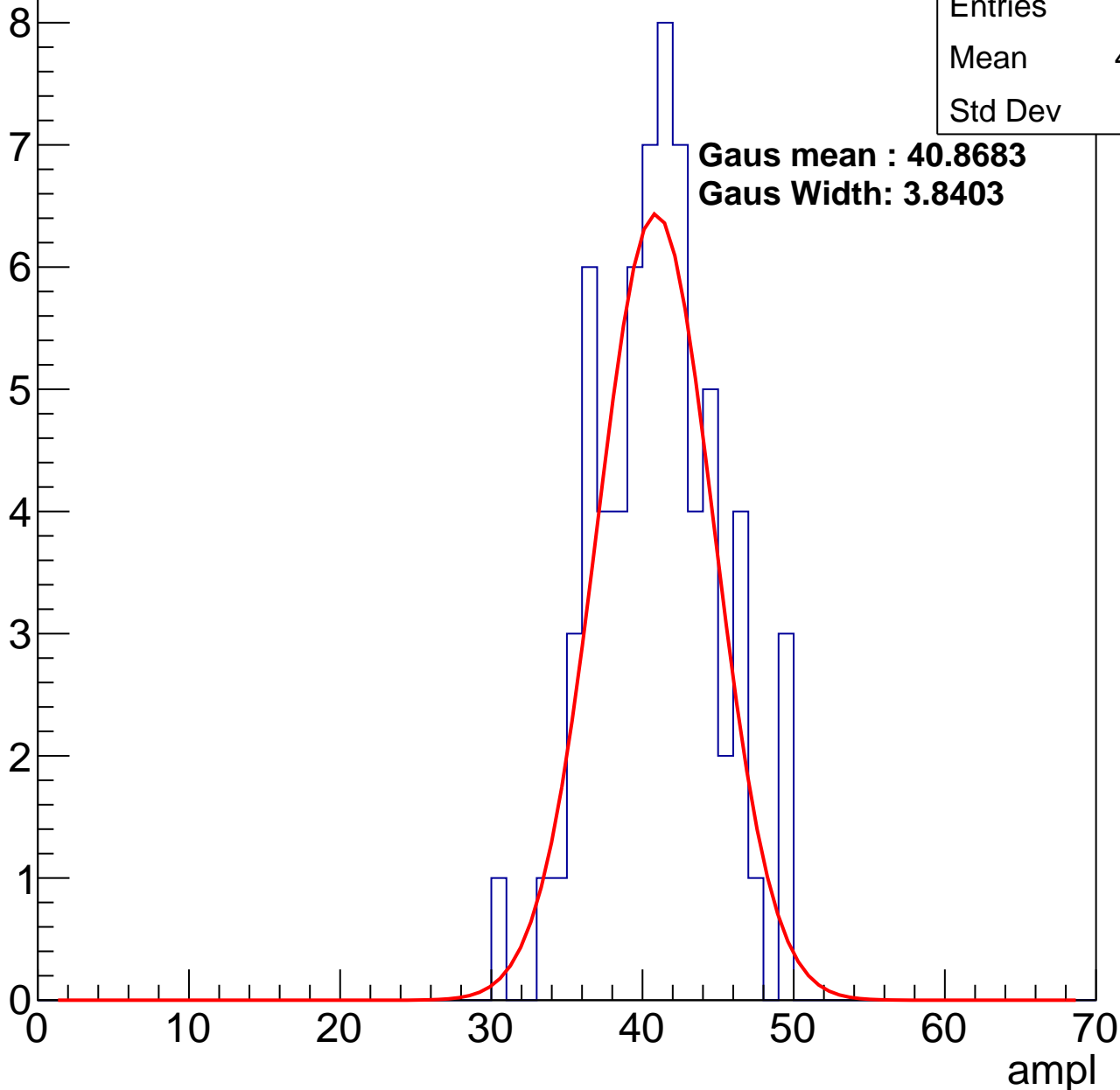
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	40.51
Std Dev	3.93

Gaus mean : 40.8683

Gaus Width: 3.8403

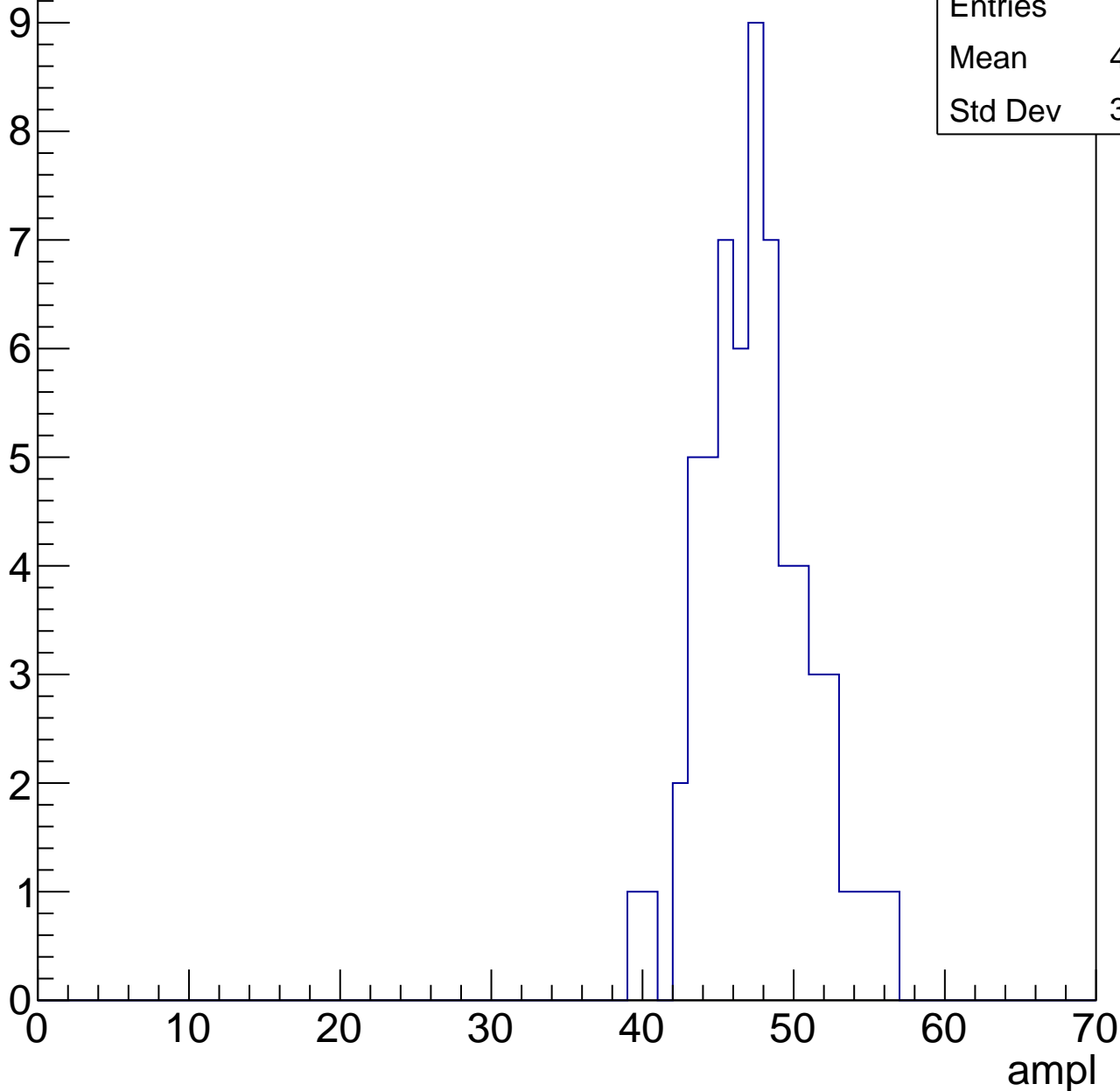


B0L102S, U5-ch78, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	47.07
Std Dev	3.468

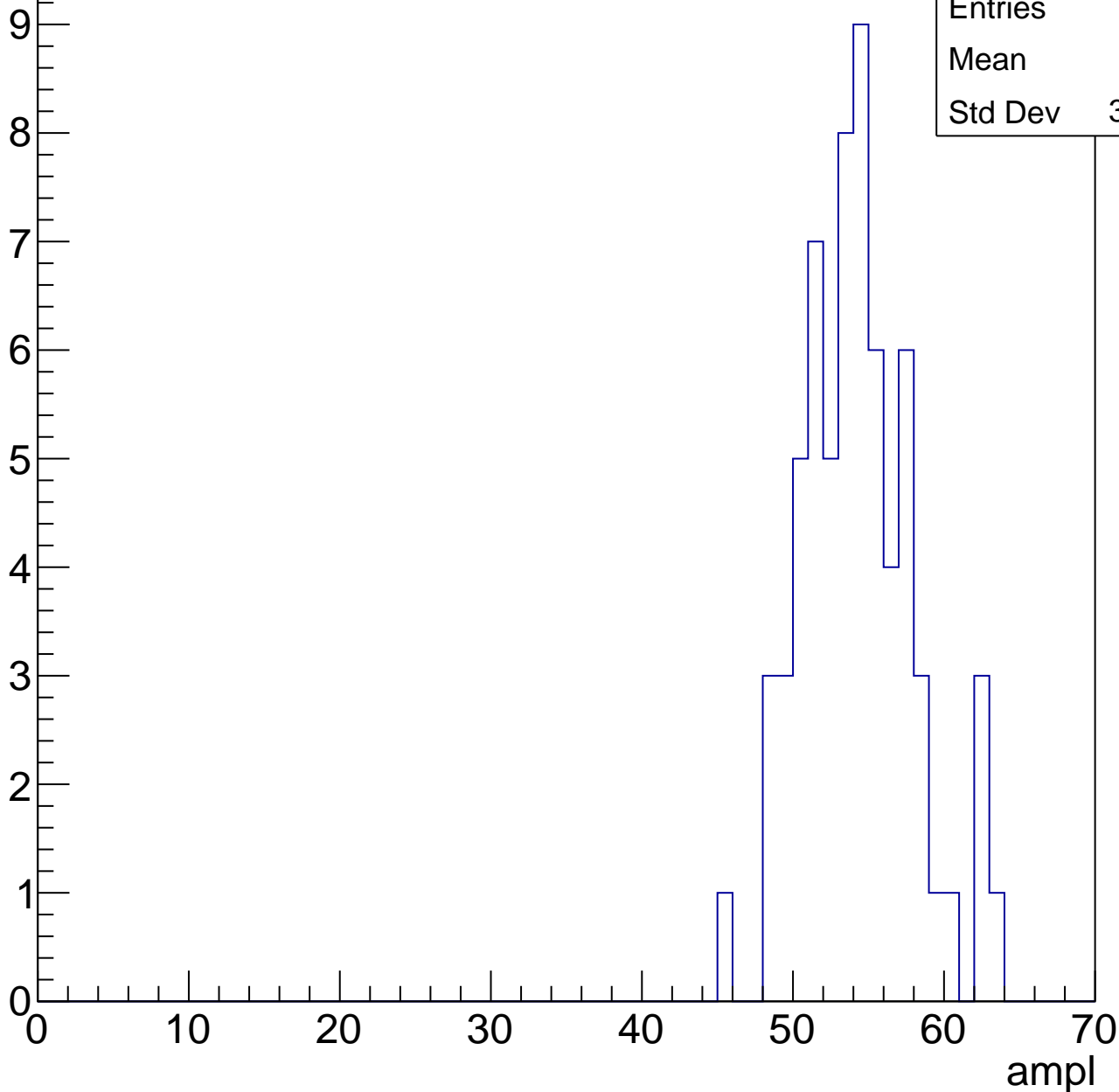


B0L102S, U5-ch78, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	53.8
Std Dev	3.673

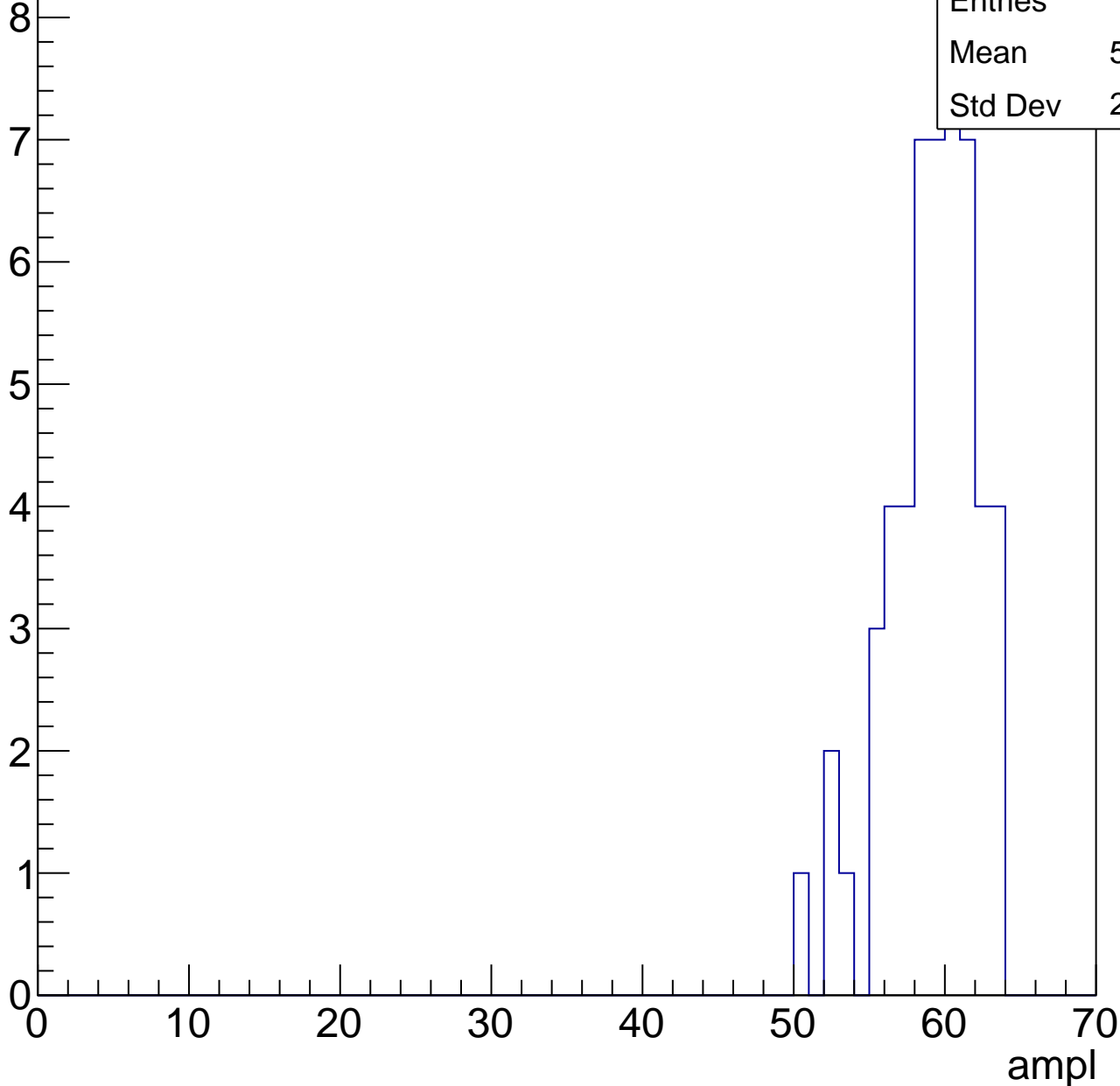


B0L102S, U5-ch78, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

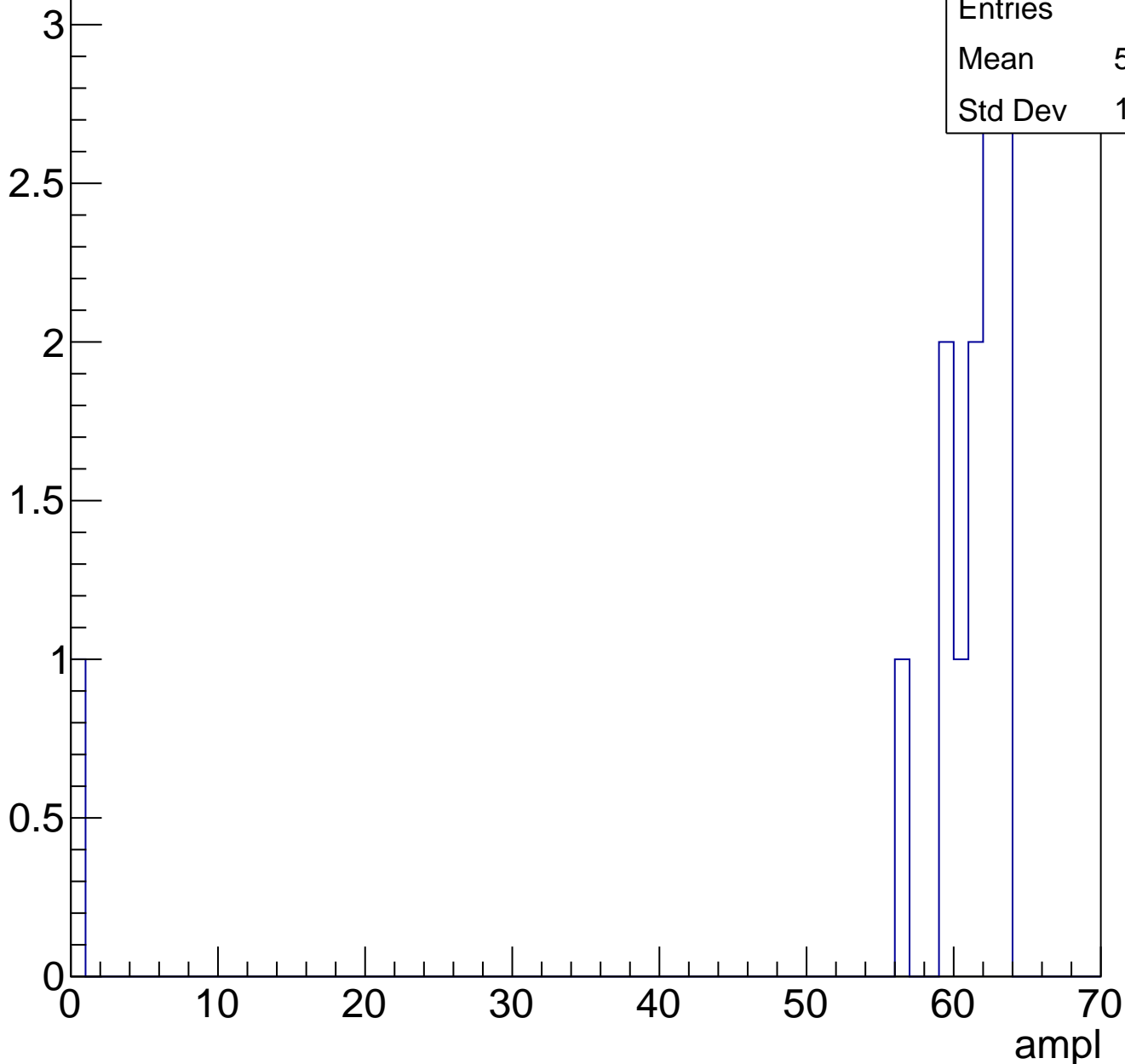
Entries	52
Mean	58.65
Std Dev	2.948



B0L102S, U5-ch78, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch78, adc7

calib_packv5_042523_0143.root, FC#12, port B1

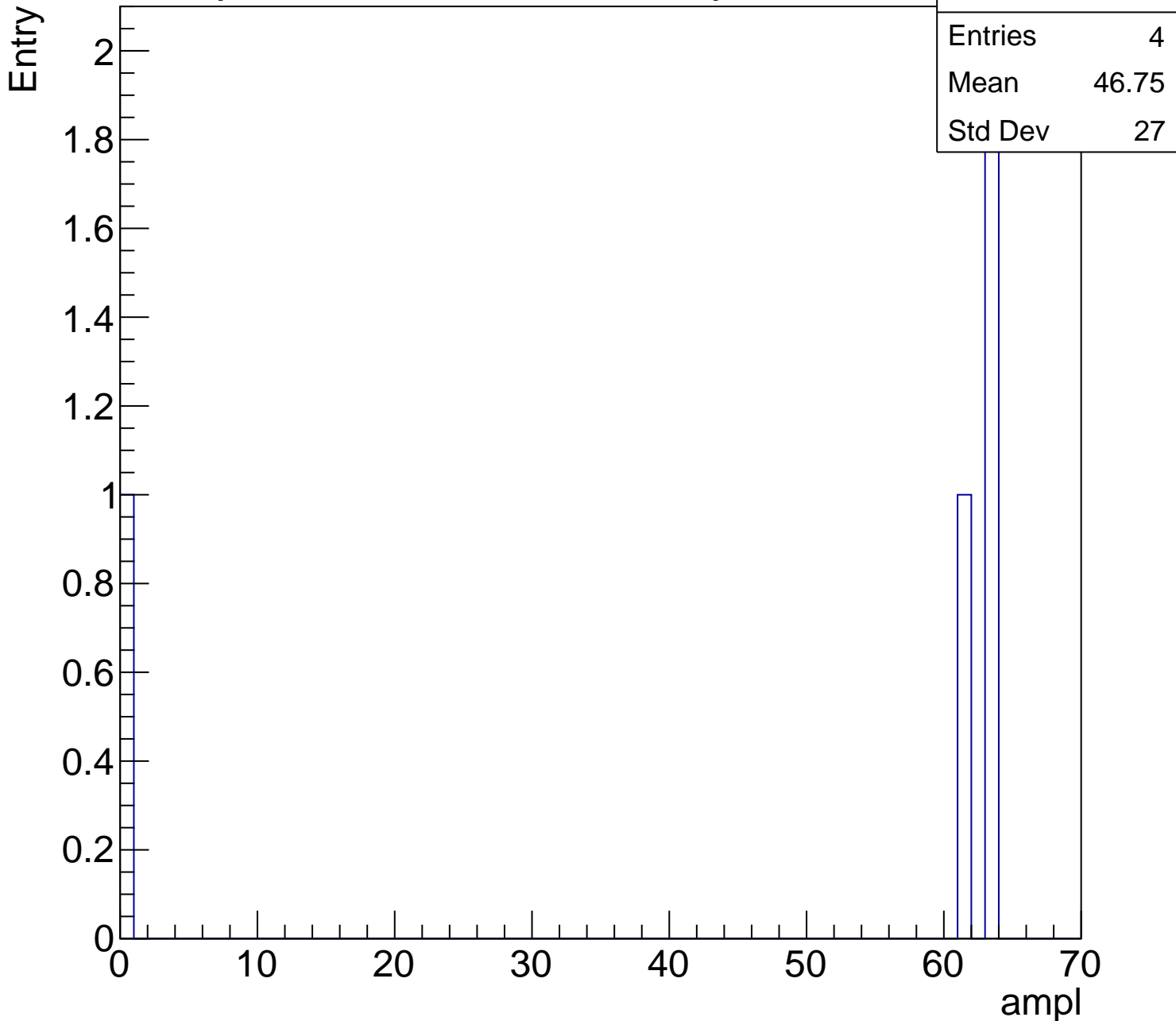
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch79, adc0

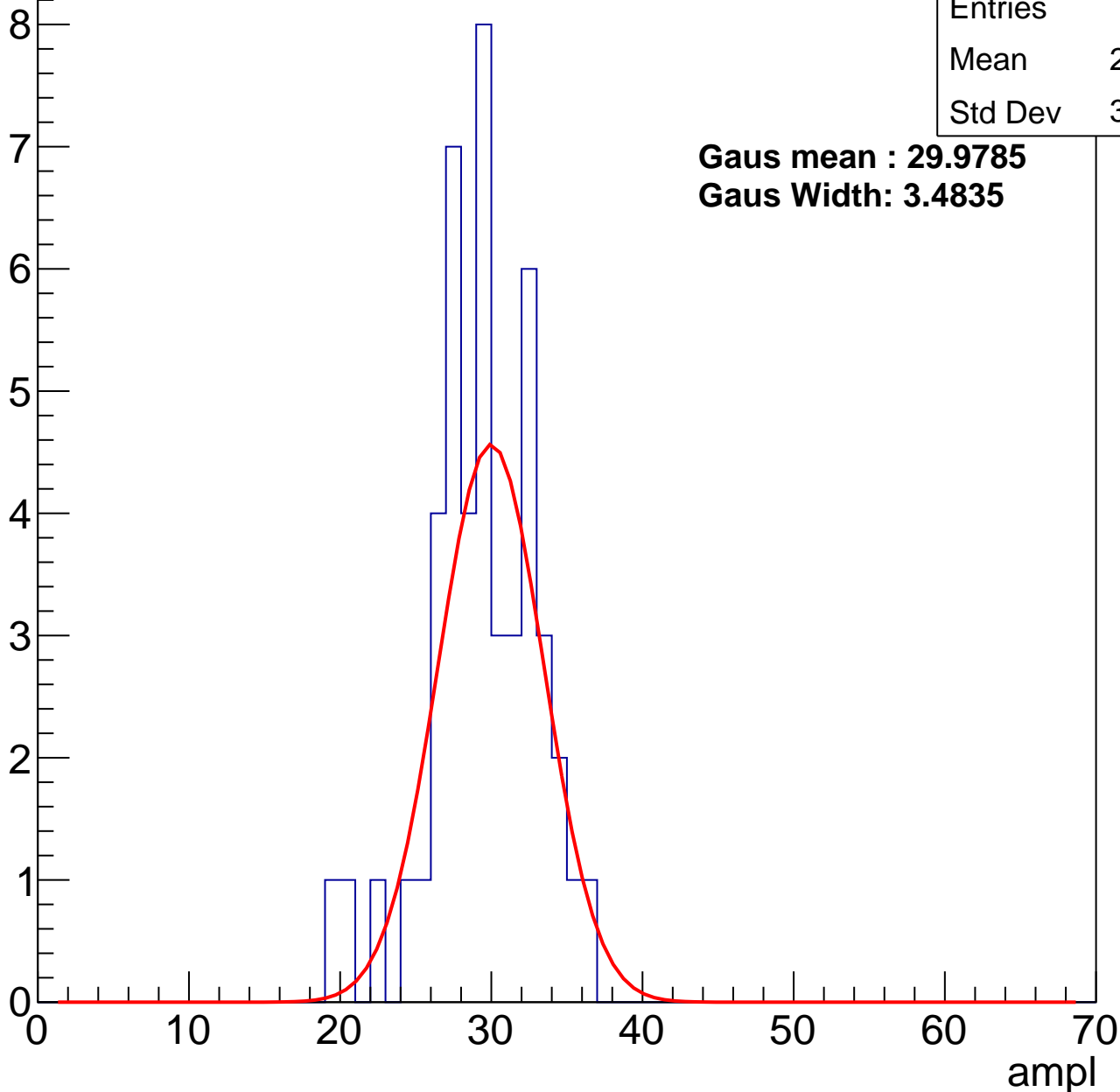
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	28.94
Std Dev	3.545

Gaus mean : 29.9785

Gaus Width: 3.4835



B0L102S, U5-ch79, adc1

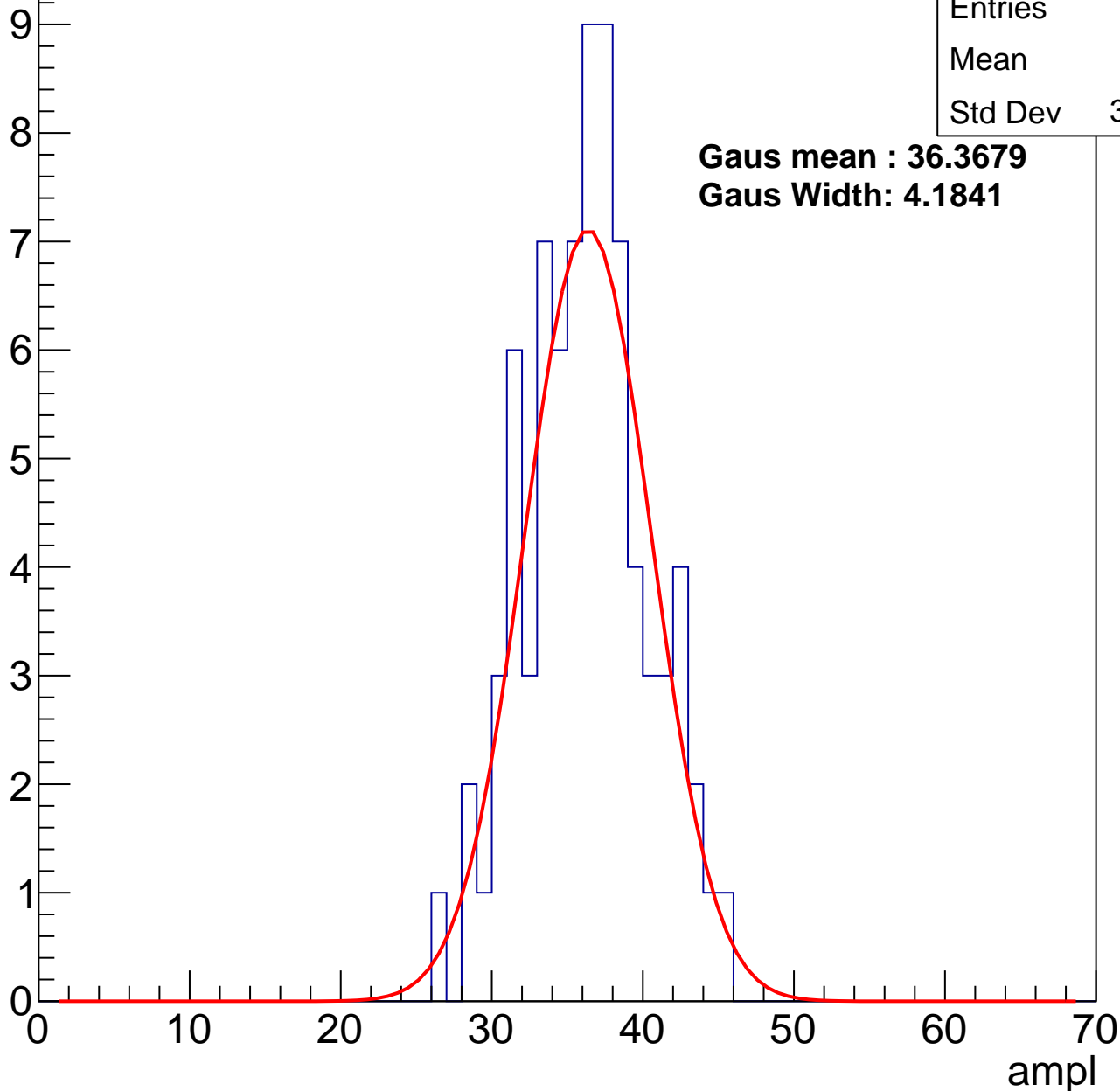
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.8
Std Dev	3.989

Gaus mean : 36.3679

Gaus Width: 4.1841



B0L102S, U5-ch79, adc2

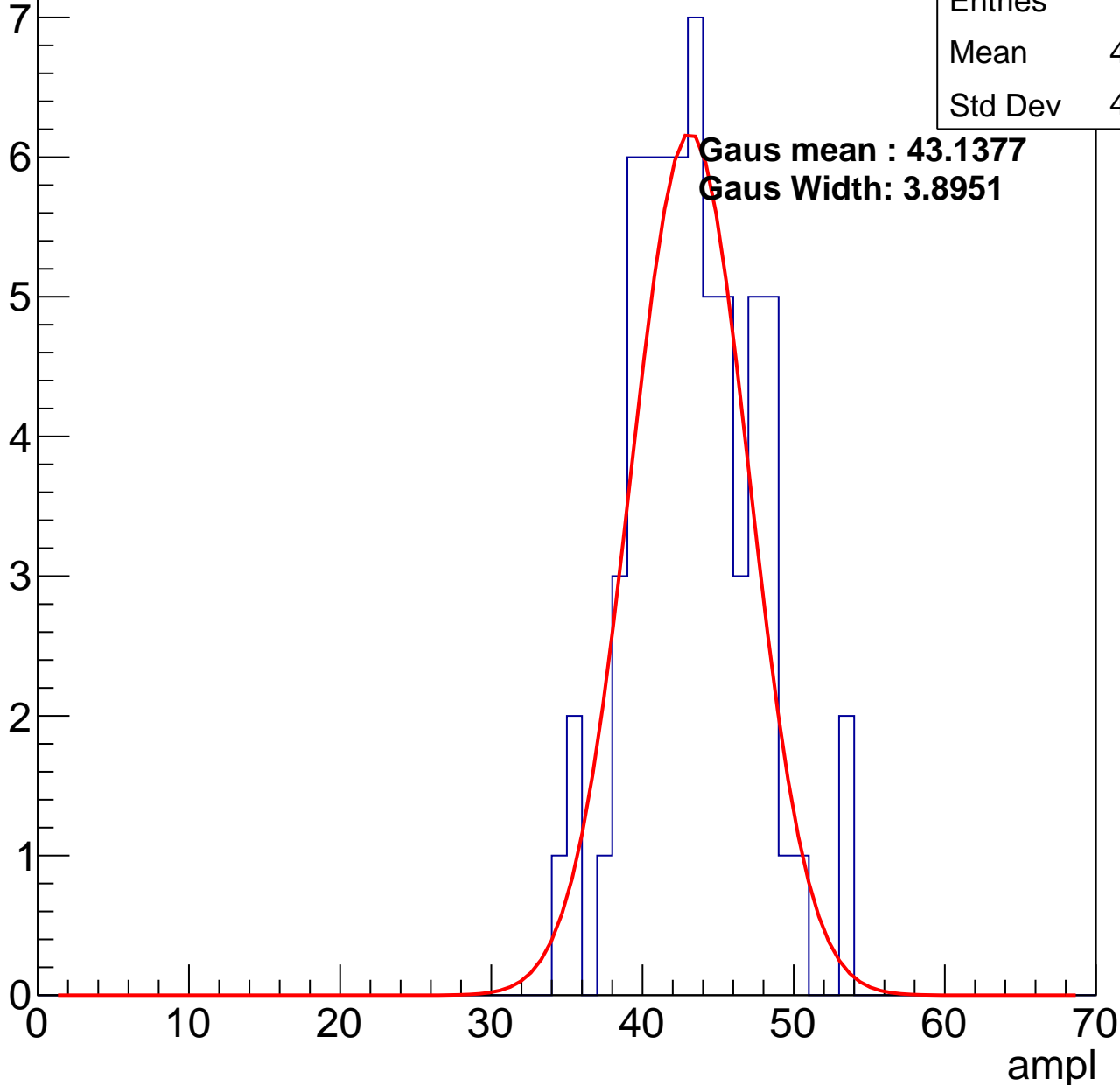
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	42.94
Std Dev	4.003

Gaus mean : 43.1377

Gaus Width: 3.8951

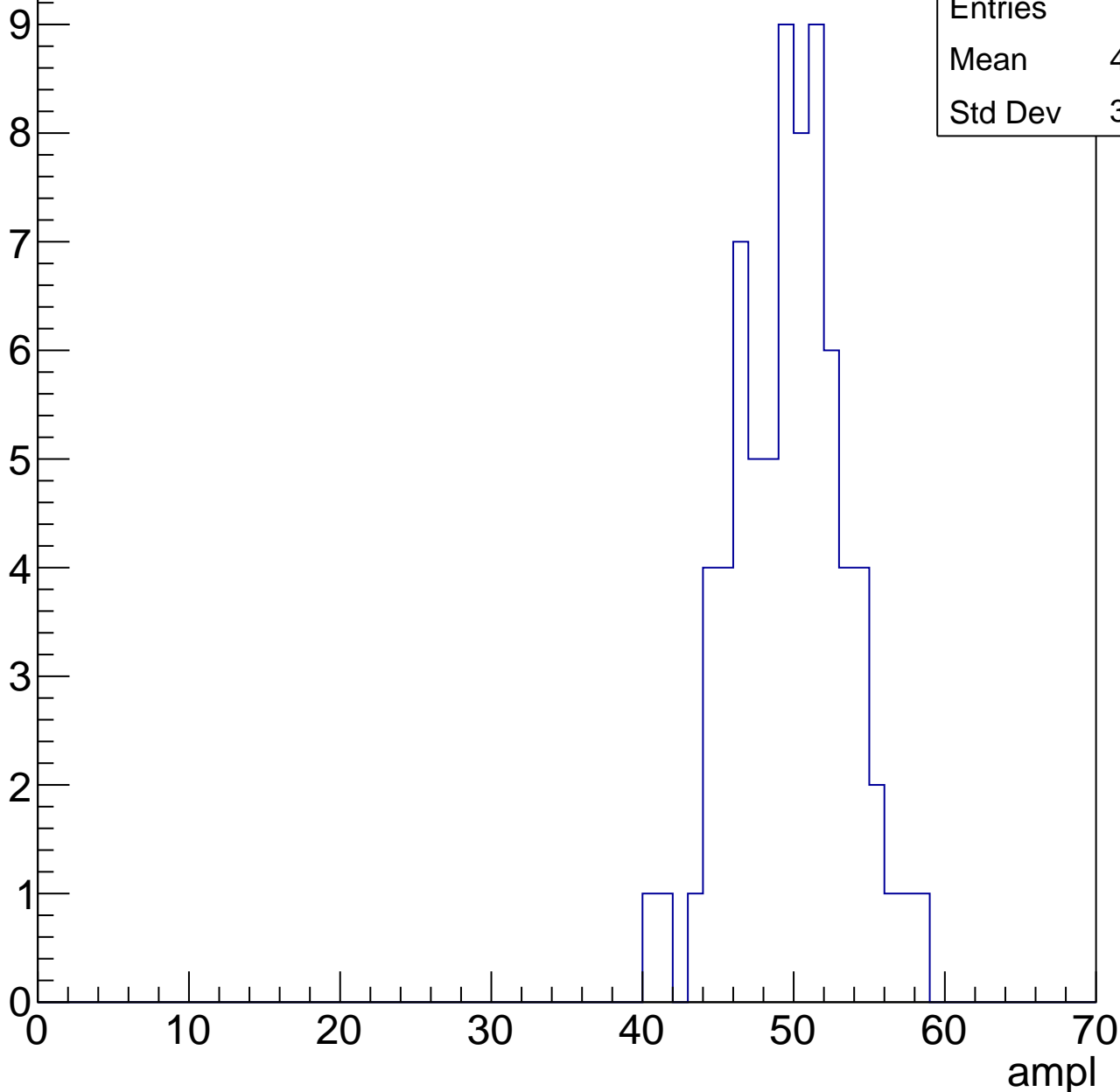


B0L102S, U5-ch79, adc3

calib_packv5_042523_0143.root, FC#12, port B1

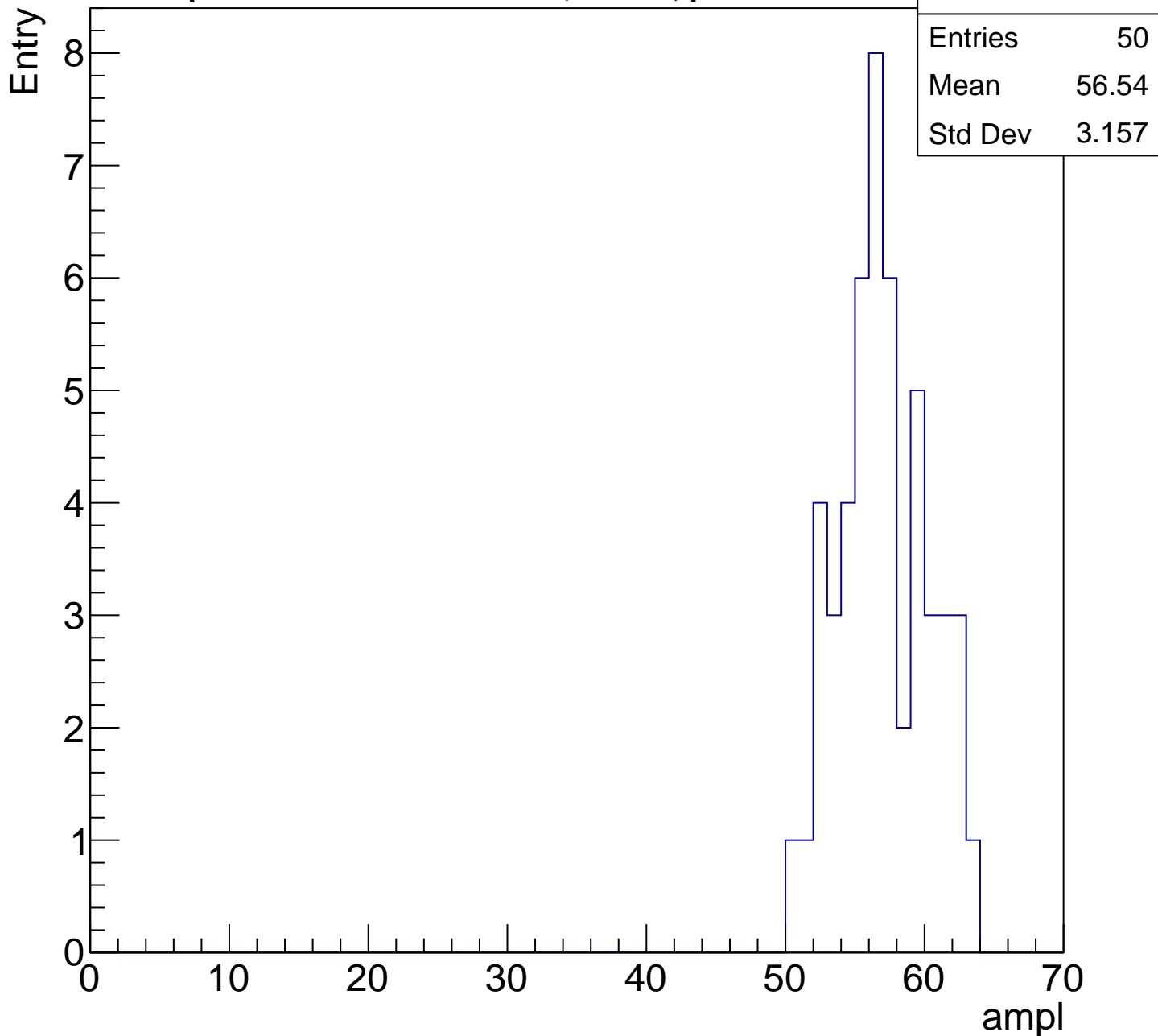
Entry

Entries	73
Mean	49.29
Std Dev	3.617



B0L102S, U5-ch79, adc4

calib_packv5_042523_0143.root, FC#12, port B1

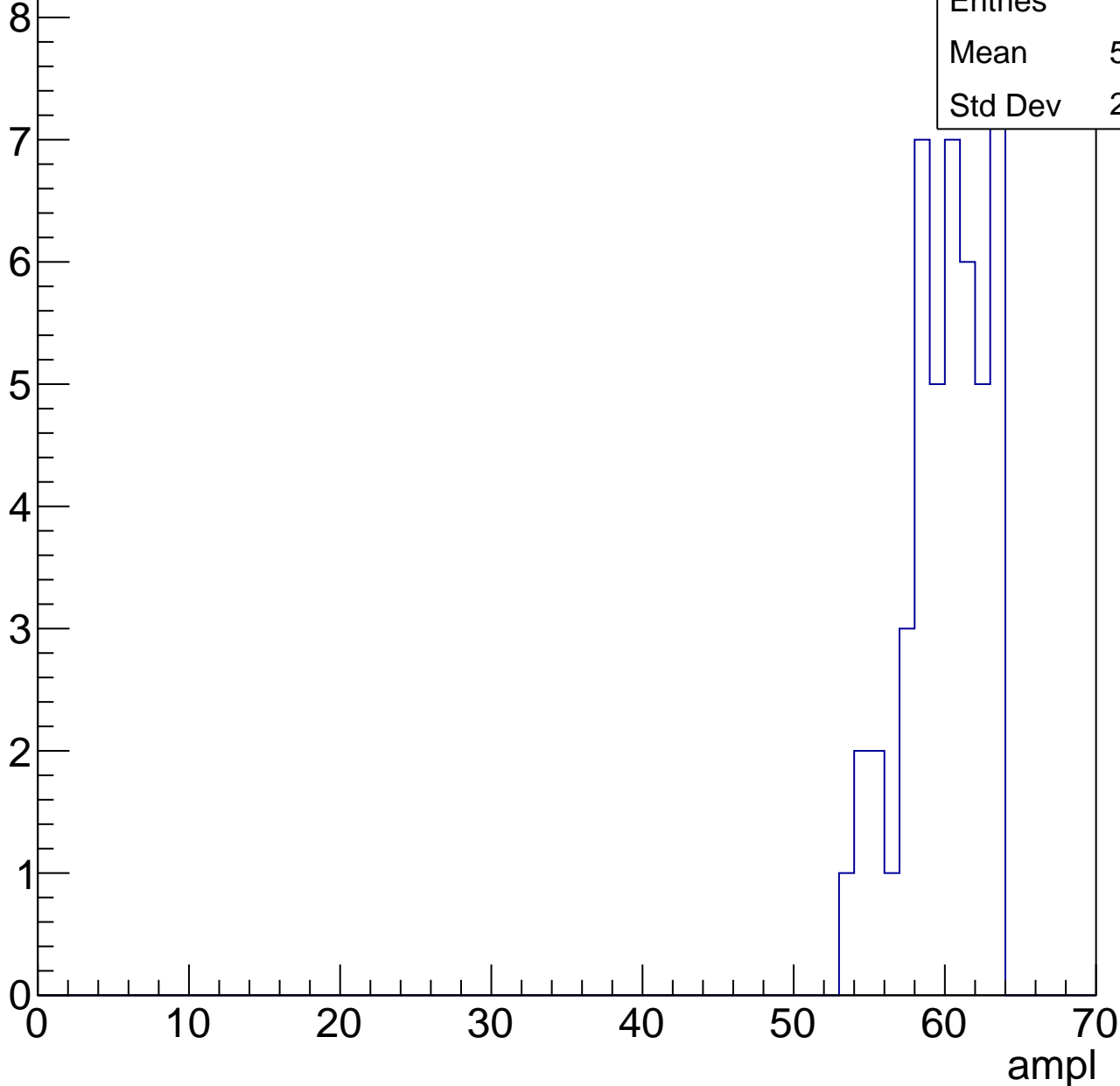


B0L102S, U5-ch79, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

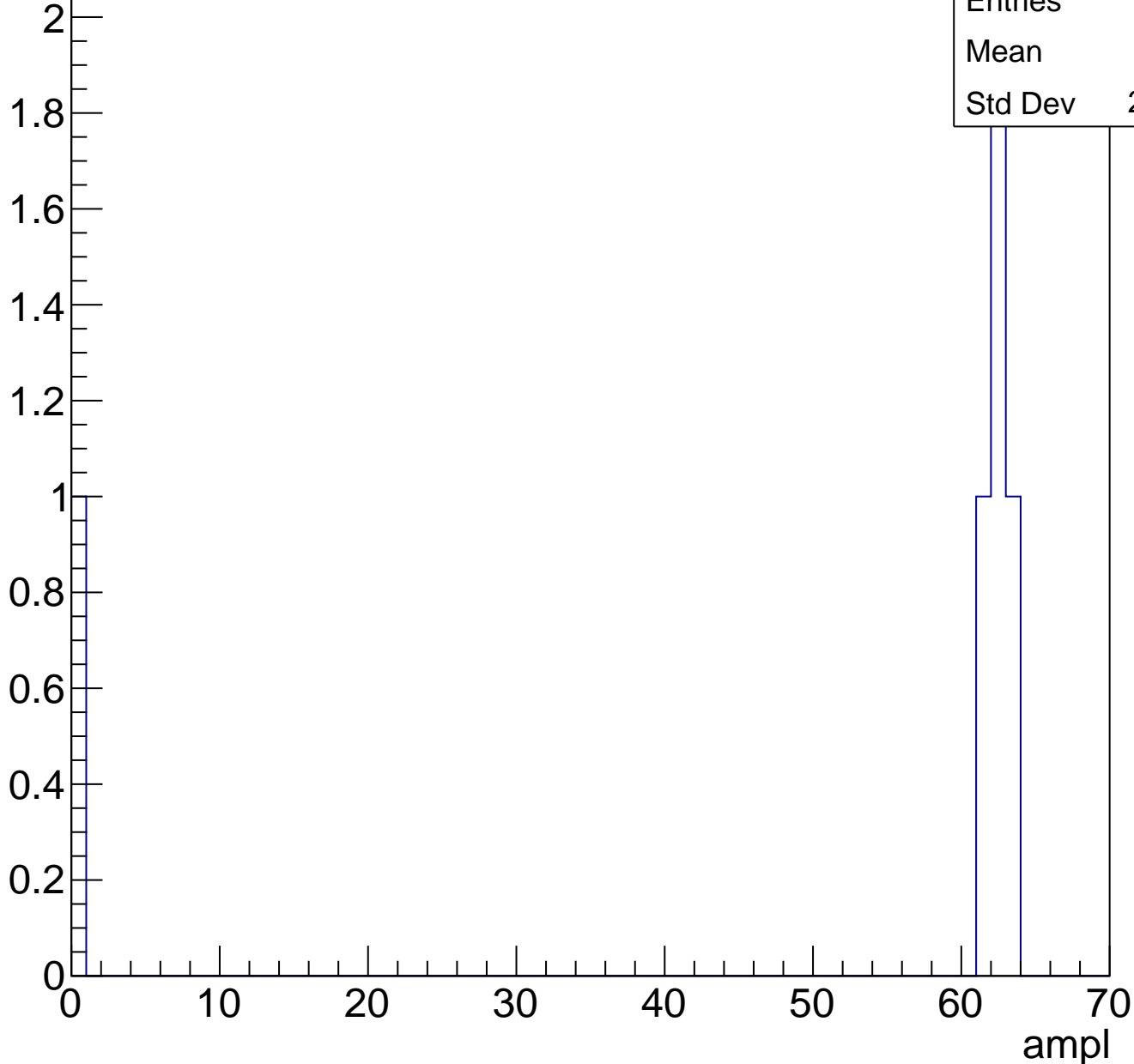
Entries	47
Mean	59.55
Std Dev	2.672



B0L102S, U5-ch79, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch79, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch80, adc0

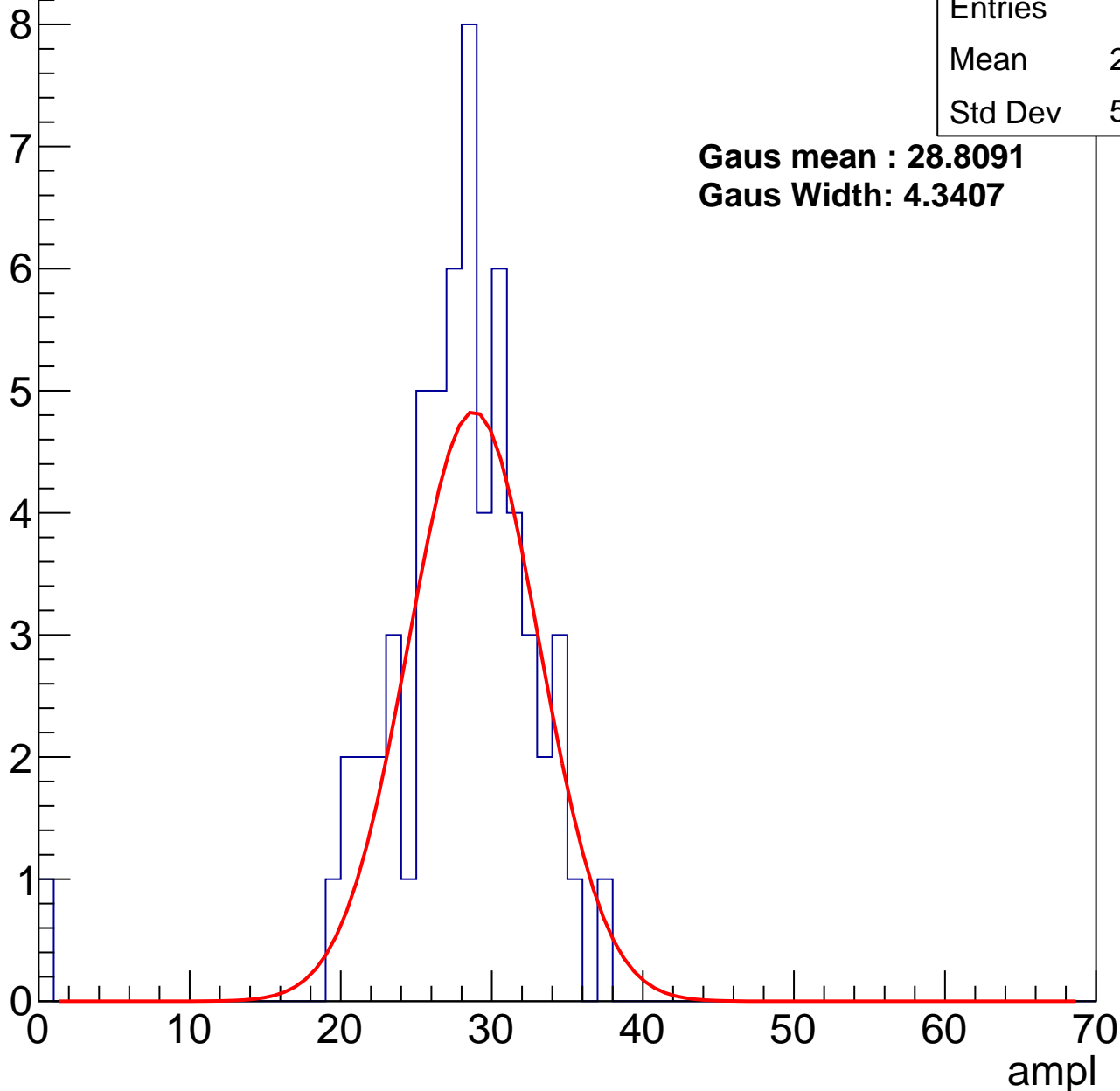
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	27.25
Std Dev	5.309

Gaus mean : 28.8091

Gaus Width: 4.3407



B0L102S, U5-ch80, adc1

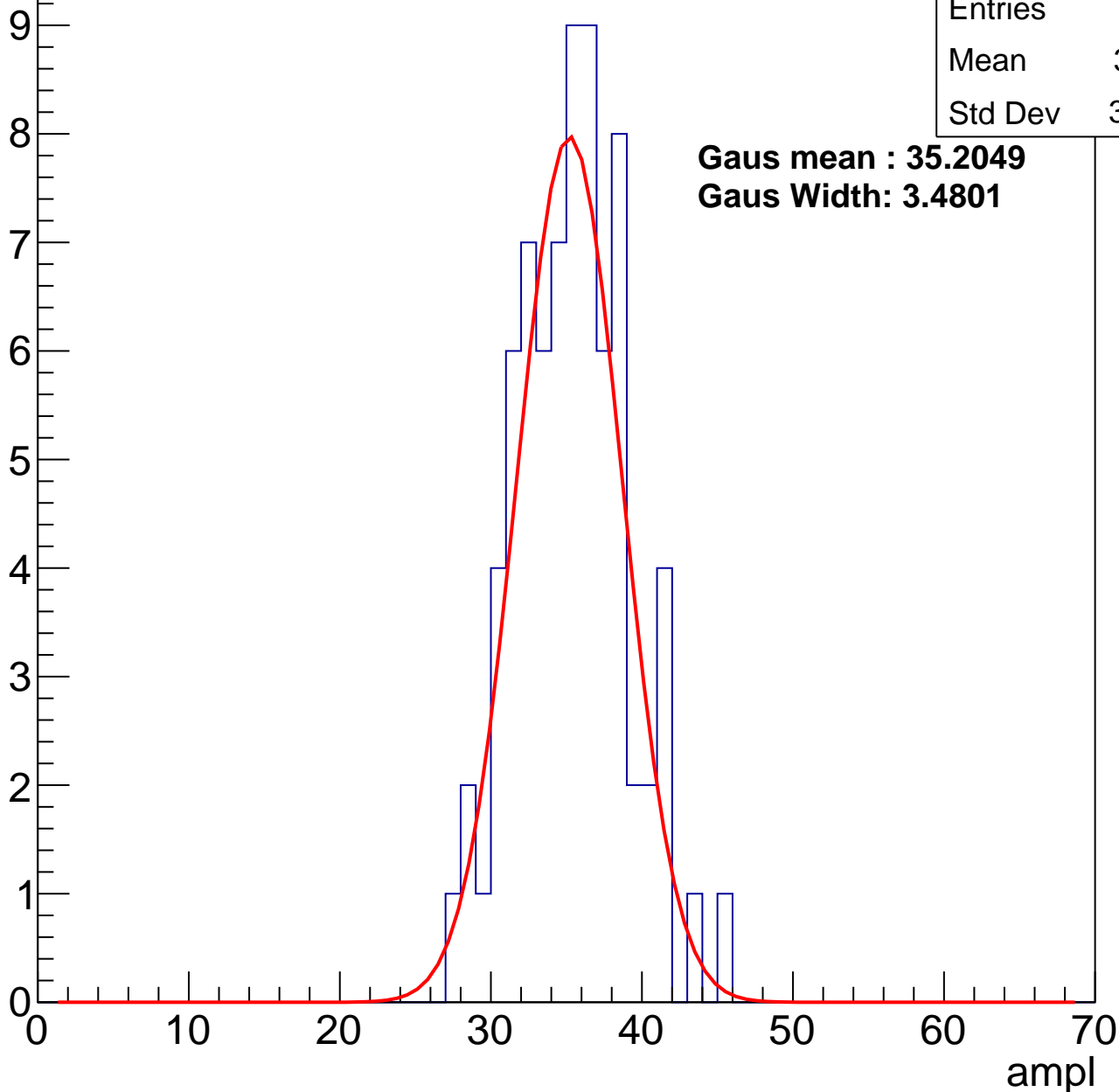
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	34.91
Std Dev	3.595

Gaus mean : 35.2049

Gaus Width: 3.4801



B0L102S, U5-ch80, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	56
Mean	42.29
Std Dev	3.277

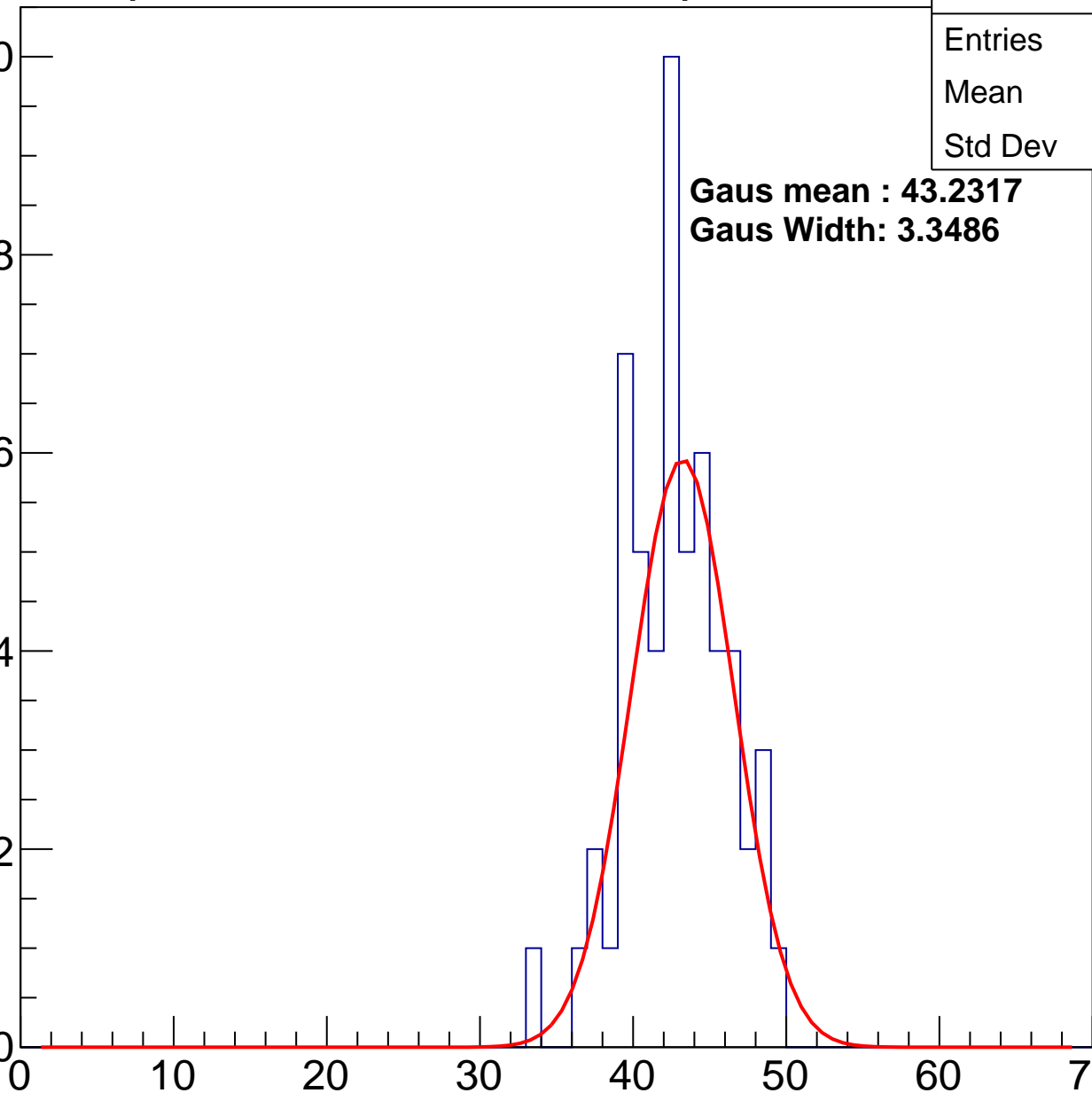
Gaus mean : 43.2317

Gaus Width: 3.3486

Entry

10
8
6
4
2
0

ampl

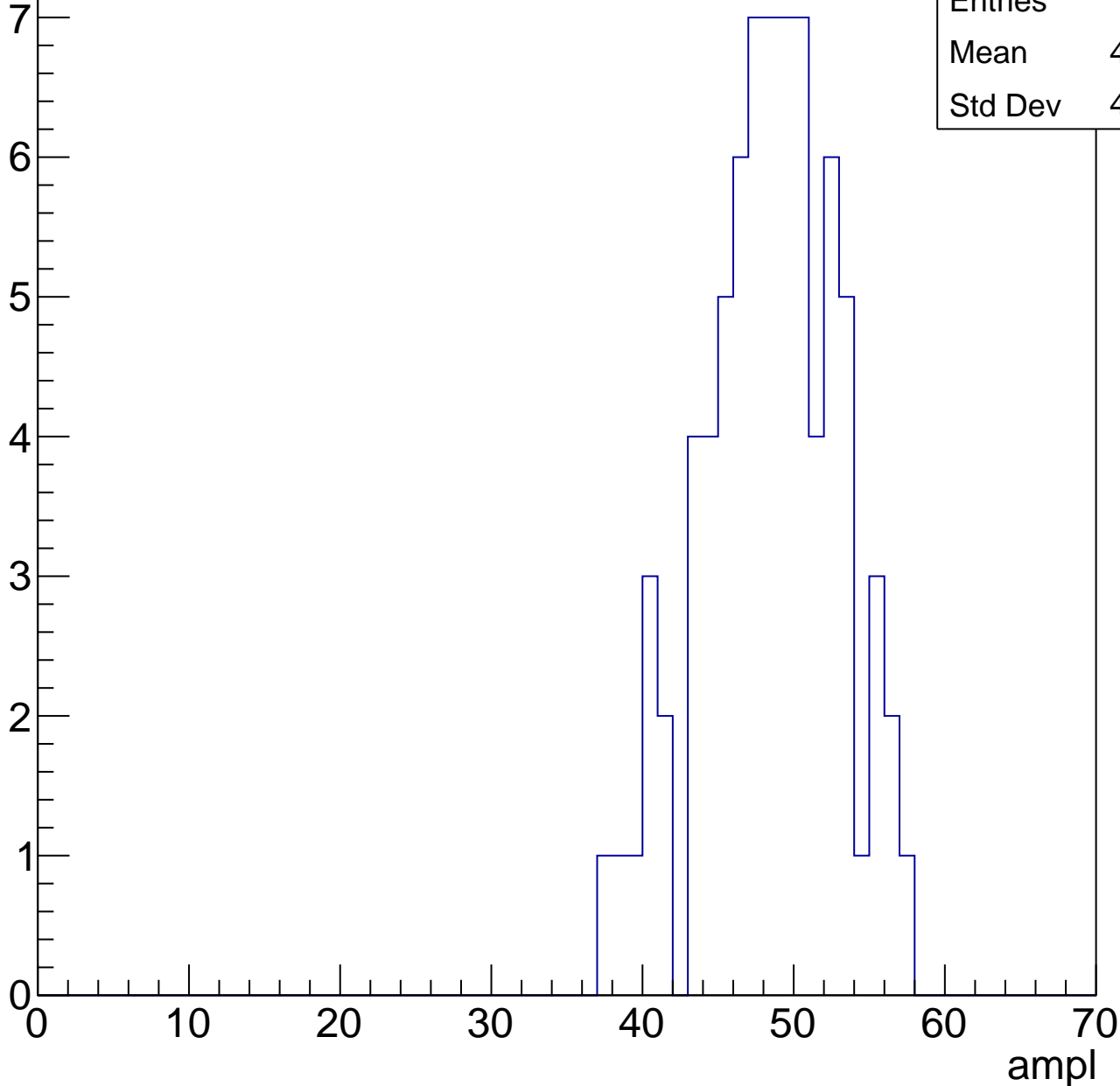


B0L102S, U5-ch80, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	47.95
Std Dev	4.437

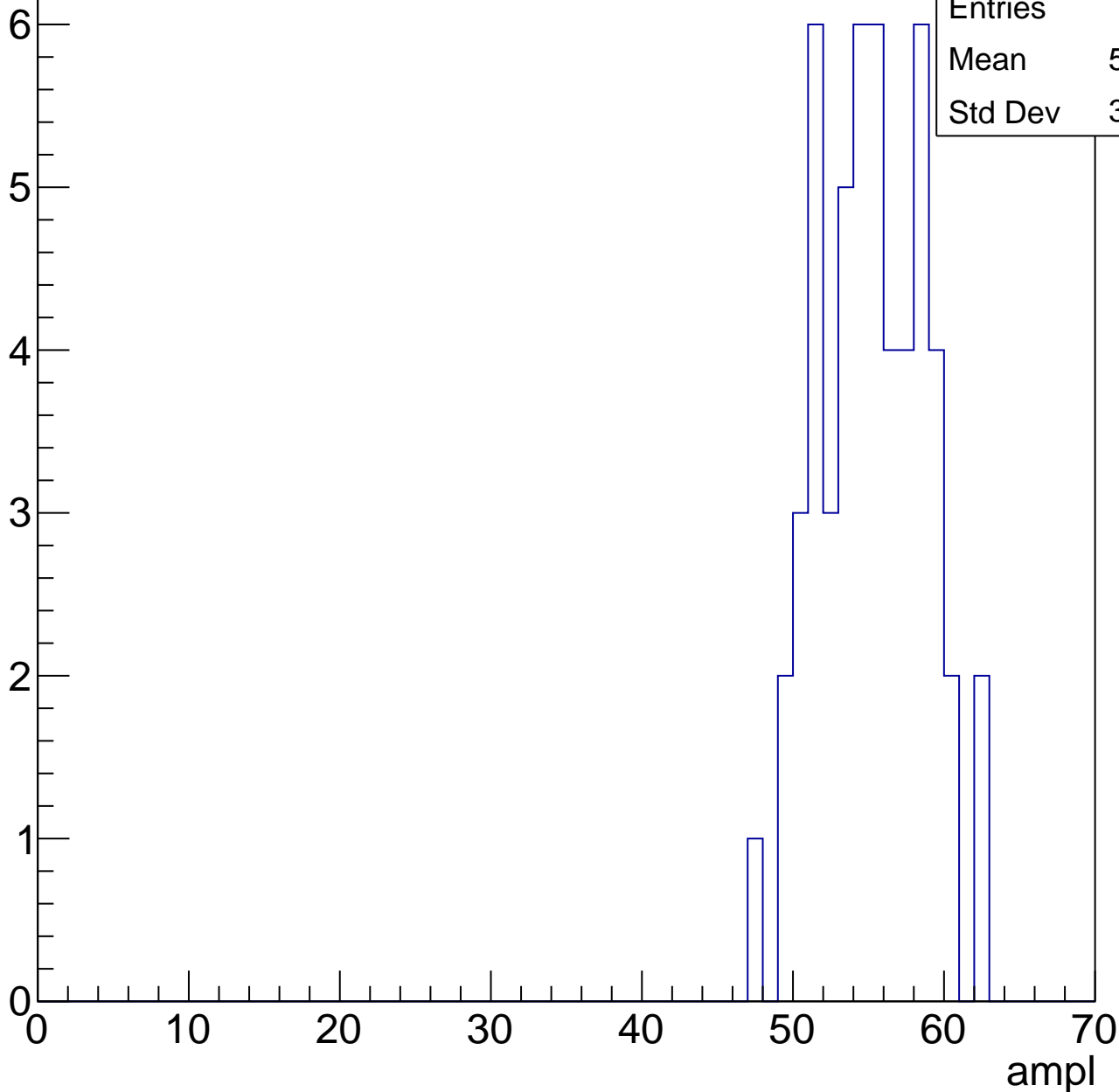


B0L102S, U5-ch80, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

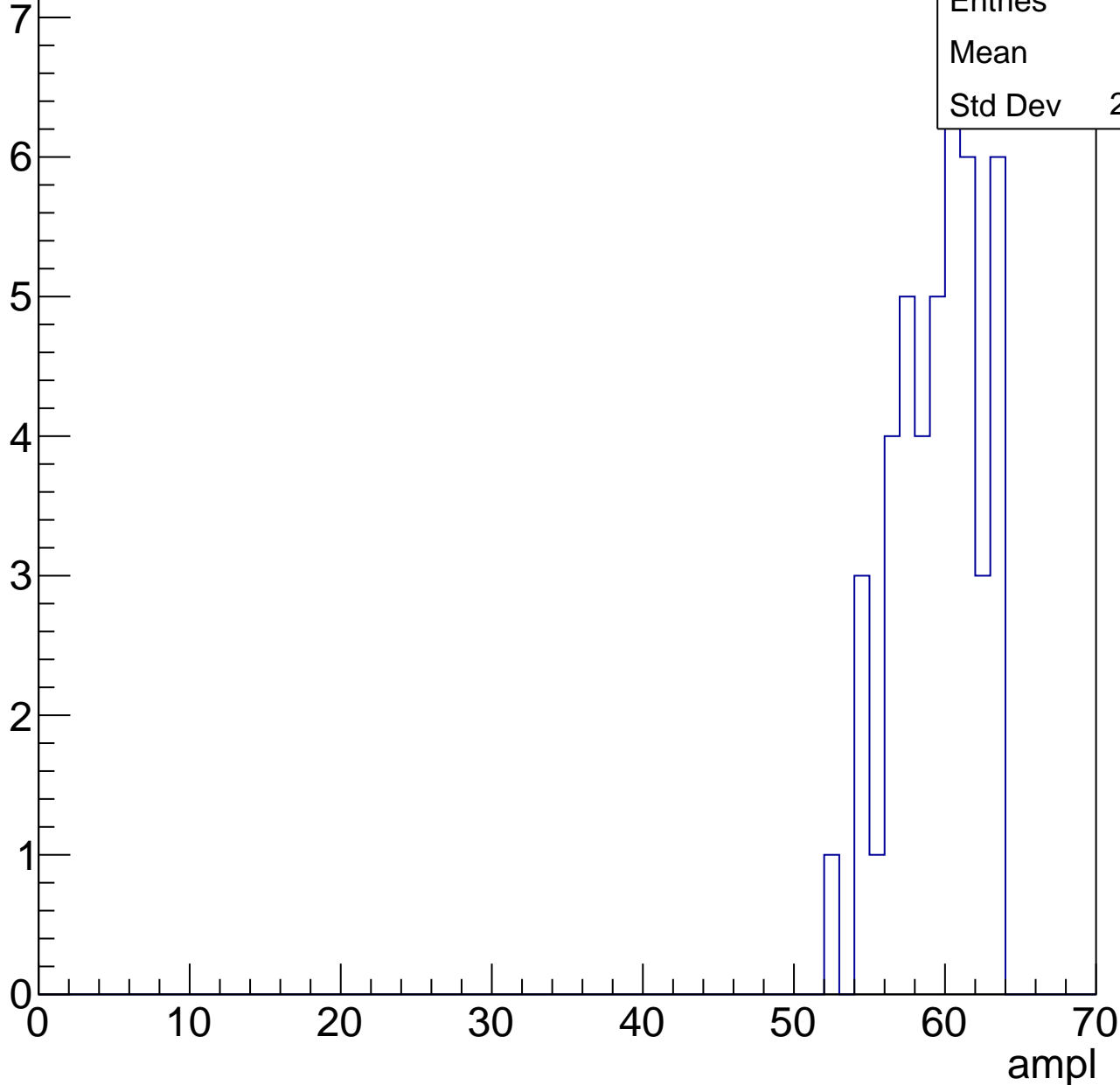
Entries	54
Mean	54.74
Std Dev	3.449



B0L102S, U5-ch80, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

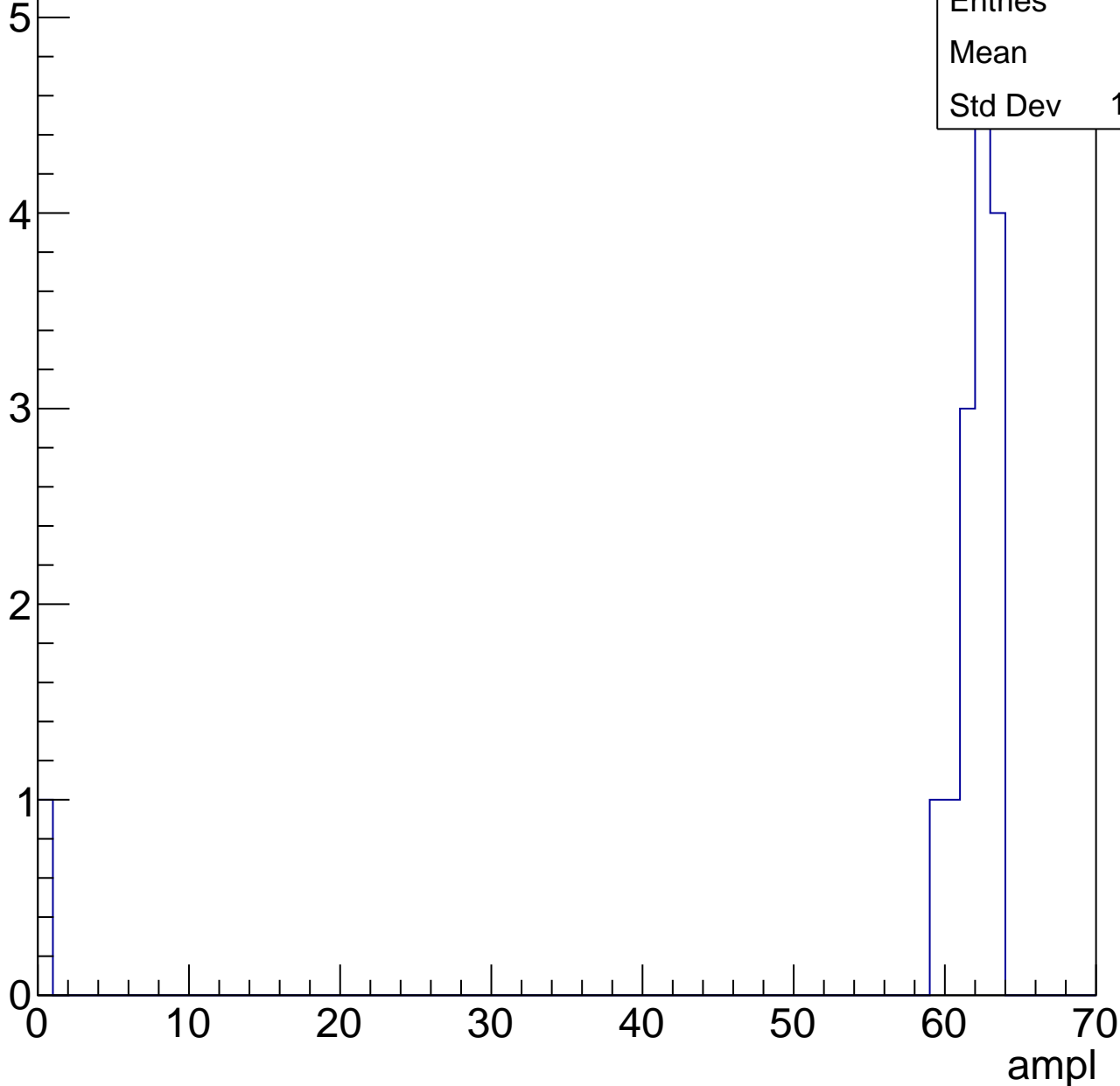


B0L102S, U5-ch80, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.6
Std Dev	15.44



B0L102S, U5-ch80, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch81, adc0

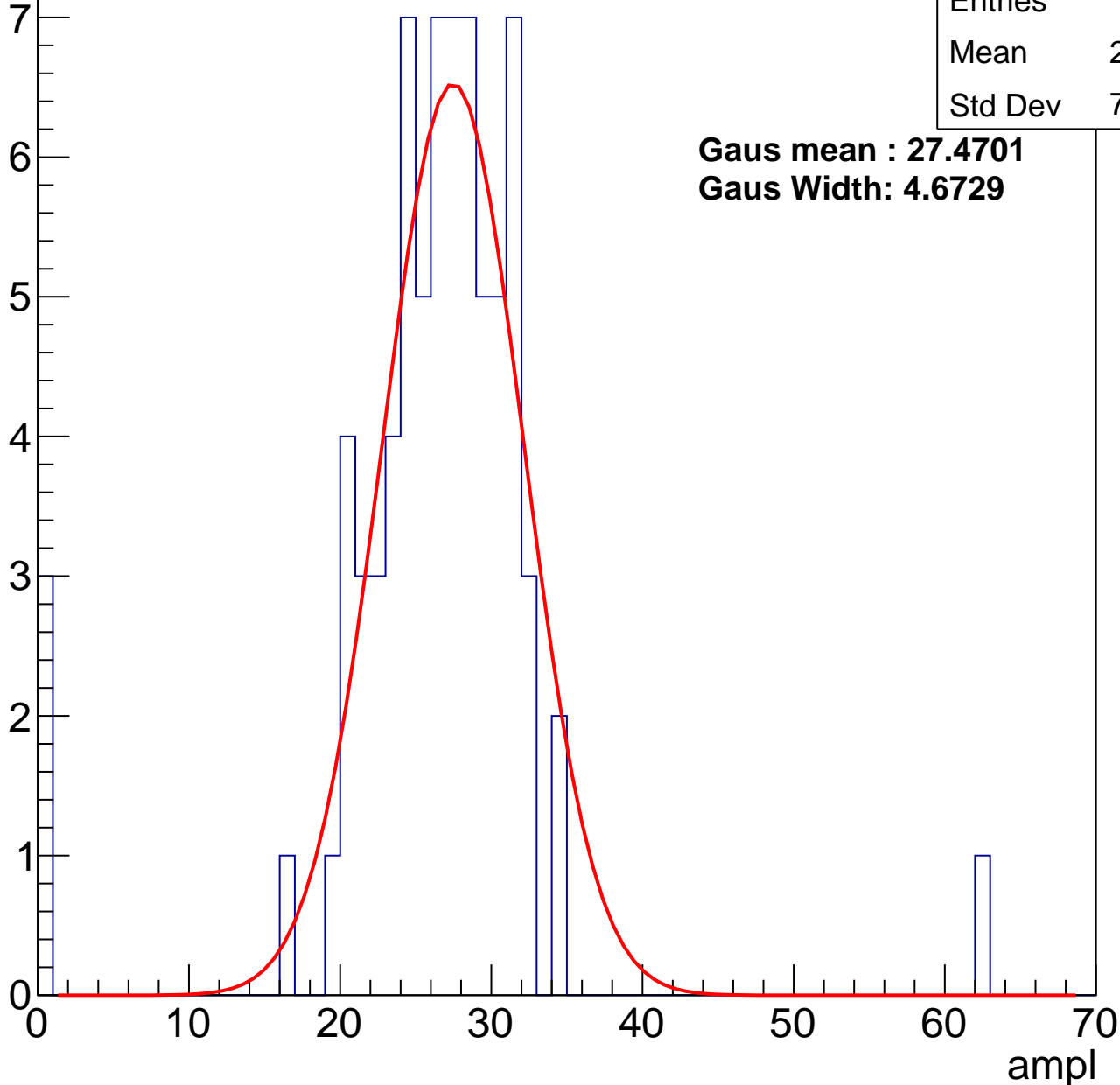
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	25.79
Std Dev	7.639

Gaus mean : 27.4701

Gaus Width: 4.6729



B0L102S, U5-ch81, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	70
Mean	33.73
Std Dev	3.776

Gaus mean : 34.3567

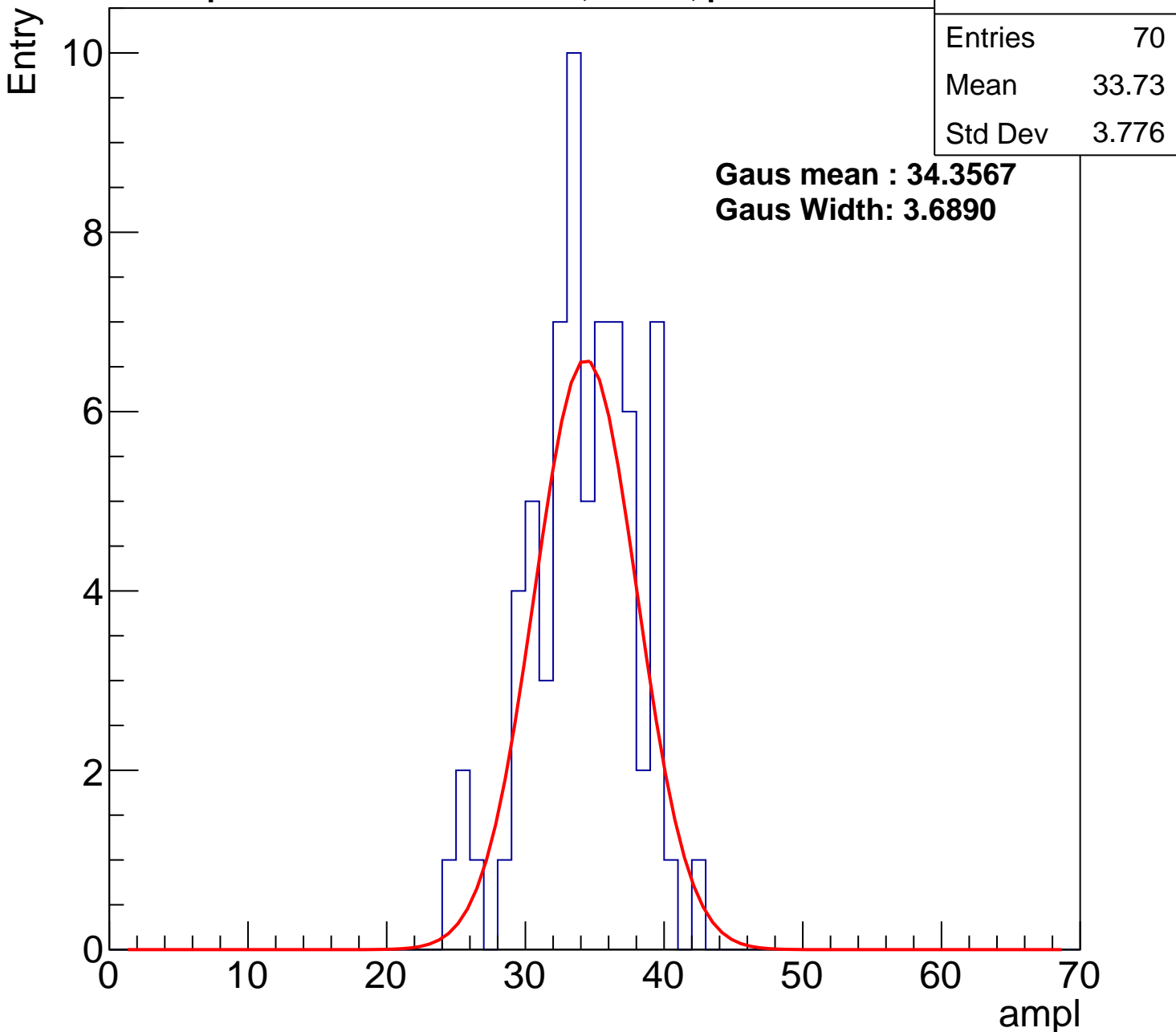
Gaus Width: 3.6890

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch81, adc2

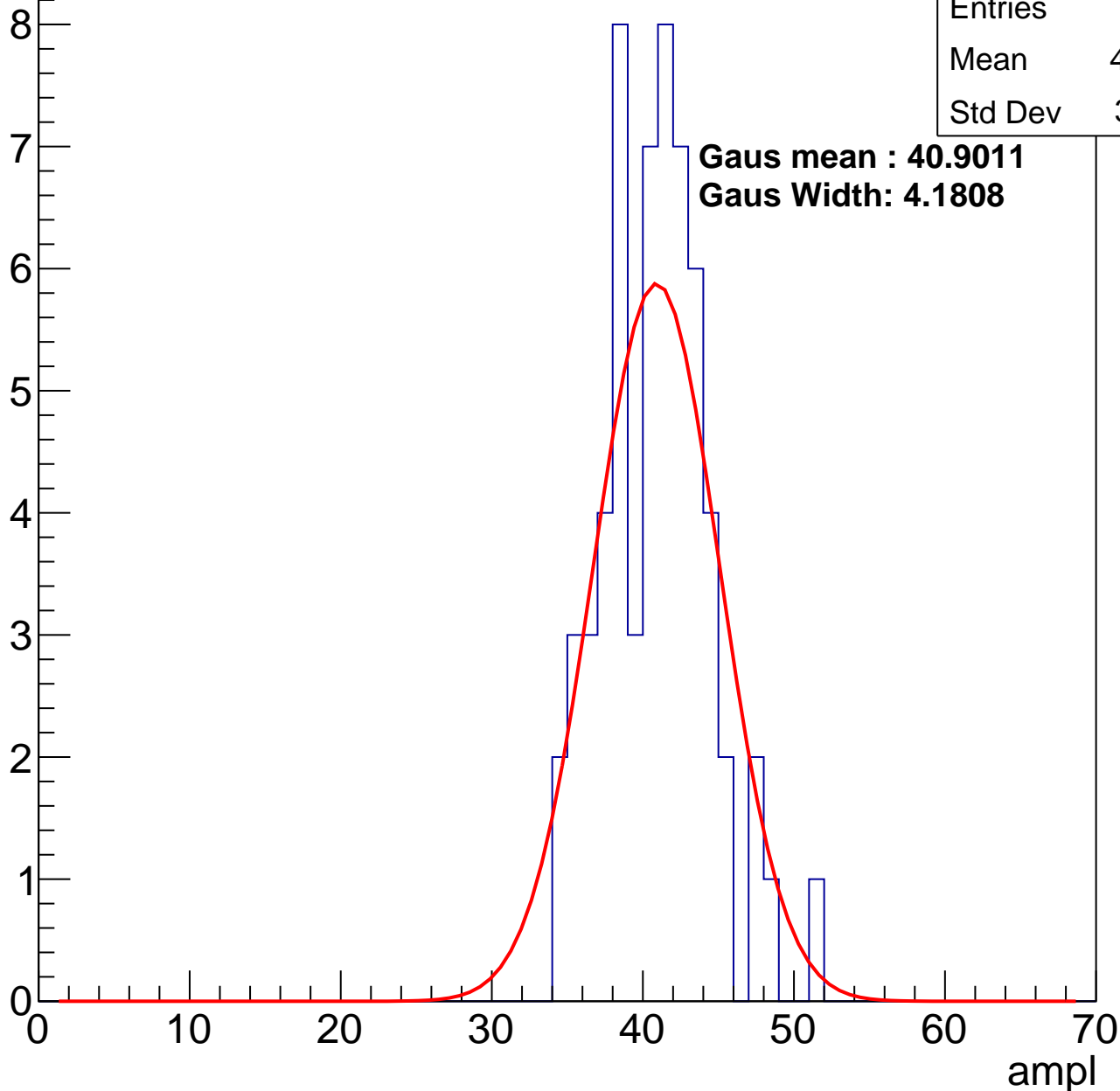
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	40.48
Std Dev	3.481

Gaus mean : 40.9011

Gaus Width: 4.1808



B0L102S, U5-ch81, adc3

calib_packv5_042523_0143.root, FC#12, port B1

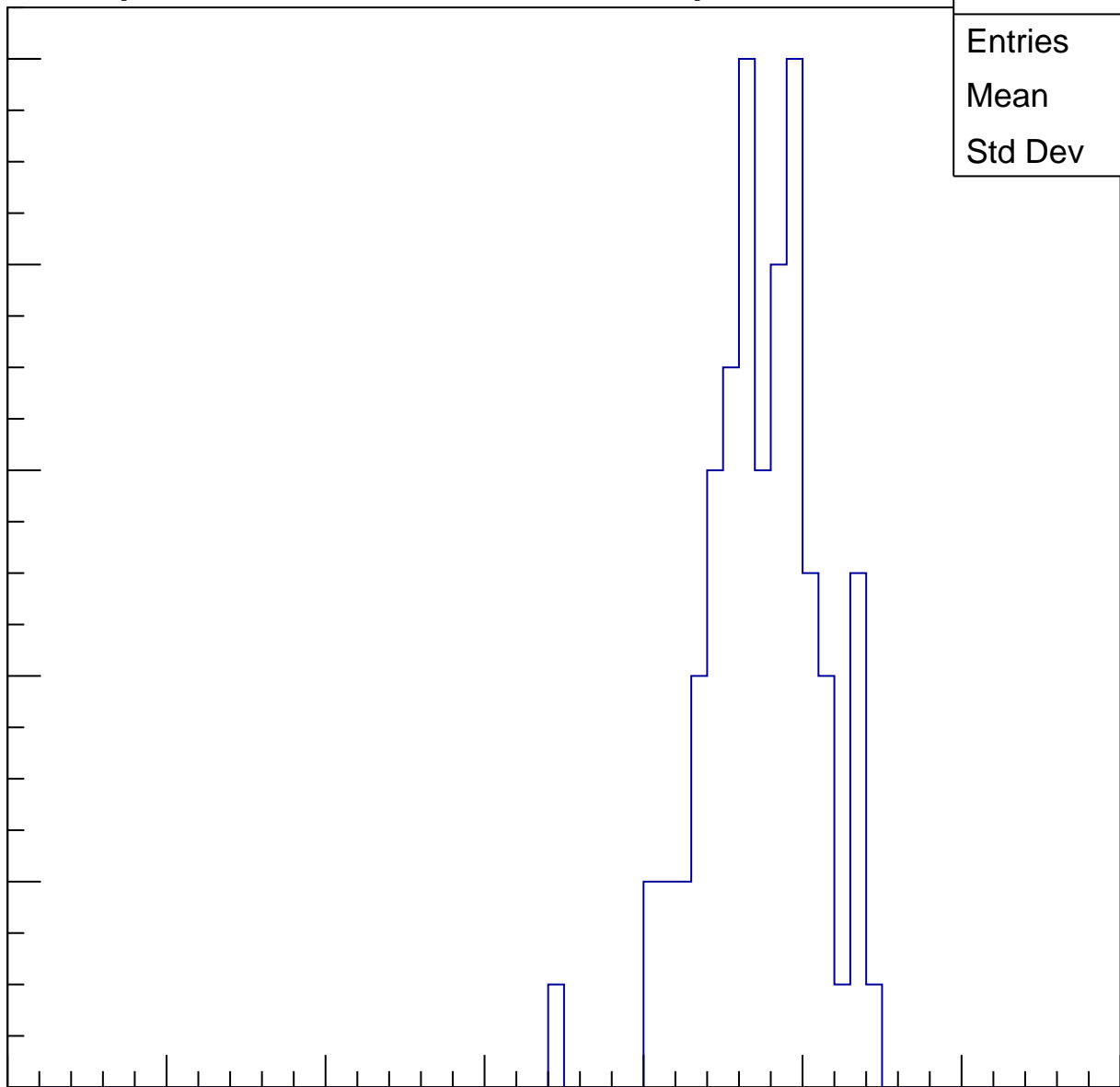
Entries	74
Mean	46.92
Std Dev	3.593

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

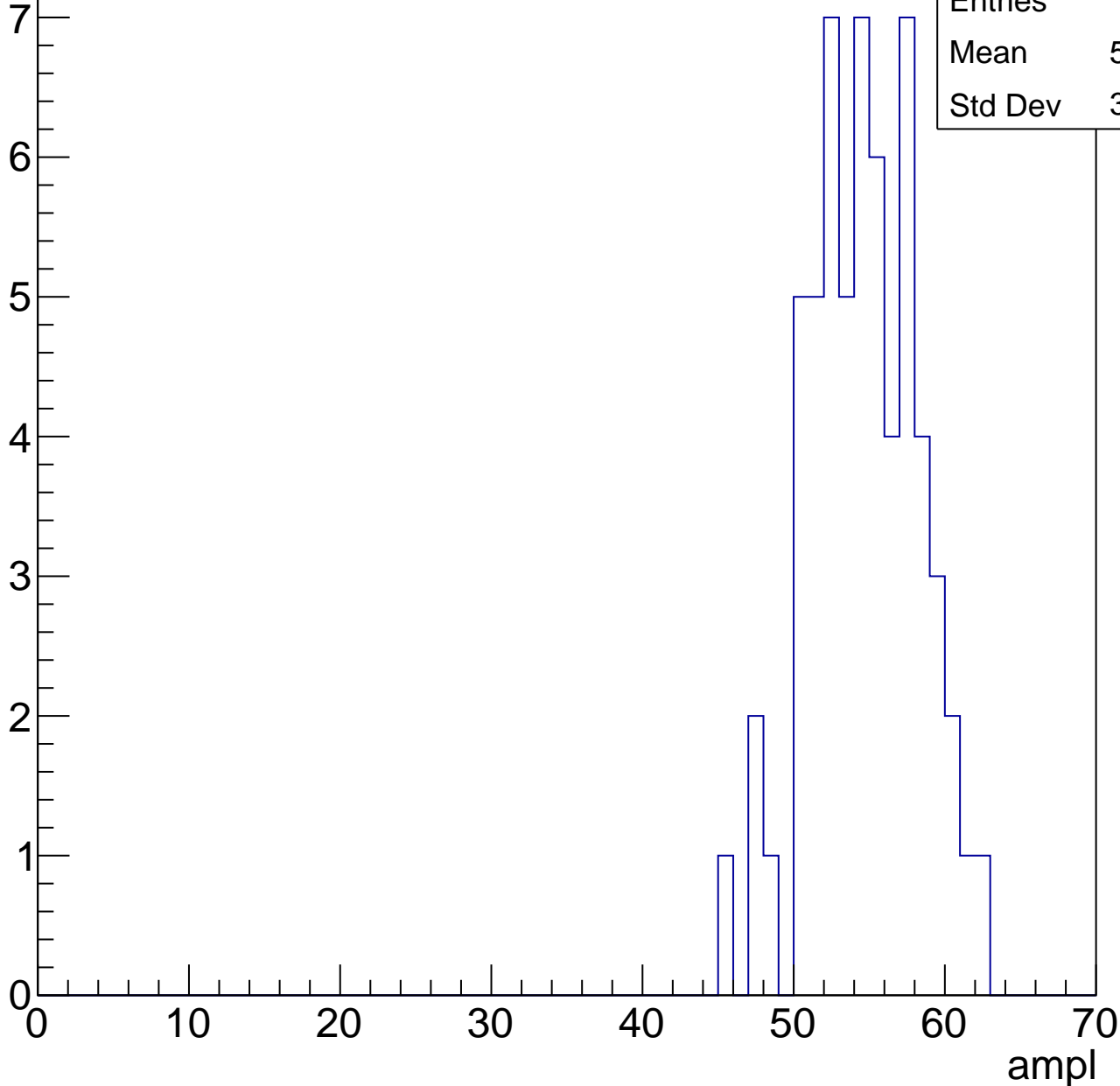


B0L102S, U5-ch81, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	54.16
Std Dev	3.577



B0L102S, U5-ch81, adc5

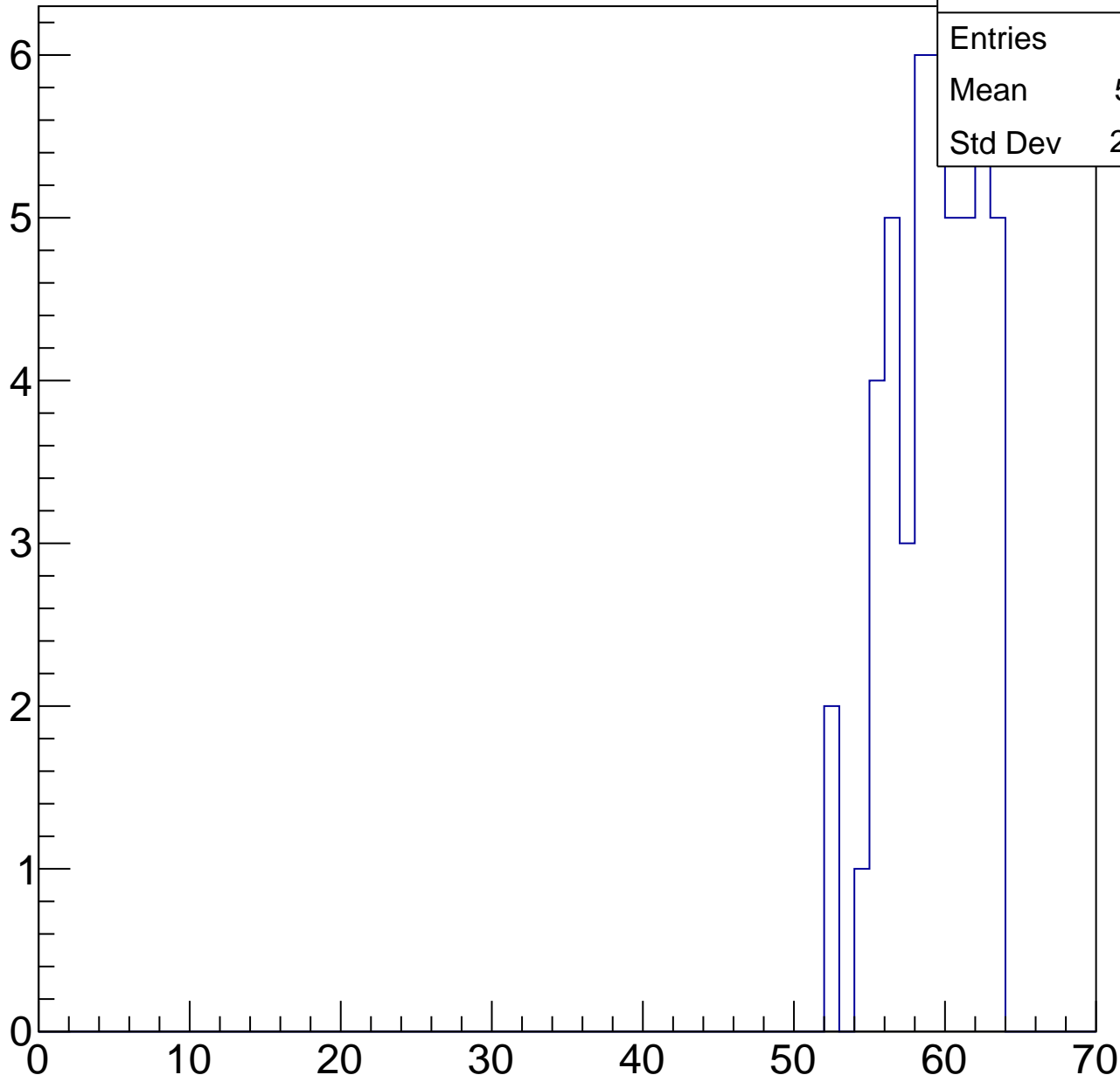
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

Entries	48
Mean	58.81
Std Dev	2.913

ampl

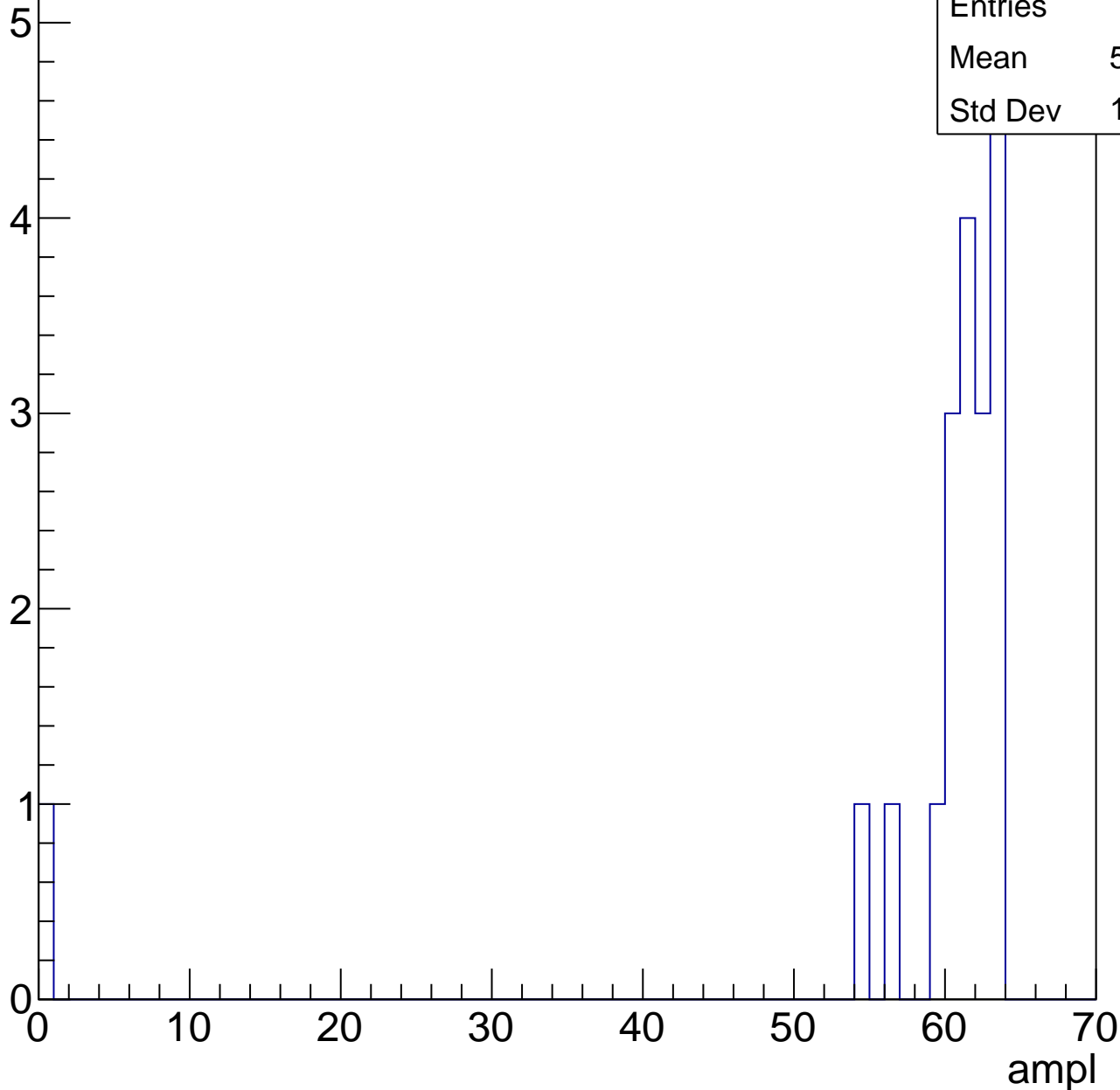


B0L102S, U5-ch81, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	19
Mean	57.58
Std Dev	13.77



B0L102S, U5-ch81, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch82, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	93
Mean	26.71
Std Dev	4.902

Gaus mean : 27.5315

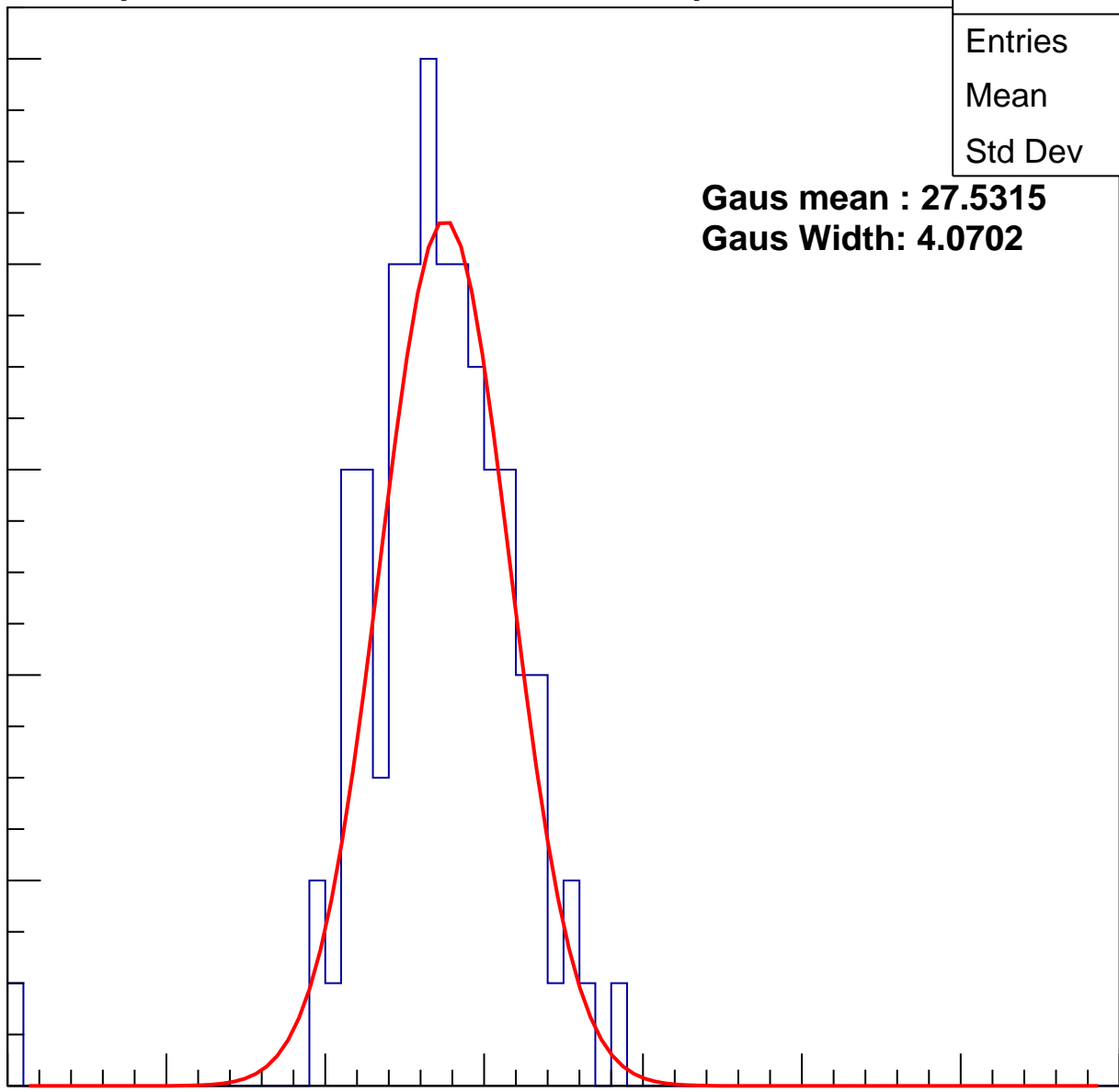
Gaus Width: 4.0702

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch82, adc1

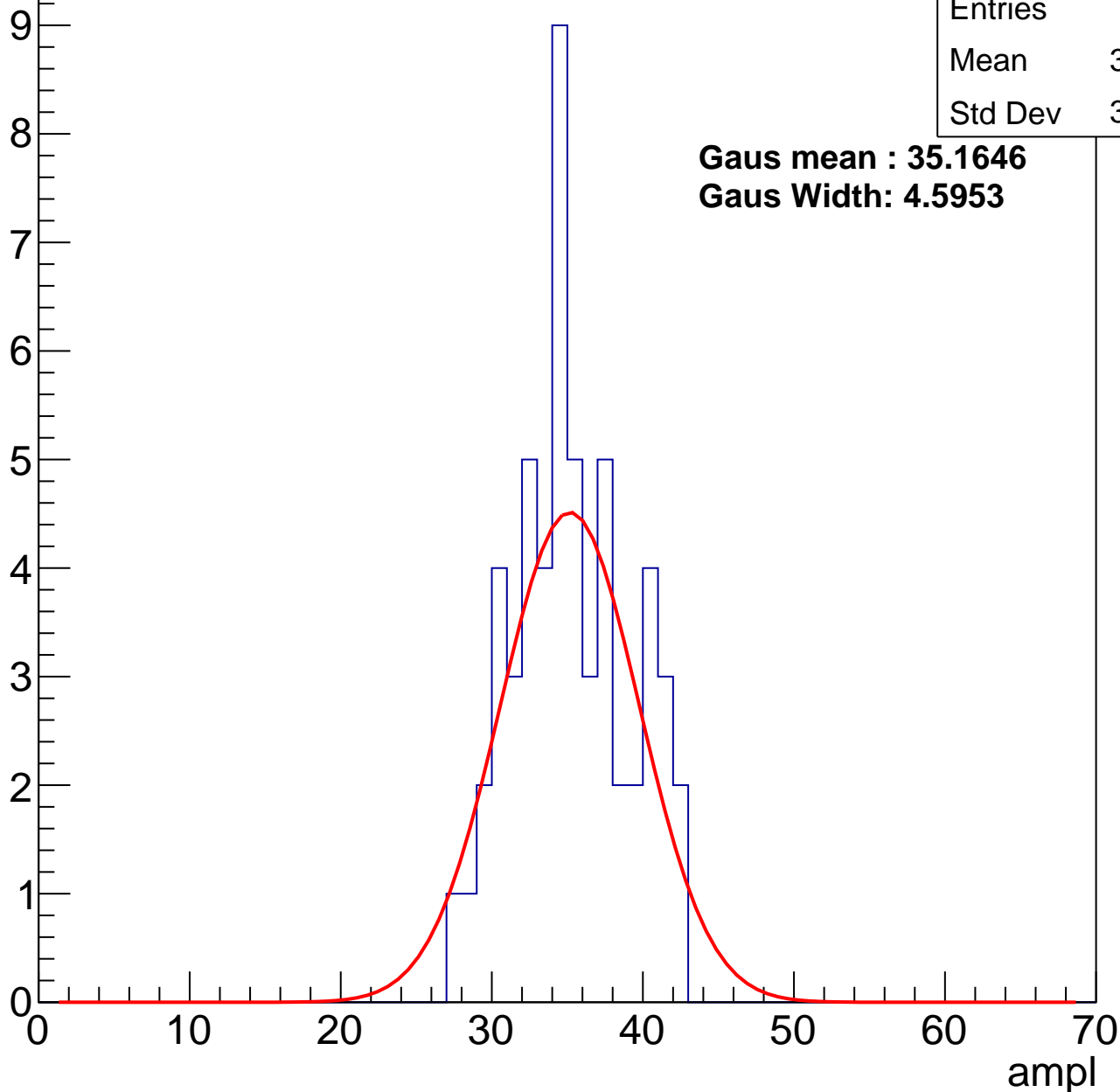
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	34.78
Std Dev	3.764

Gaus mean : 35.1646

Gaus Width: 4.5953



B0L102S, U5-ch82, adc2

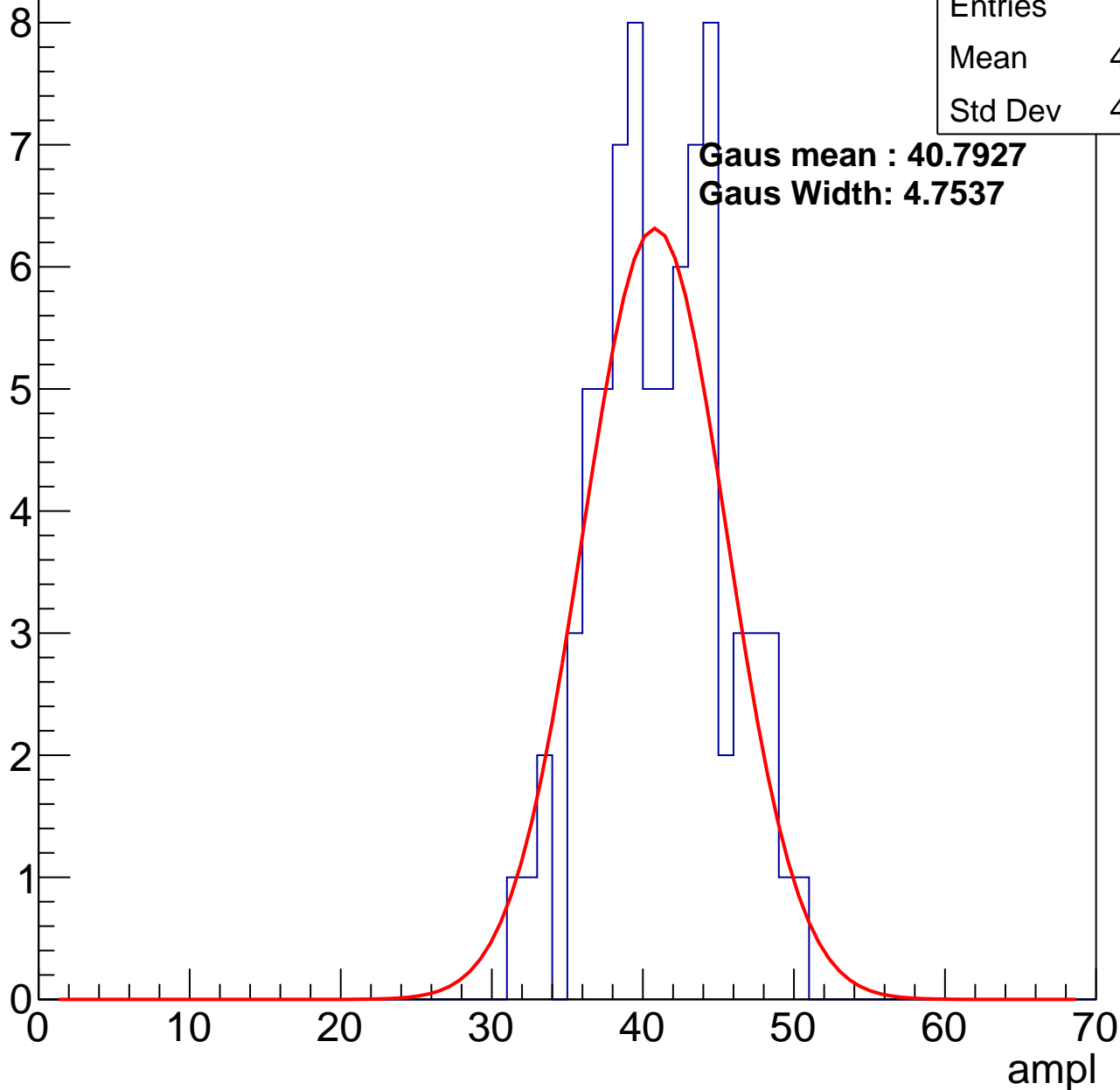
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	40.78
Std Dev	4.185

Gaus mean : 40.7927

Gaus Width: 4.7537

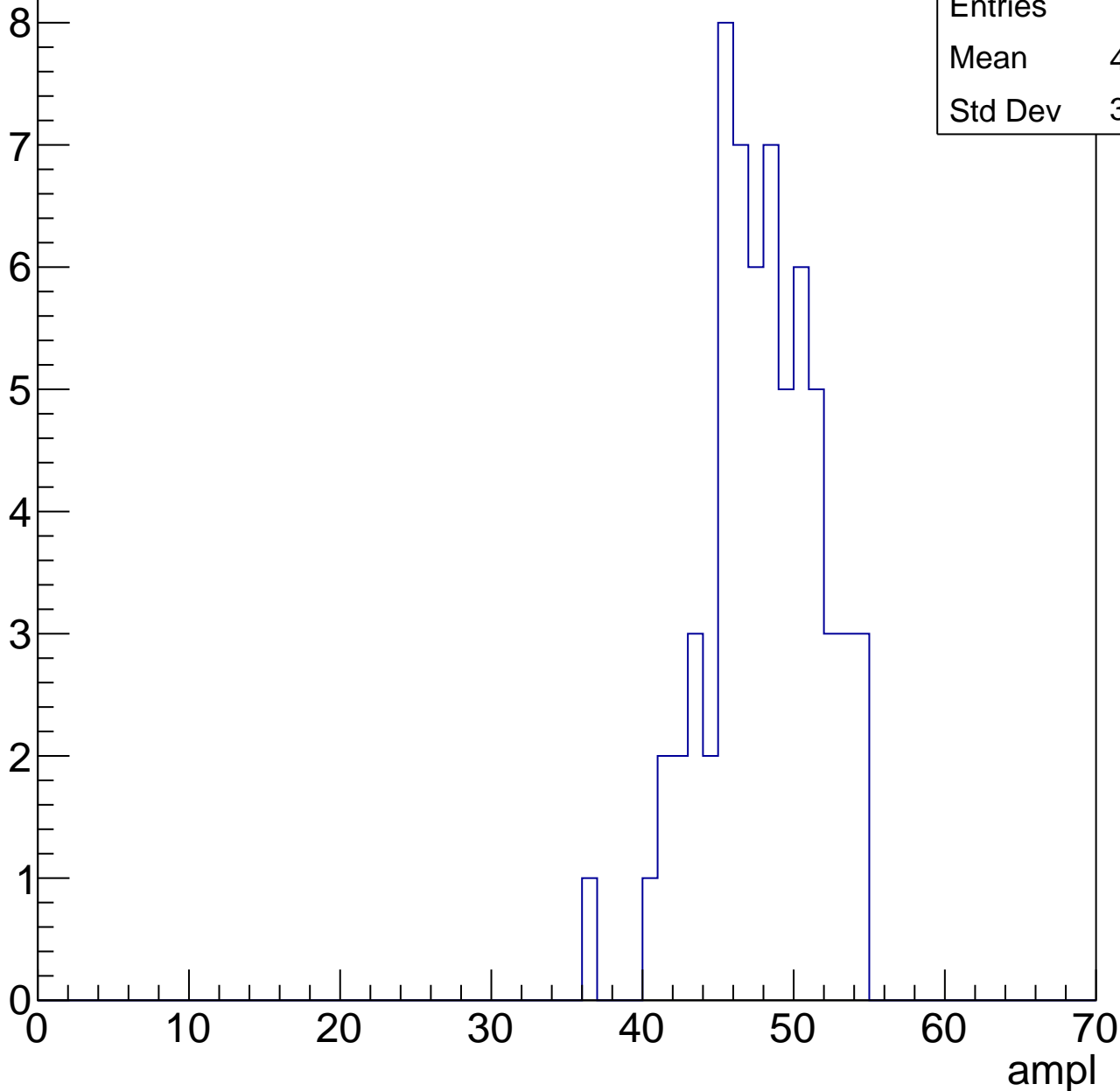


B0L102S, U5-ch82, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	47.44
Std Dev	3.699

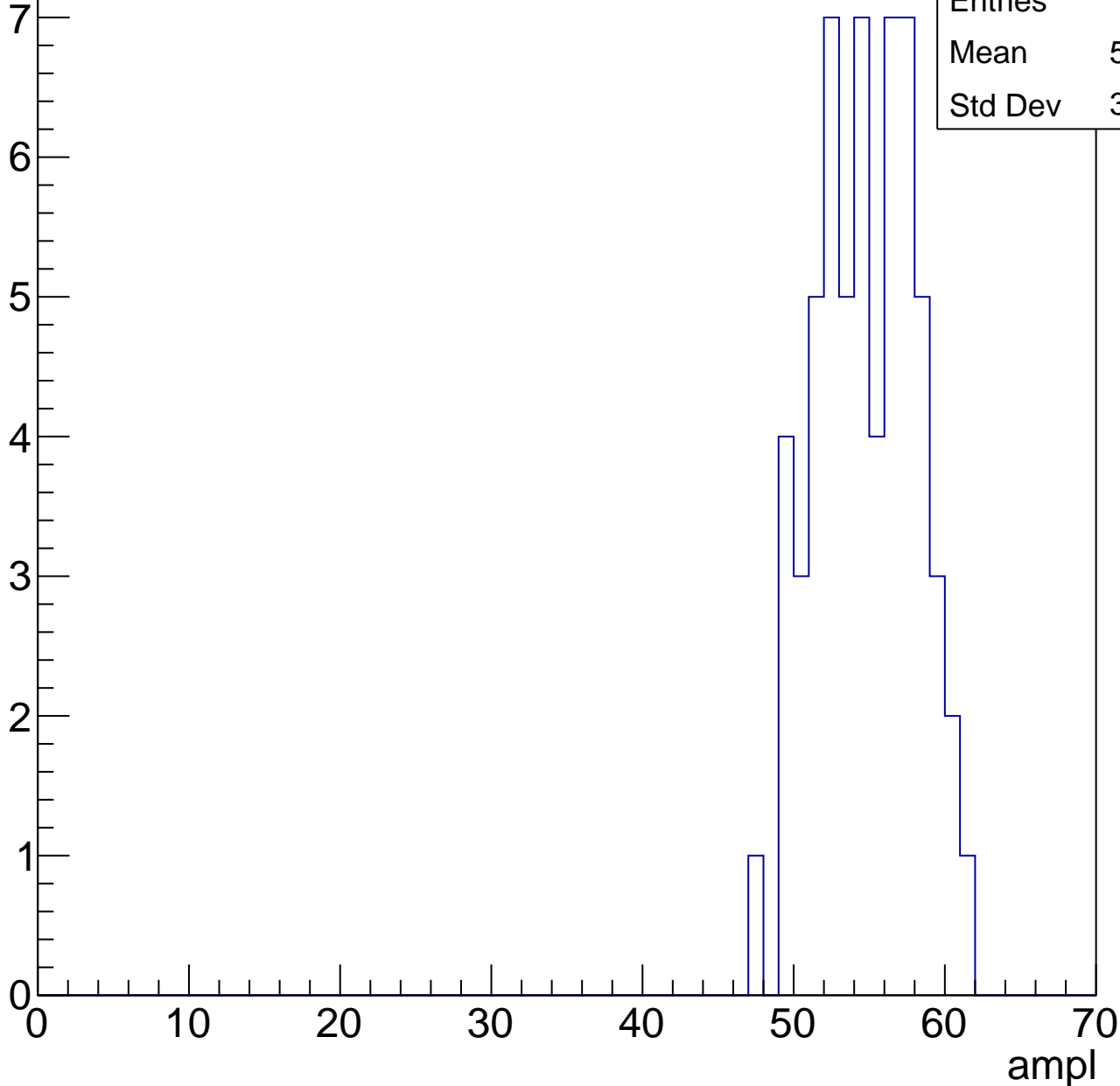


B0L102S, U5-ch82, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	54.33
Std Dev	3.238

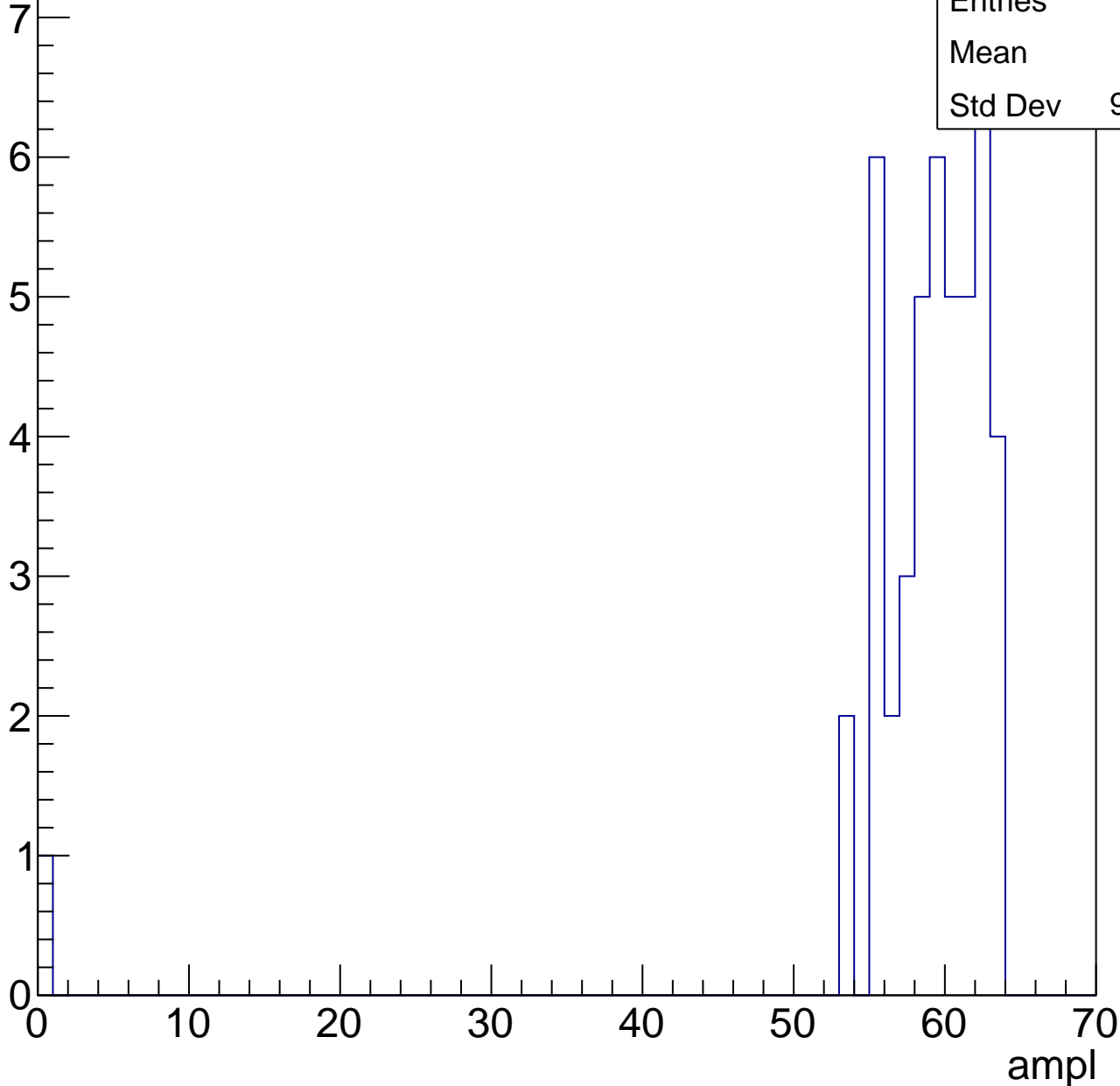


B0L102S, U5-ch82, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	57.7
Std Dev	9.038

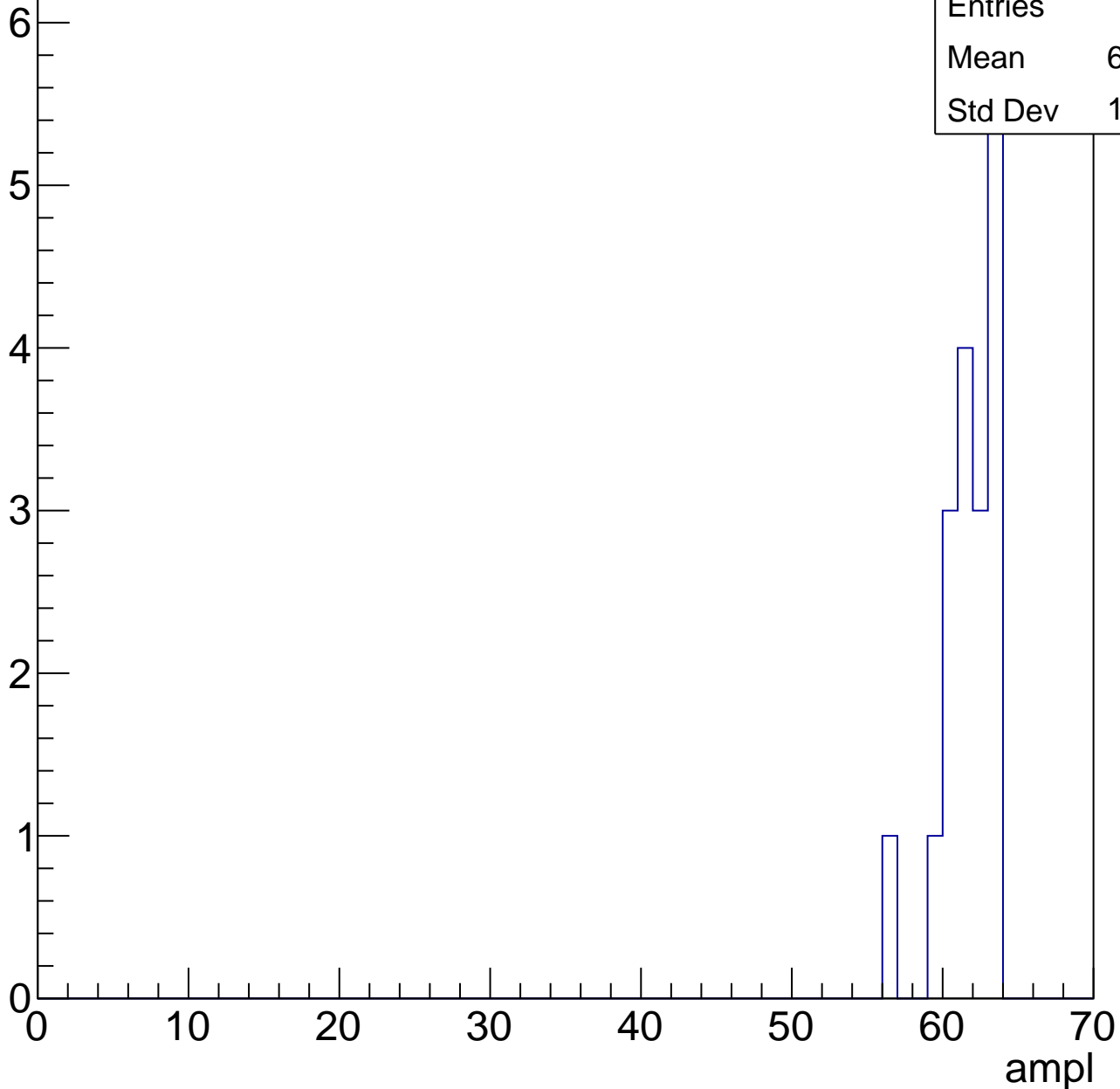


B0L102S, U5-ch82, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	18
Mean	61.28
Std Dev	1.789



B0L102S, U5-ch82, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch83, adc0

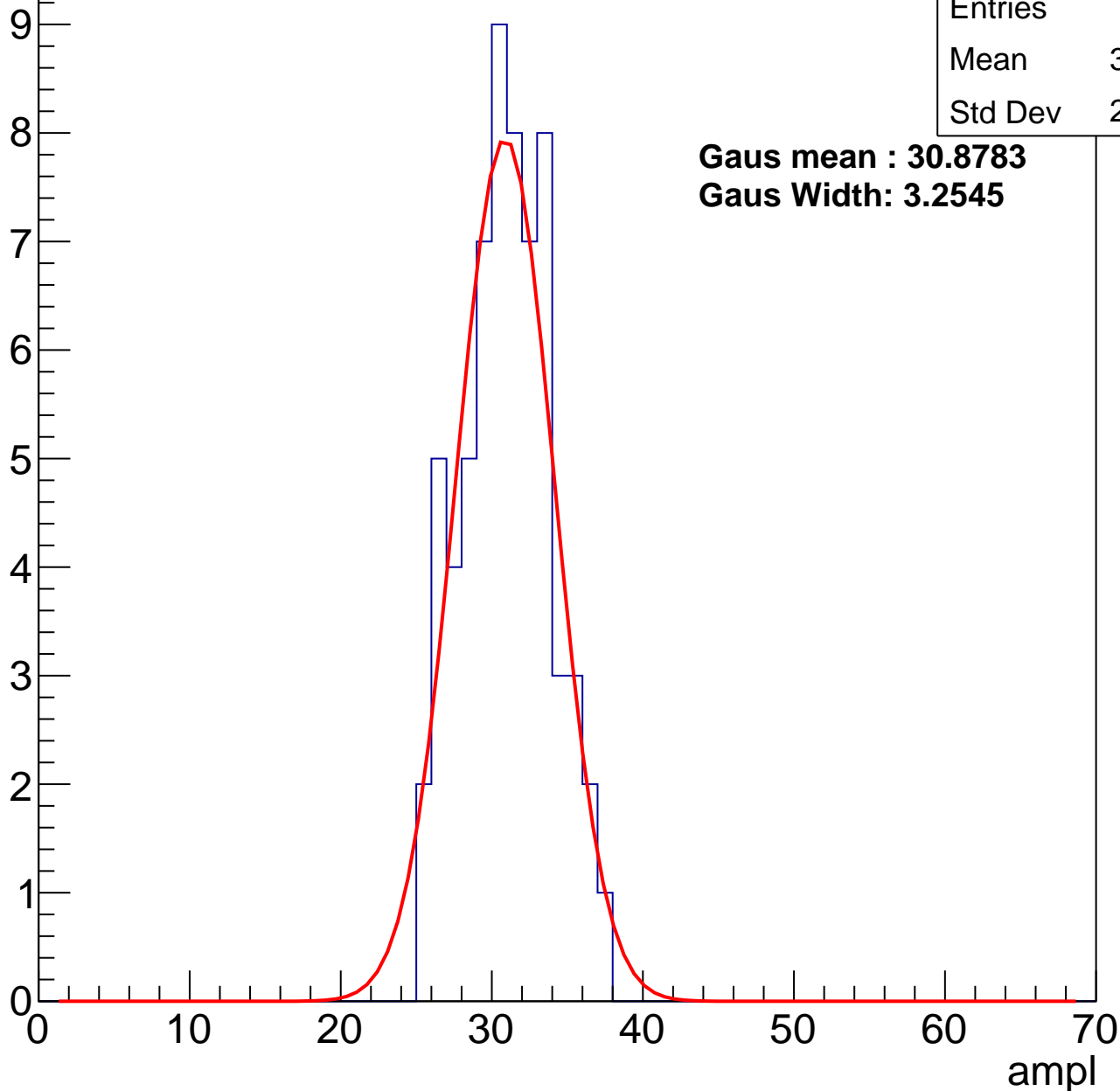
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	30.52
Std Dev	2.872

Gaus mean : 30.8783

Gaus Width: 3.2545



B0L102S, U5-ch83, adc1

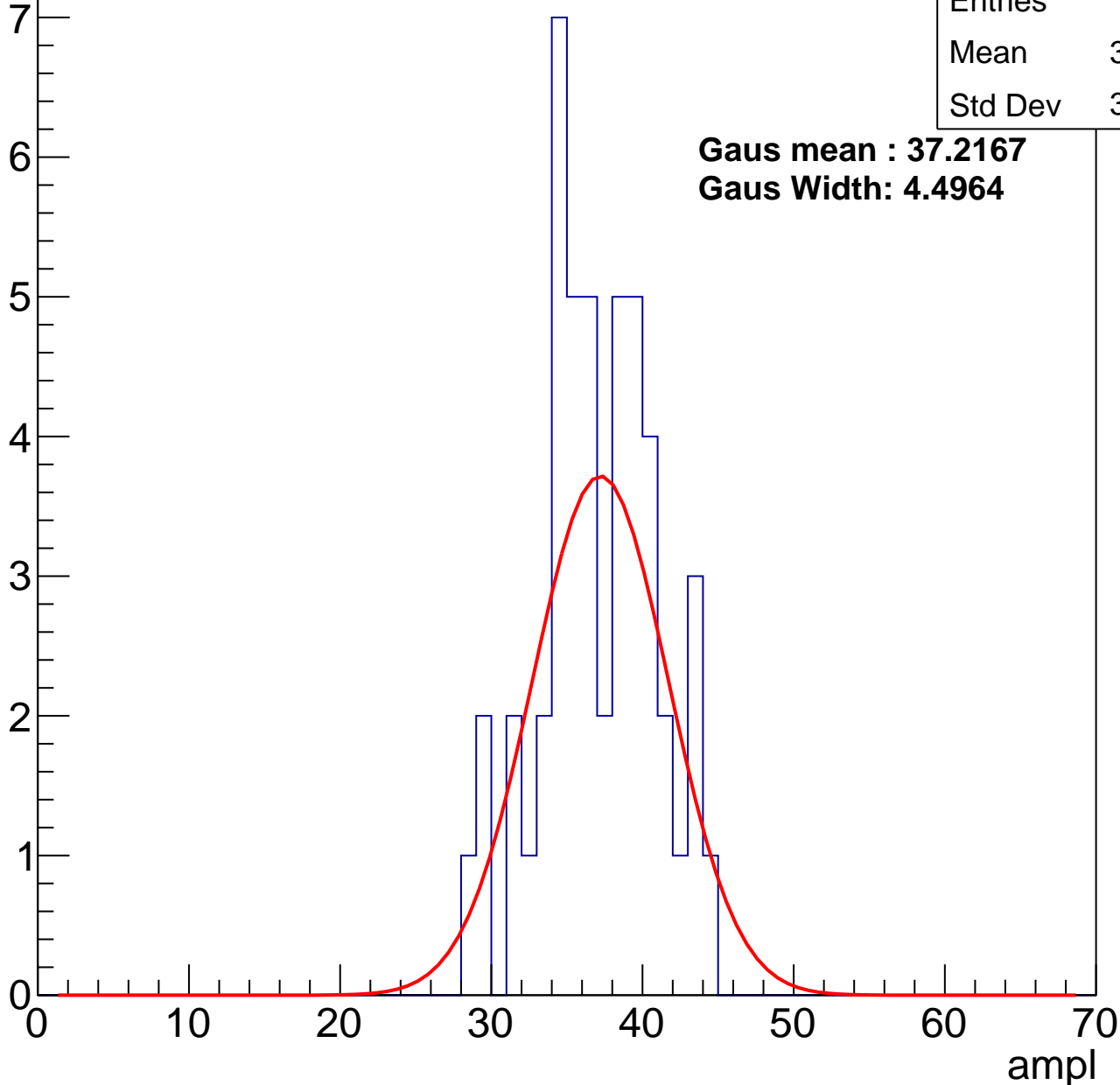
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	36.56
Std Dev	3.796

Gaus mean : 37.2167

Gaus Width: 4.4964



B0L102S, U5-ch83, adc2

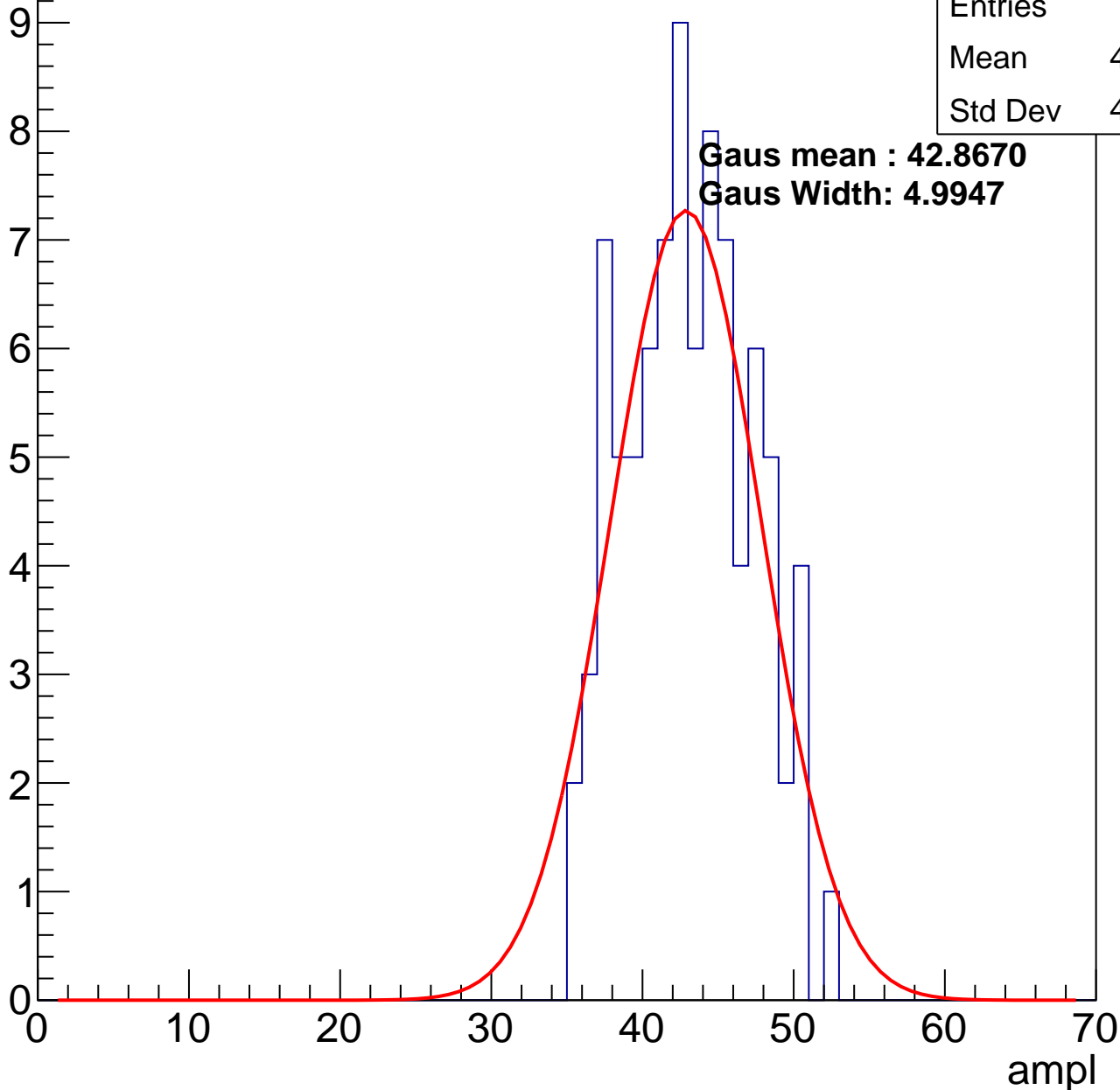
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	42.62
Std Dev	4.086

Gaus mean : 42.8670

Gaus Width: 4.9947

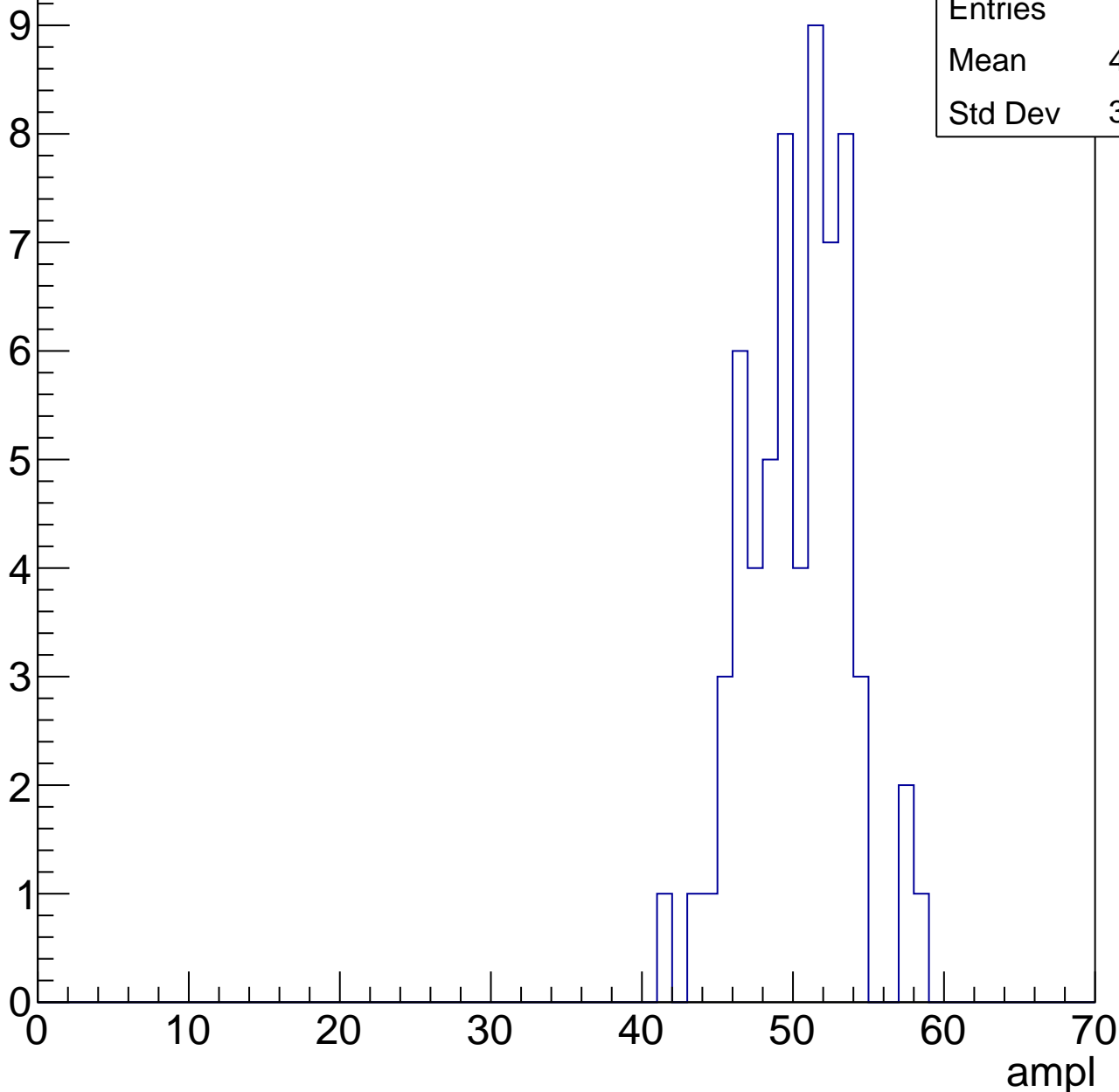


B0L102S, U5-ch83, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	49.84
Std Dev	3.377

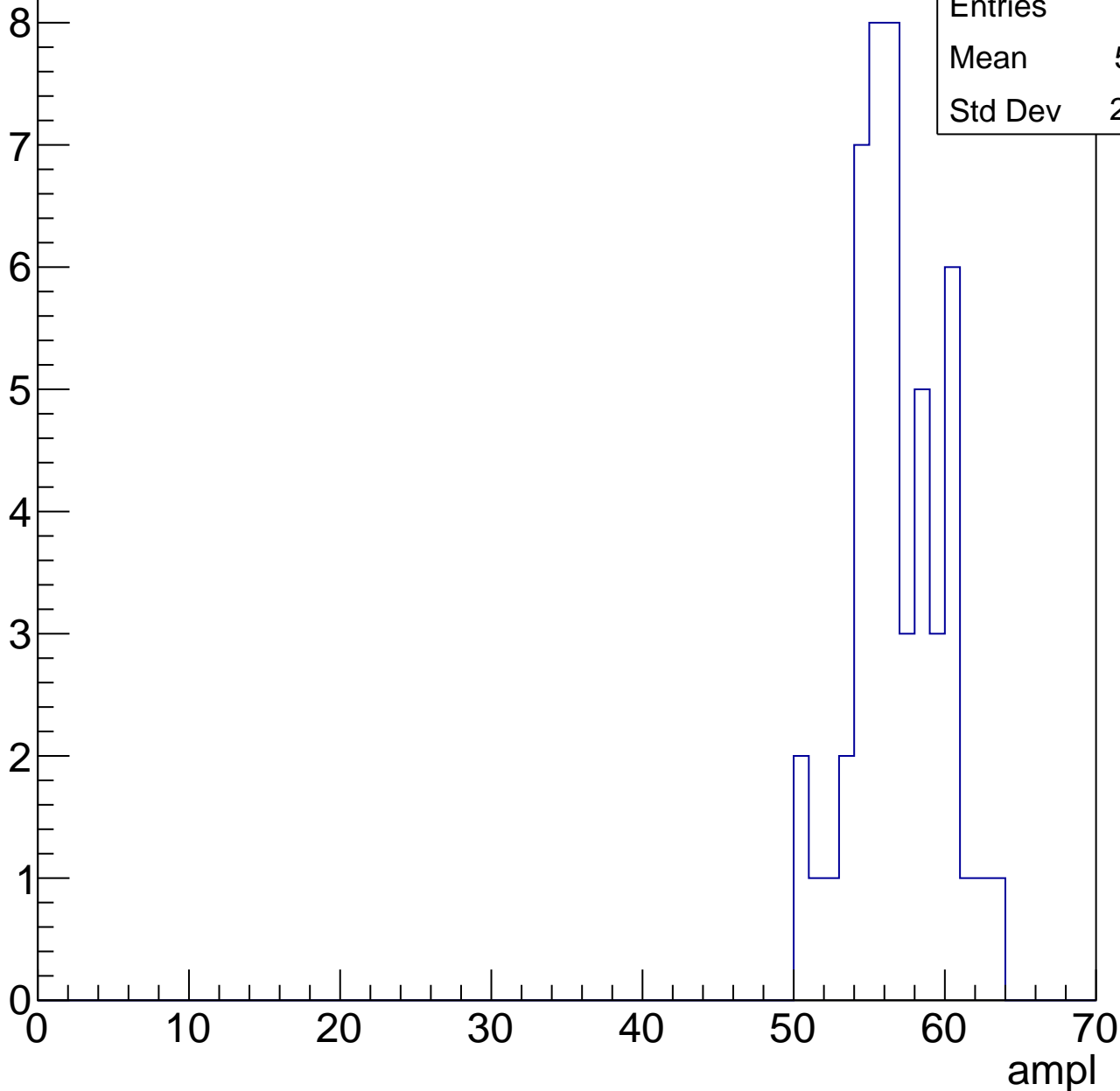


B0L102S, U5-ch83, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	56.31
Std Dev	2.922

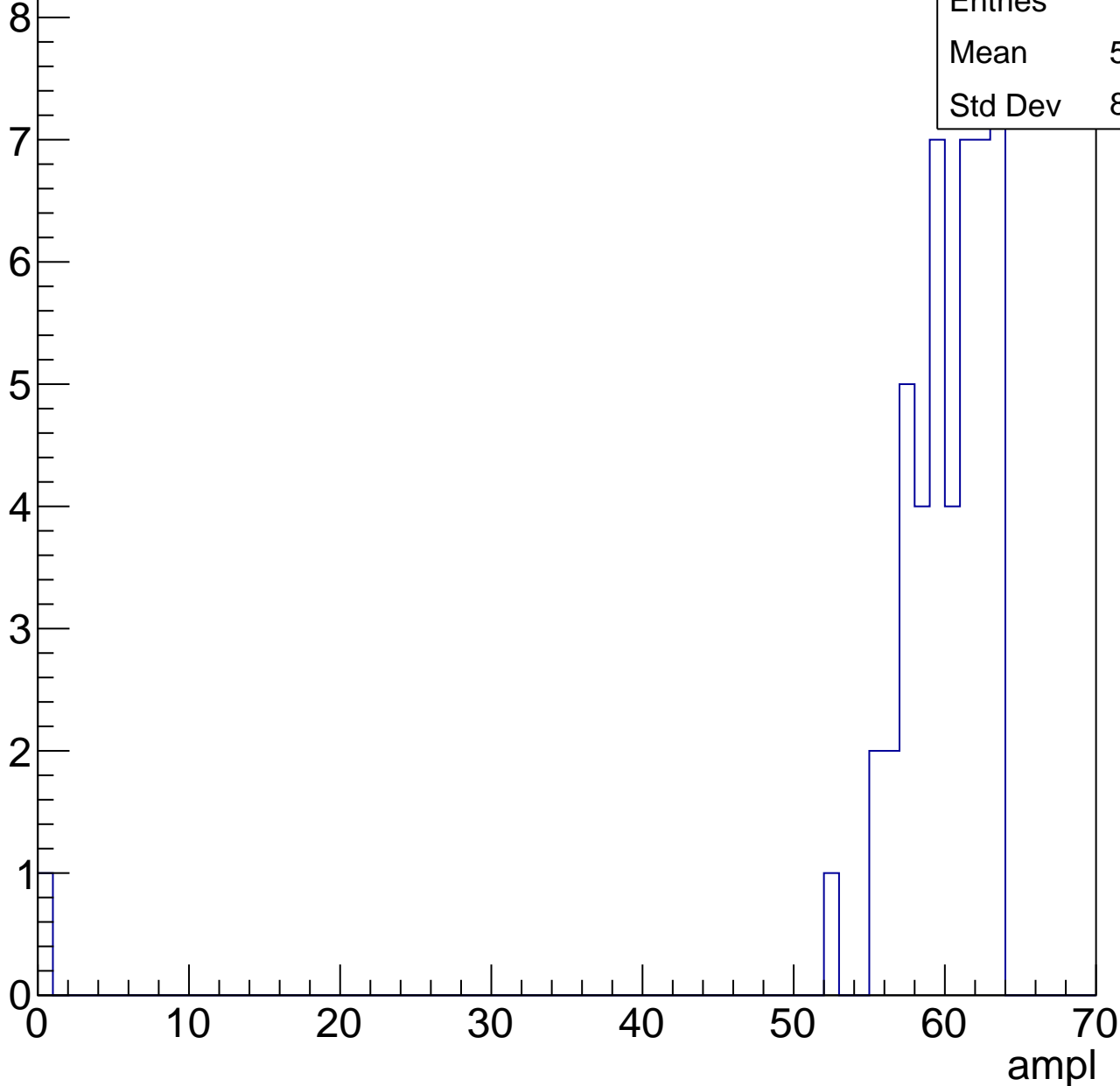


B0L102S, U5-ch83, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	58.52
Std Dev	8.916



B0L102S, U5-ch83, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L102S, U5-ch83, adc7

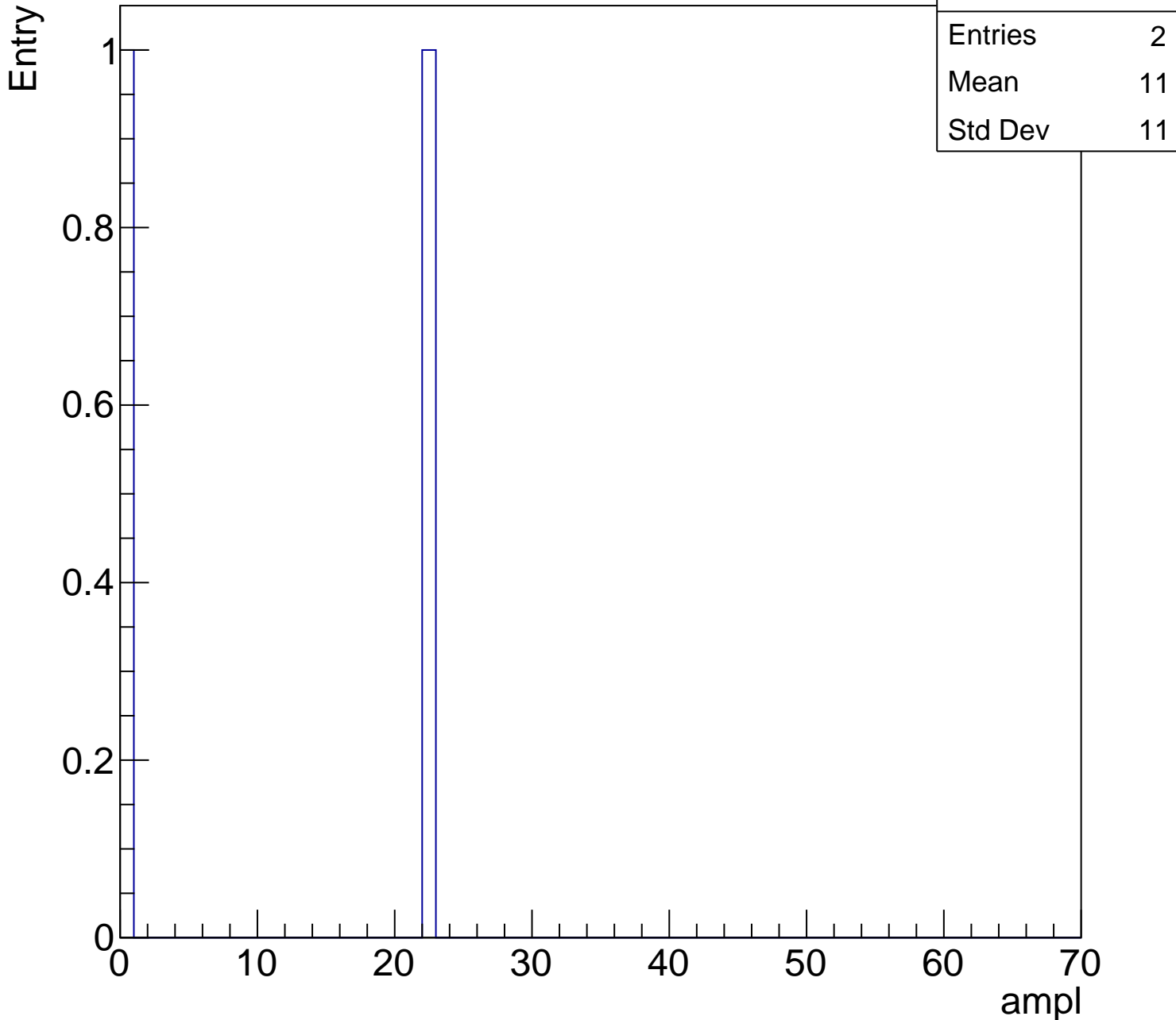
calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	11
Std Dev	11

ampl



B0L102S, U5-ch84, adc0

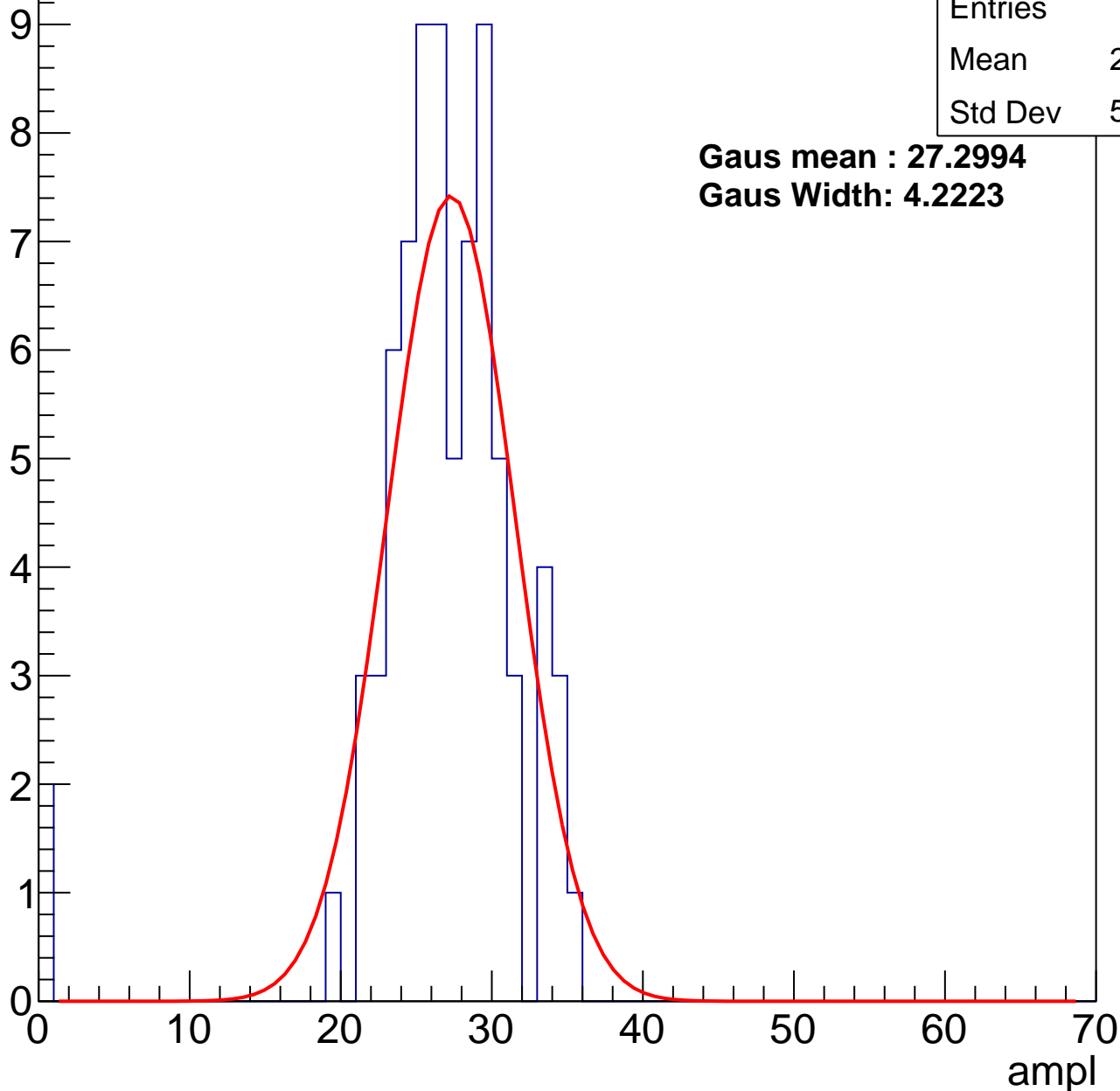
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	26.19
Std Dev	5.525

Gaus mean : 27.2994

Gaus Width: 4.2223



B0L102S, U5-ch84, adc1

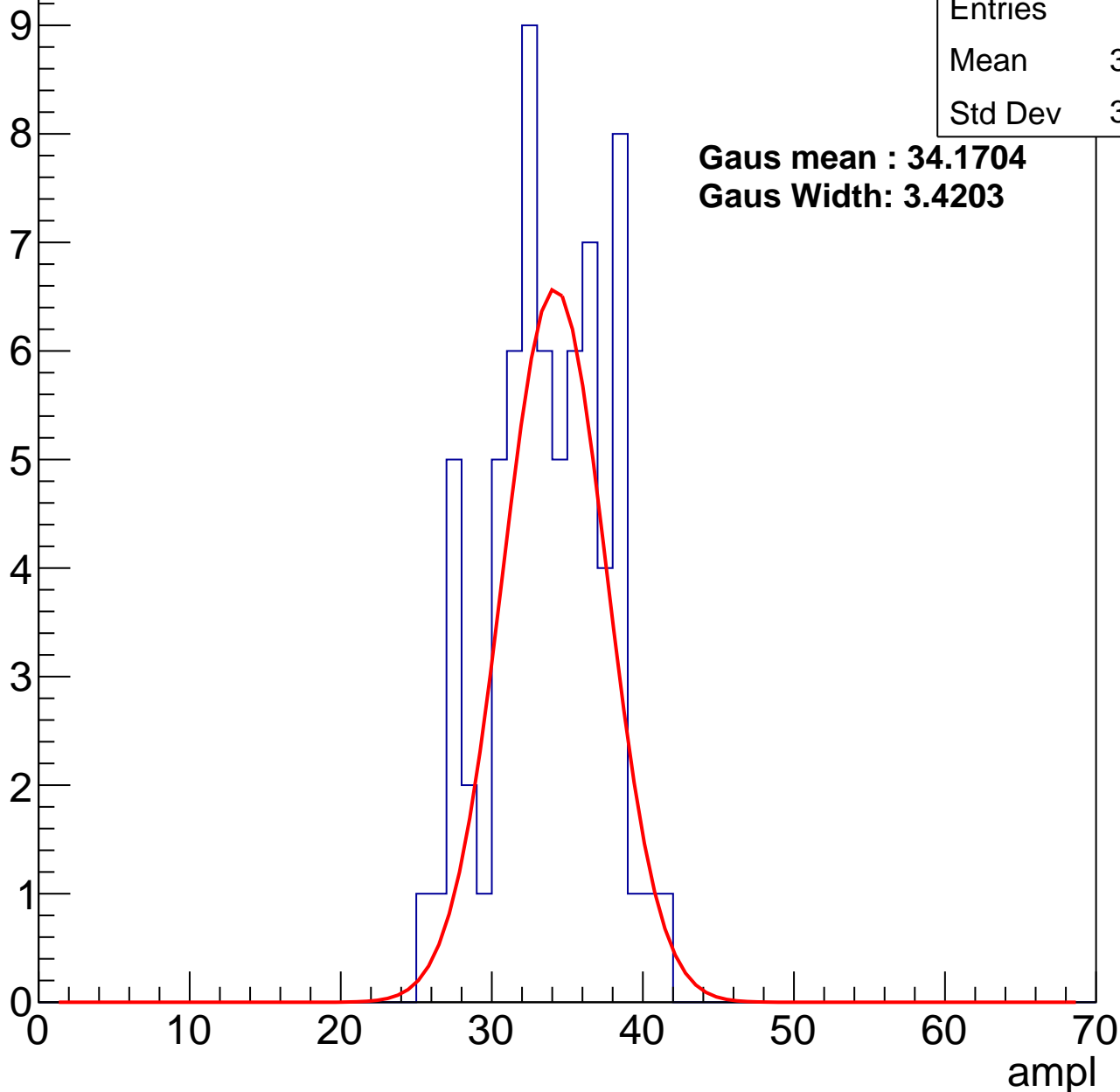
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	33.29
Std Dev	3.683

Gaus mean : 34.1704

Gaus Width: 3.4203



B0L102S, U5-ch84, adc2

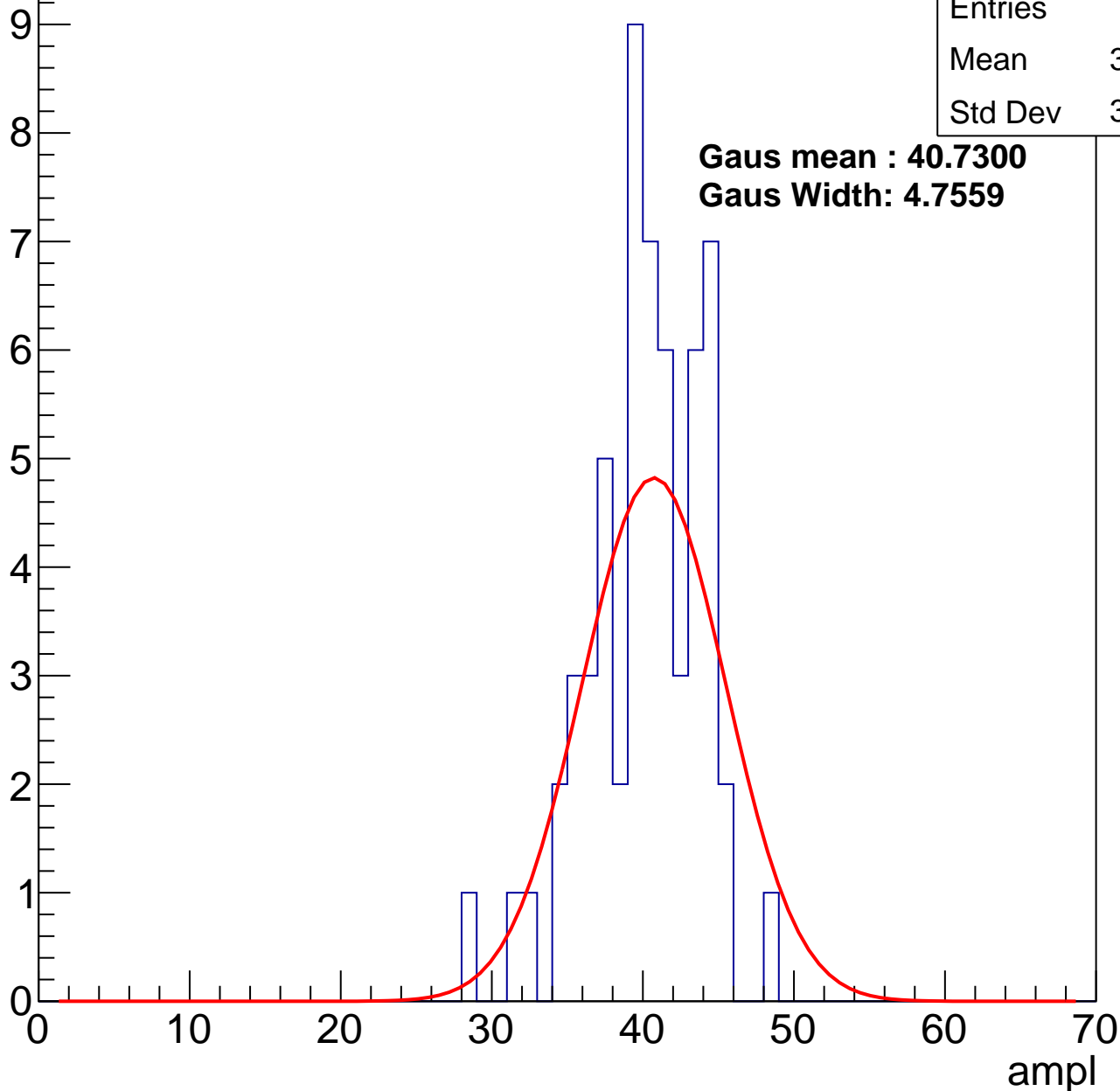
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	39.66
Std Dev	3.767

Gaus mean : 40.7300

Gaus Width: 4.7559

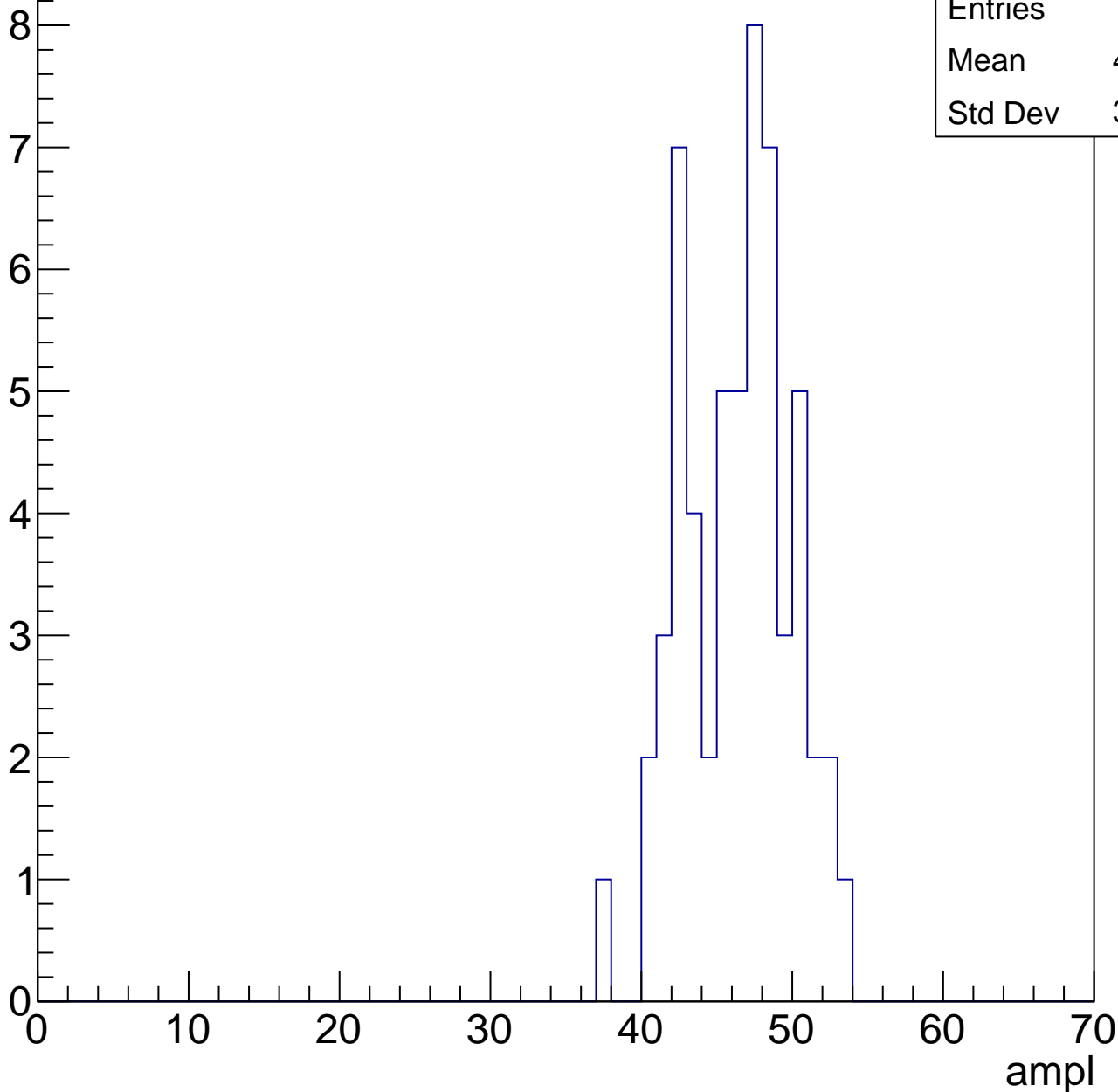


B0L102S, U5-ch84, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	45.91
Std Dev	3.511

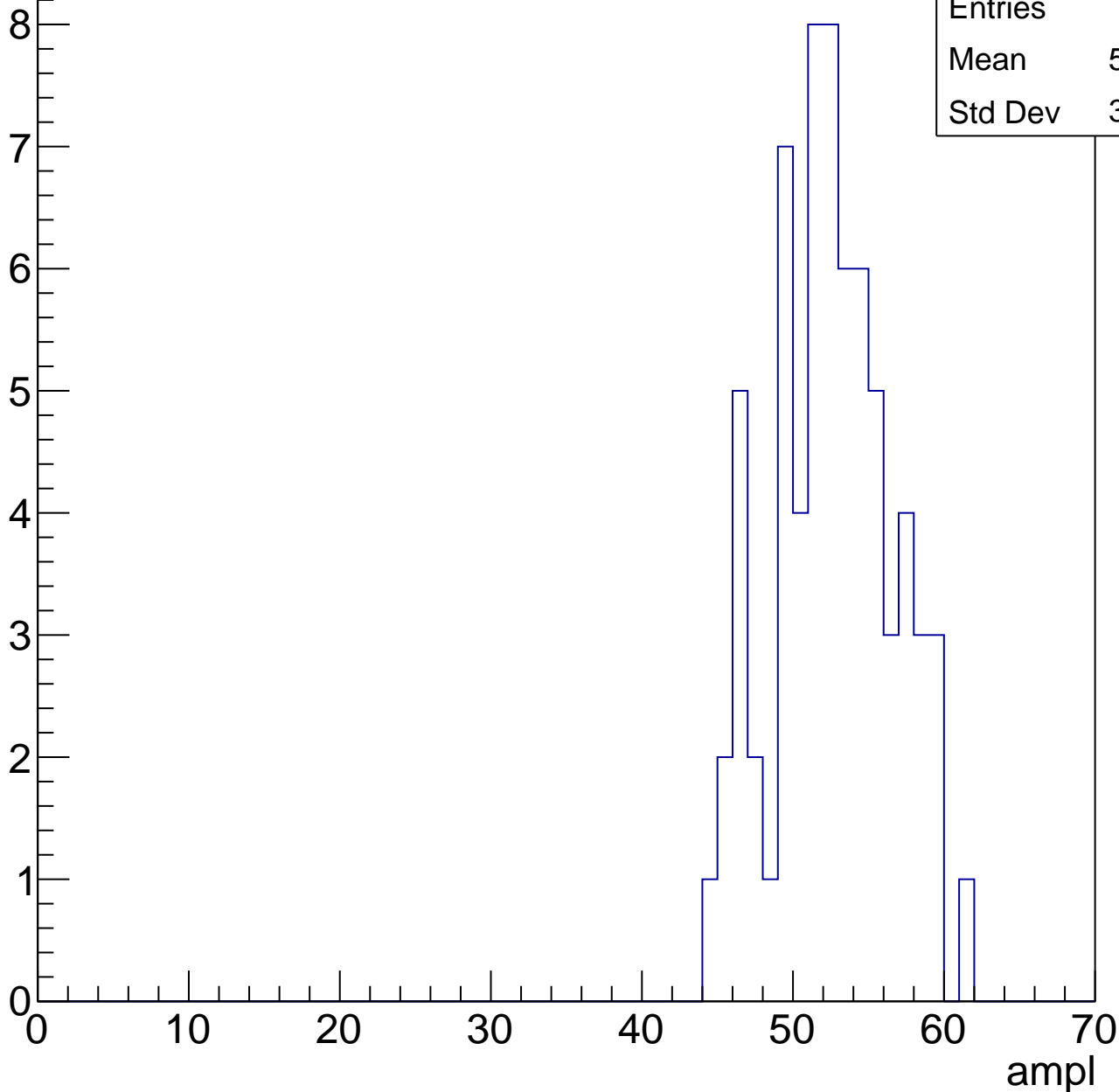


B0L102S, U5-ch84, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	52.14
Std Dev	3.906

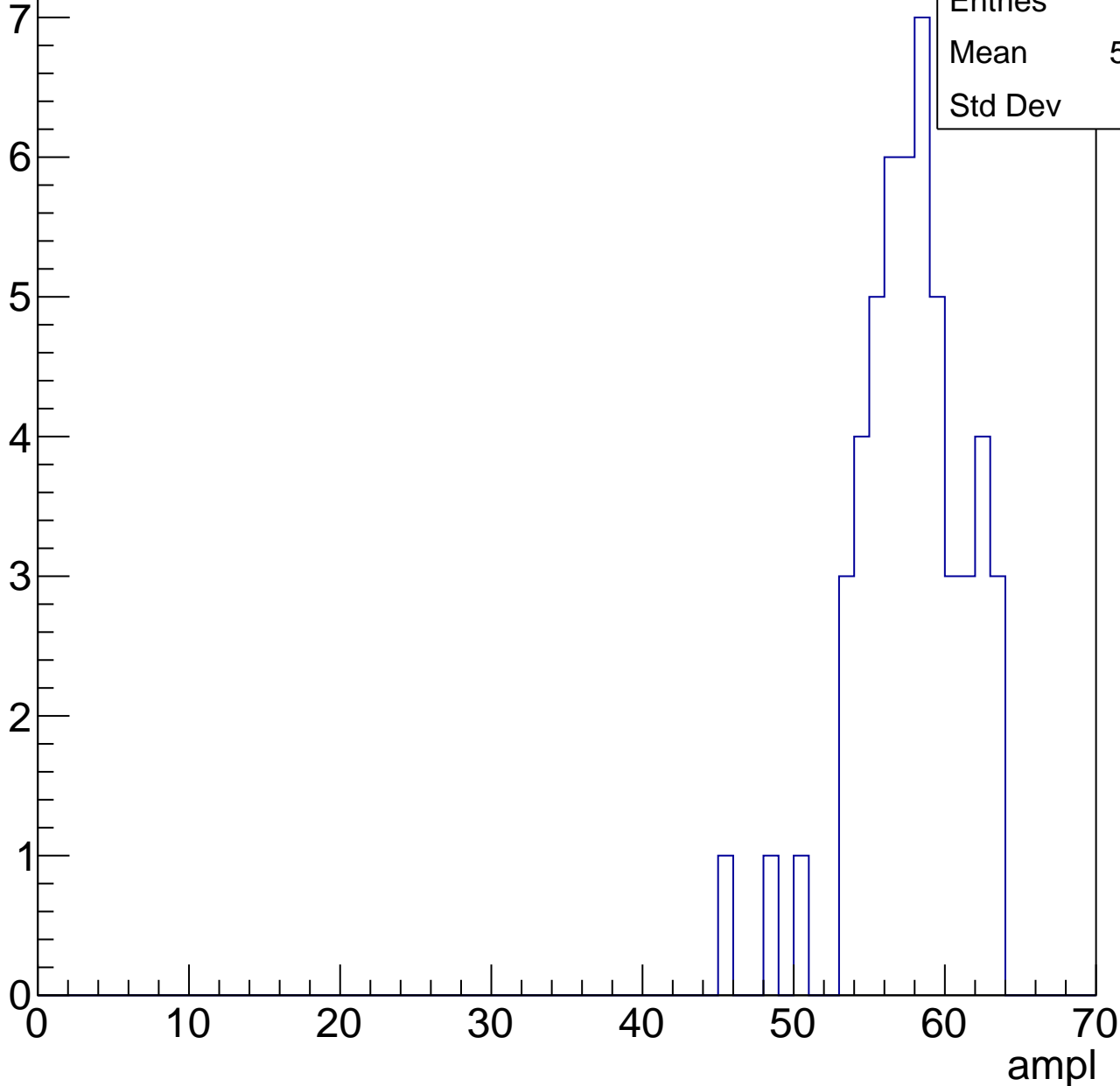


B0L102S, U5-ch84, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

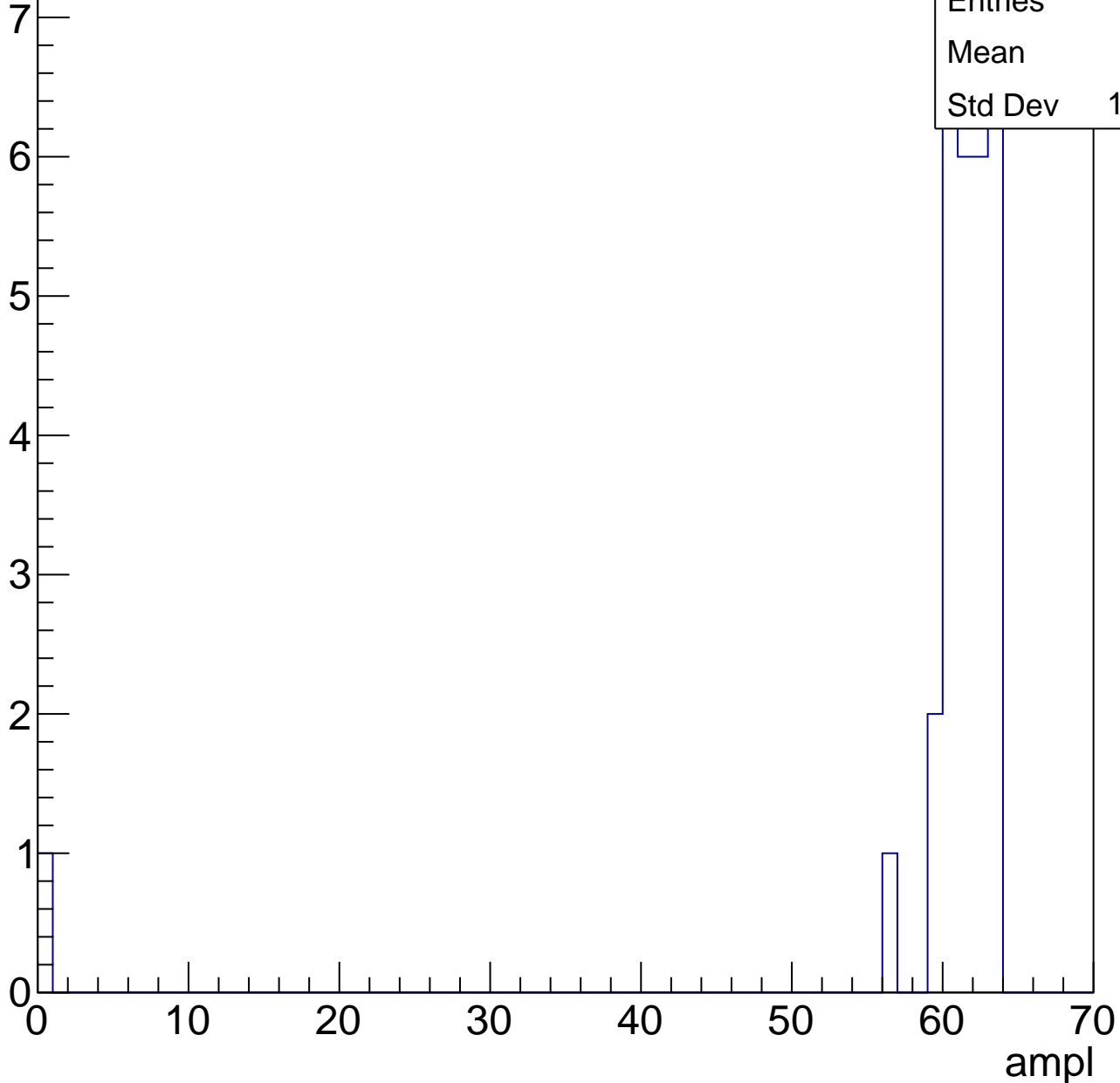
Entries	52
Mean	57.15
Std Dev	3.65



B0L102S, U5-ch84, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch84, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch85, adc0

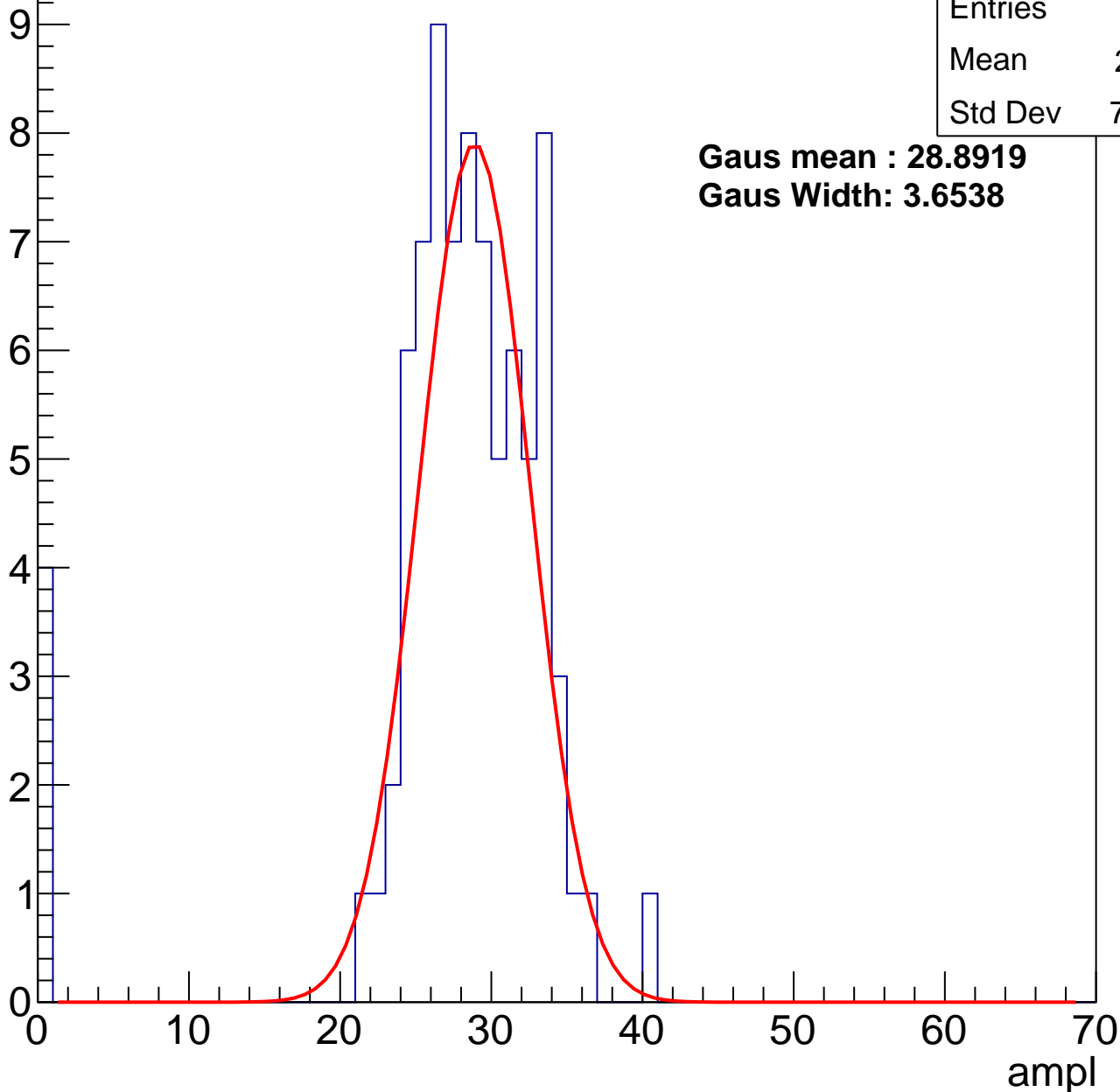
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	27.21
Std Dev	7.112

Gaus mean : 28.8919

Gaus Width: 3.6538



B0L102S, U5-ch85, adc1

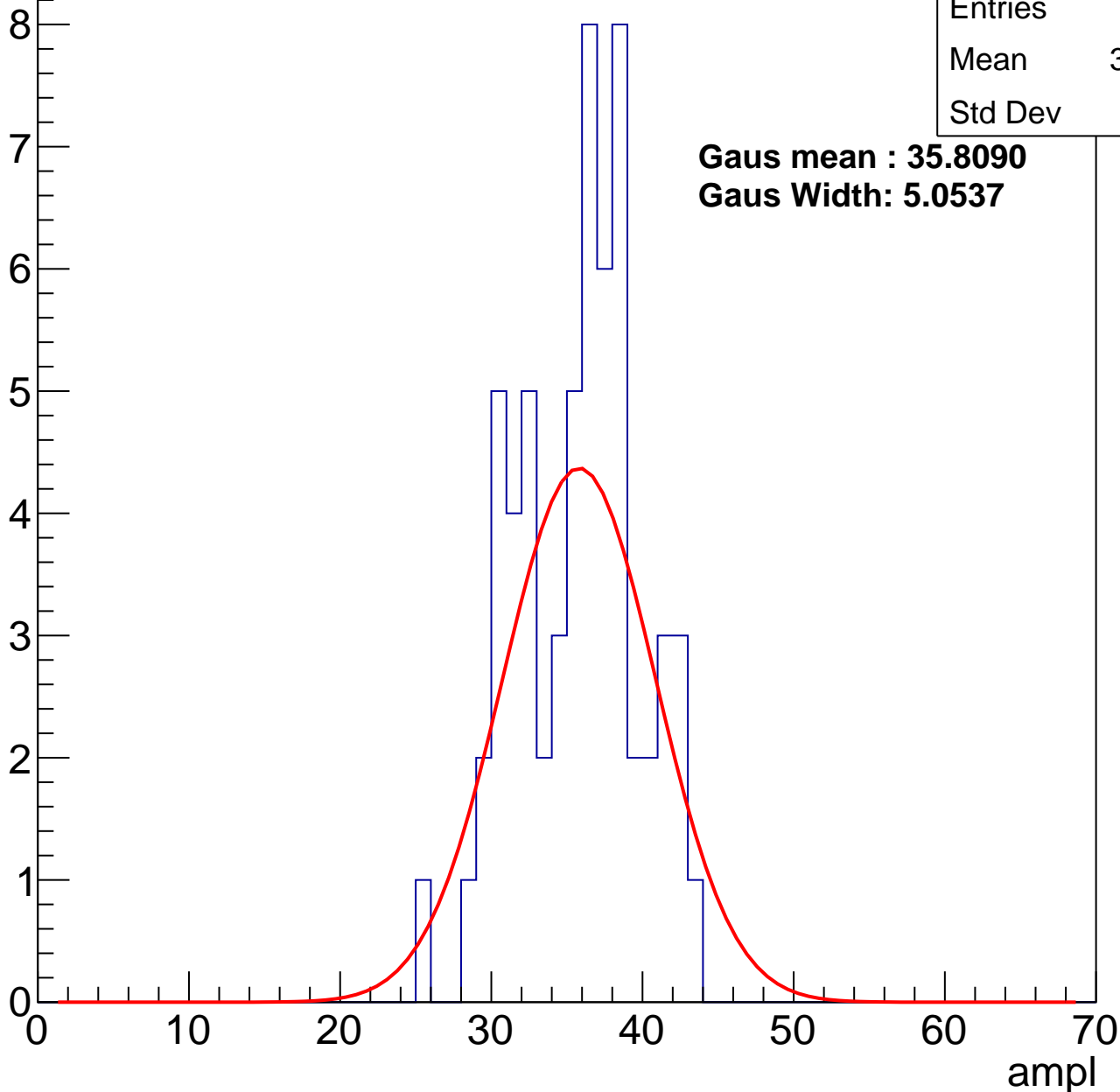
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	35.28
Std Dev	3.98

Gaus mean : 35.8090

Gaus Width: 5.0537



B0L102S, U5-ch85, adc2

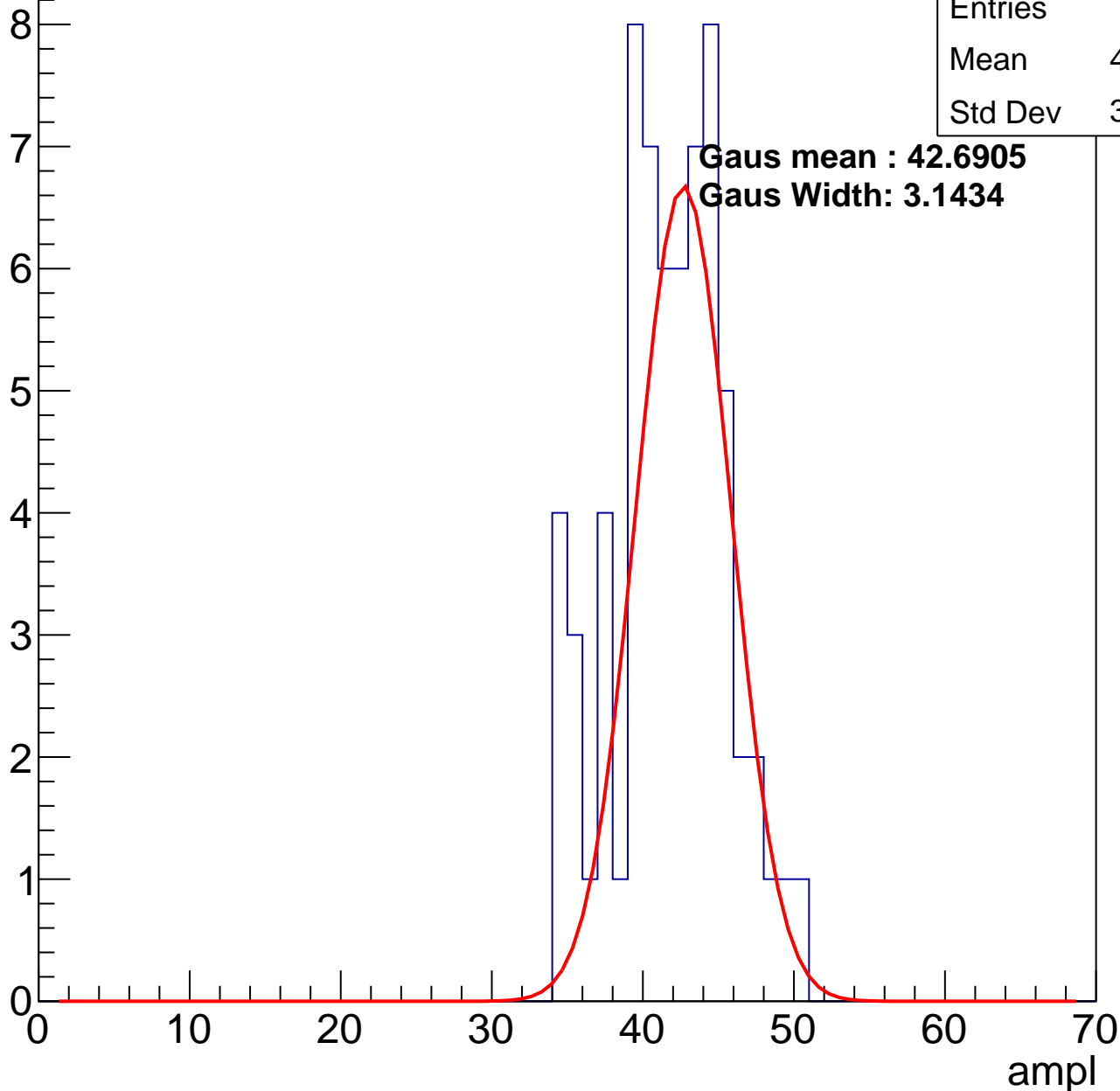
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	41.25
Std Dev	3.743

Gaus mean : 42.6905

Gaus Width: 3.1434

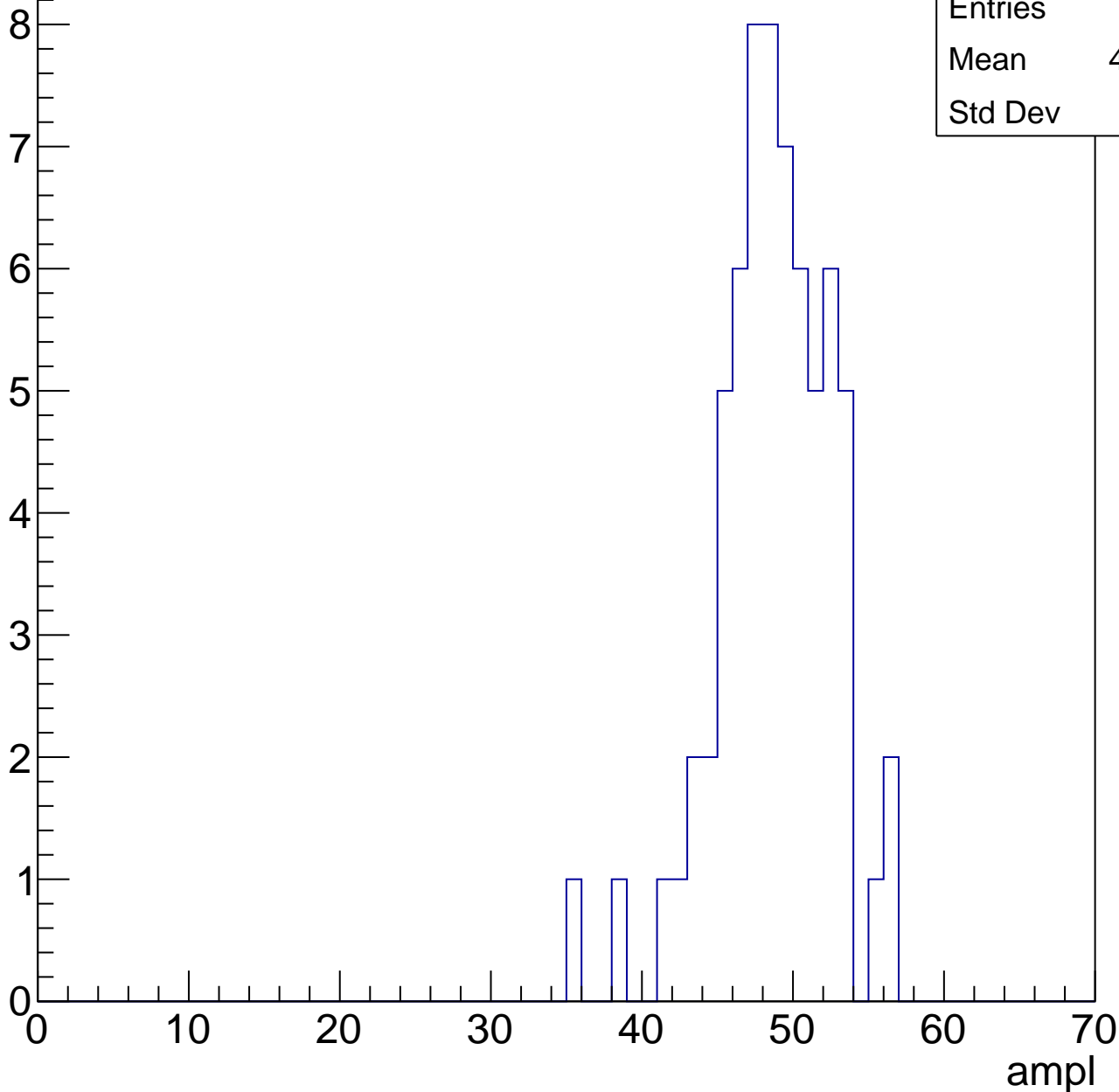


B0L102S, U5-ch85, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	48.25
Std Dev	3.83



B0L102S, U5-ch85, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	56
Mean	54.16
Std Dev	3.609

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

ampl

B0L102S, U5-ch85, adc5

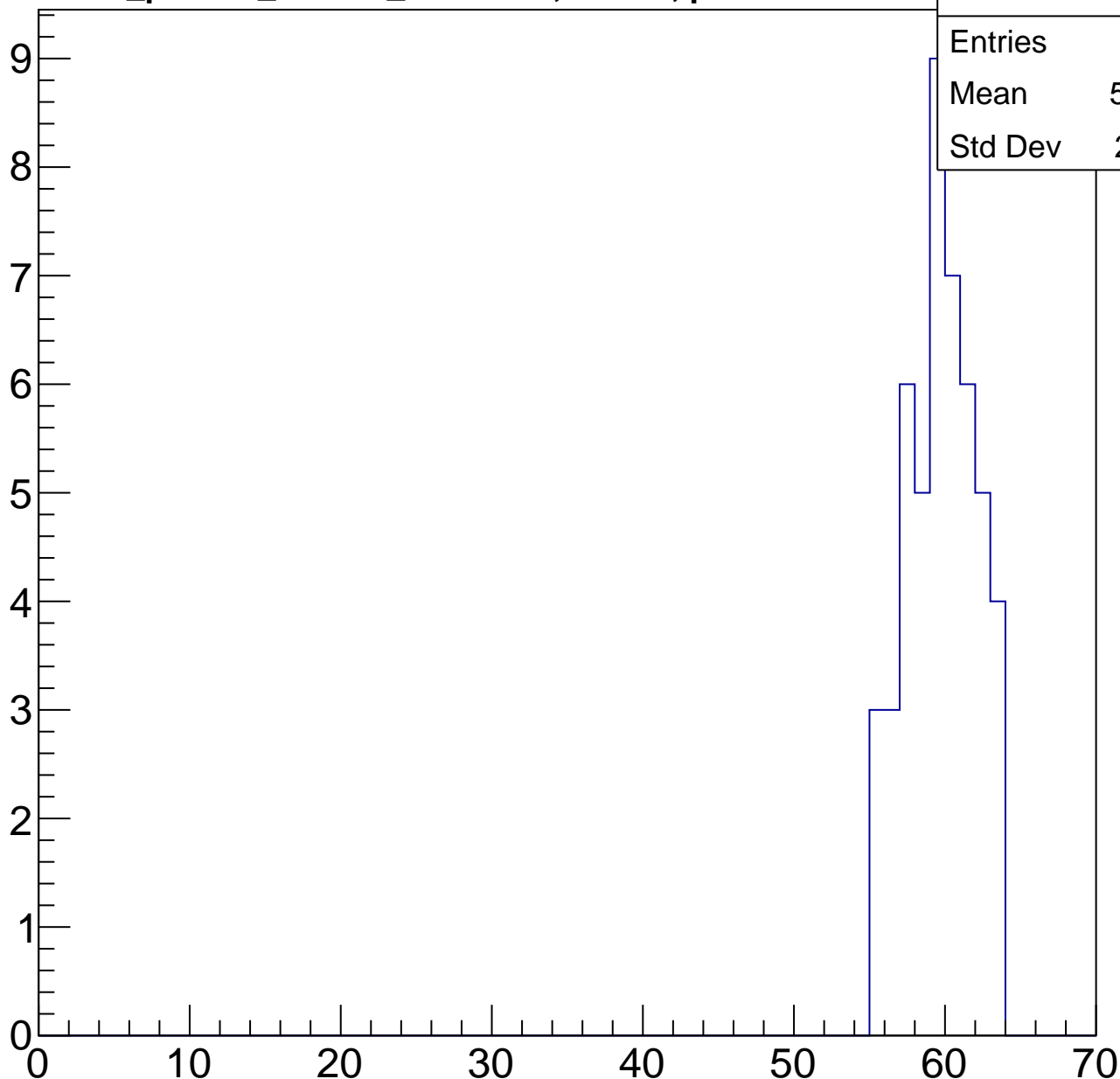
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.25
Std Dev	2.241

ampl

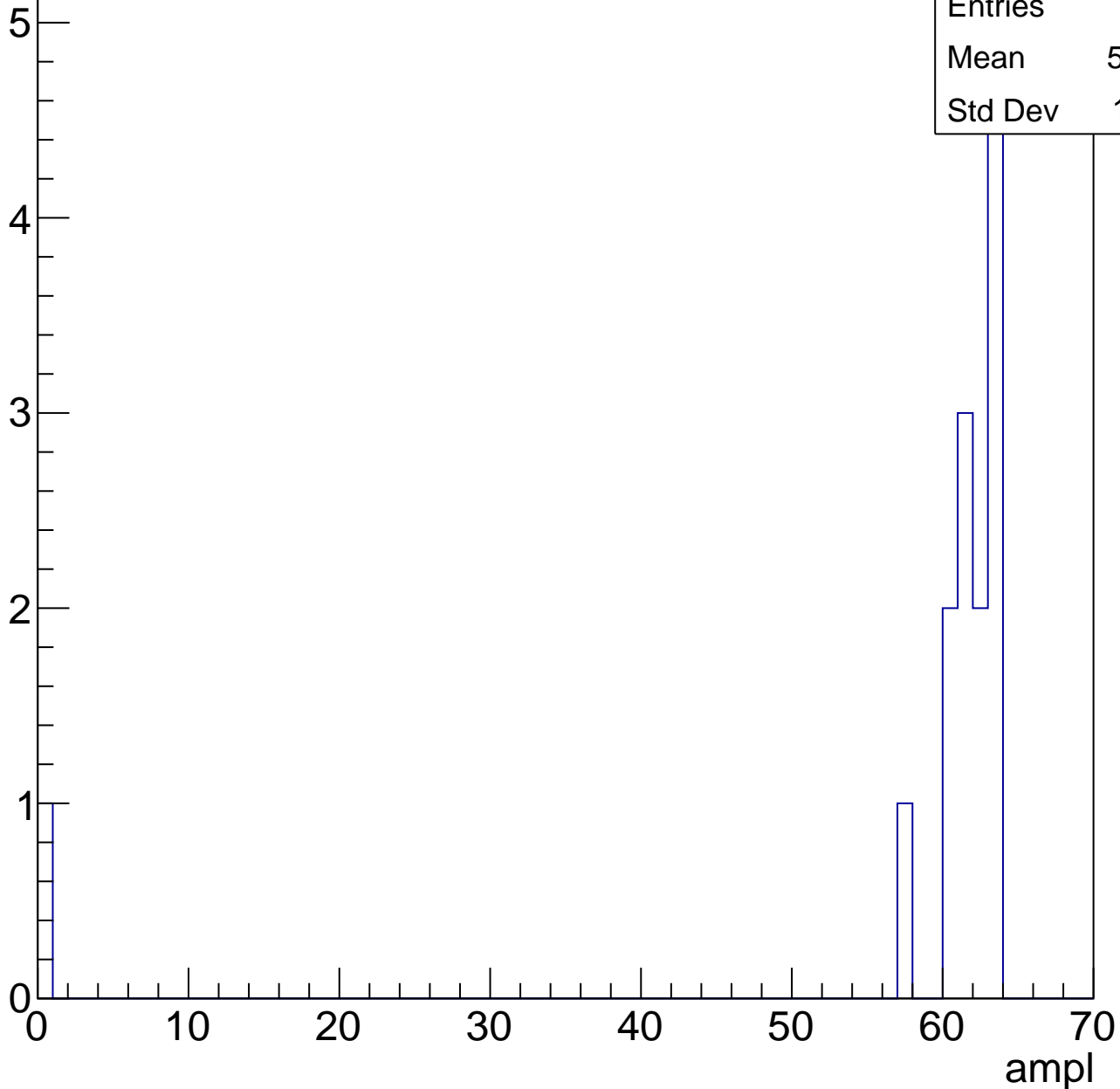


B0L102S, U5-ch85, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

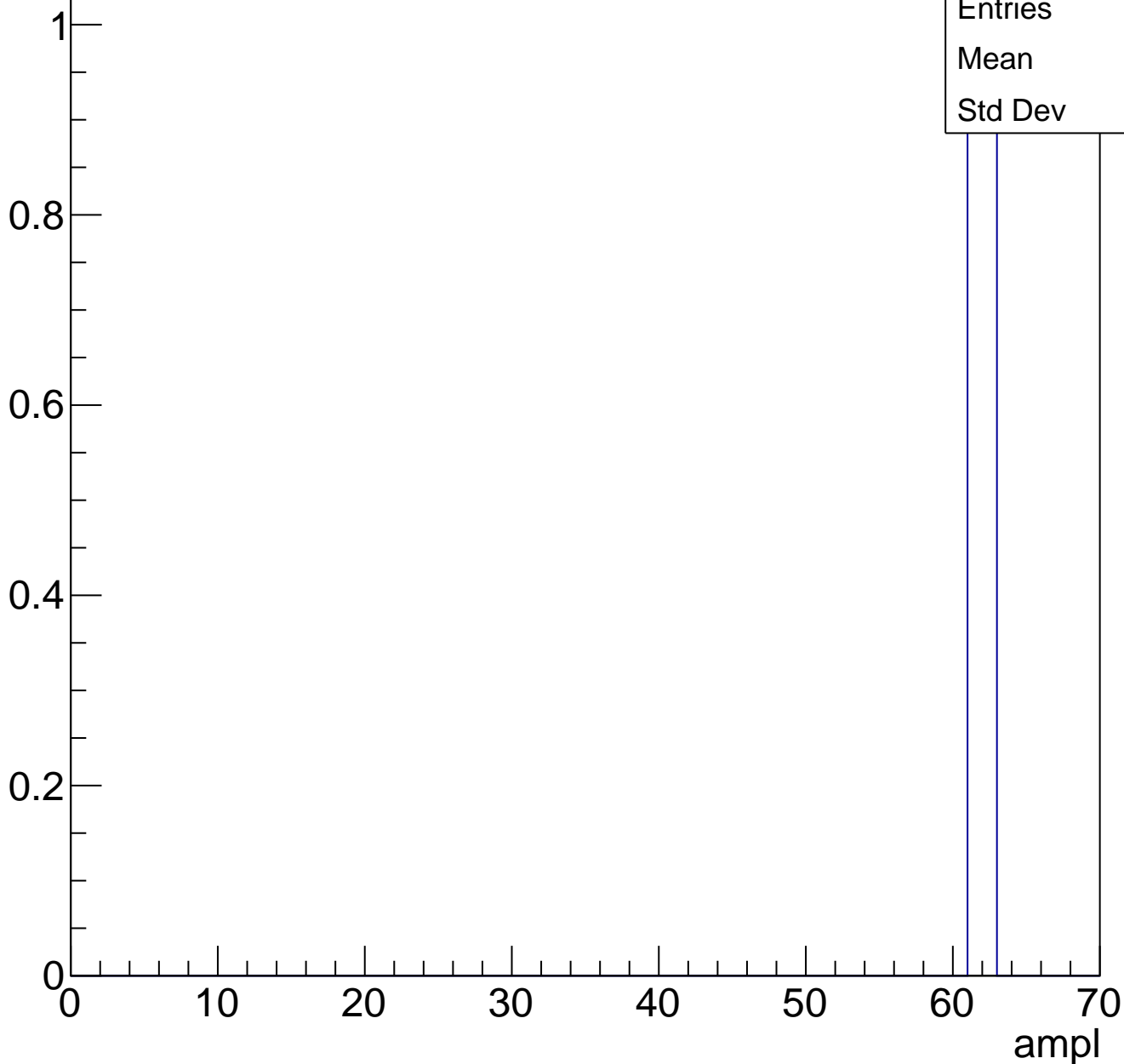
Entries	14
Mean	57.07
Std Dev	15.91



B0L102S, U5-ch85, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch86, adc0

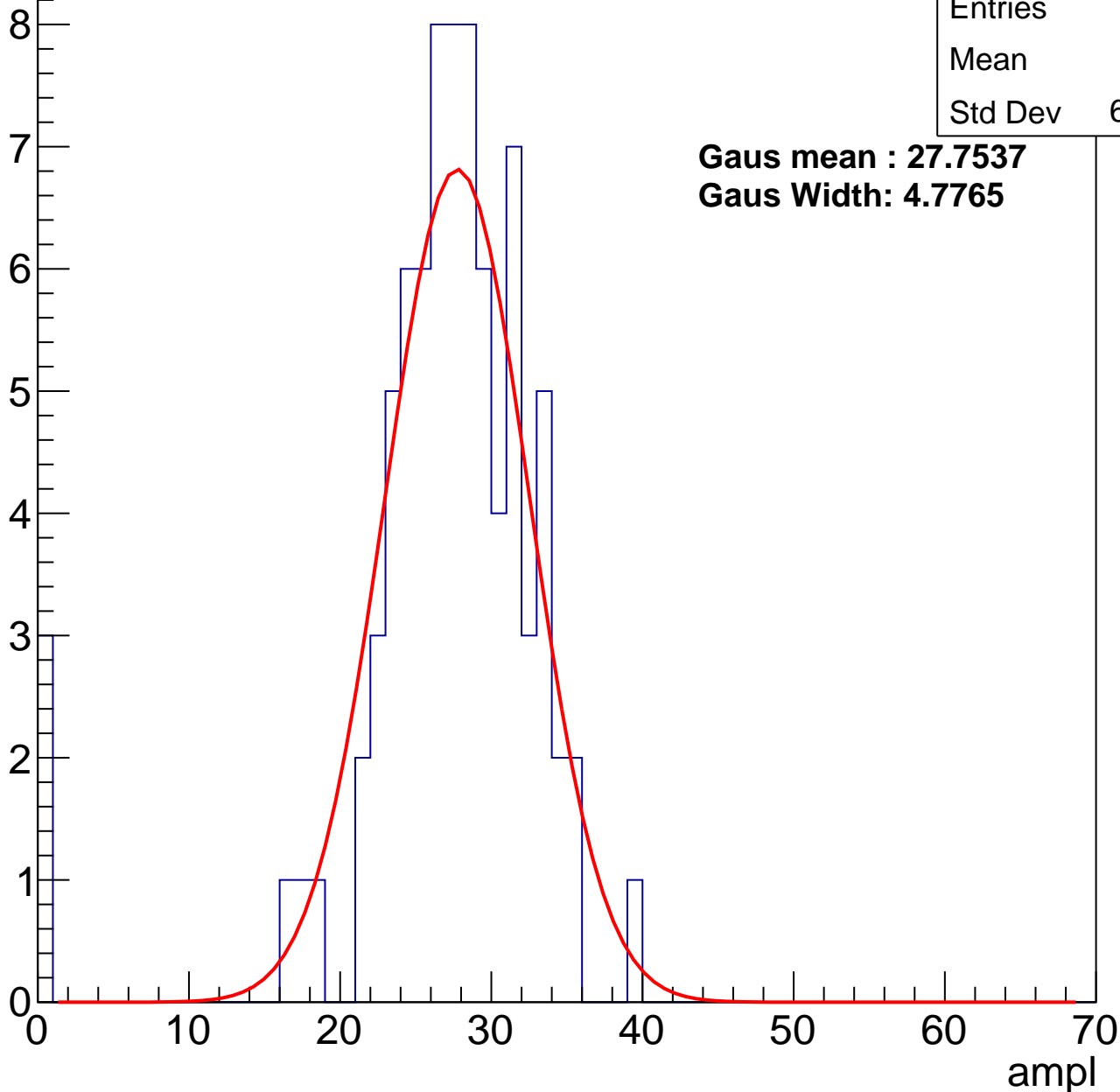
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	26.4
Std Dev	6.605

Gaus mean : 27.7537

Gaus Width: 4.7765



B0L102S, U5-ch86, adc1

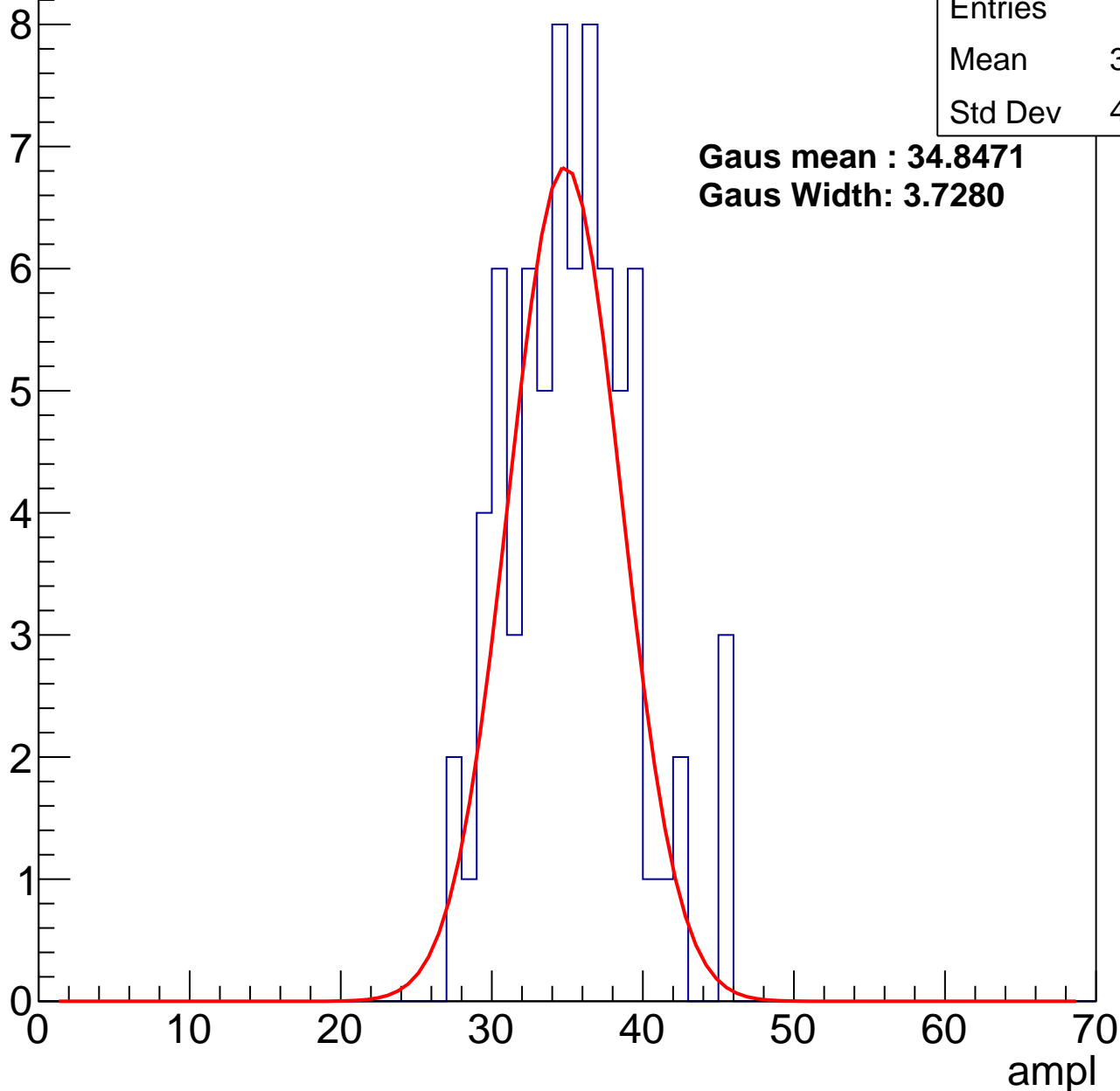
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	34.85
Std Dev	4.107

Gaus mean : 34.8471

Gaus Width: 3.7280



B0L102S, U5-ch86, adc2

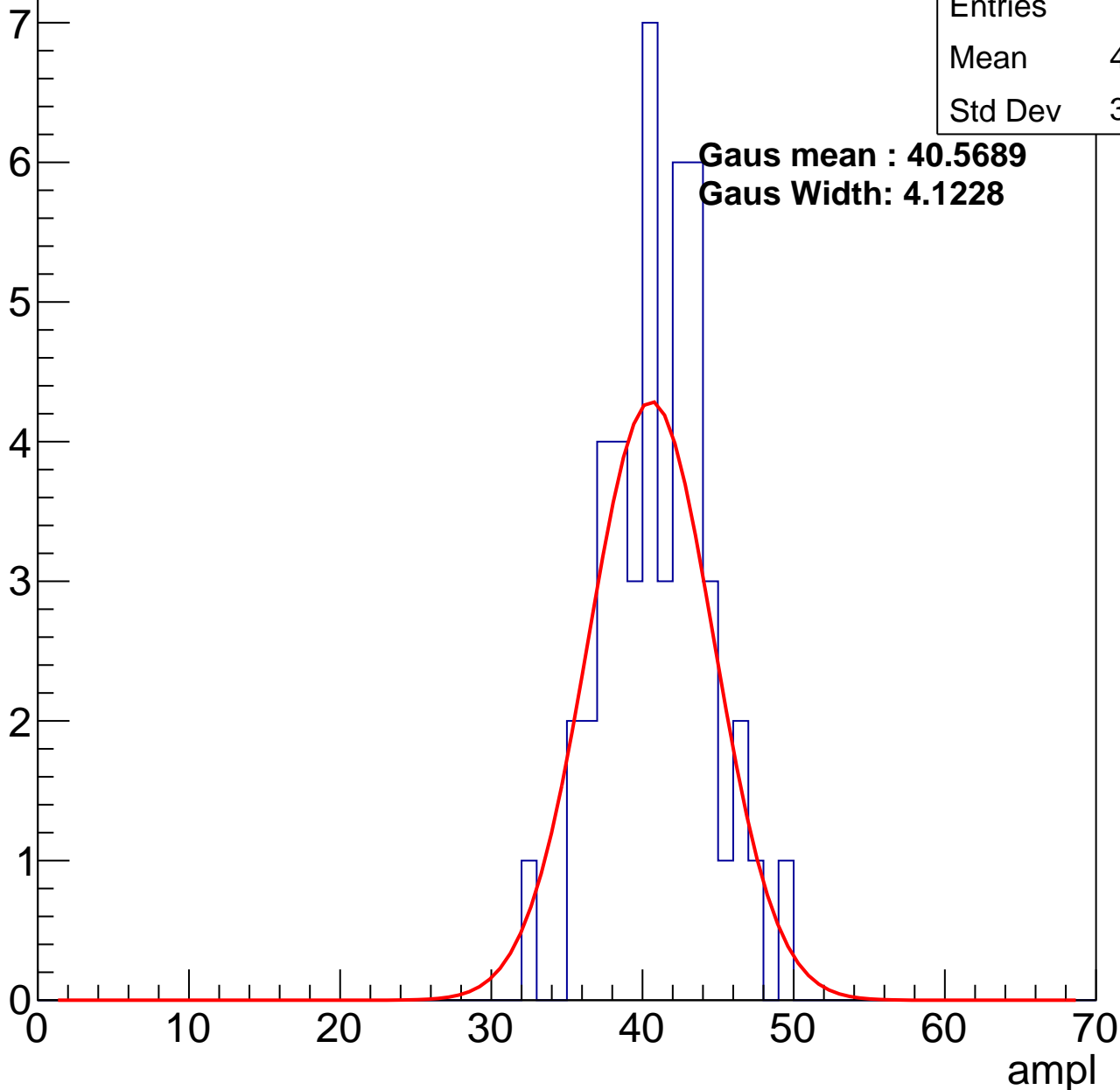
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	40.63
Std Dev	3.422

Gaus mean : 40.5689

Gaus Width: 4.1228

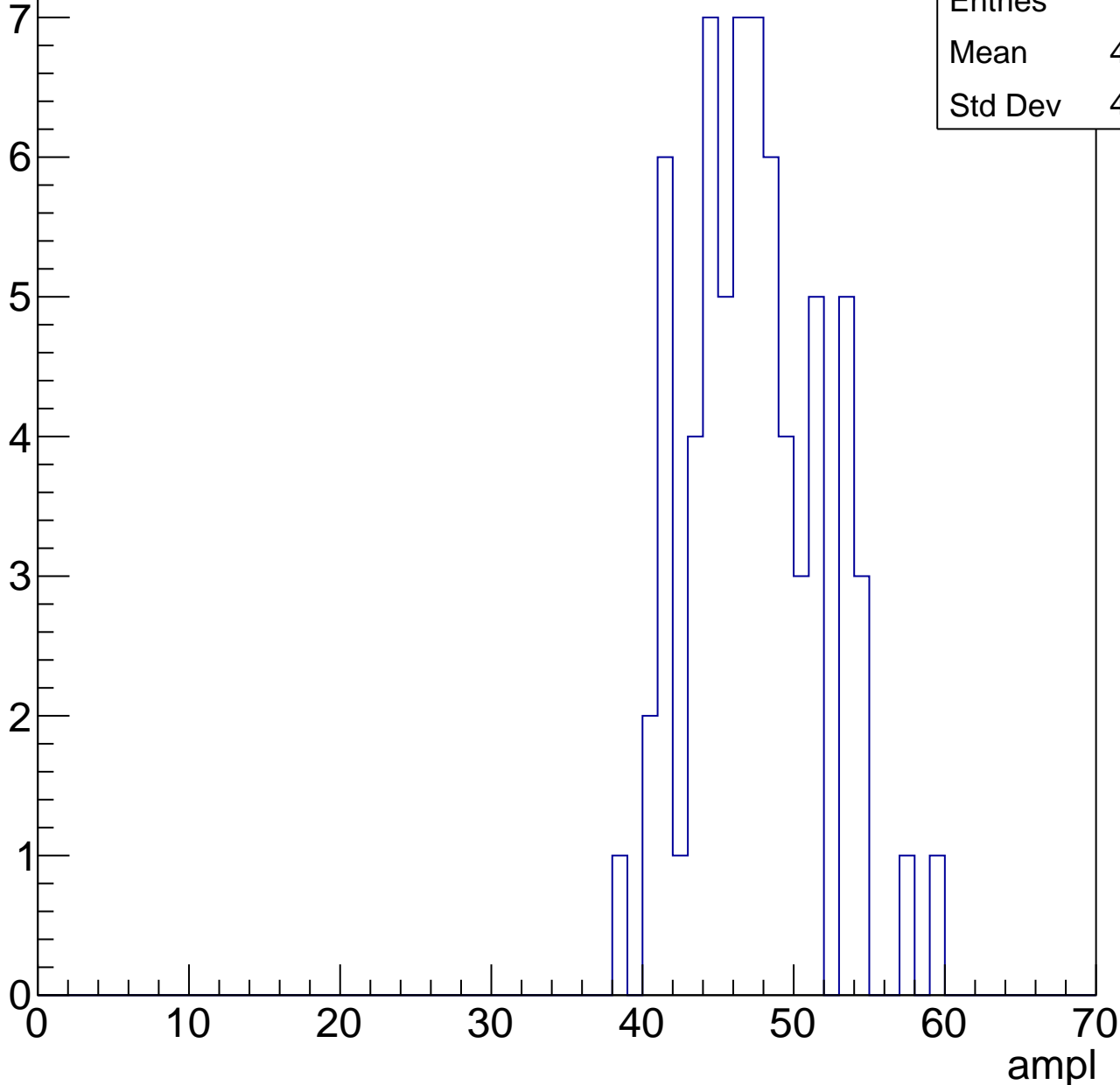


B0L102S, U5-ch86, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

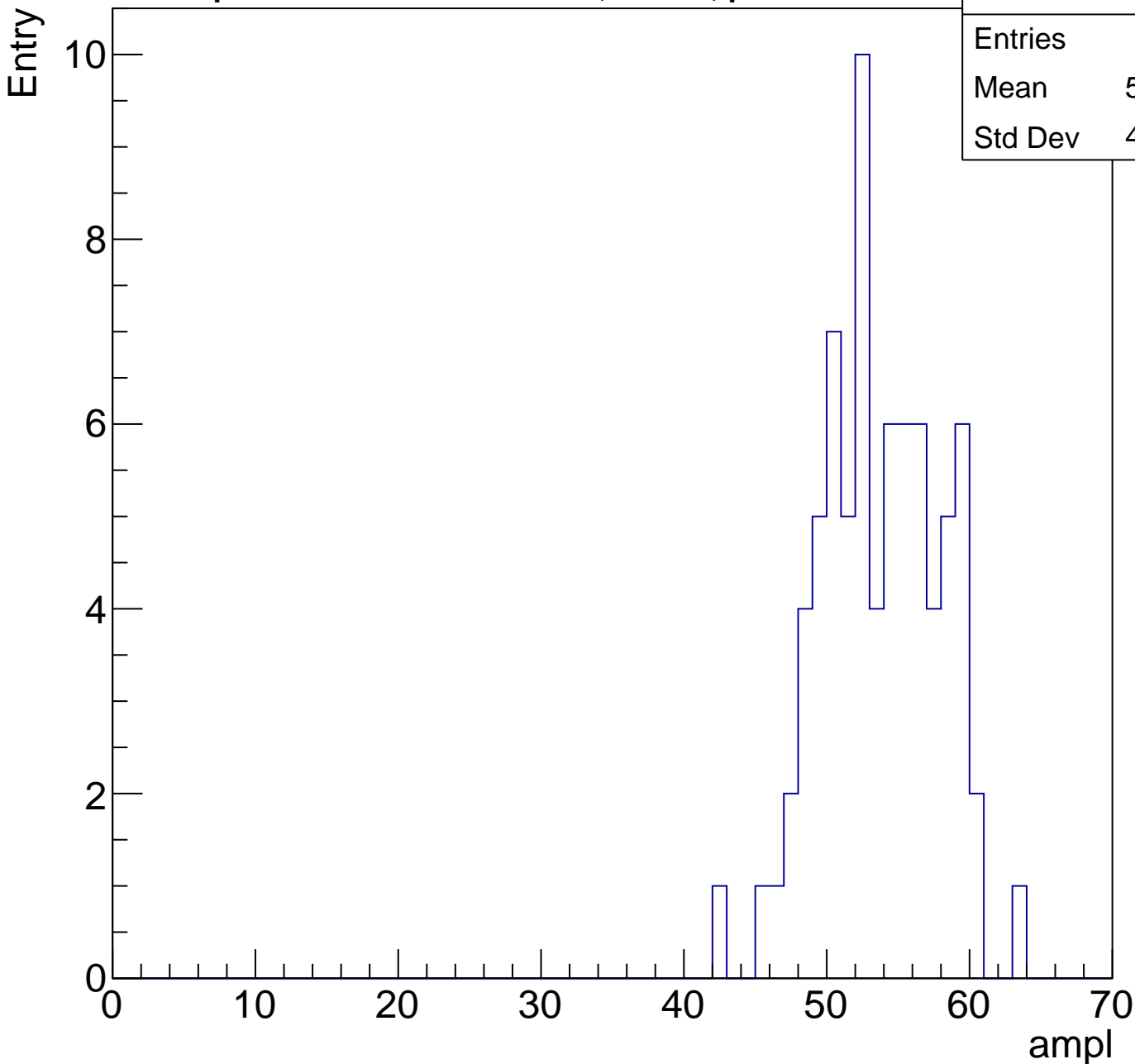
Entries	68
Mean	46.97
Std Dev	4.328



B0L102S, U5-ch86, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	53.24
Std Dev	4.075

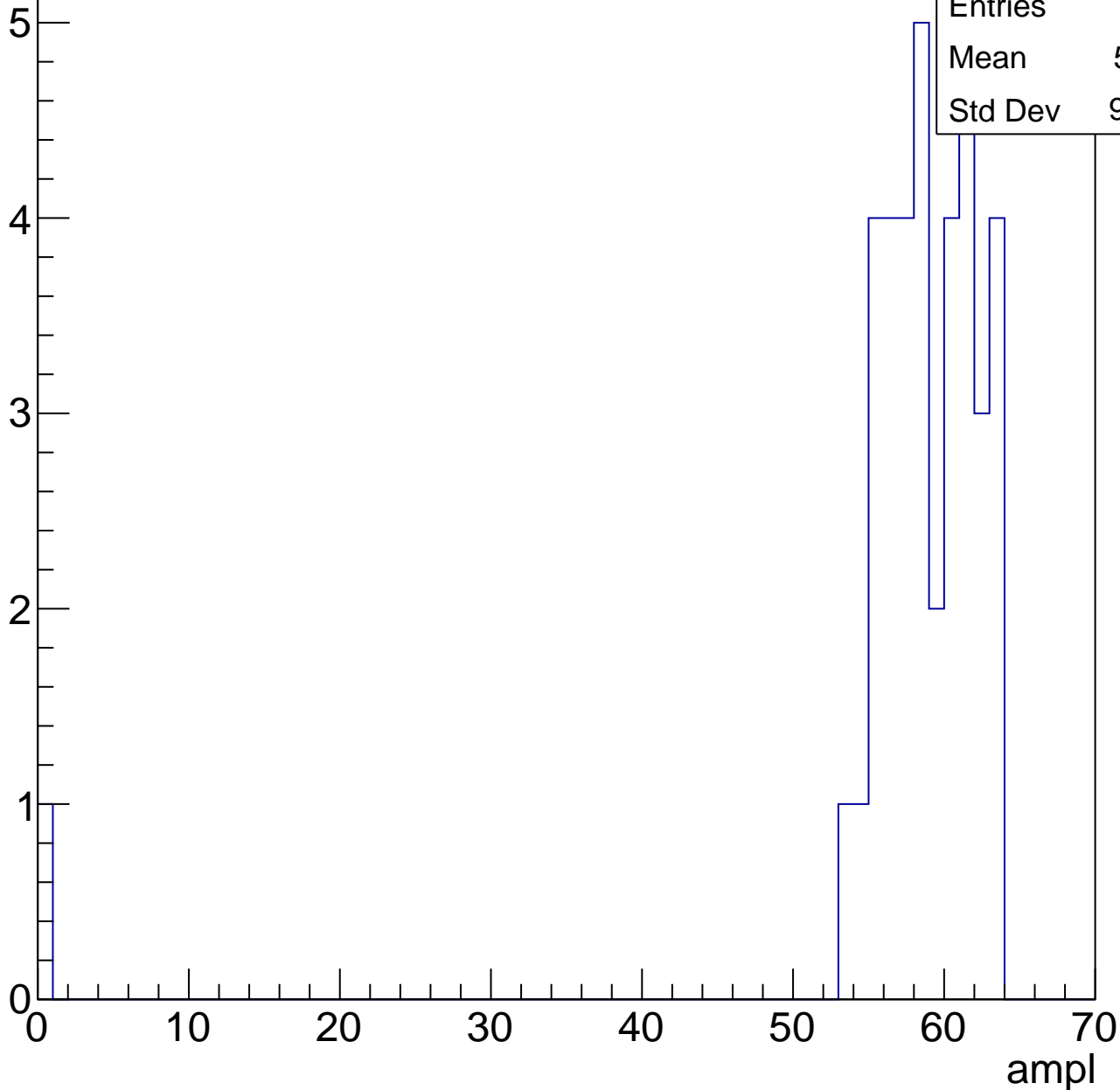


B0L102S, U5-ch86, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	38
Mean	57.11
Std Dev	9.789

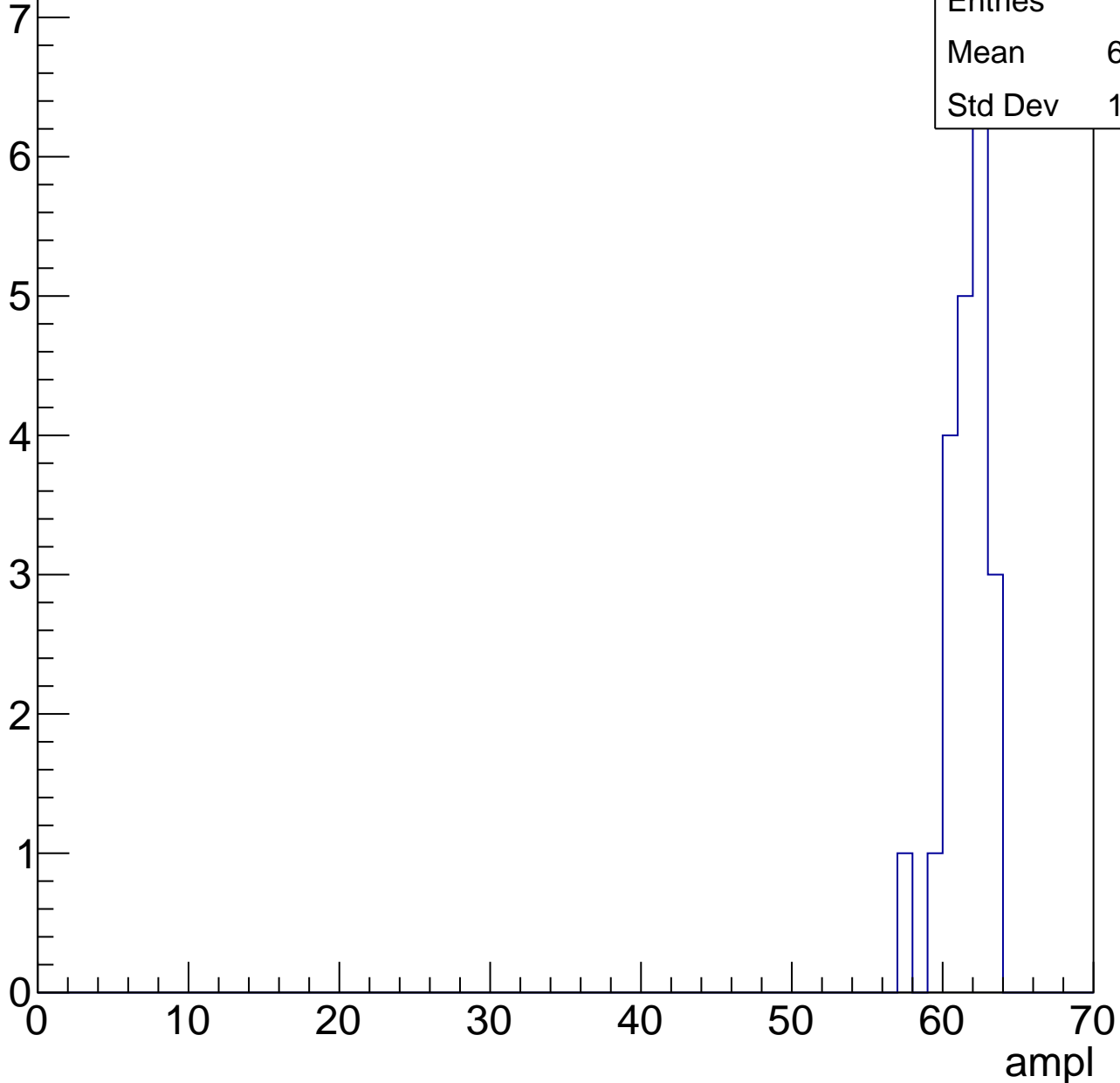


B0L102S, U5-ch86, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	21
Mean	61.14
Std Dev	1.424



B0L102S, U5-ch86, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	42
Std Dev	29.7

ampl

B0L102S, U5-ch87, adc0

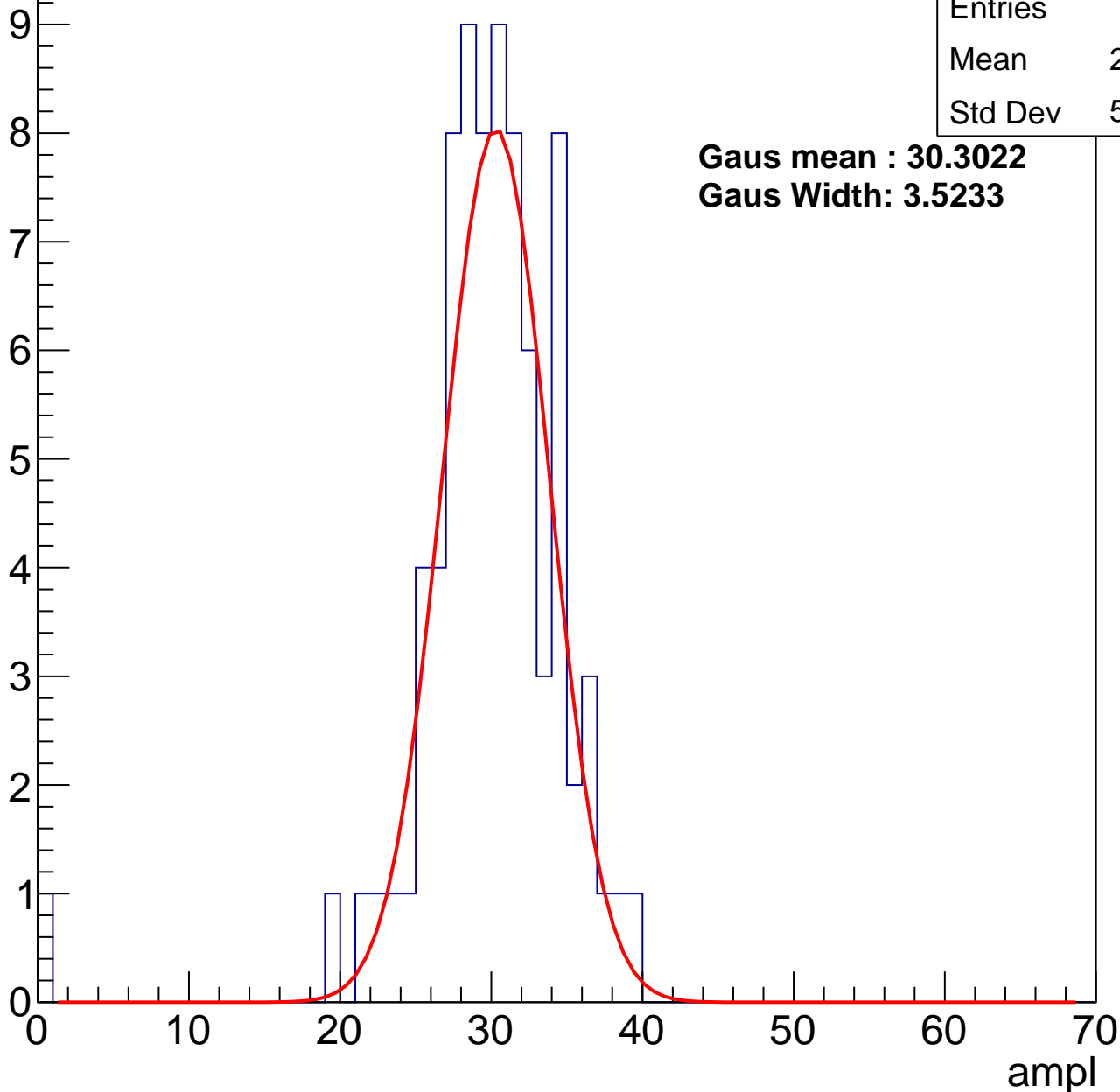
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	81
Mean	29.46
Std Dev	5.026

Gaus mean : 30.3022

Gaus Width: 3.5233



B0L102S, U5-ch87, adc1

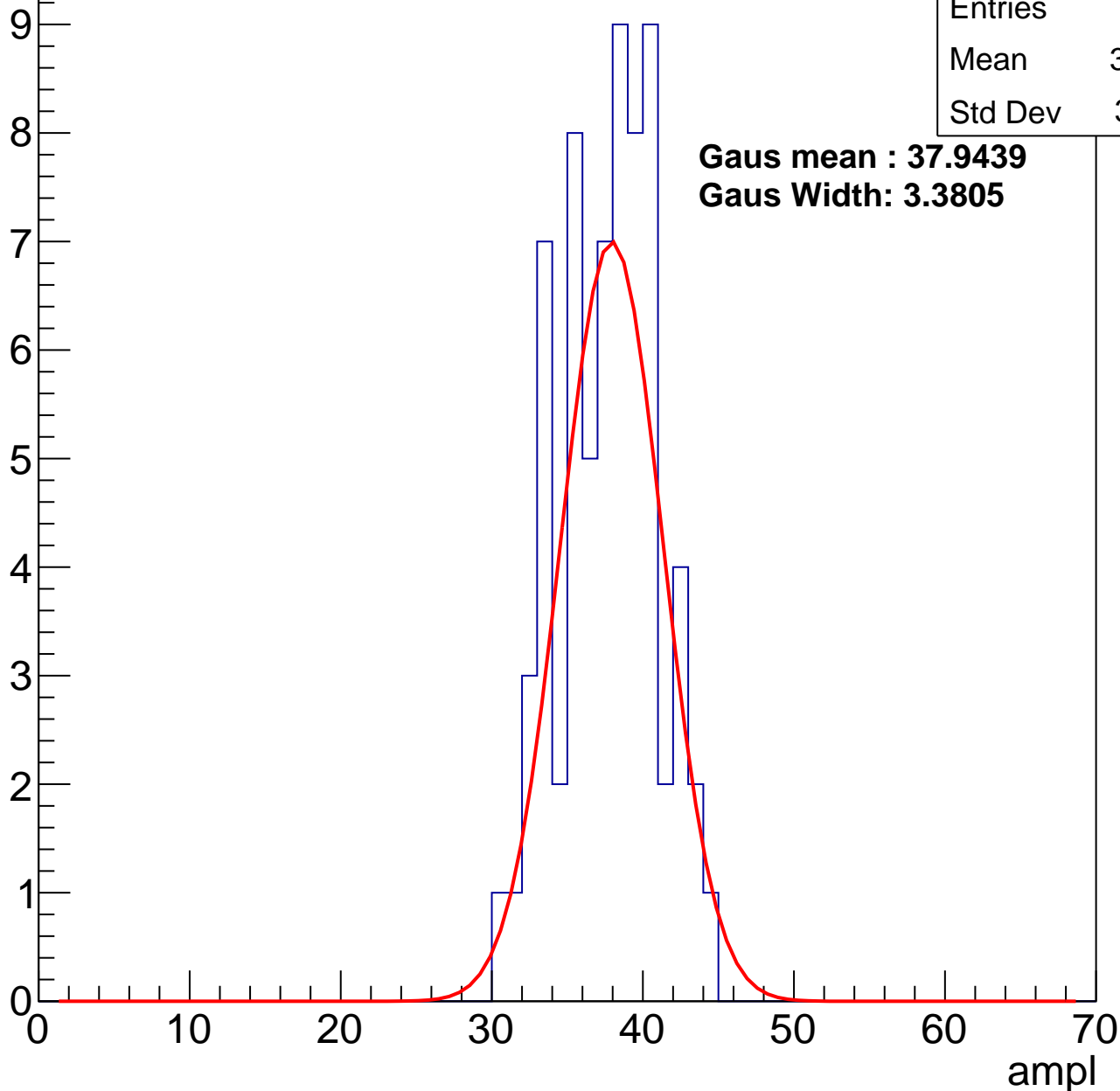
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	37.23
Std Dev	3.181

Gaus mean : 37.9439

Gaus Width: 3.3805



B0L102S, U5-ch87, adc2

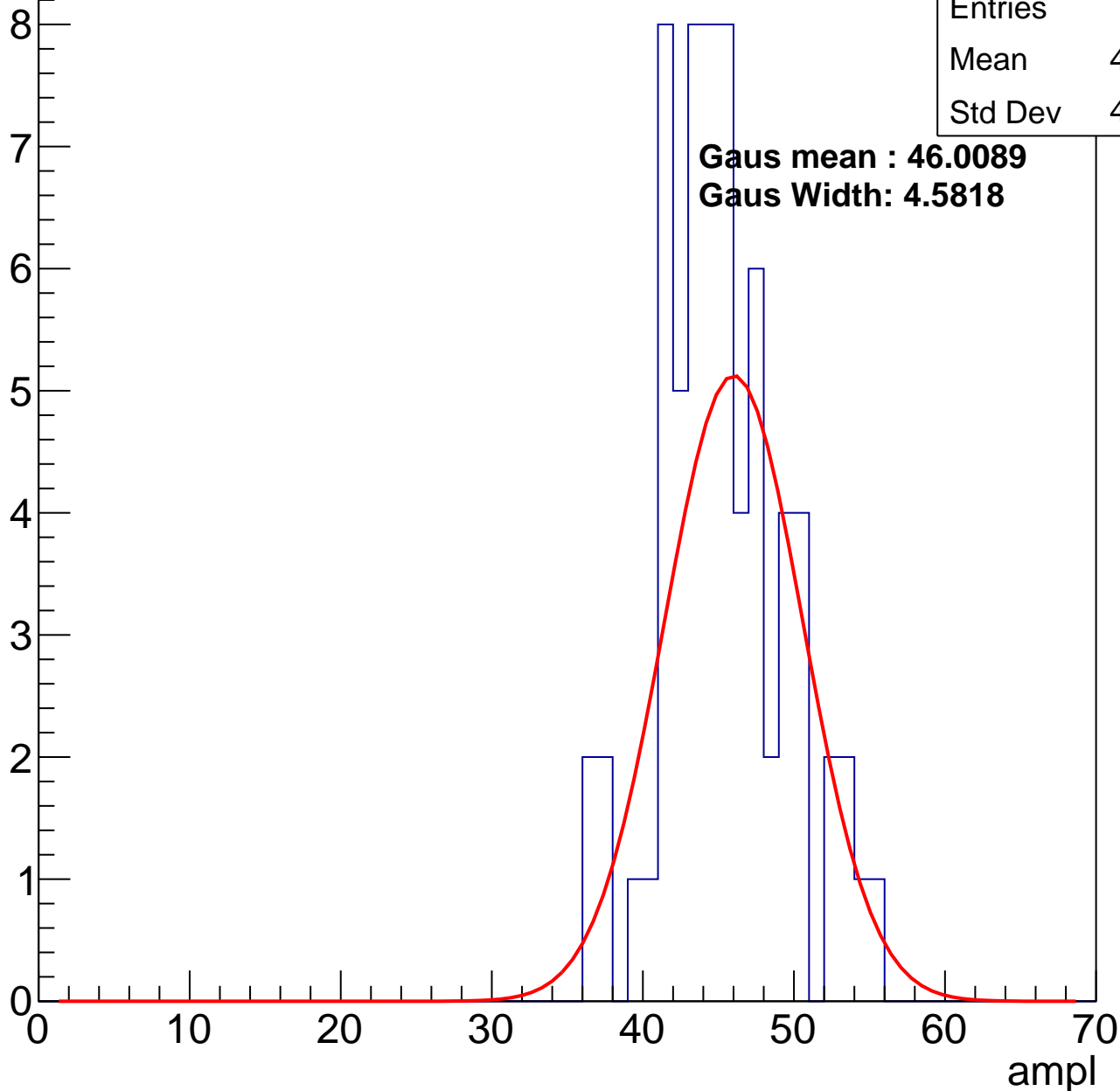
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	44.87
Std Dev	4.135

Gaus mean : 46.0089

Gaus Width: 4.5818

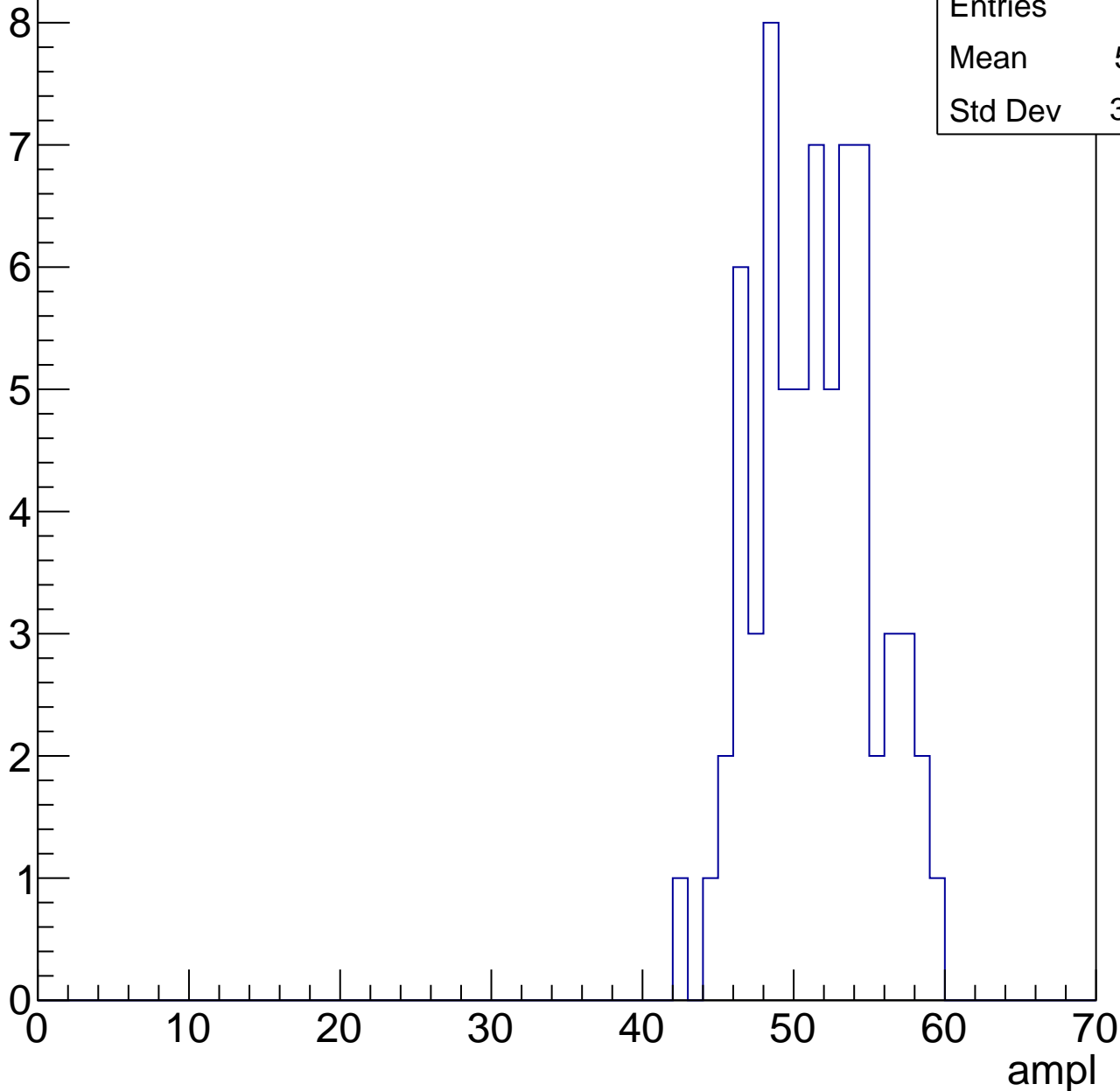


B0L102S, U5-ch87, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	50.91
Std Dev	3.776

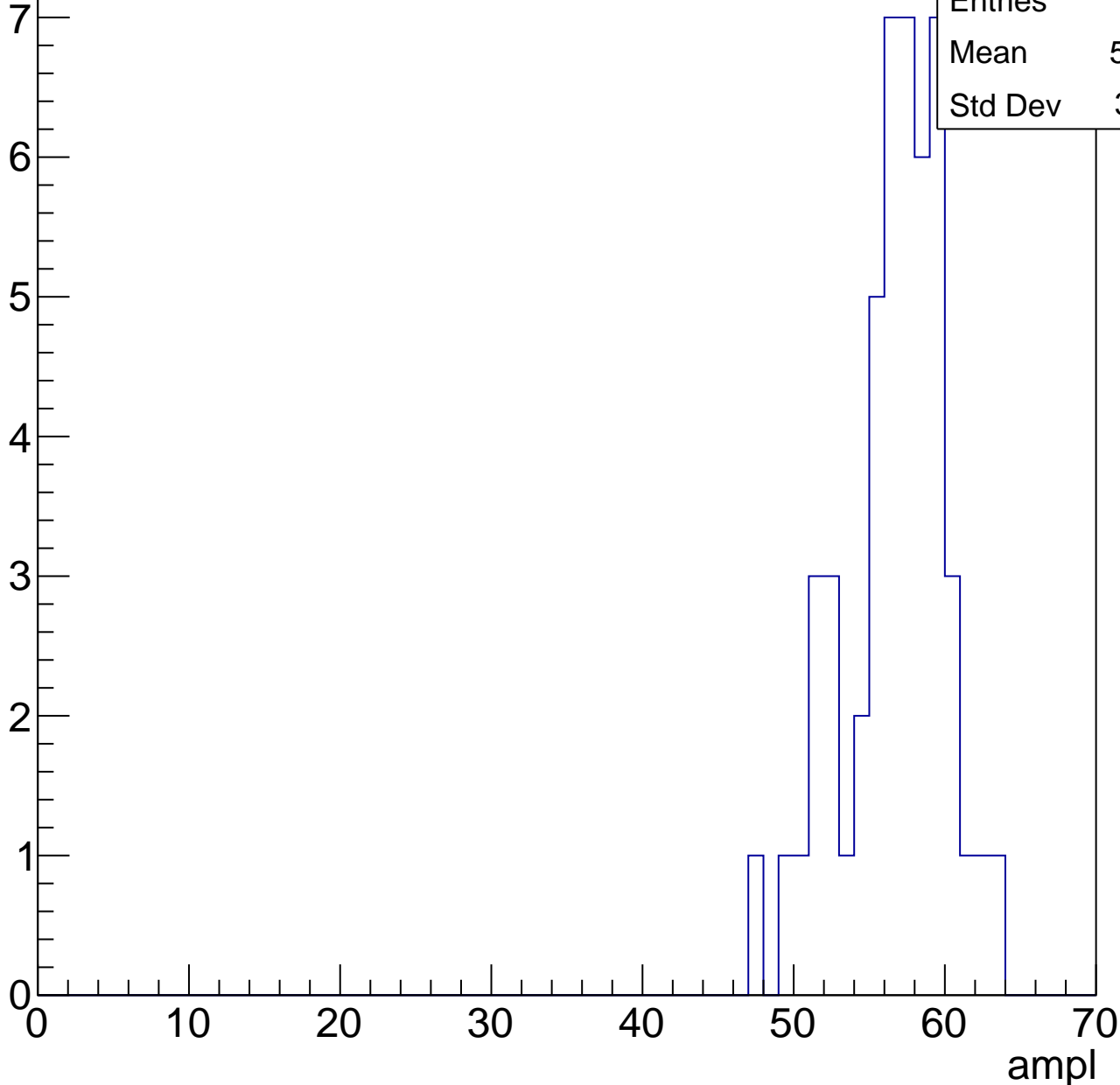


B0L102S, U5-ch87, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

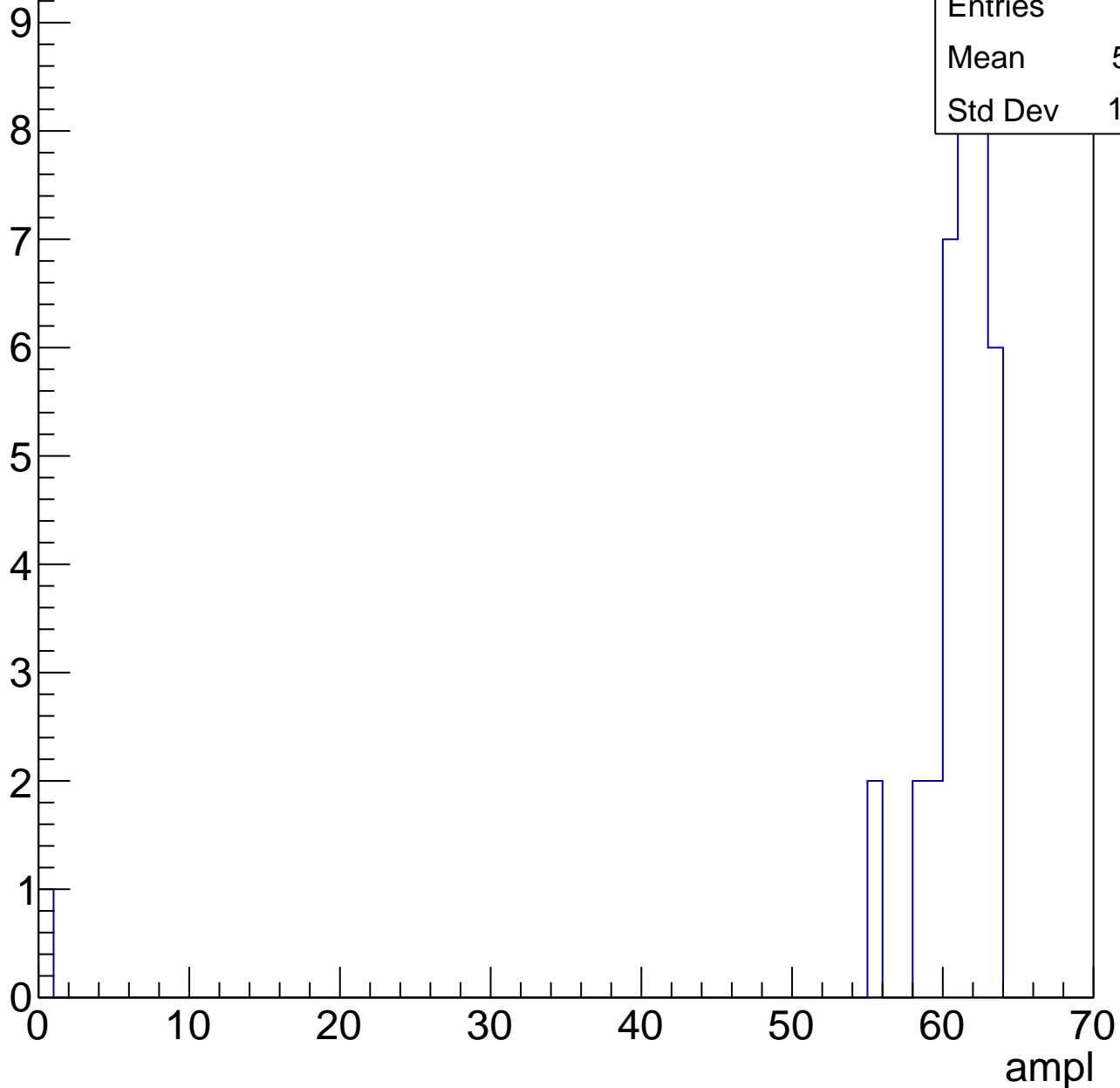
Entries	50
Mean	56.18
Std Dev	3.351



B0L102S, U5-ch87, adc5

calib_packv5_042523_0143.root, FC#12, port B1

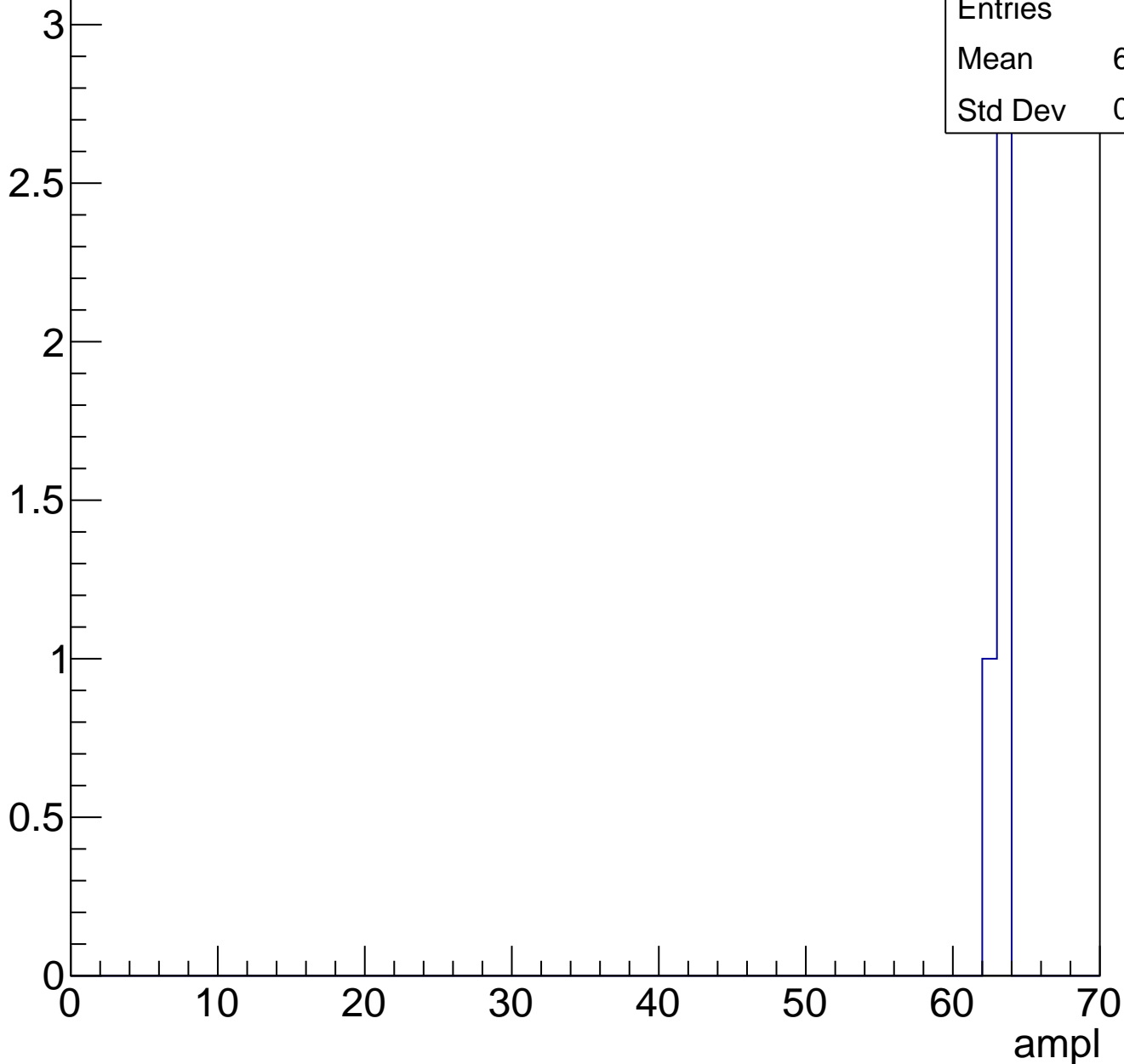
Entry



B0L102S, U5-ch87, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch87, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch88, adc0

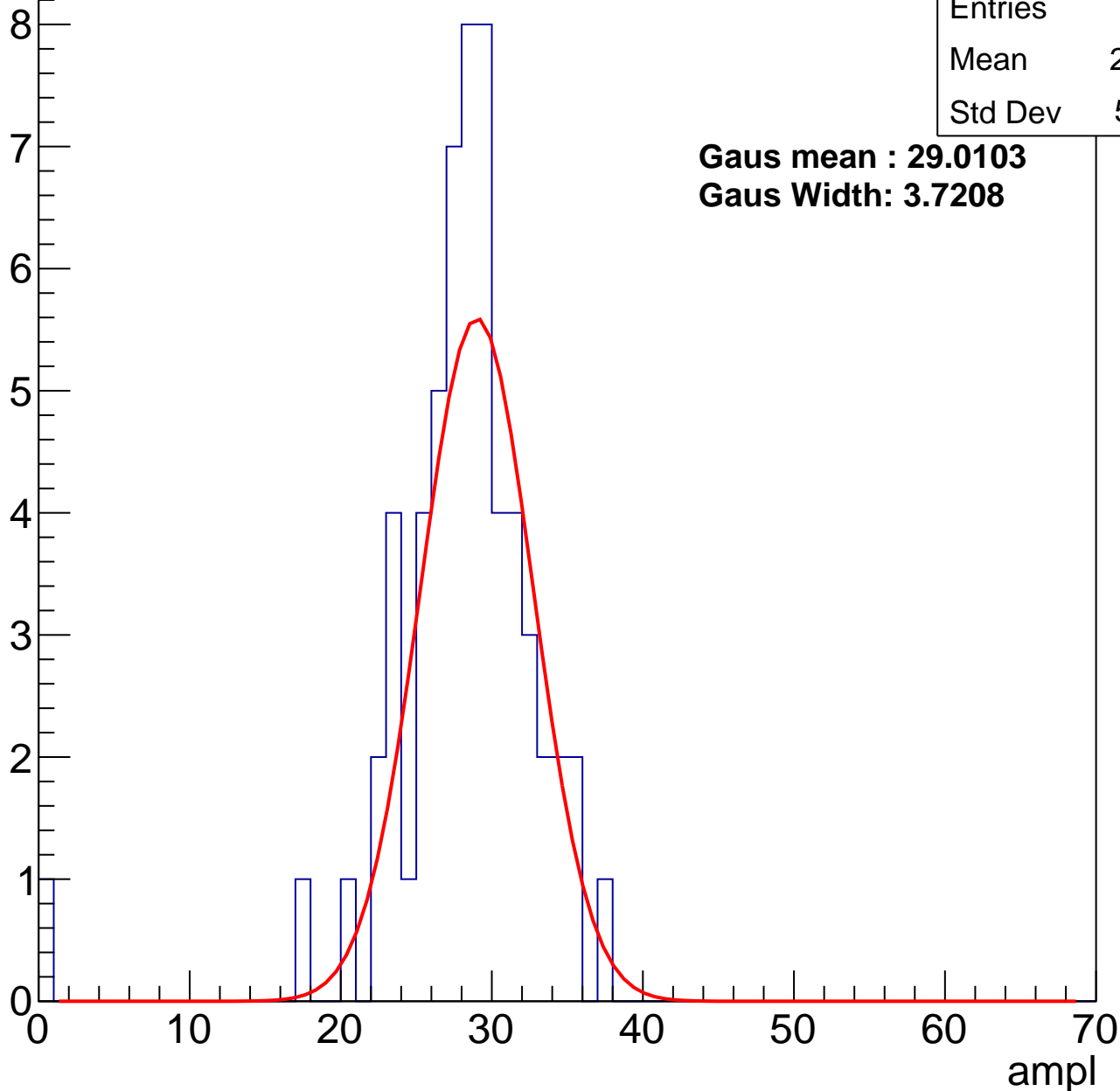
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	27.55
Std Dev	5.191

Gaus mean : 29.0103

Gaus Width: 3.7208



B0L102S, U5-ch88, adc1

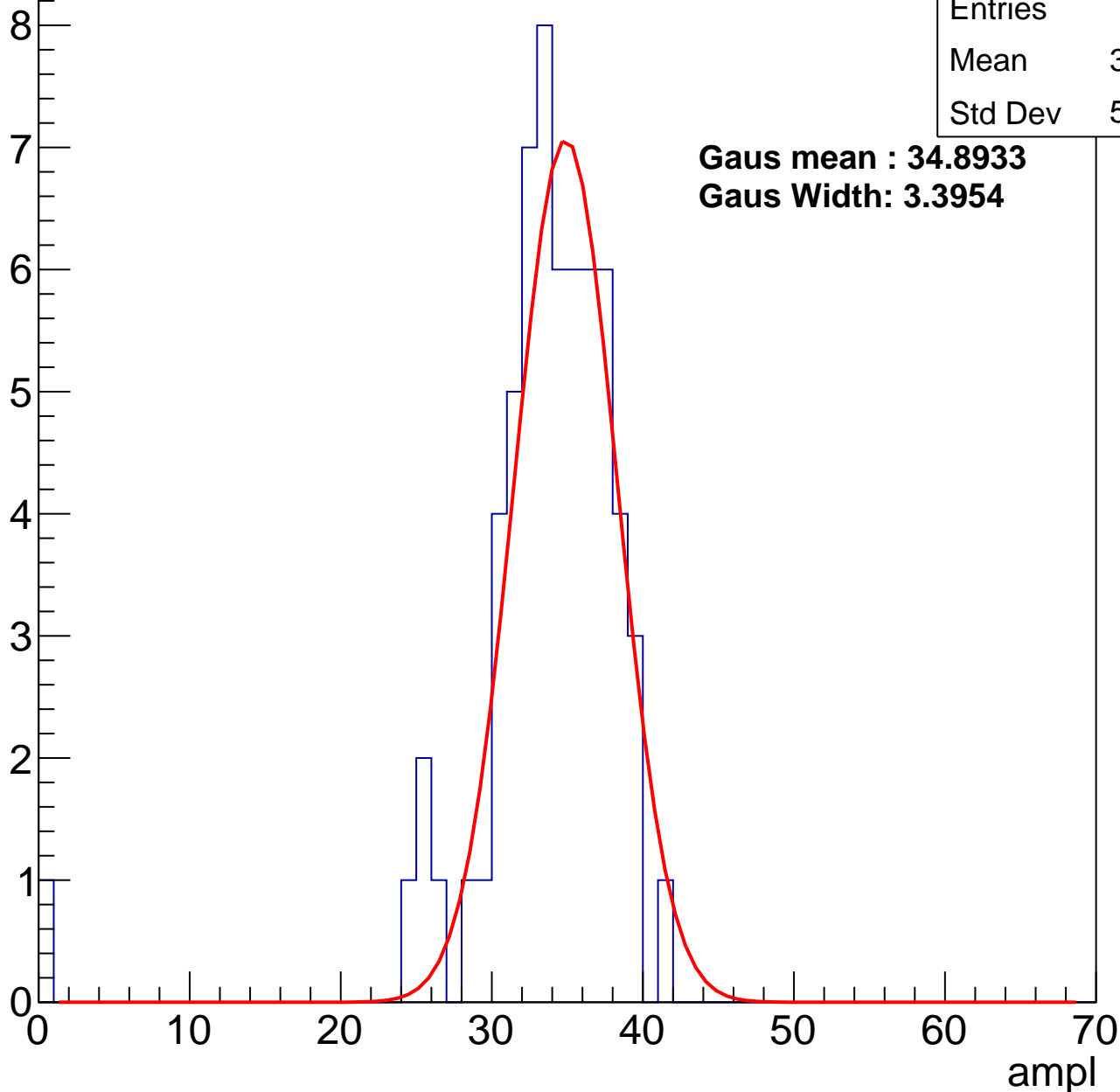
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	33.05
Std Dev	5.496

Gaus mean : 34.8933

Gaus Width: 3.3954



B0L102S, U5-ch88, adc2

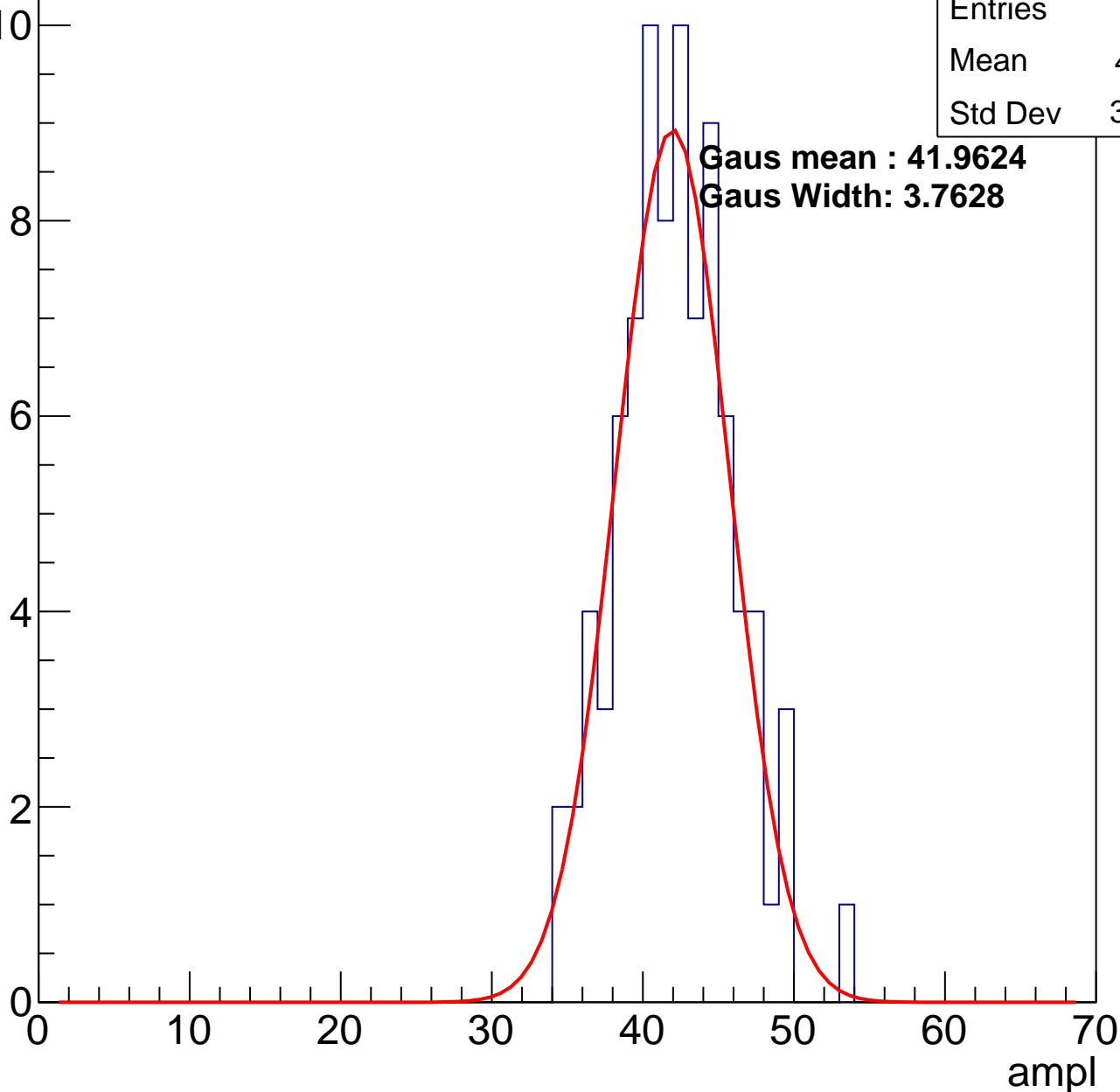
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	87
Mean	41.71
Std Dev	3.729

Gaus mean : 41.9624

Gaus Width: 3.7628

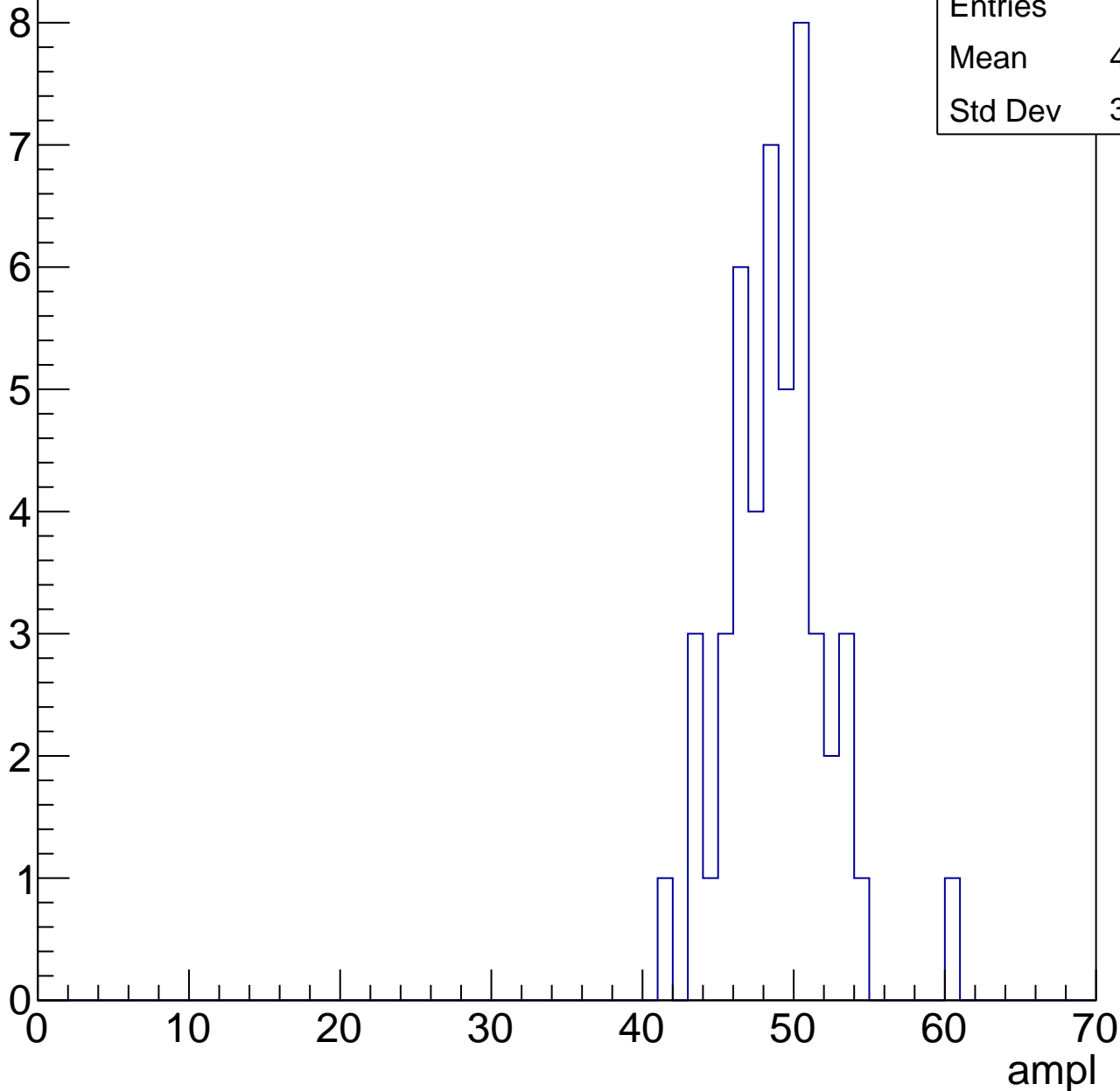


B0L102S, U5-ch88, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	48.42
Std Dev	3.347

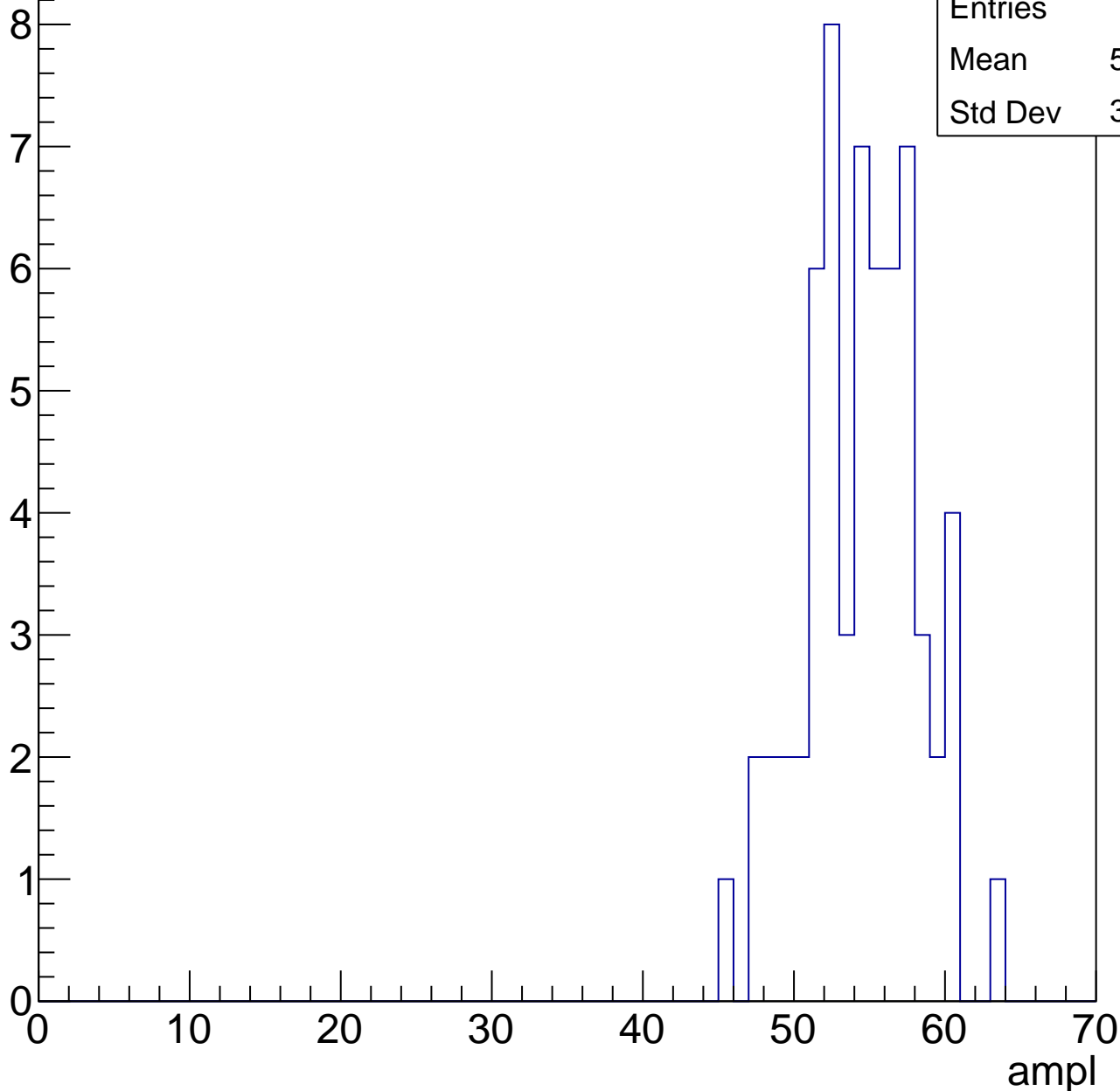


B0L102S, U5-ch88, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	54.06
Std Dev	3.676

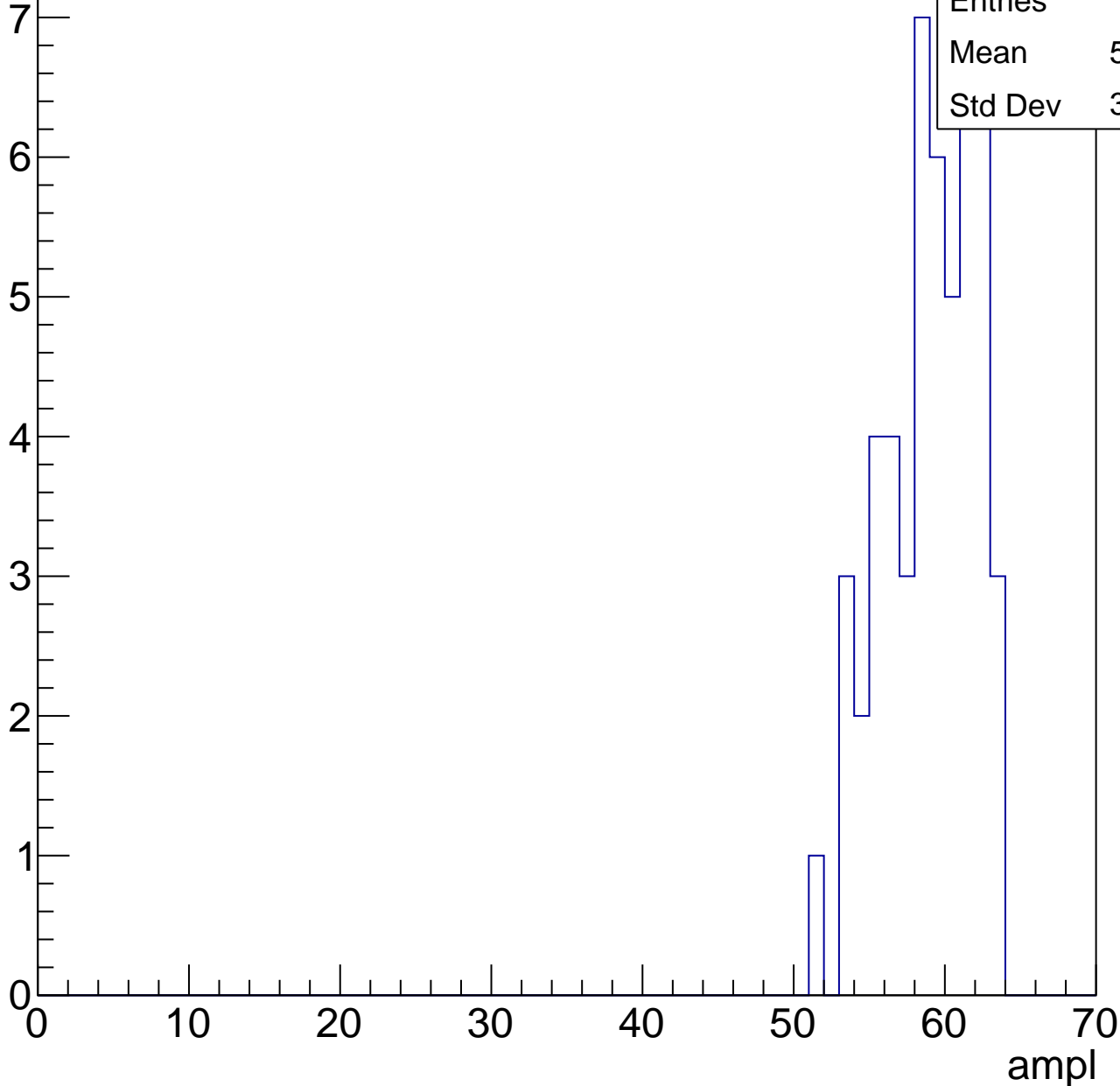


B0L102S, U5-ch88, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	58.52
Std Dev	3.016

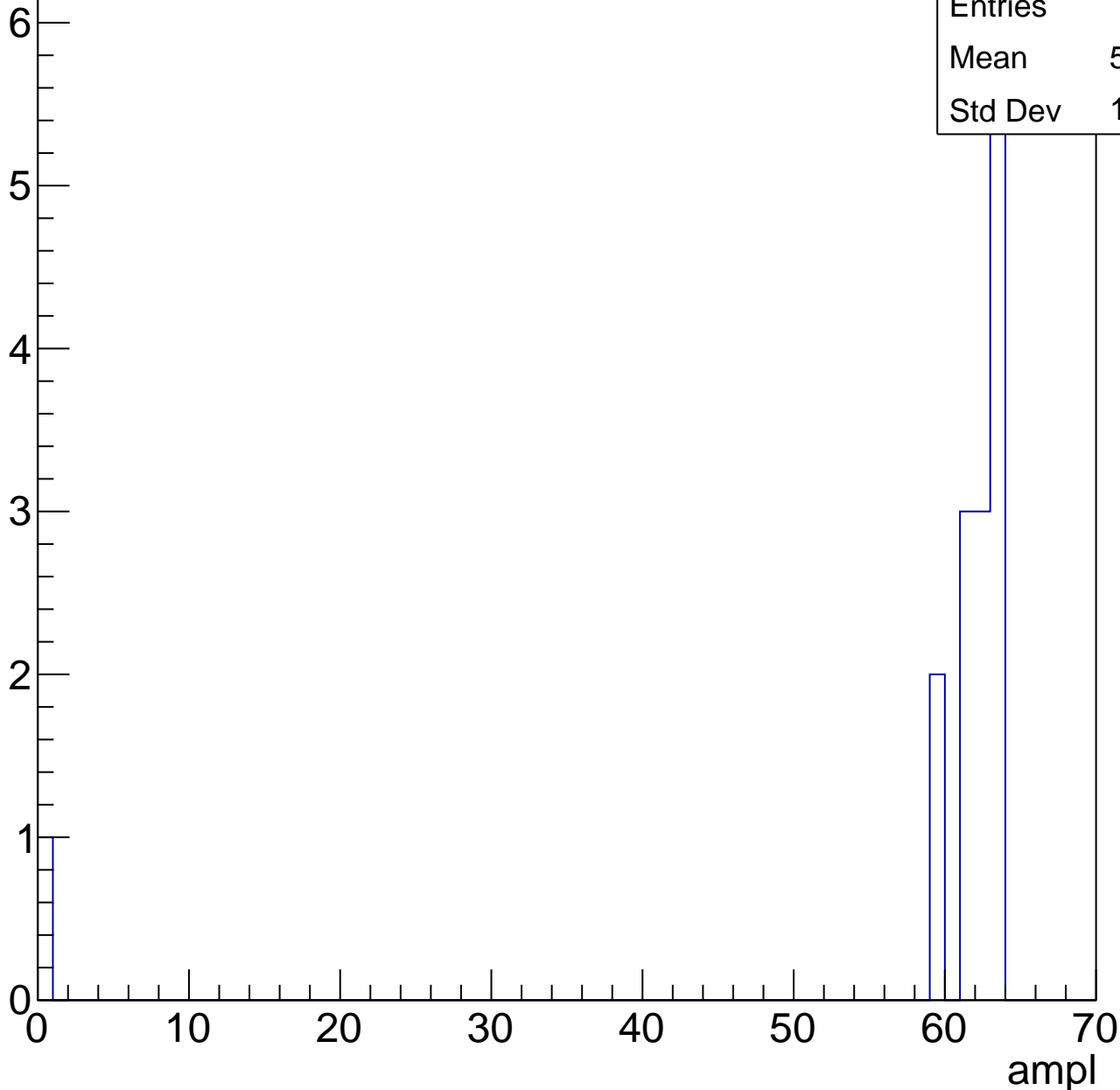


B0L102S, U5-ch88, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.67
Std Dev	15.47



B0L102S, U5-ch88, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch89, adc0

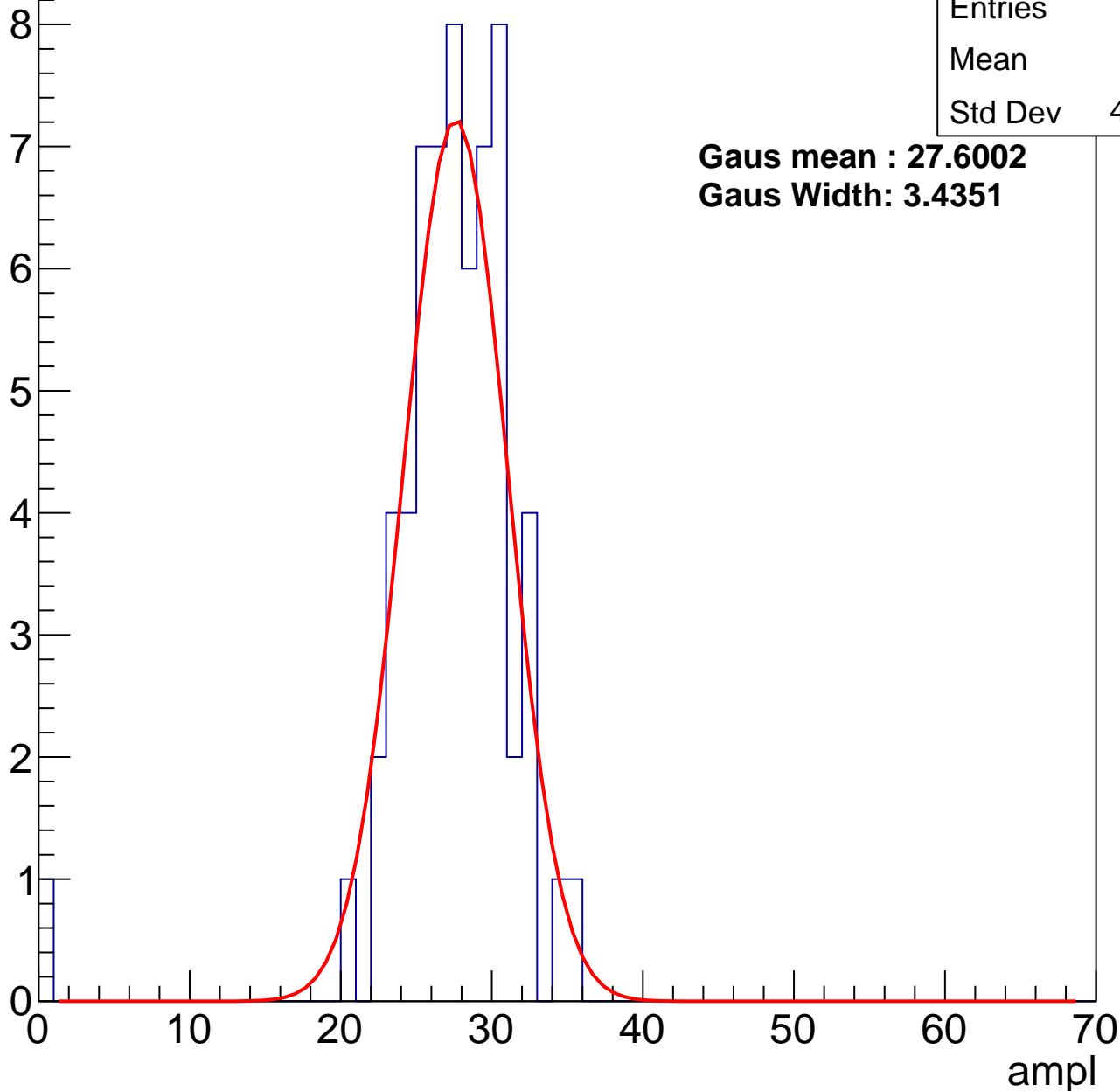
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	26.9
Std Dev	4.566

Gaus mean : 27.6002

Gaus Width: 3.4351



B0L102S, U5-ch89, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	79
Mean	33.38
Std Dev	4.11

Gaus mean : 34.1386

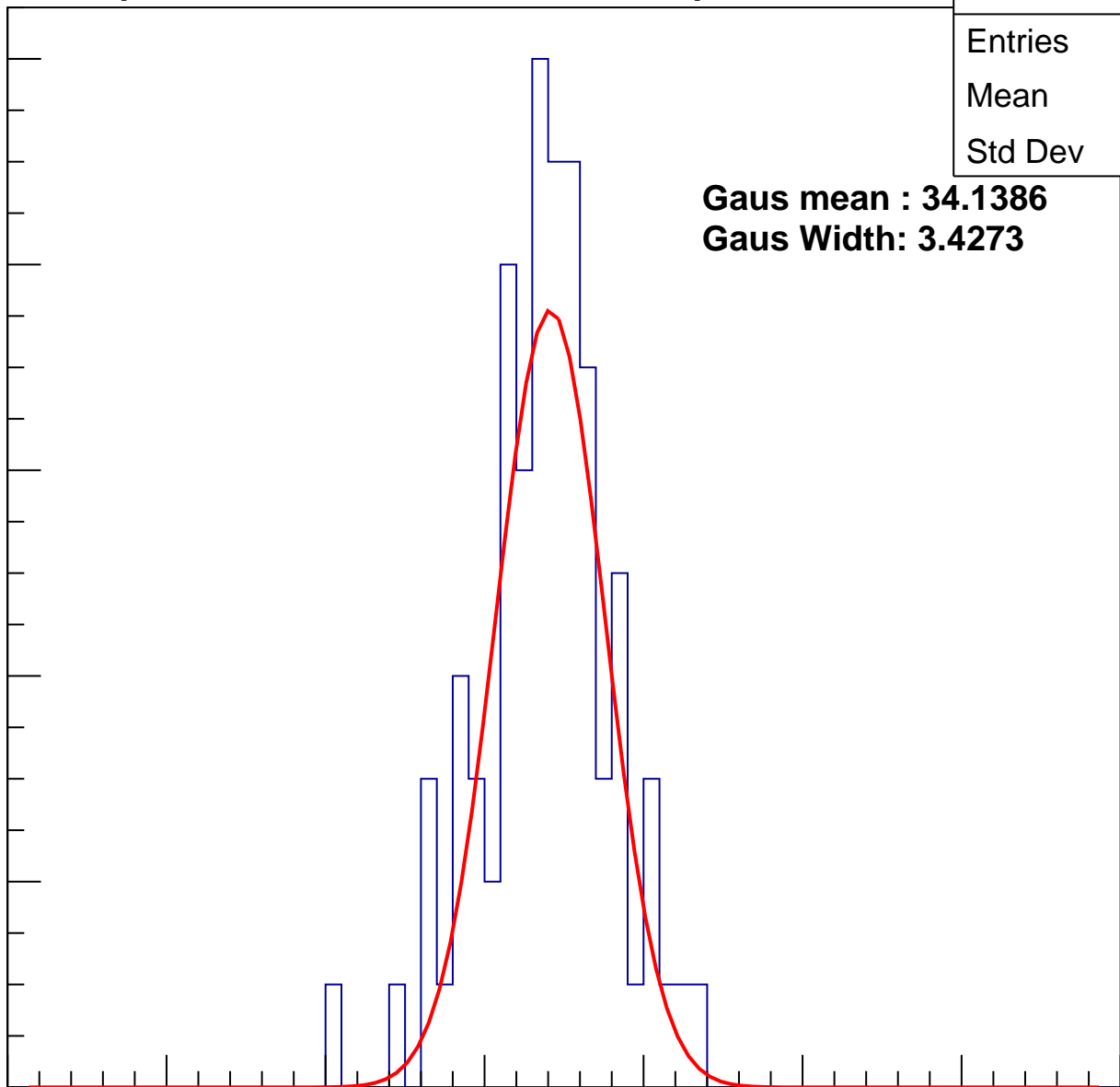
Gaus Width: 3.4273

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch89, adc2

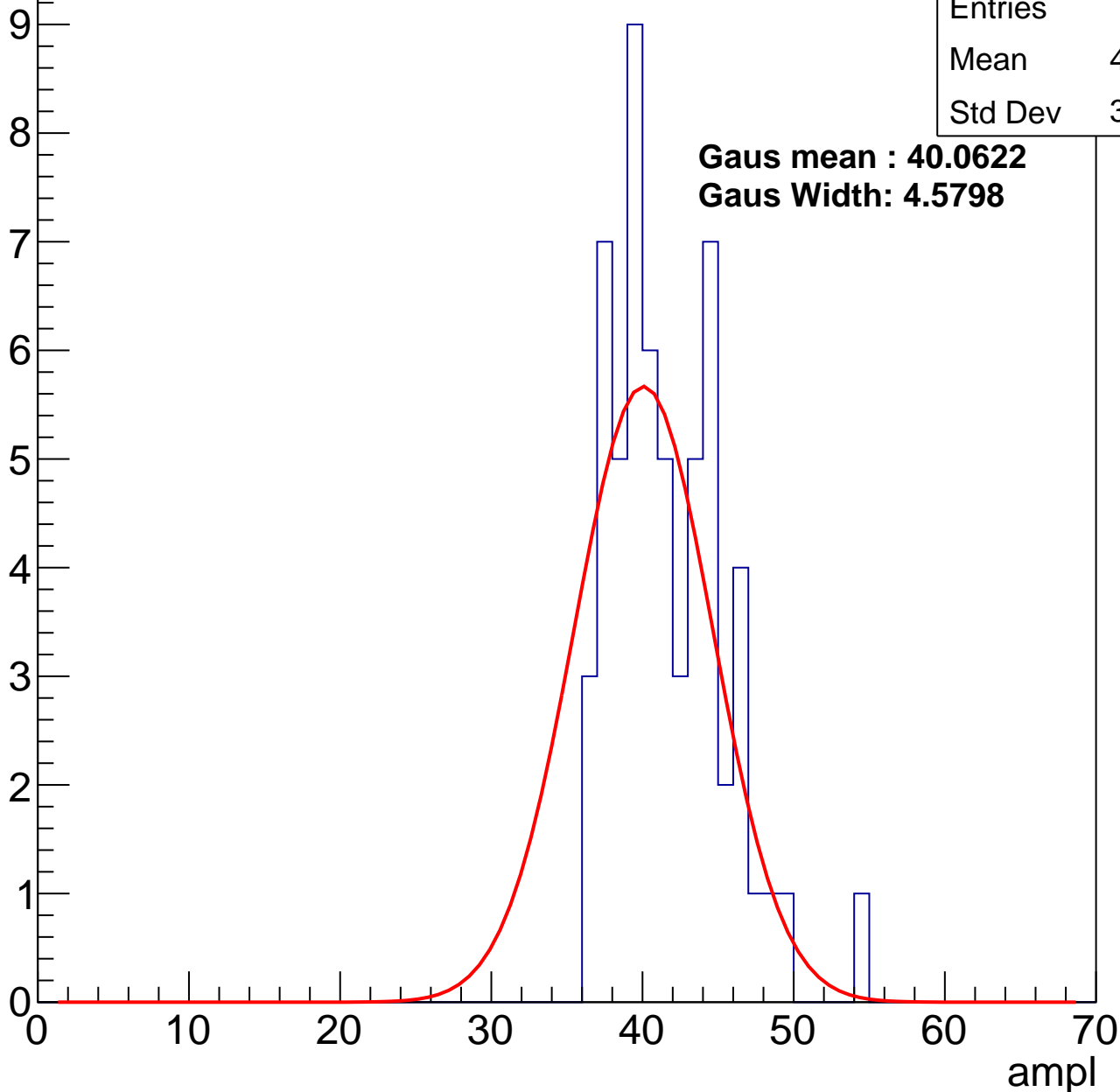
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.23
Std Dev	3.667

Gaus mean : 40.0622

Gaus Width: 4.5798

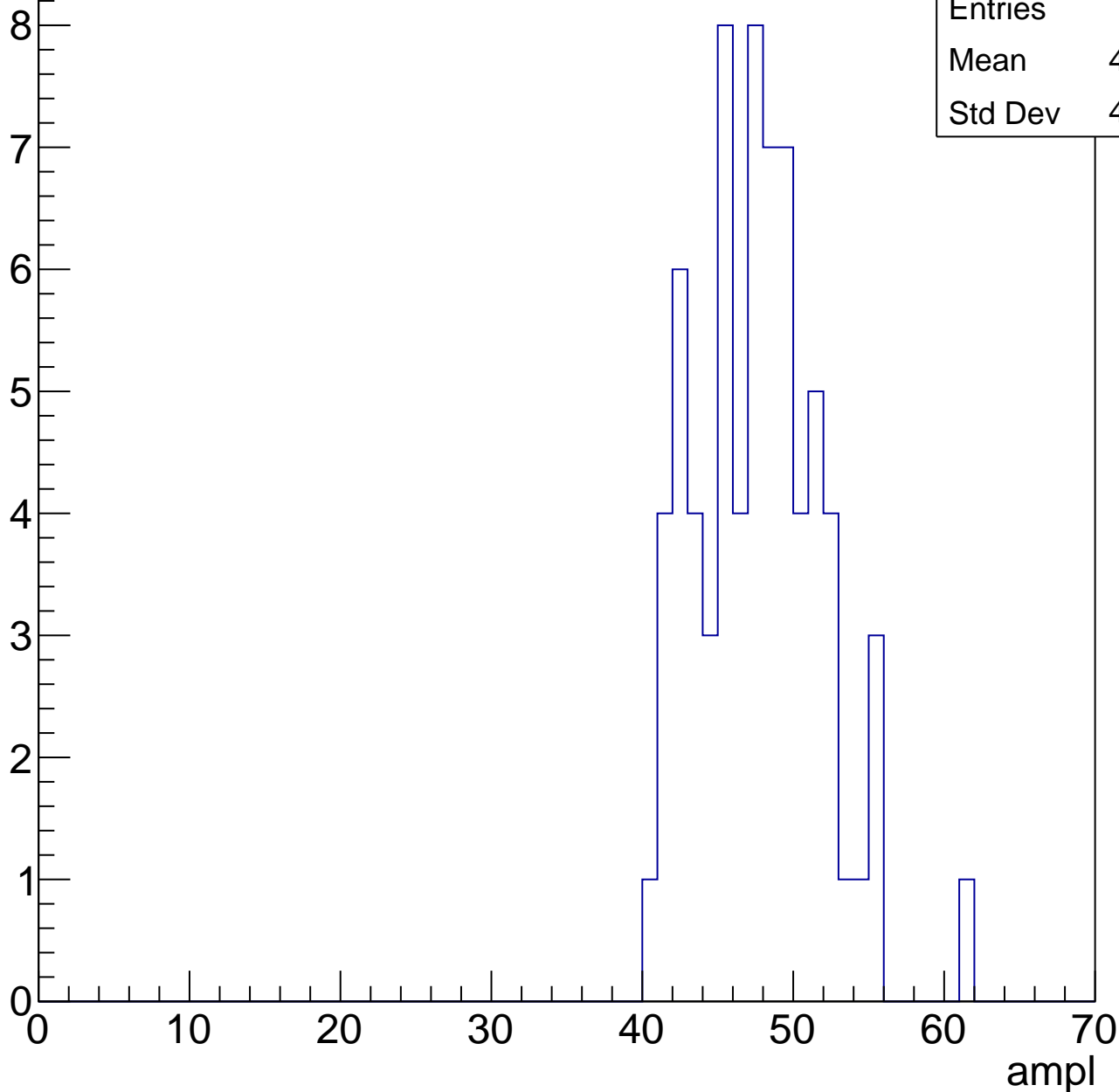


B0L102S, U5-ch89, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	47.25
Std Dev	4.093

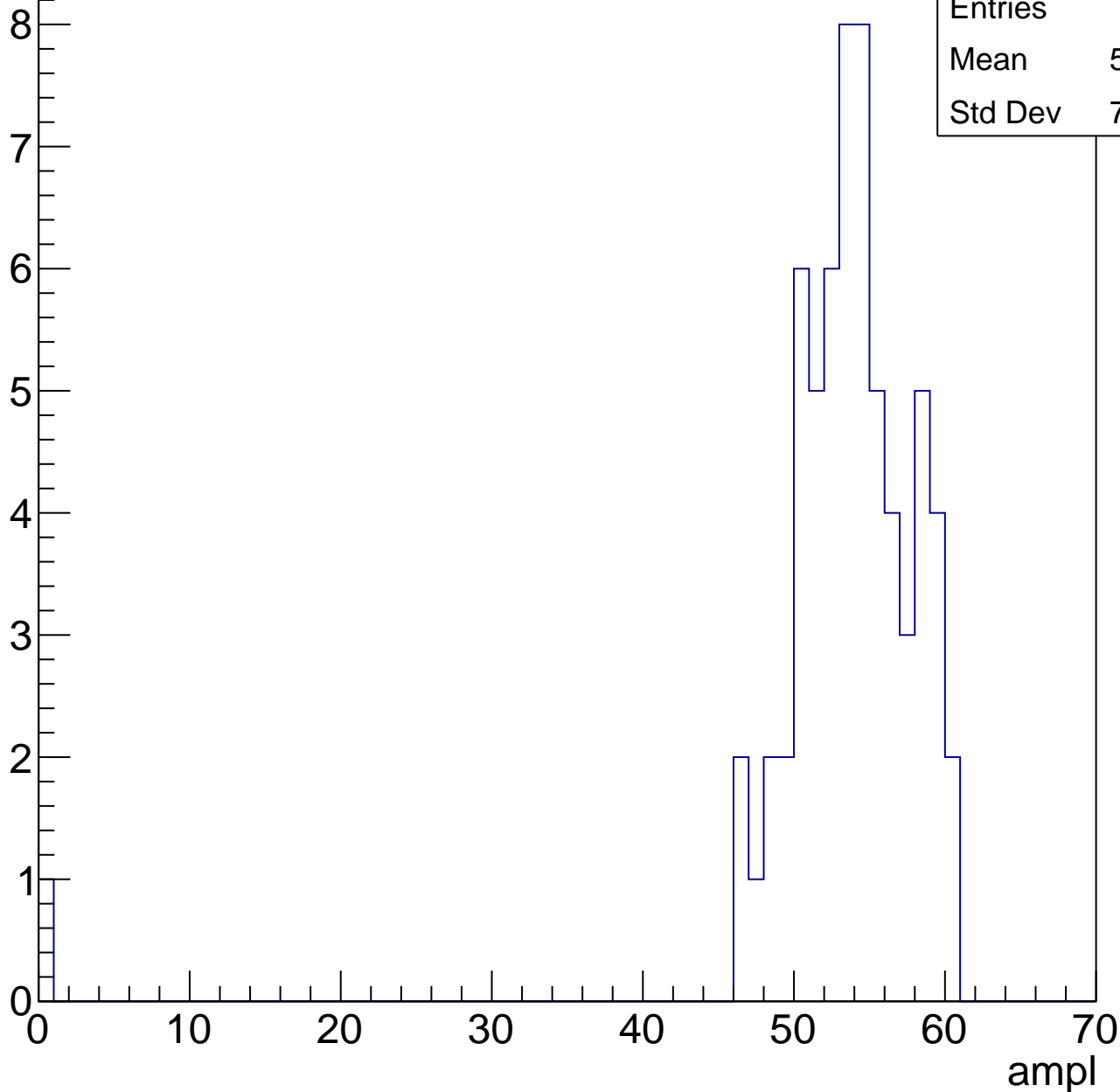


B0L102S, U5-ch89, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	52.69
Std Dev	7.477

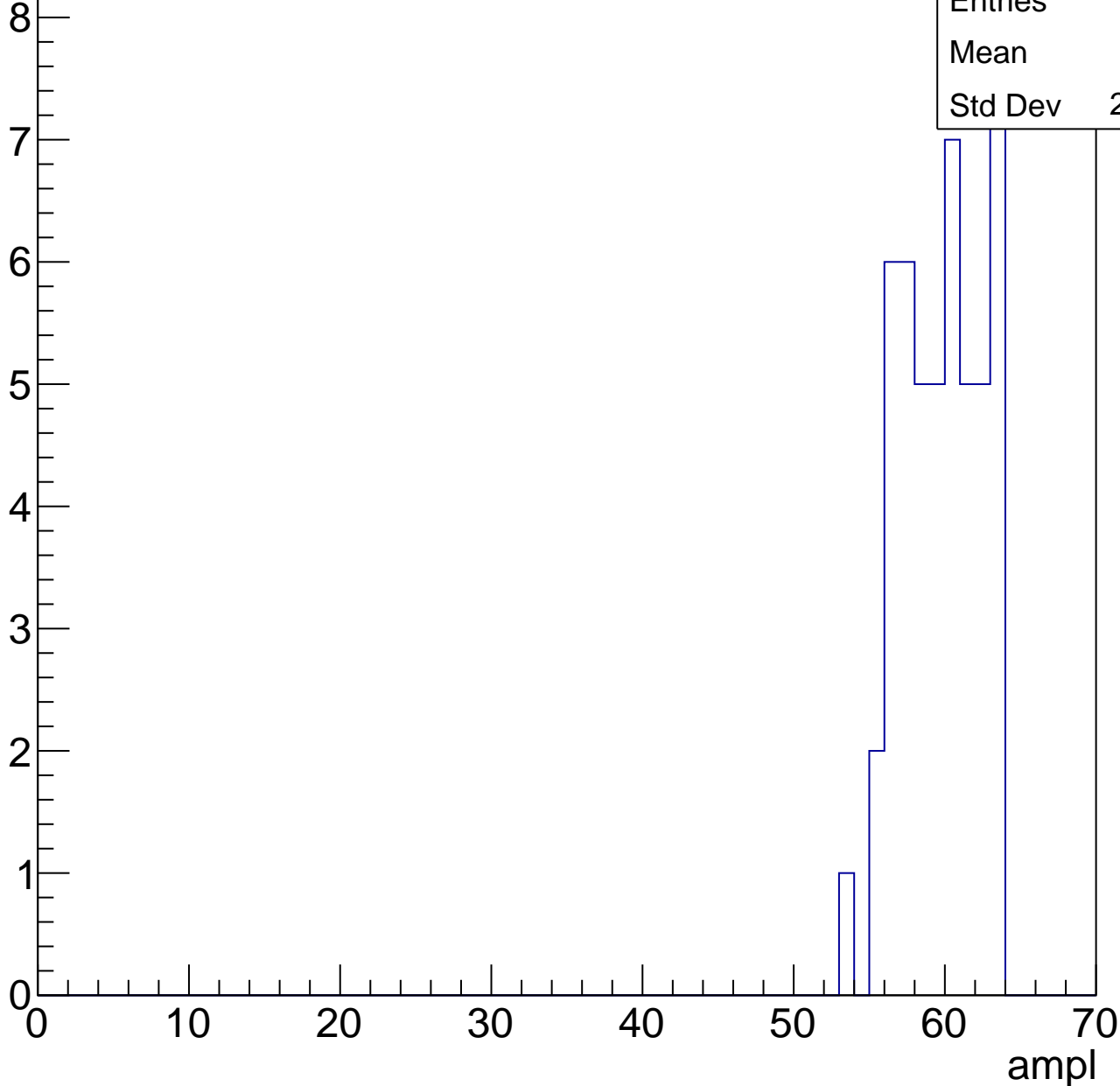


B0L102S, U5-ch89, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	59.3
Std Dev	2.632

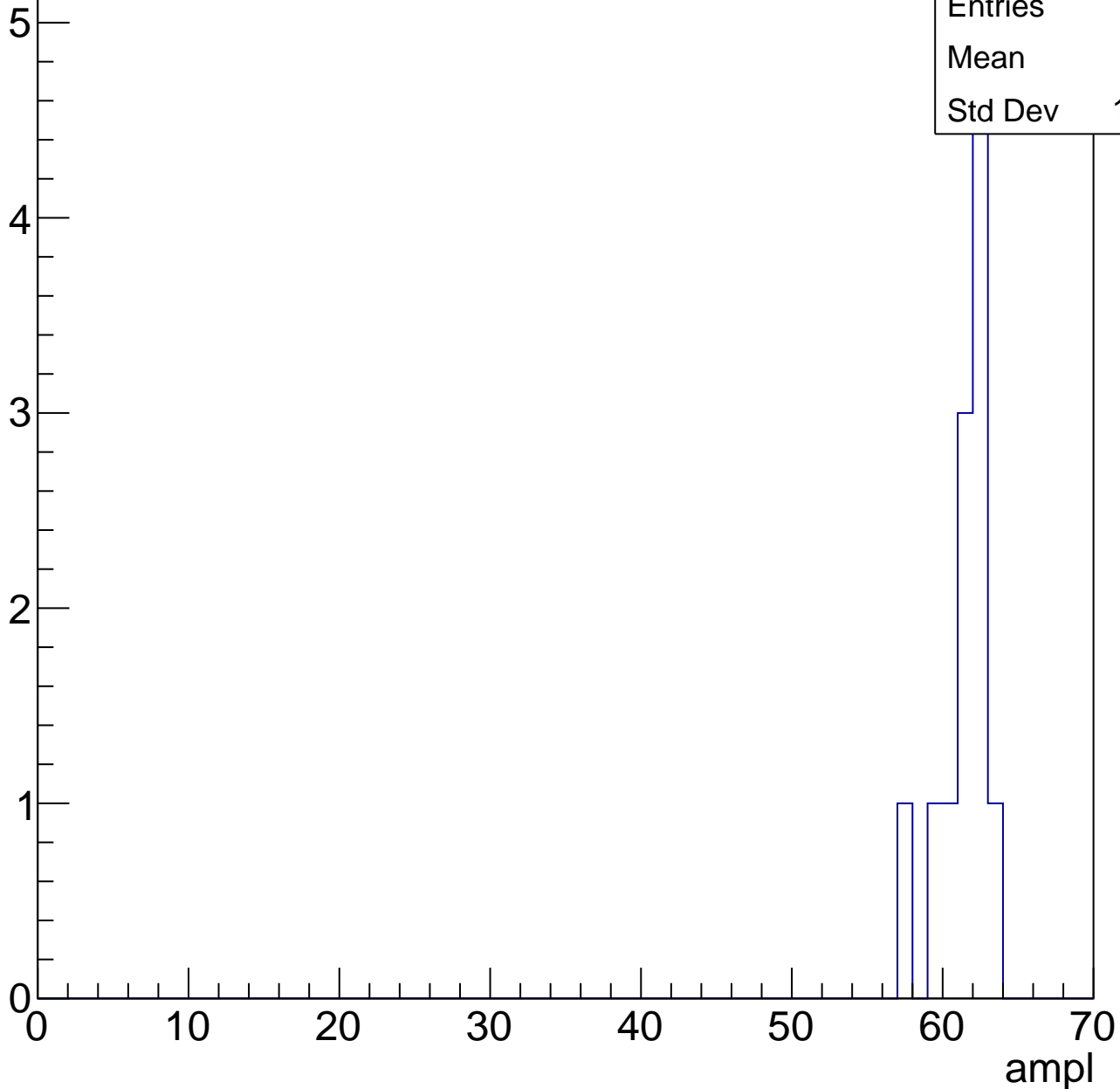


B0L102S, U5-ch89, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

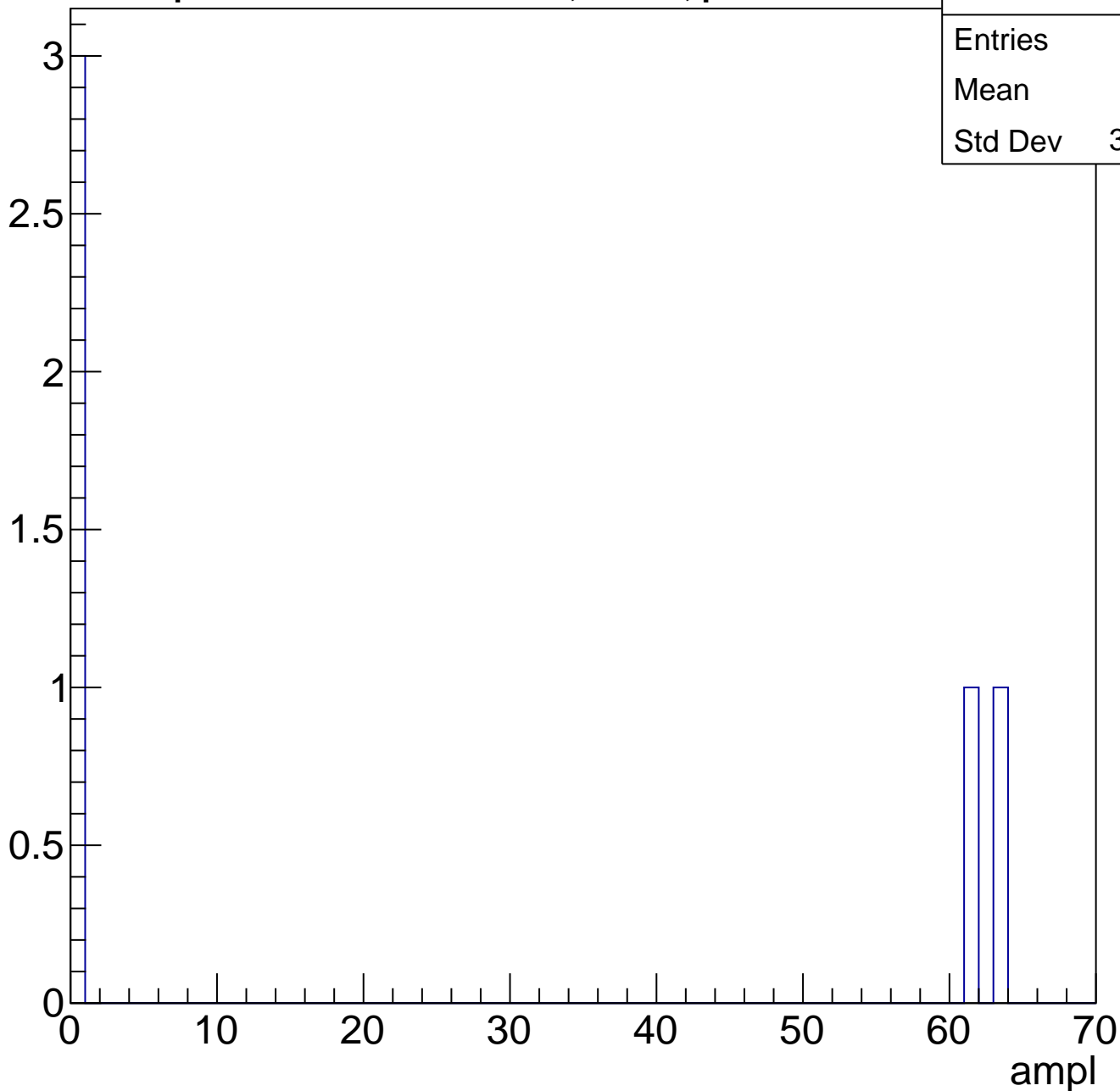
Entries	12
Mean	61
Std Dev	1.581



B0L102S, U5-ch89, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch90, adc0

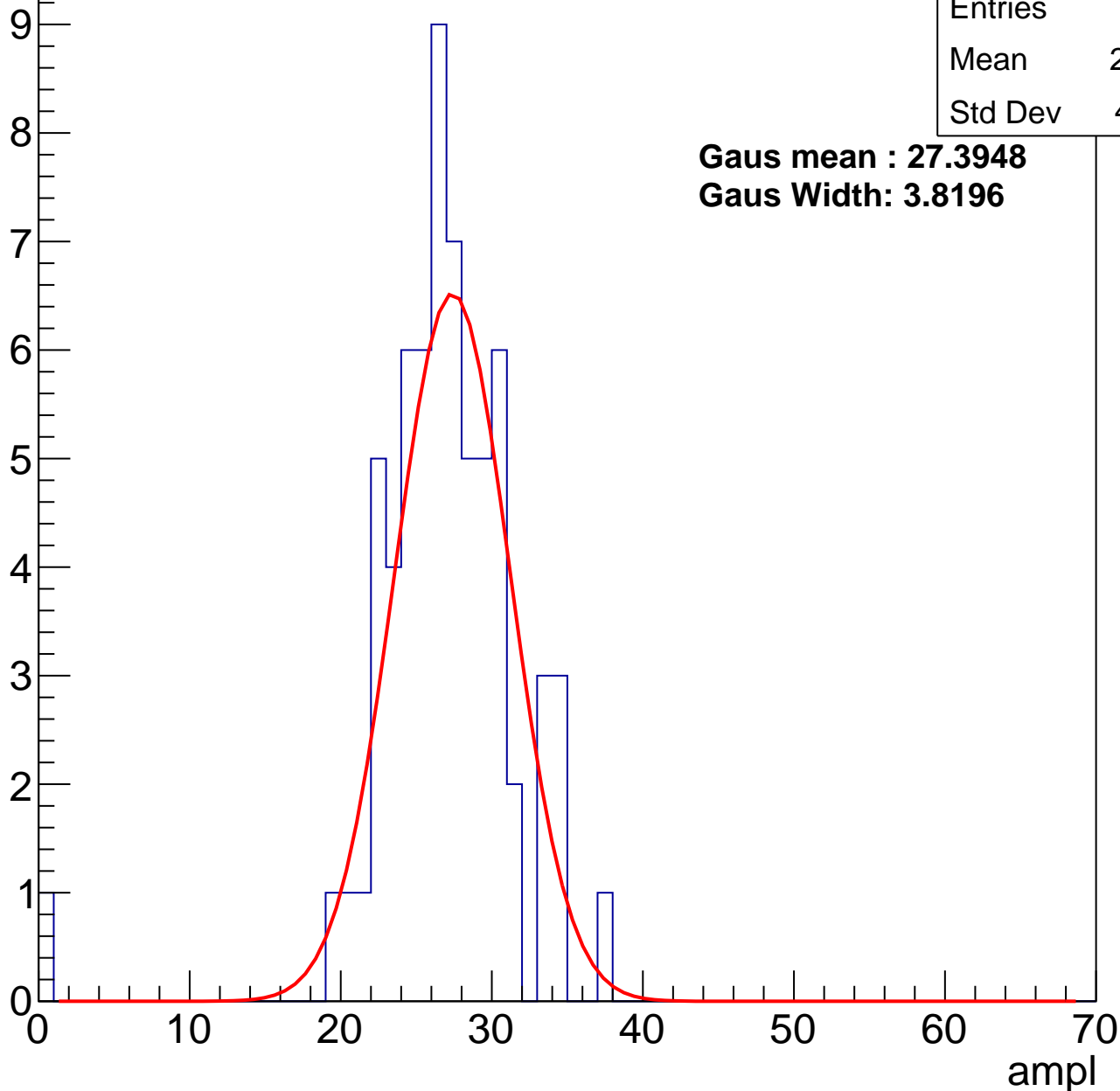
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	26.42
Std Dev	4.921

Gaus mean : 27.3948

Gaus Width: 3.8196



B0L102S, U5-ch90, adc1

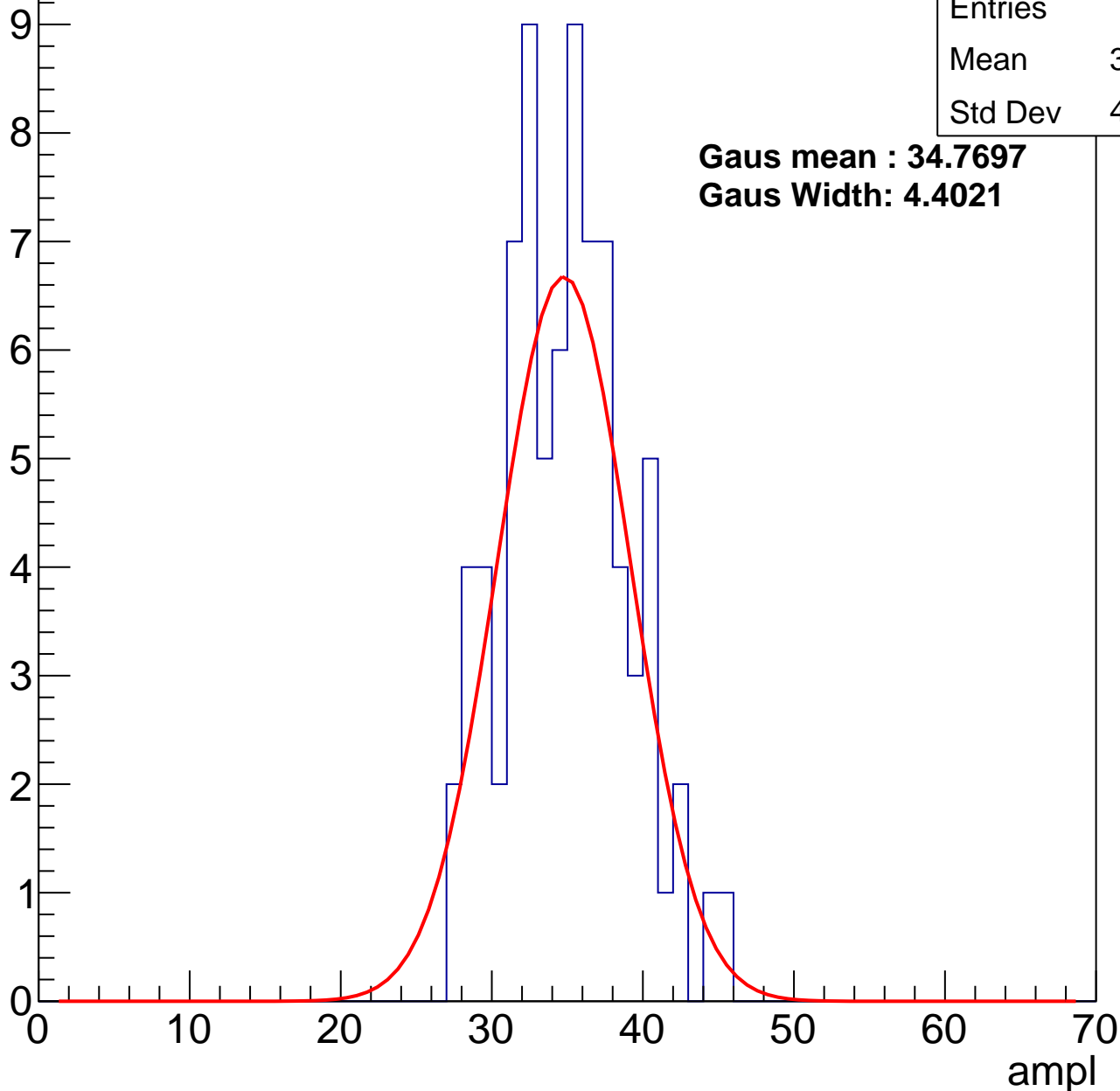
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	34.49
Std Dev	4.019

Gaus mean : 34.7697

Gaus Width: 4.4021



B0L102S, U5-ch90, adc2

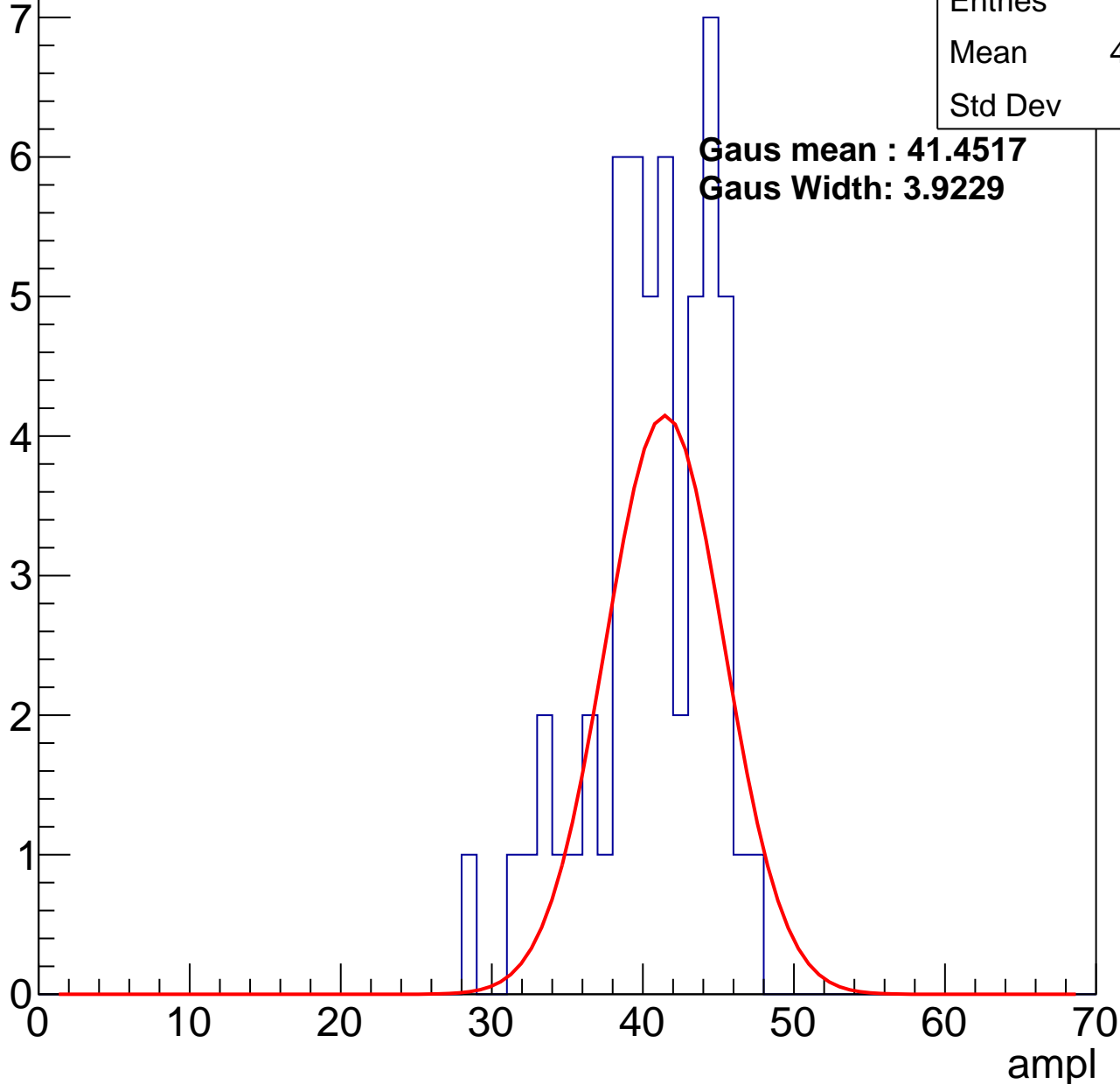
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	40.15
Std Dev	4.08

Gaus mean : 41.4517

Gaus Width: 3.9229

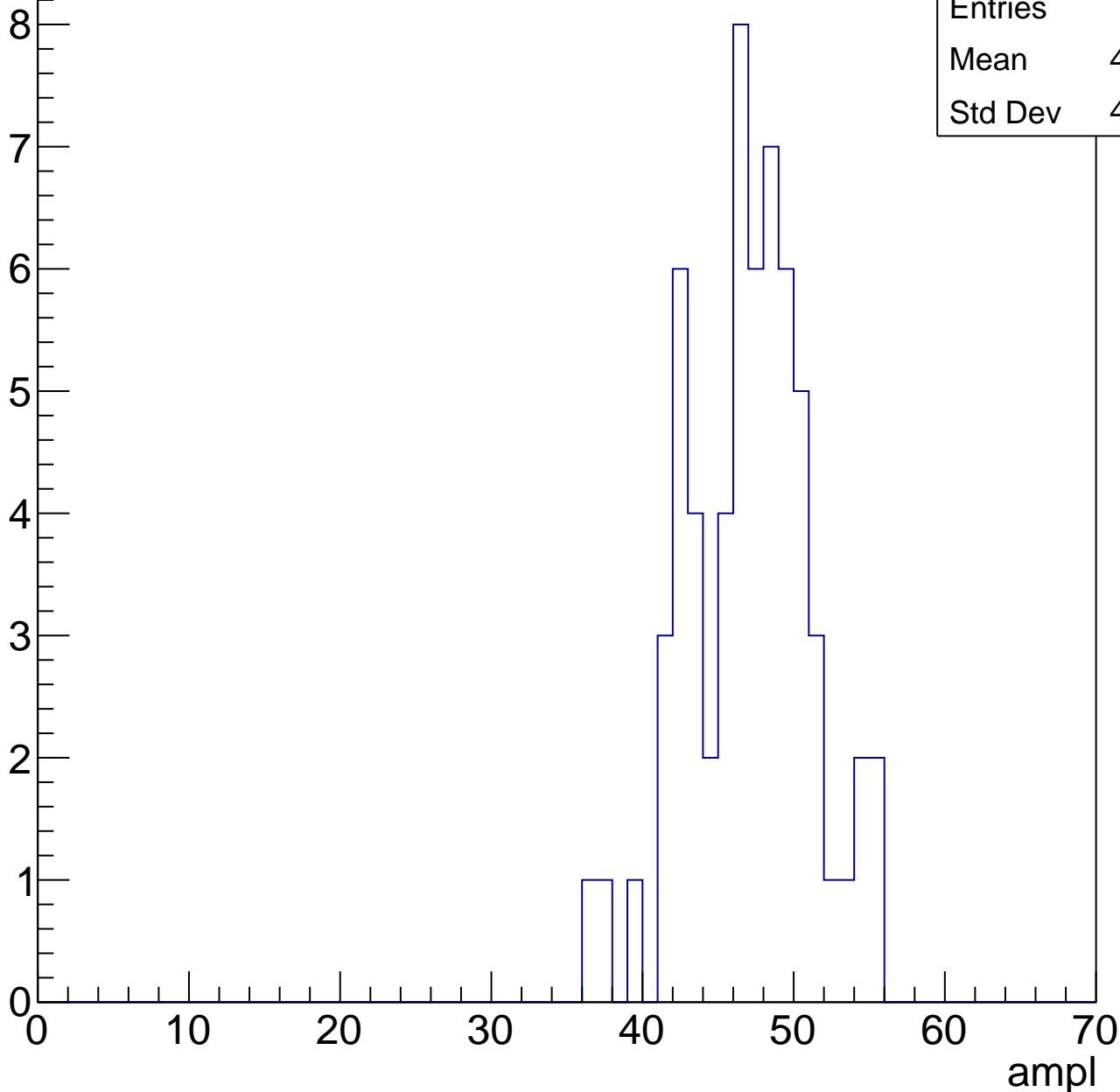


B0L102S, U5-ch90, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	46.56
Std Dev	4.078

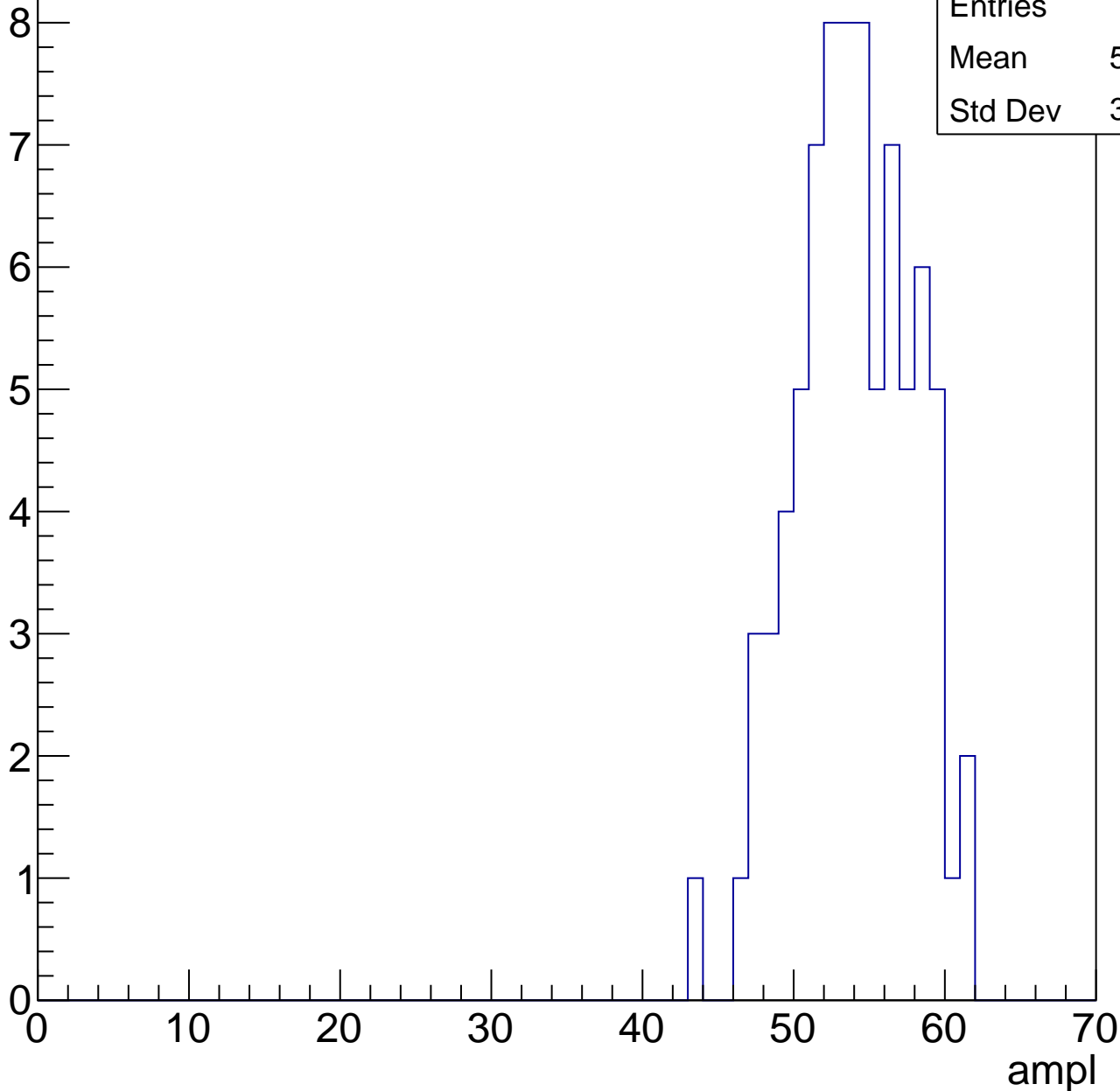


B0L102S, U5-ch90, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	53.49
Std Dev	3.792

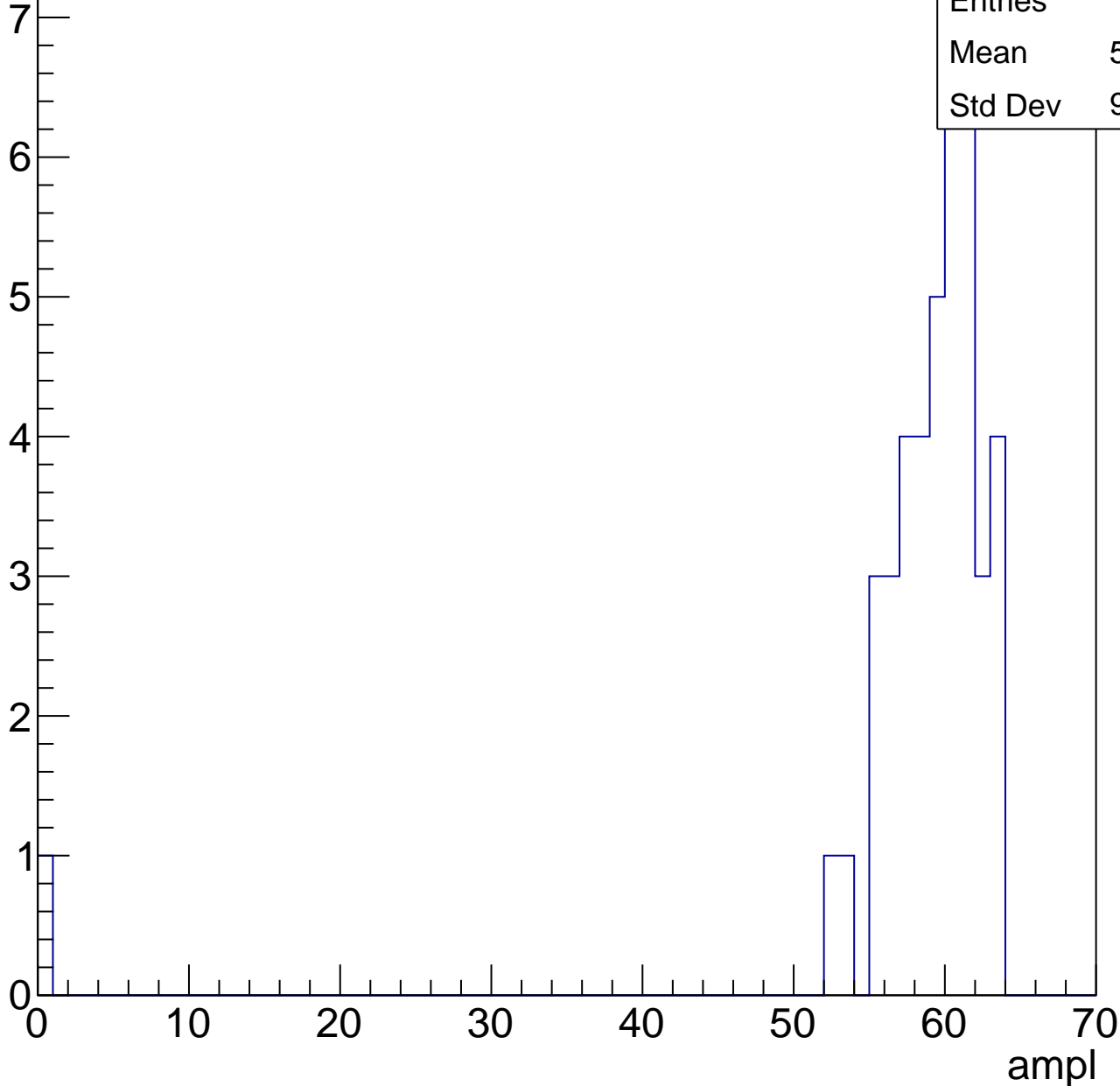


B0L102S, U5-ch90, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	57.63
Std Dev	9.284

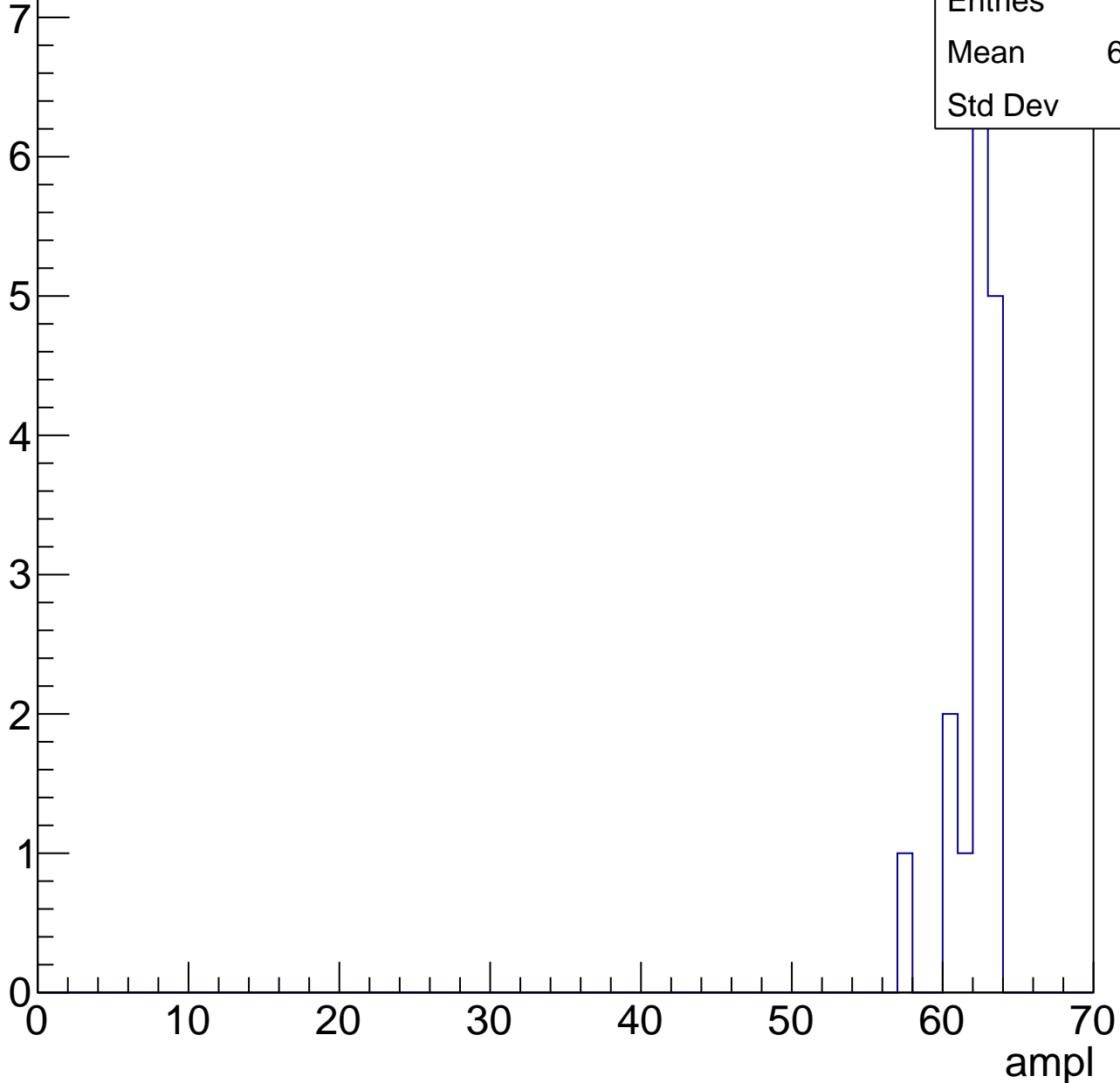


B0L102S, U5-ch90, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

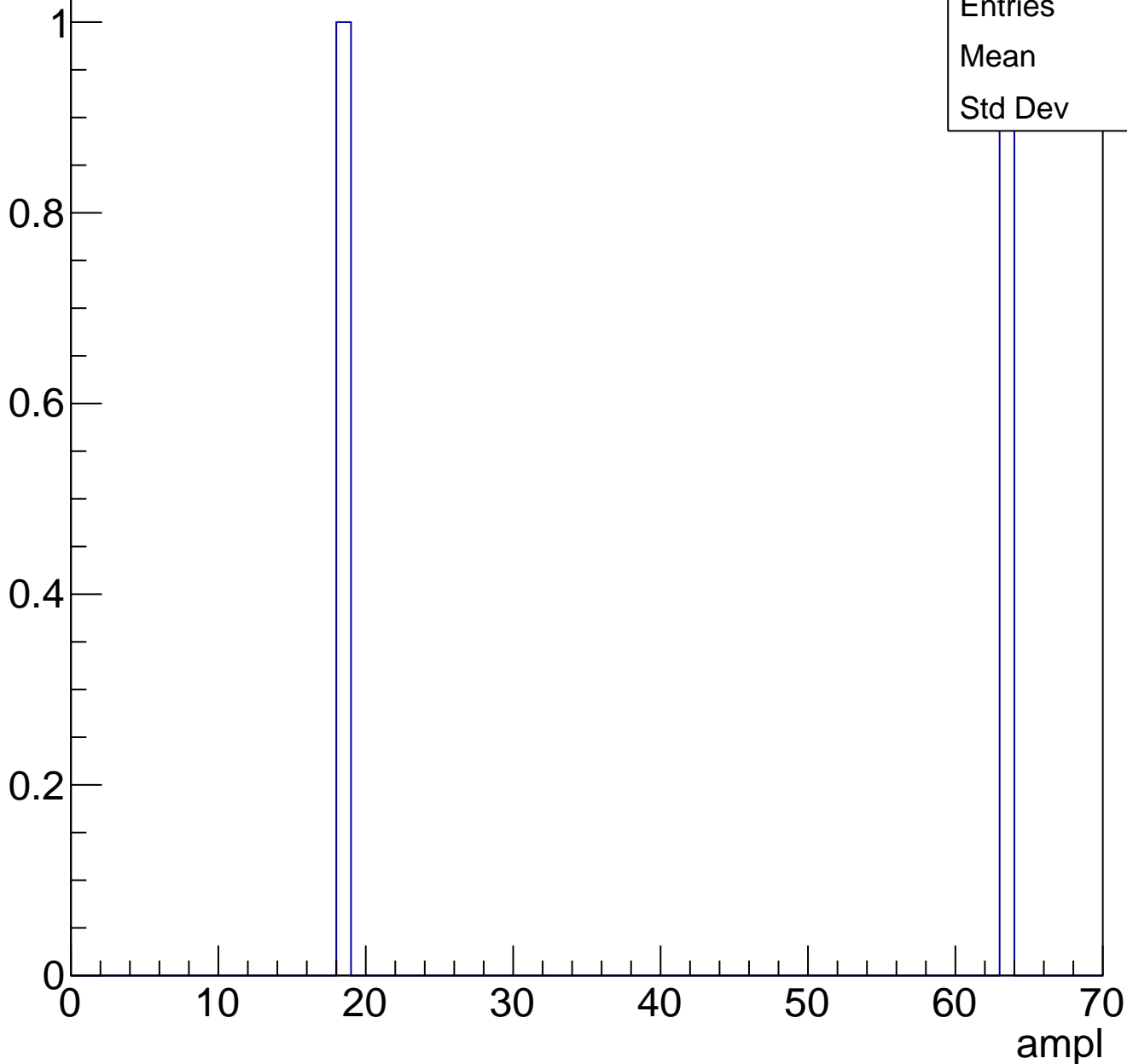
Entries	16
Mean	61.69
Std Dev	1.53



B0L102S, U5-ch90, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch91, adc0

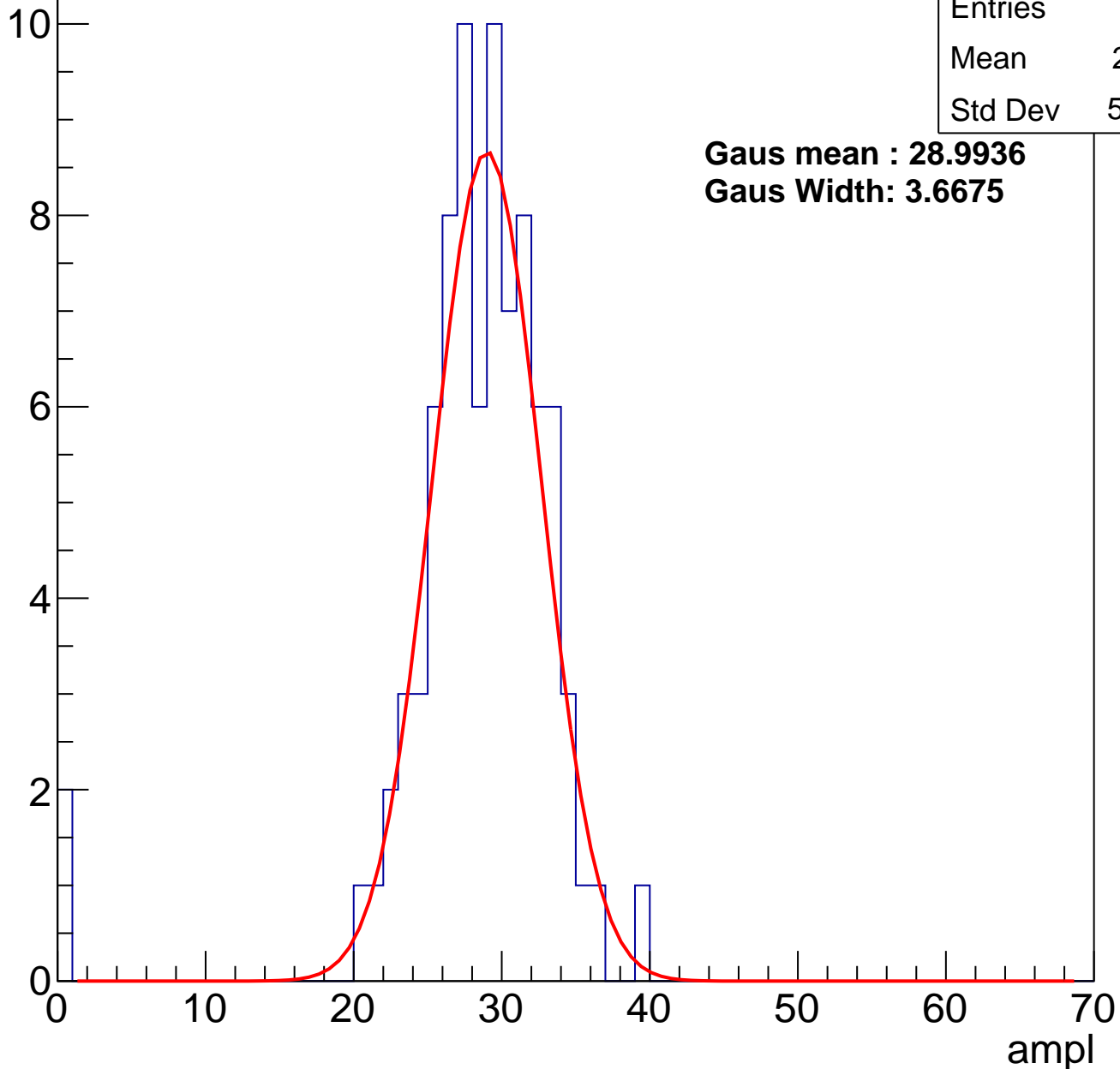
calib_packv5_042523_0143.root, FC#12, port B1

Entries	85
Mean	27.91
Std Dev	5.604

Gaus mean : 28.9936

Gaus Width: 3.6675

Entry



B0L102S, U5-ch91, adc1

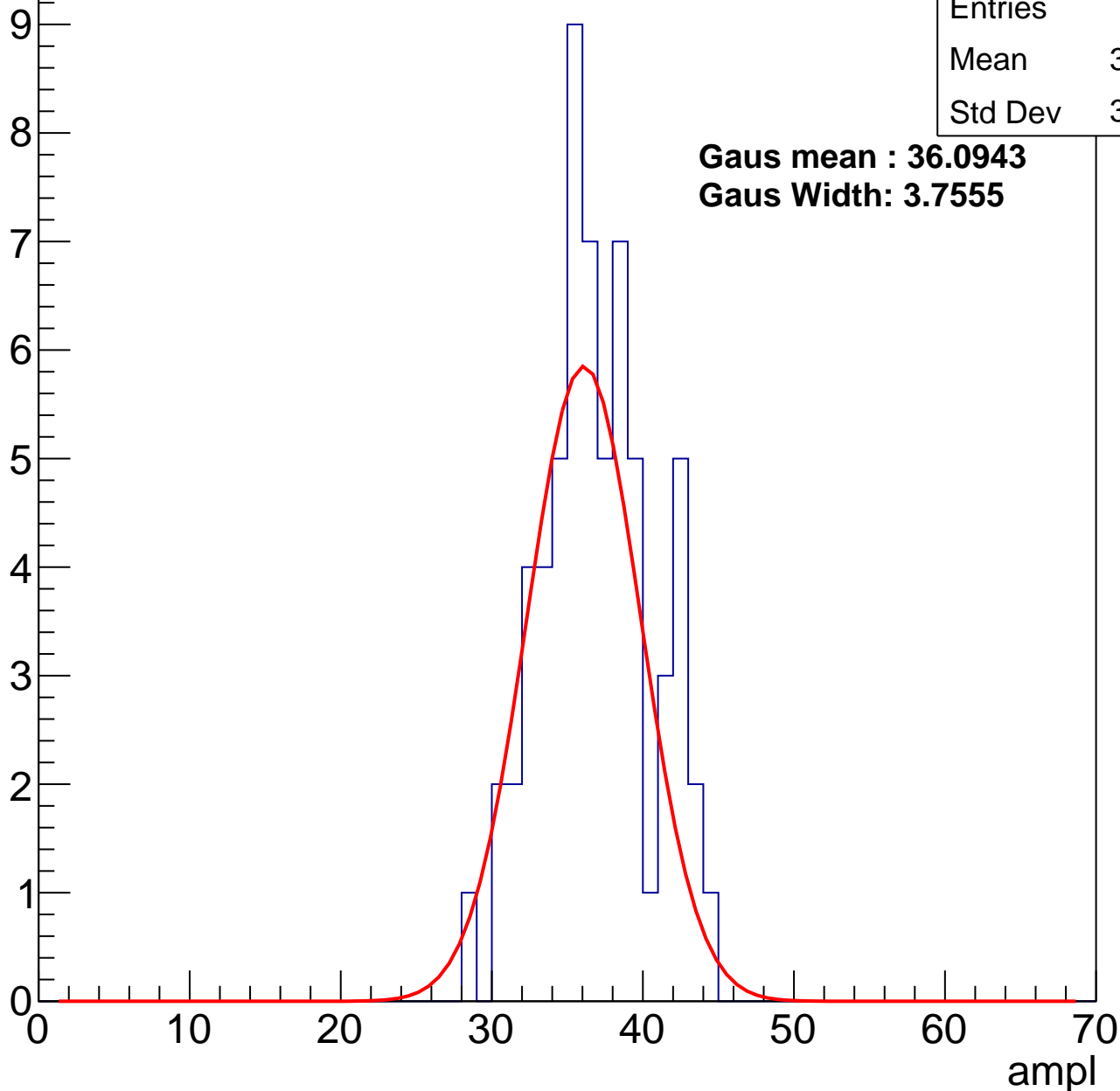
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	36.44
Std Dev	3.594

Gaus mean : 36.0943

Gaus Width: 3.7555



B0L102S, U5-ch91, adc2

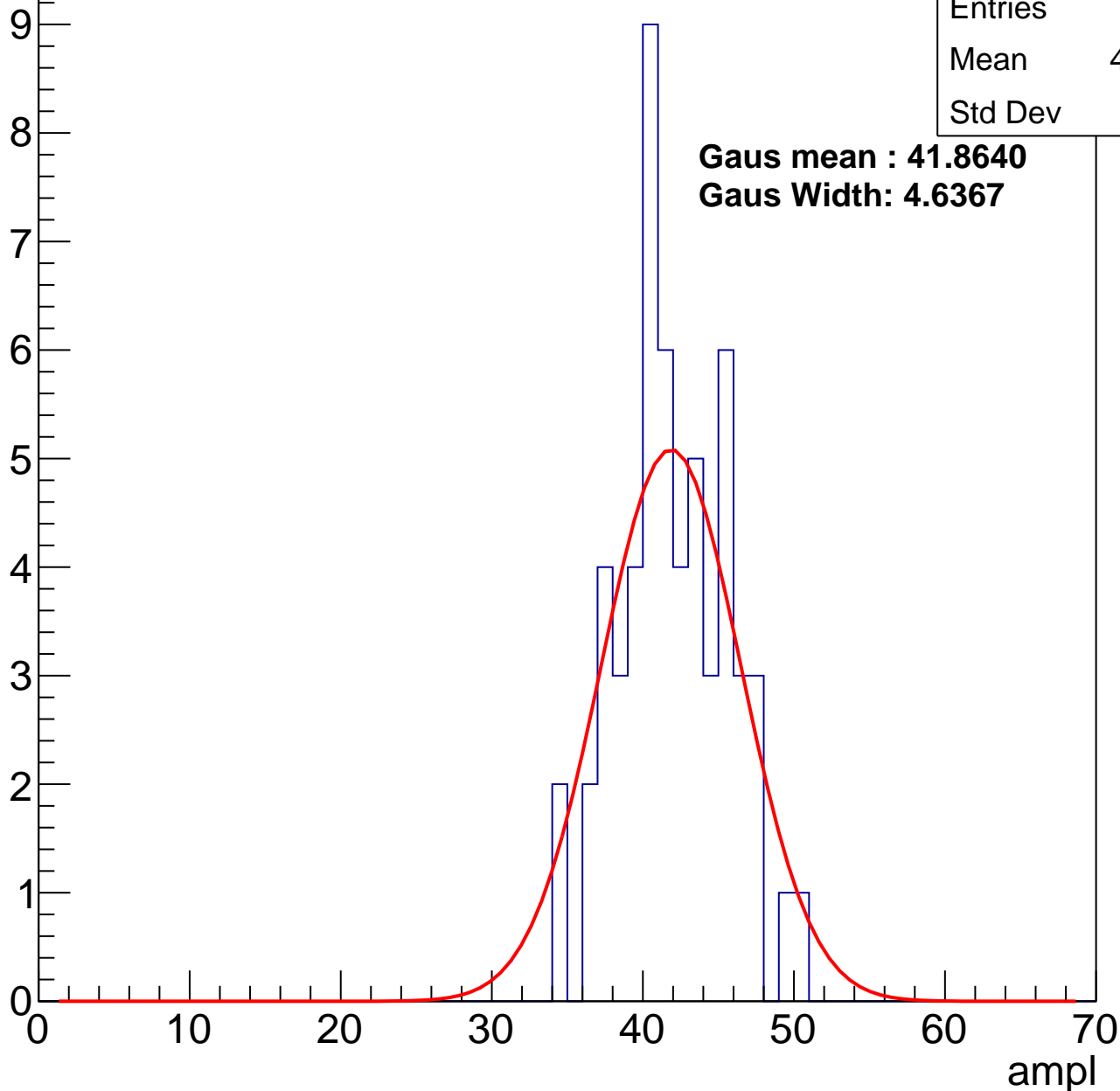
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	41.55
Std Dev	3.59

Gaus mean : 41.8640

Gaus Width: 4.6367



B0L102S, U5-ch91, adc3

calib_packv5_042523_0143.root, FC#12, port B1

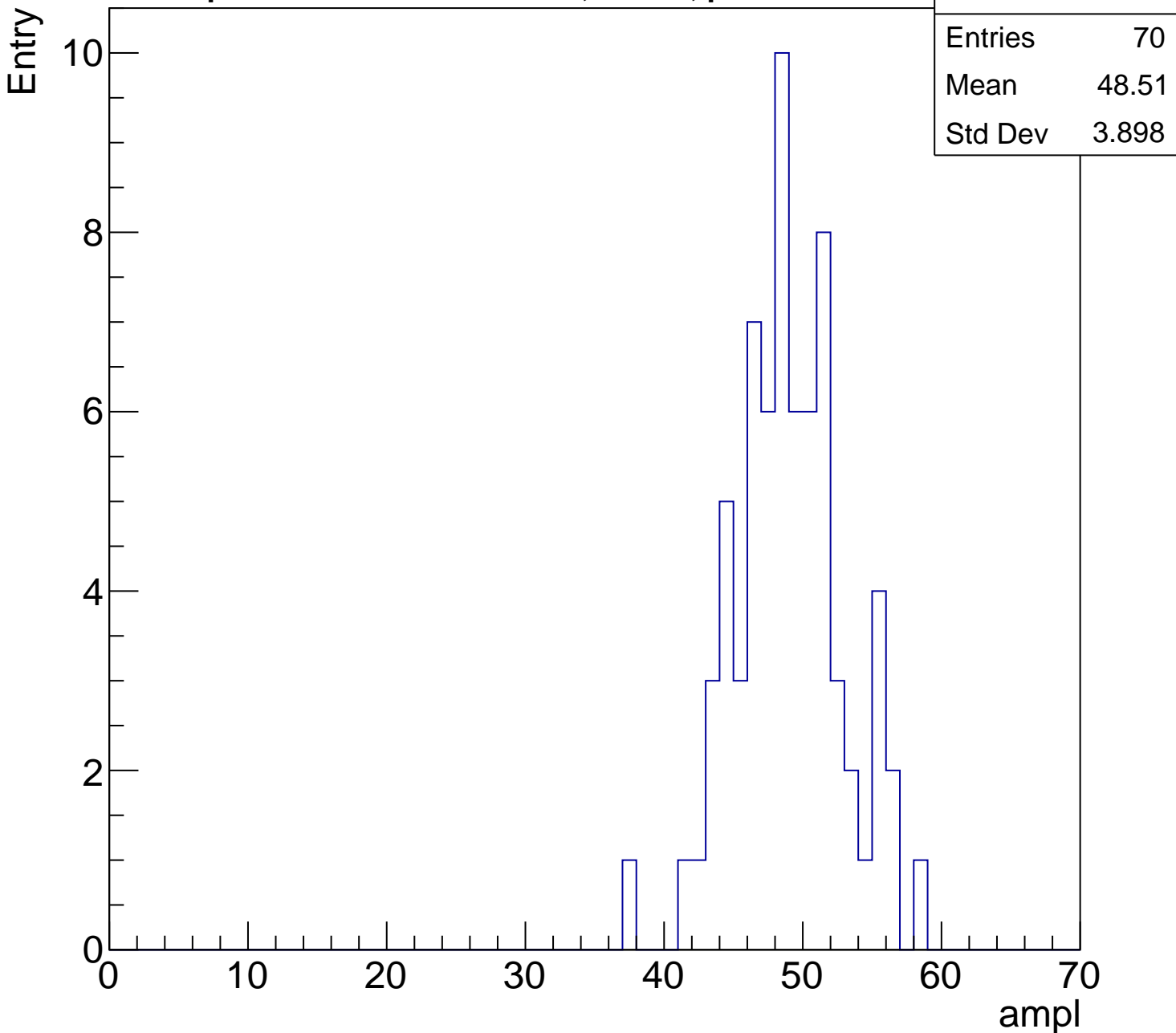
Entries	70
Mean	48.51
Std Dev	3.898

Entry

10
8
6
4
2
0

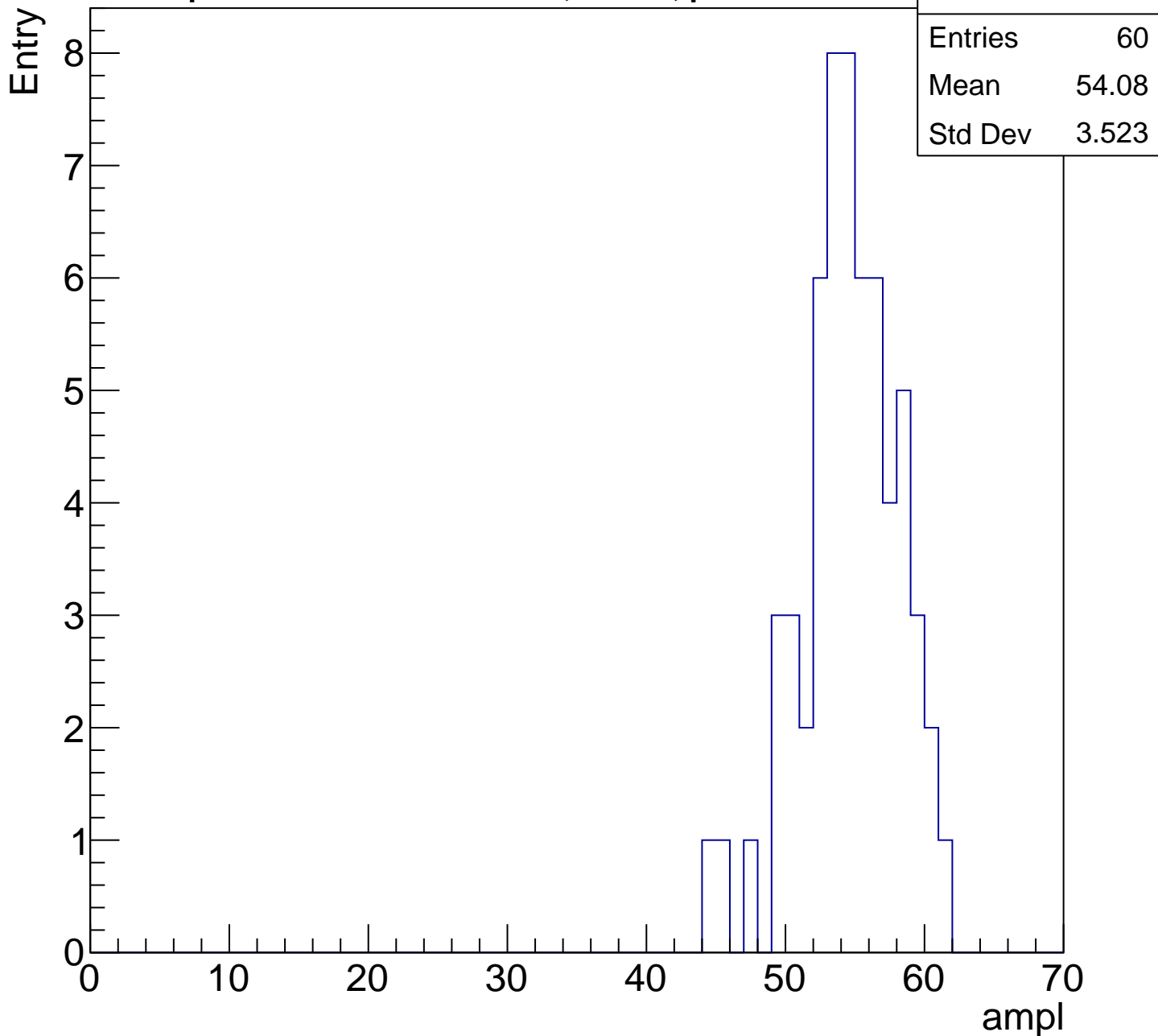
0 10 20 30 40 50 60 70

ampl



B0L102S, U5-ch91, adc4

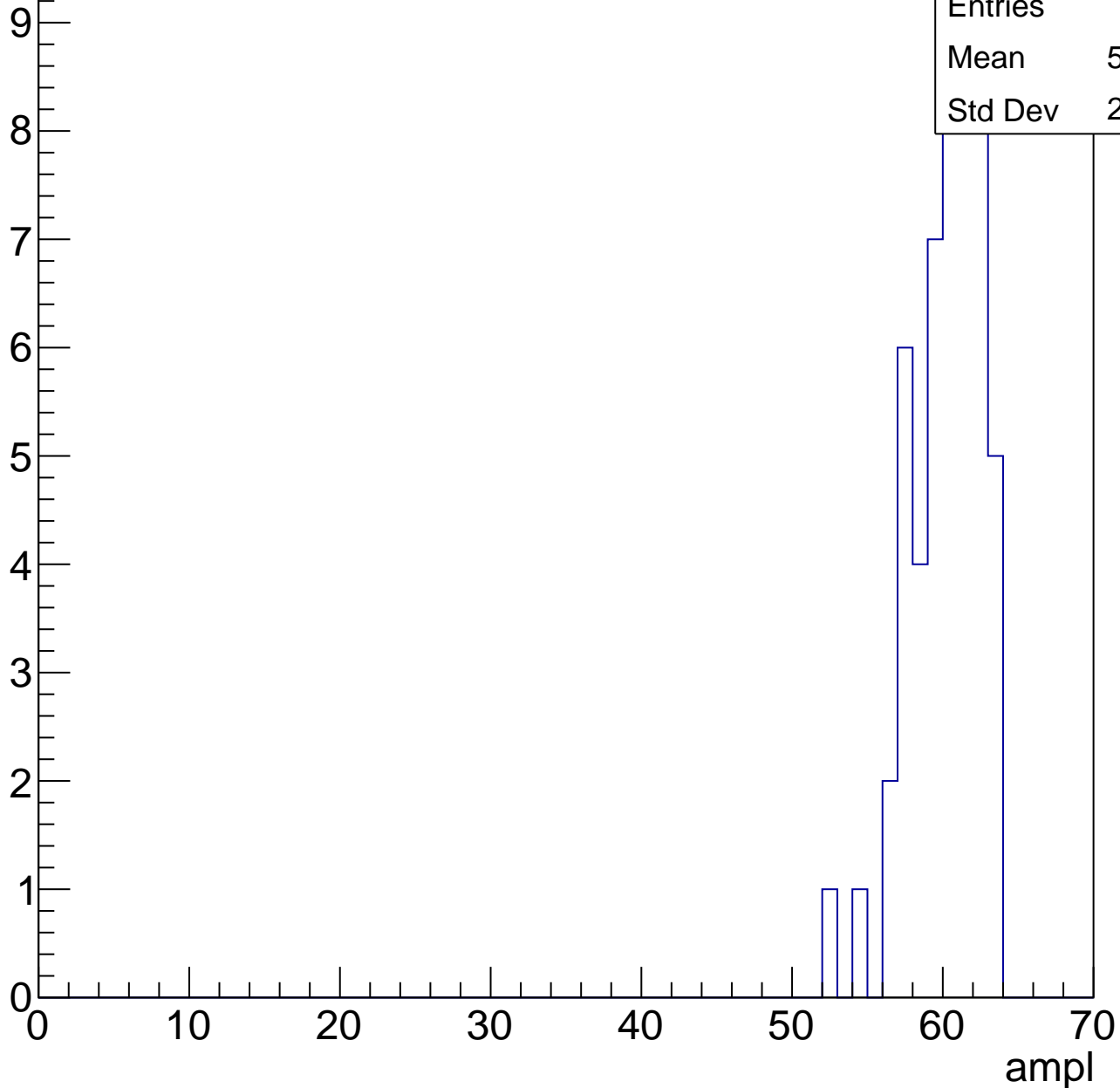
calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch91, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

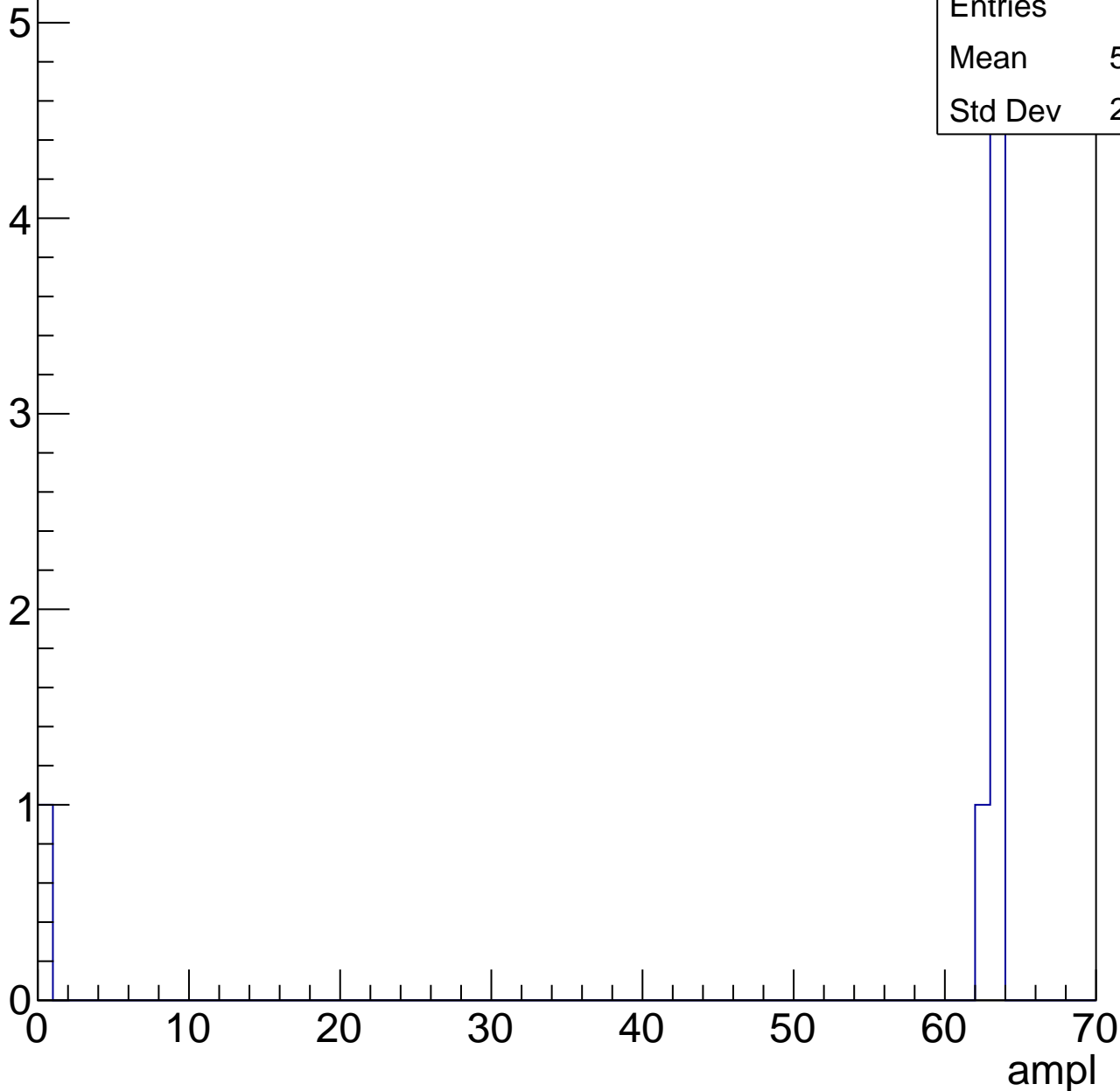


B0L102S, U5-ch91, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B0L102S, U5-ch91, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	0
Std Dev	0

B0L102S, U5-ch92, adc0

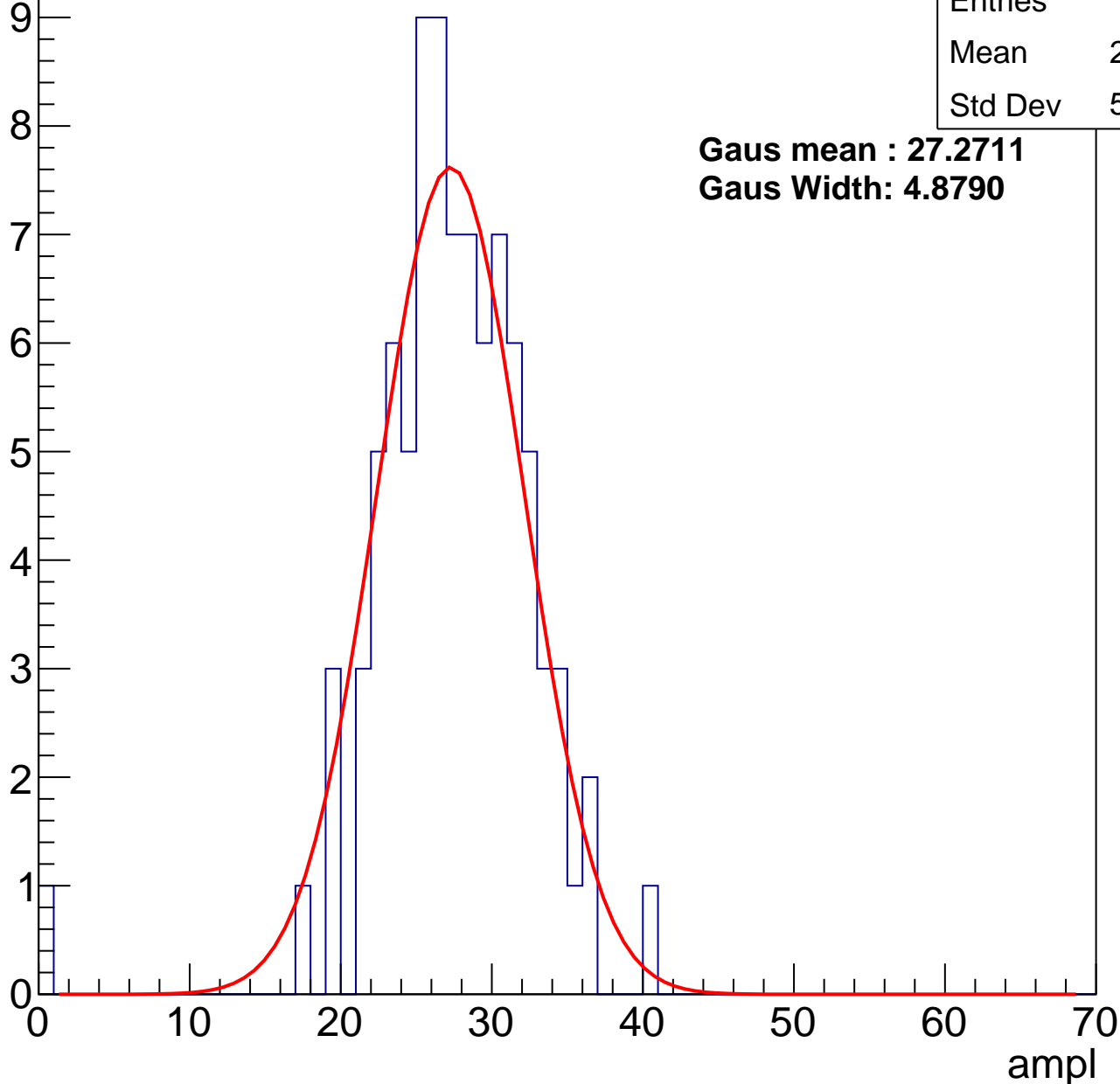
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	90
Mean	26.97
Std Dev	5.169

Gaus mean : 27.2711

Gaus Width: 4.8790



B0L102S, U5-ch92, adc1

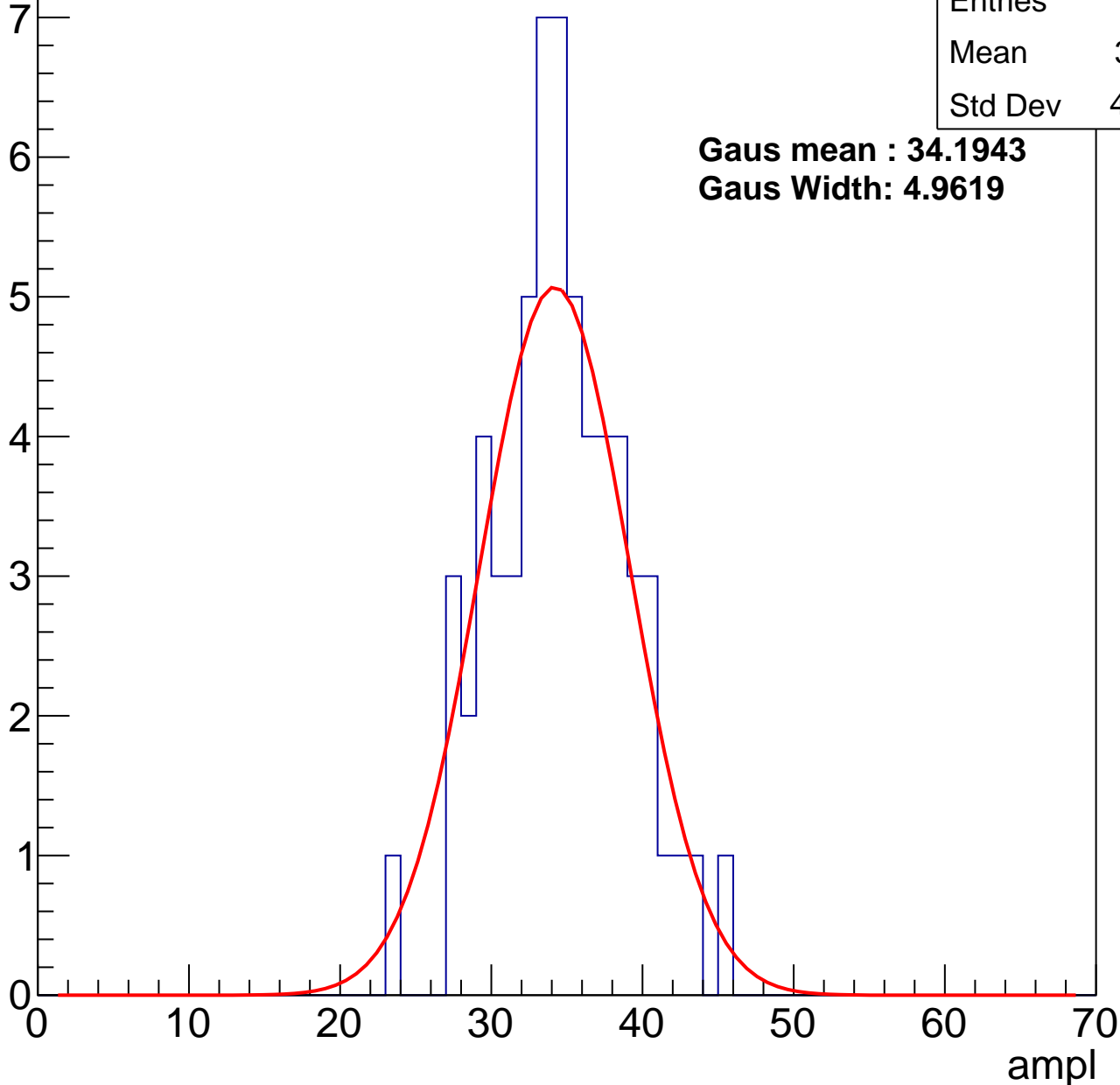
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	34.11
Std Dev	4.326

Gaus mean : 34.1943

Gaus Width: 4.9619



B0L102S, U5-ch92, adc2

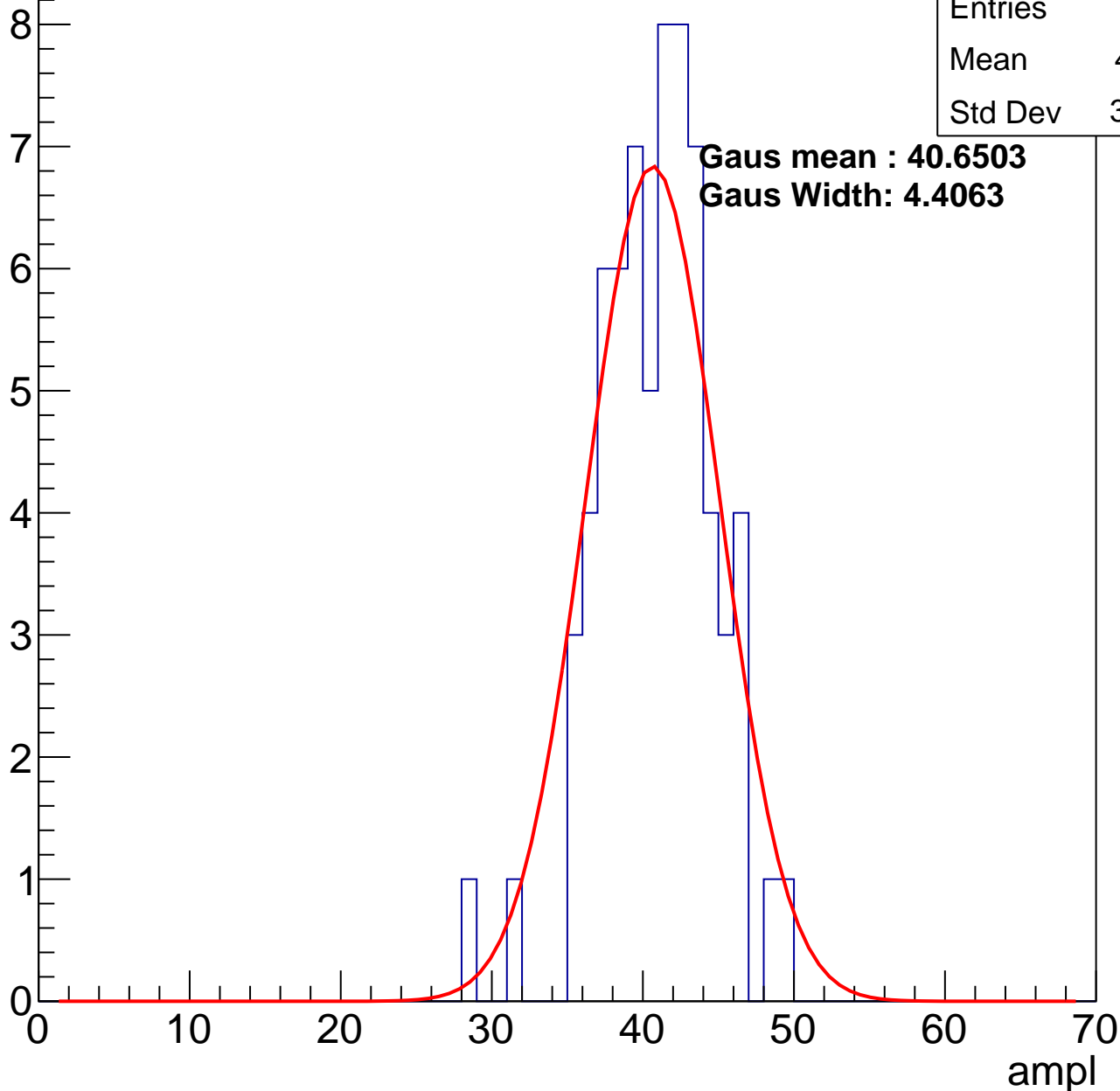
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	40.41
Std Dev	3.747

Gaus mean : 40.6503

Gaus Width: 4.4063

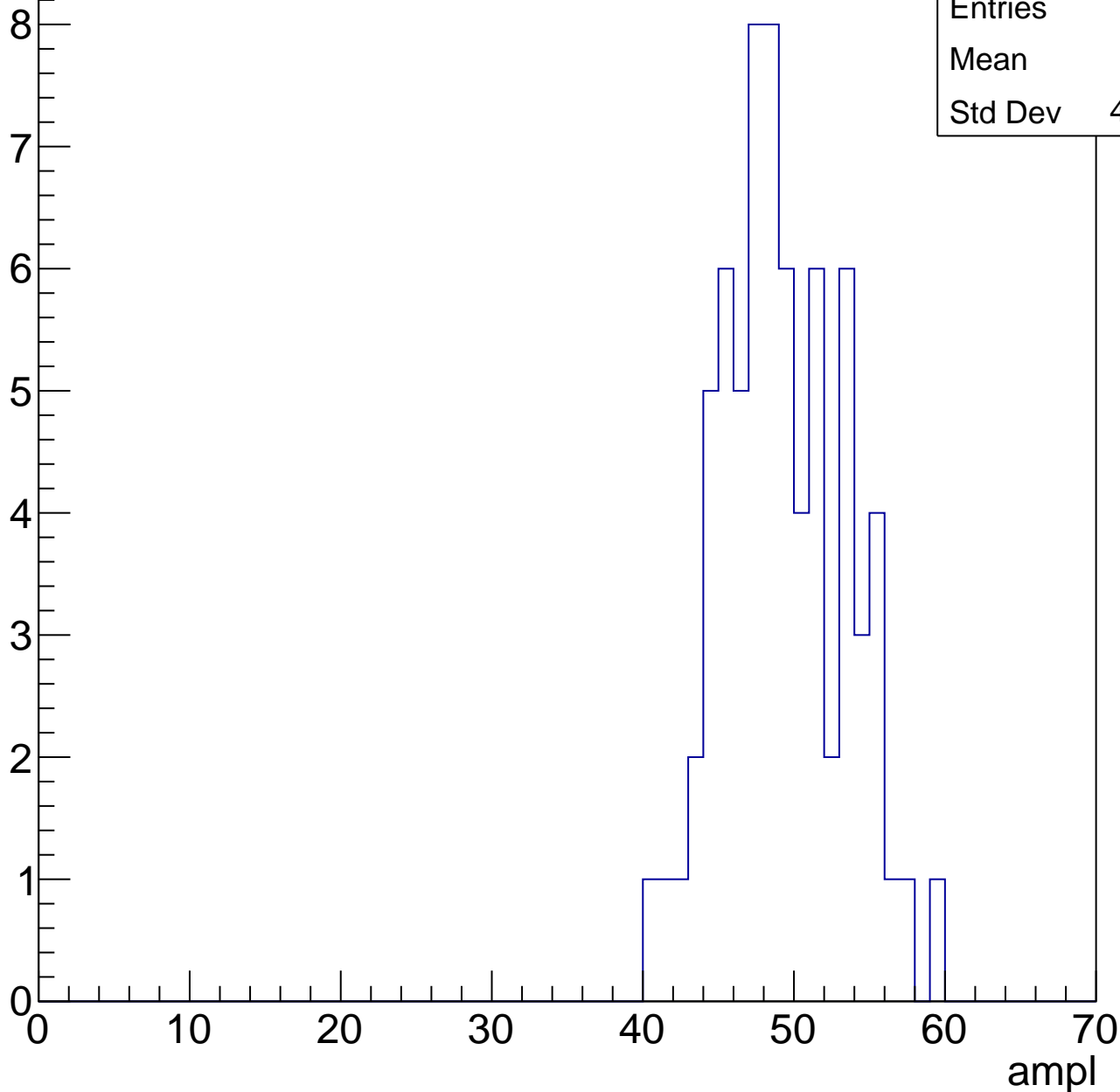


B0L102S, U5-ch92, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	48.8
Std Dev	4.013

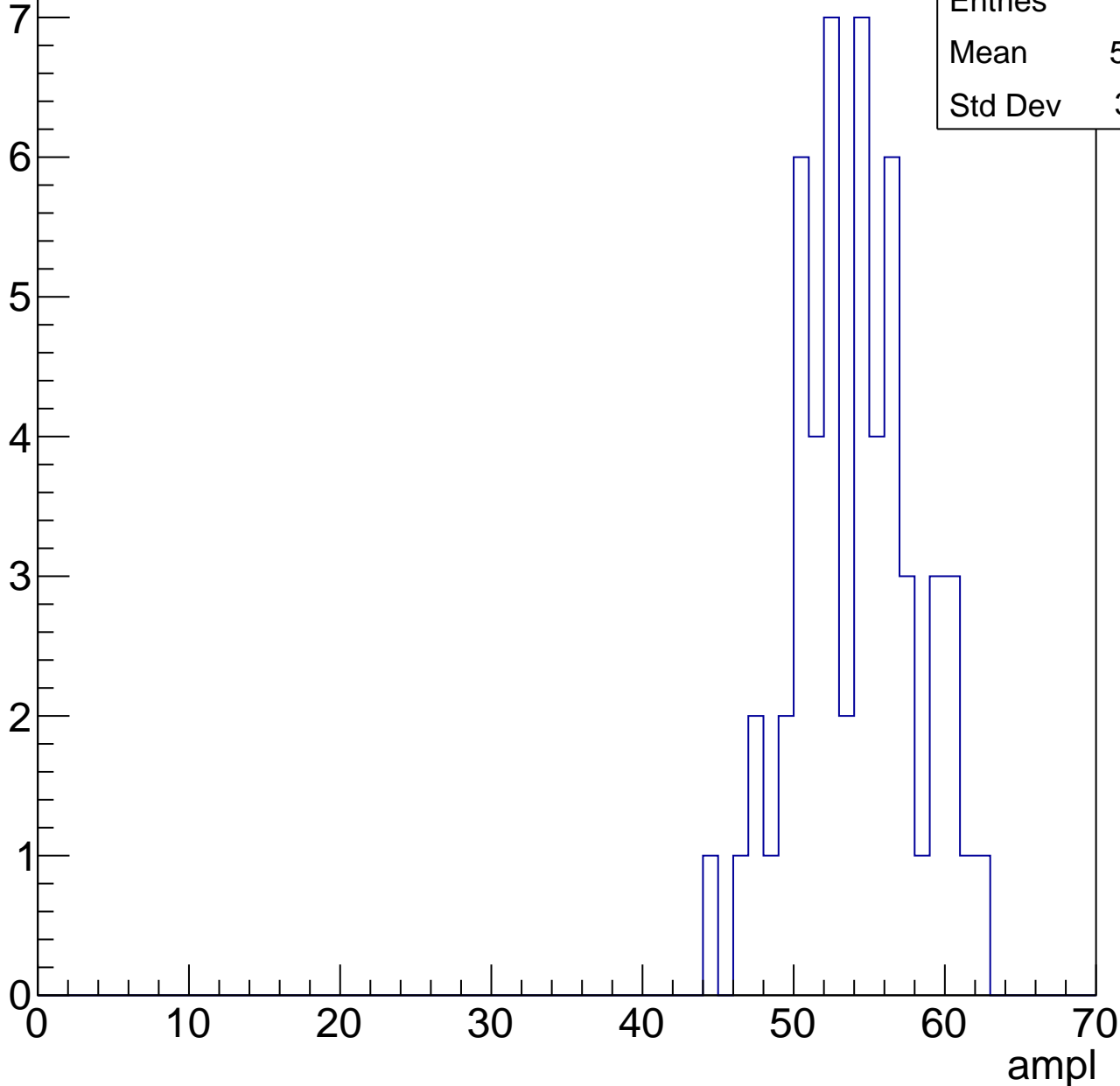


B0L102S, U5-ch92, adc4

calib_packv5_042523_0143.root, FC#12, port B1

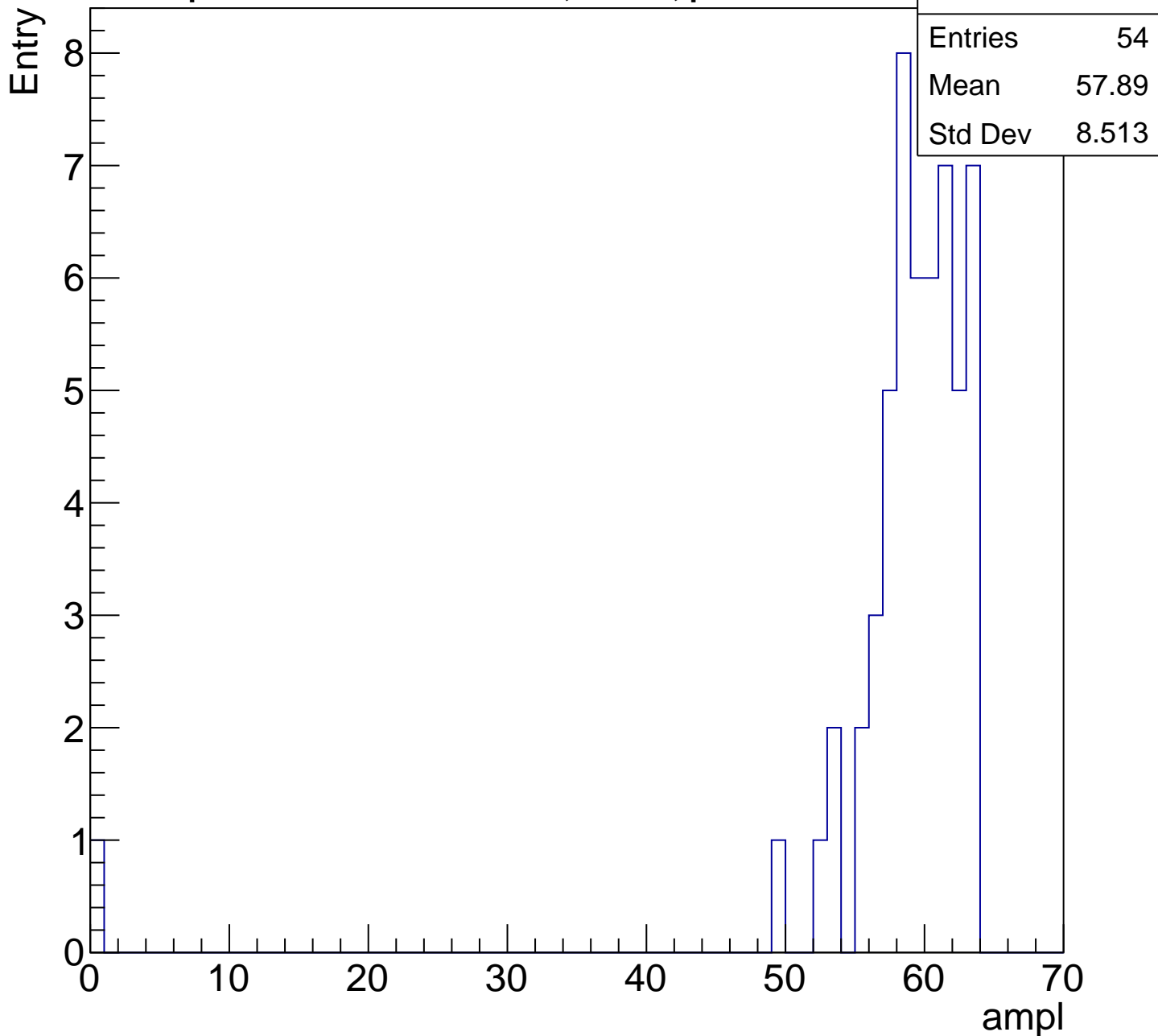
Entry

Entries	55
Mean	53.58
Std Dev	3.971



B0L102S, U5-ch92, adc5

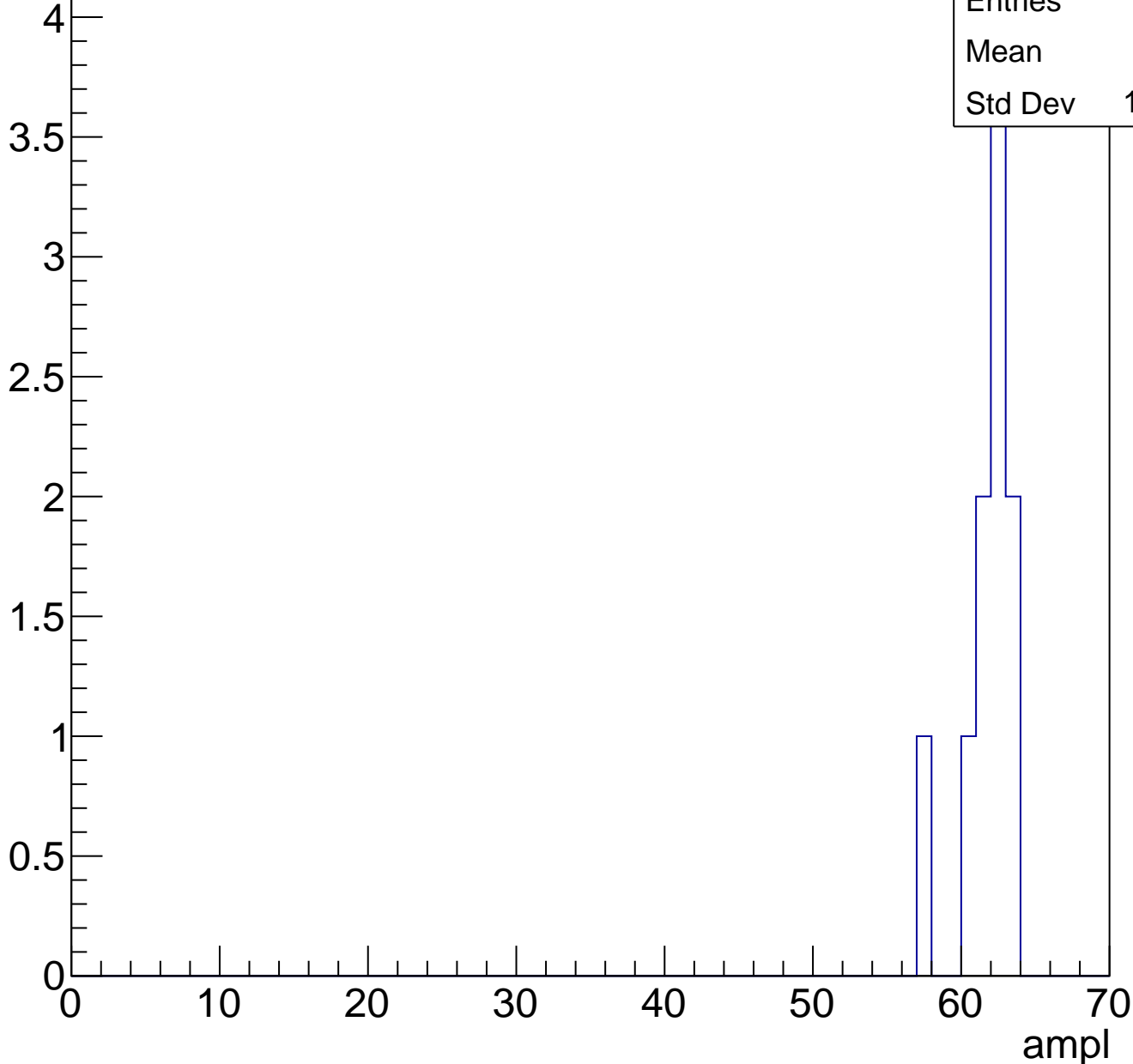
calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch92, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch92, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	2
Mean	60.5
Std Dev	2.5

B0L102S, U5-ch93, adc0

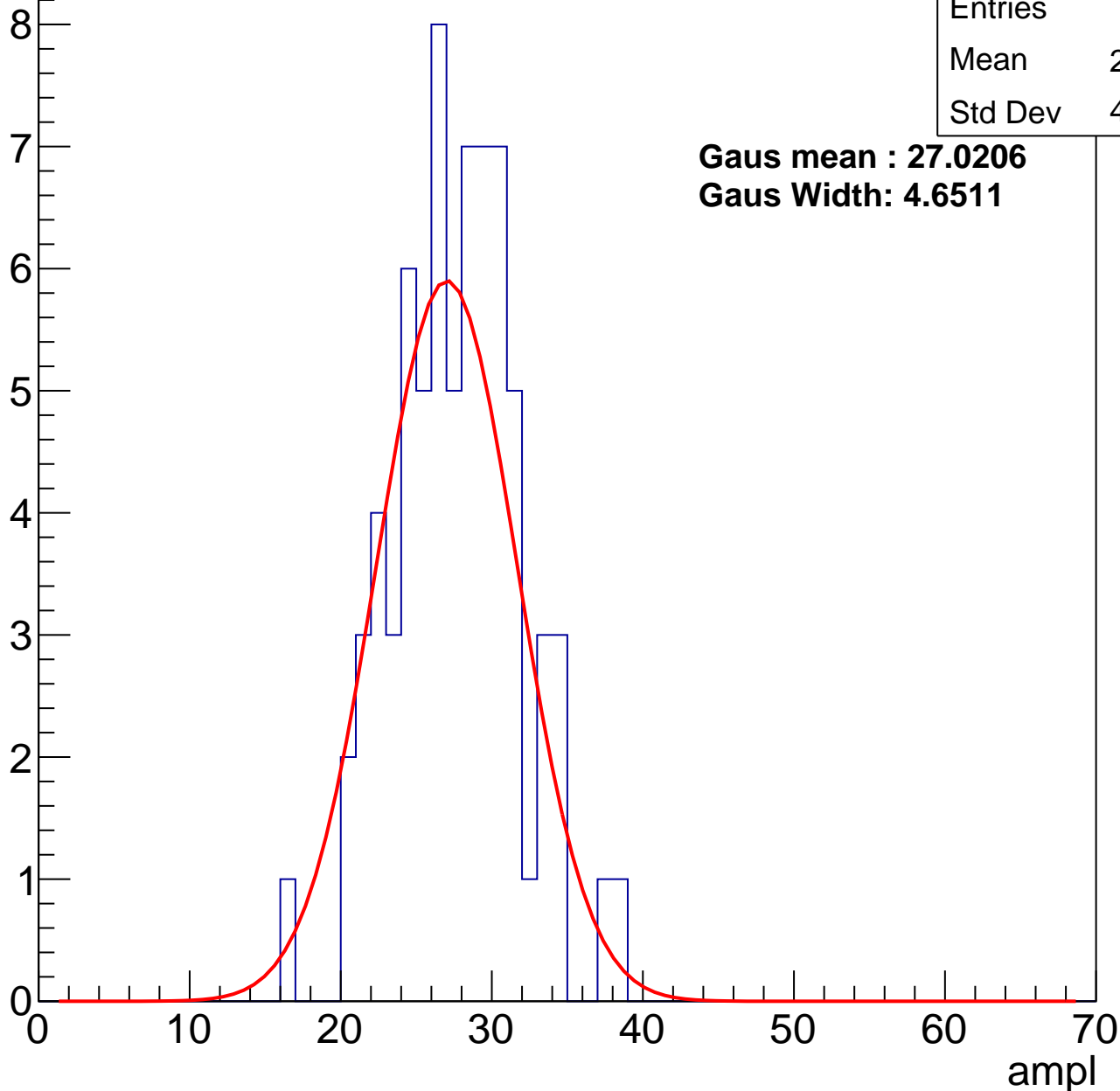
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	27.22
Std Dev	4.137

Gaus mean : 27.0206

Gaus Width: 4.6511



B0L102S, U5-ch93, adc1

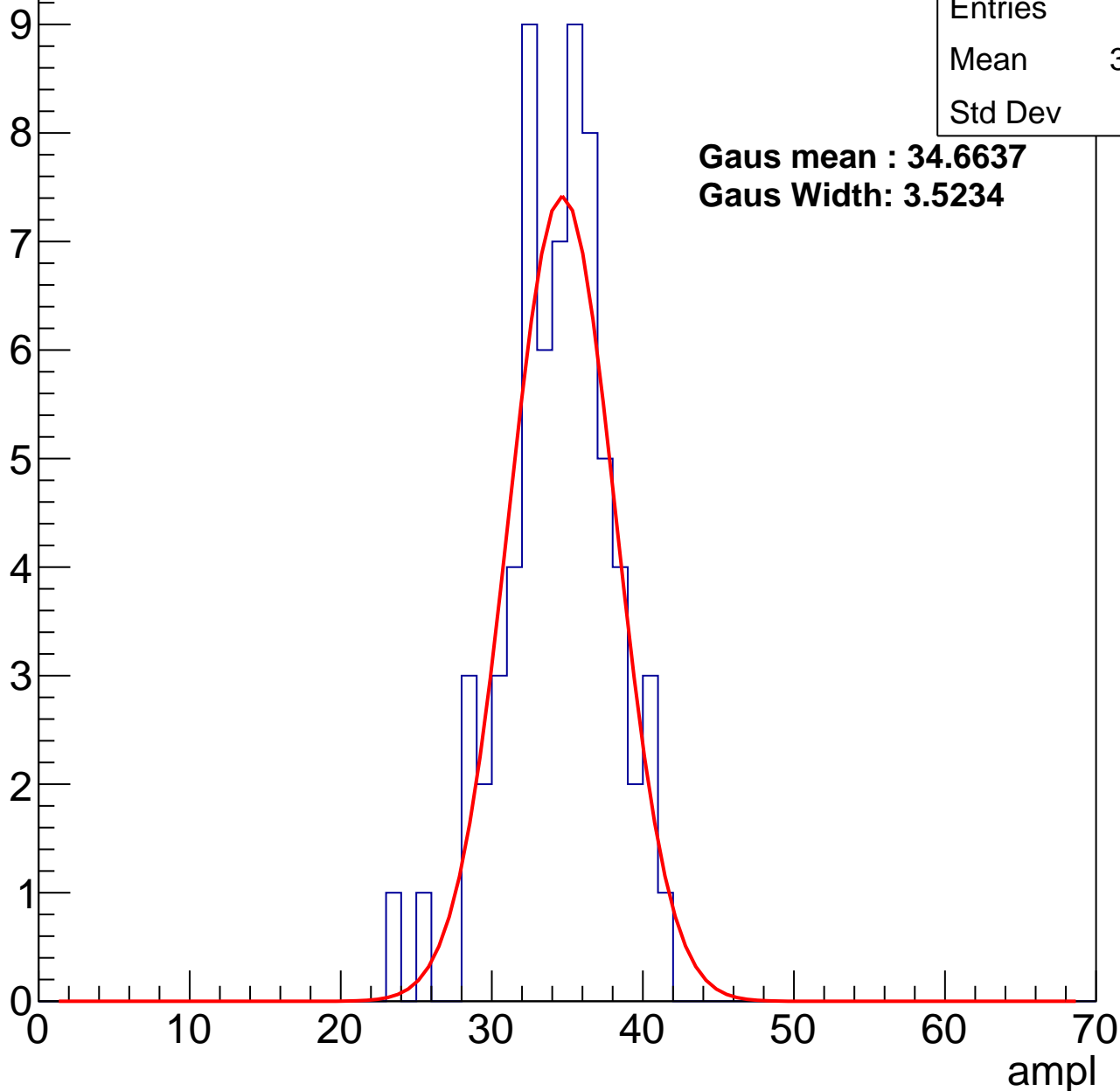
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	33.93
Std Dev	3.52

Gaus mean : 34.6637

Gaus Width: 3.5234



B0L102S, U5-ch93, adc2

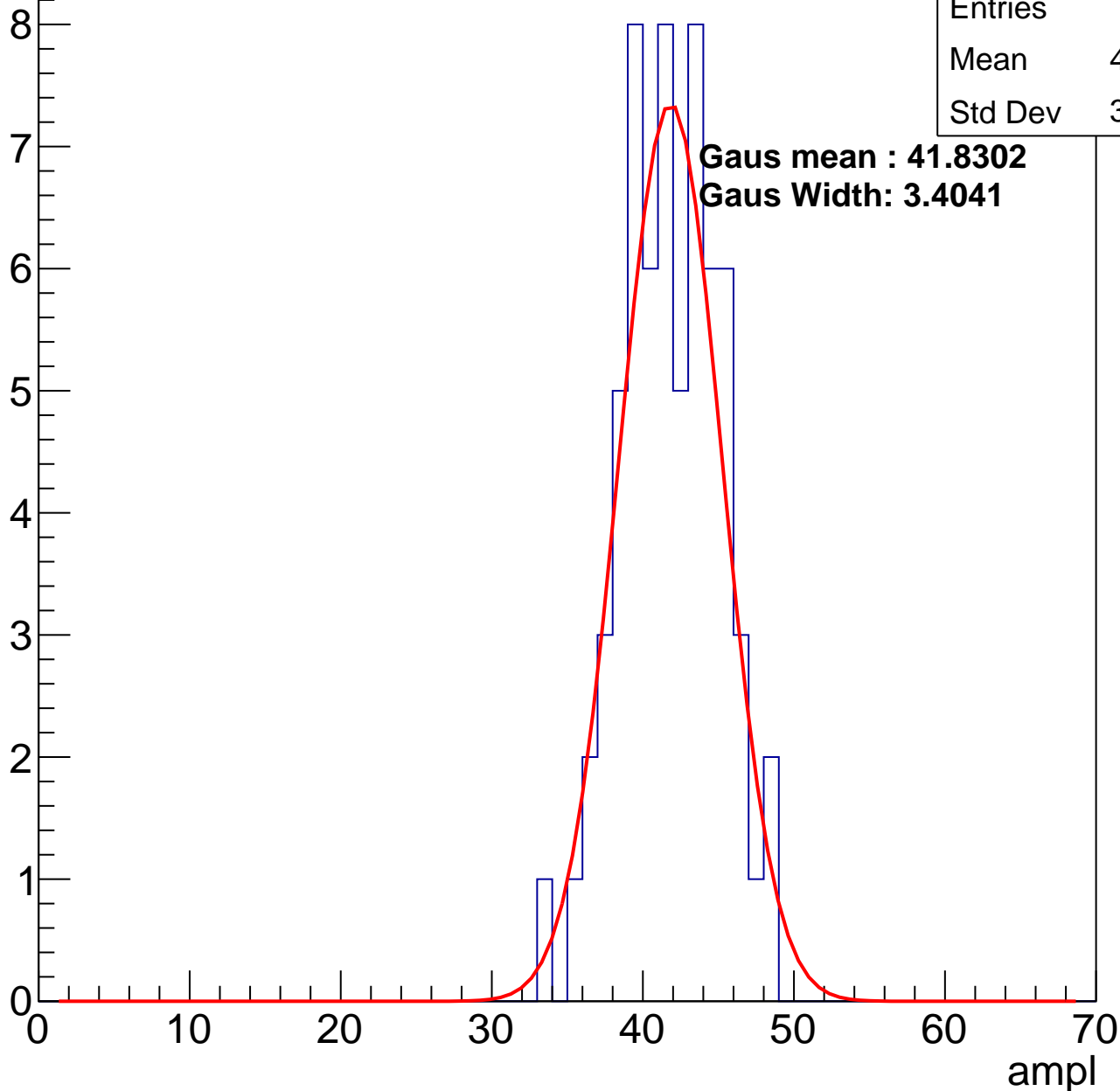
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.38
Std Dev	3.204

Gaus mean : 41.8302

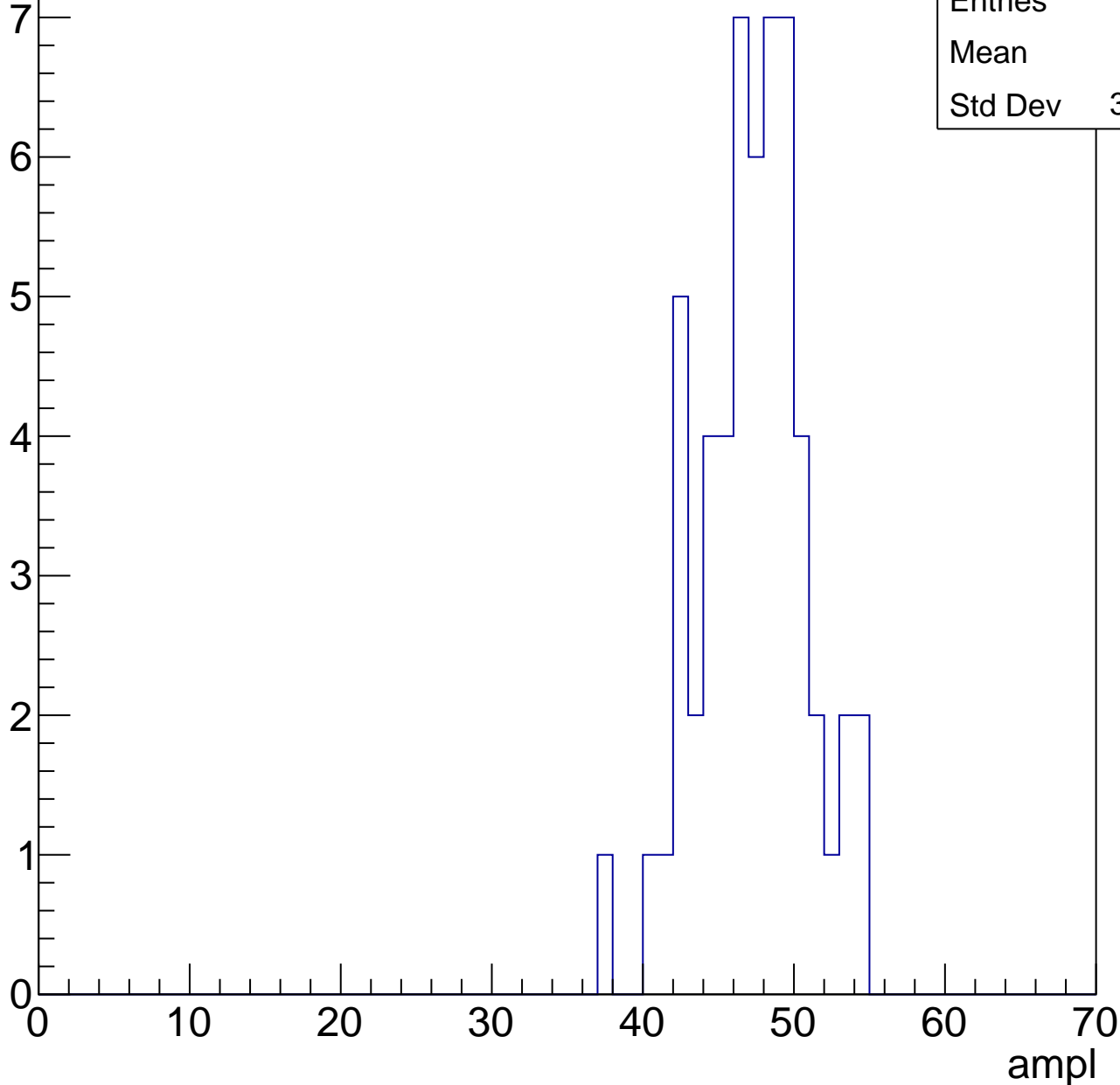
Gaus Width: 3.4041



B0L102S, U5-ch93, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



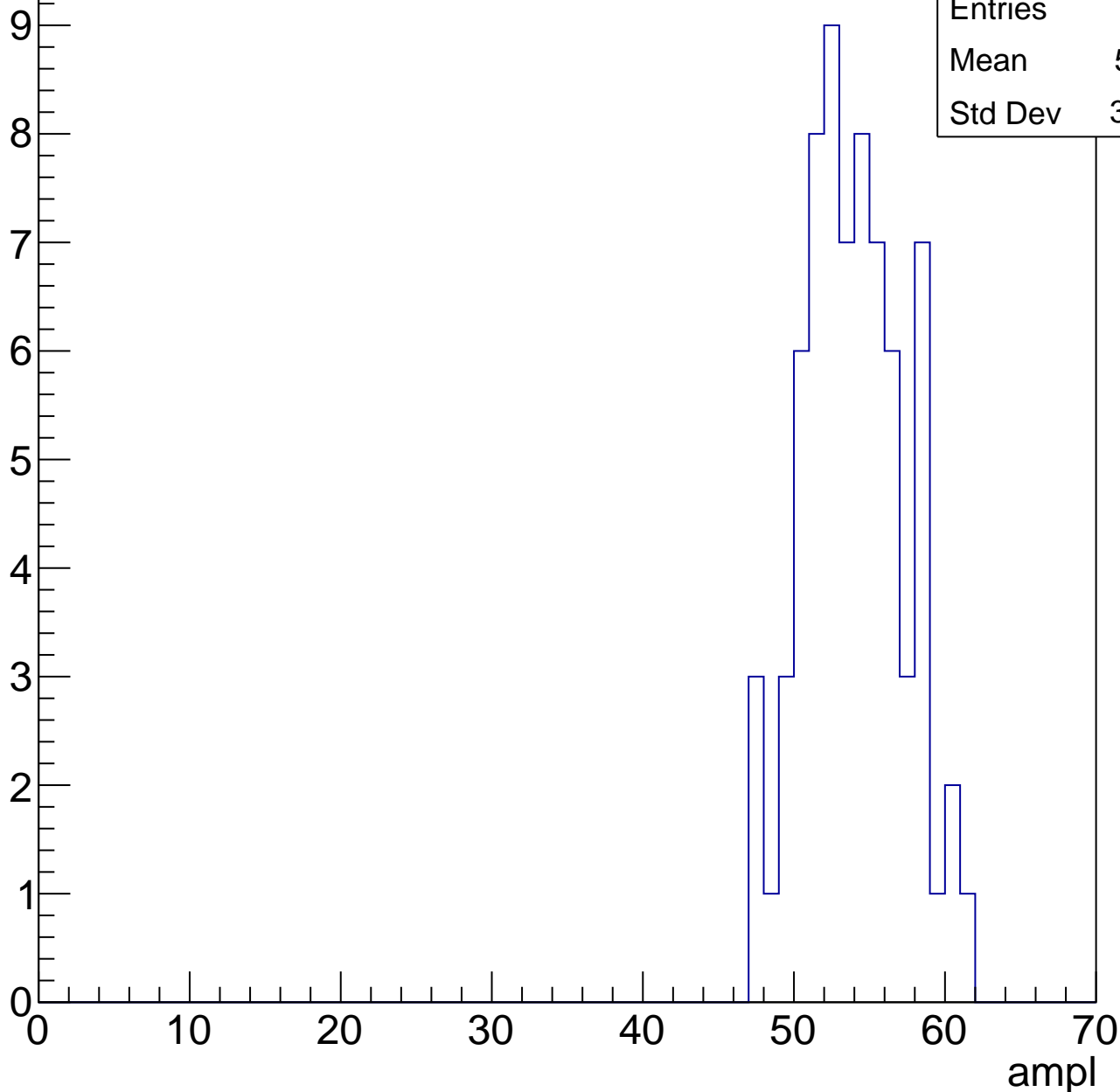
Entries	56
Mean	46.8
Std Dev	3.528

B0L102S, U5-ch93, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	53.51
Std Dev	3.266

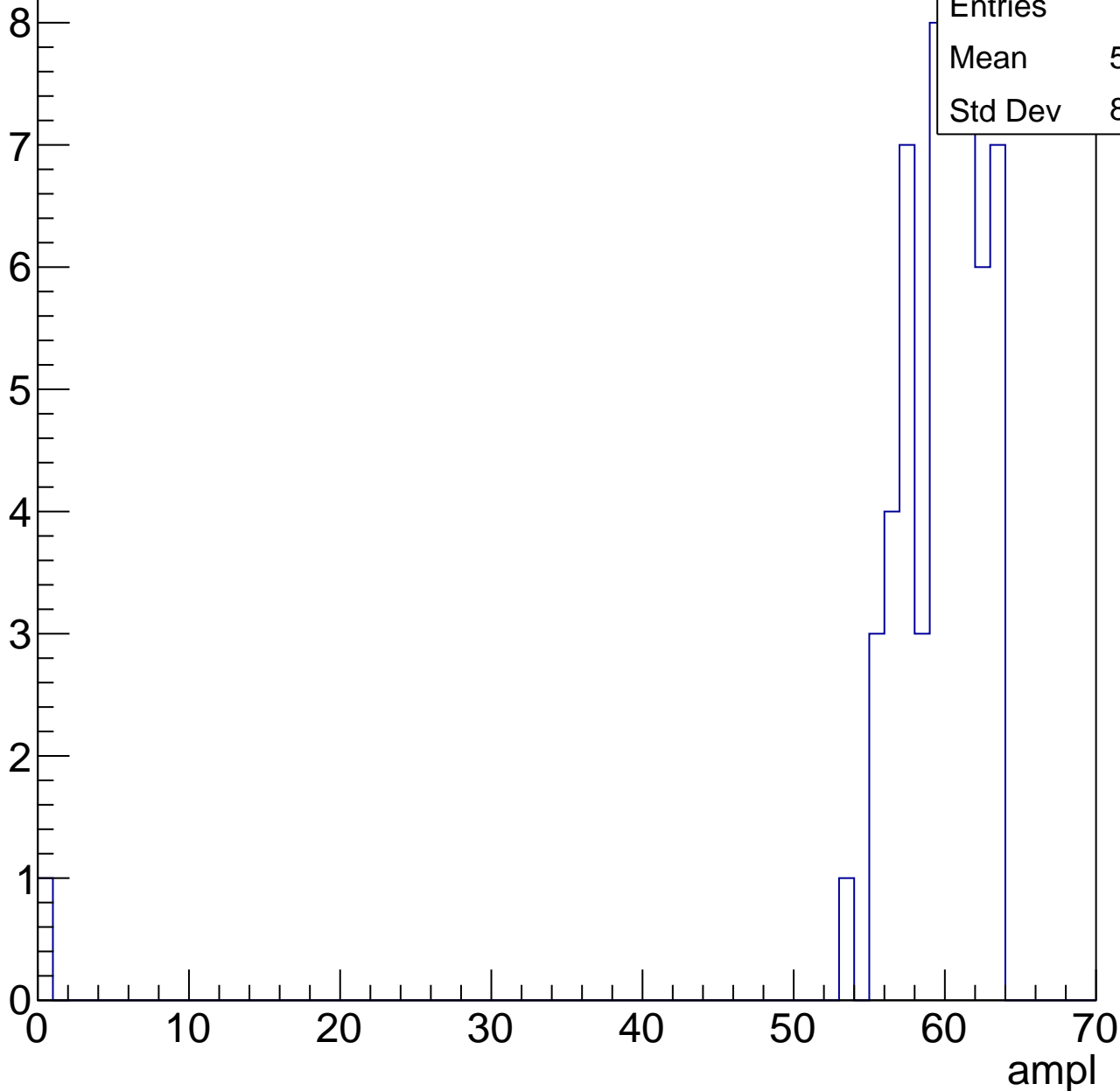


B0L102S, U5-ch93, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

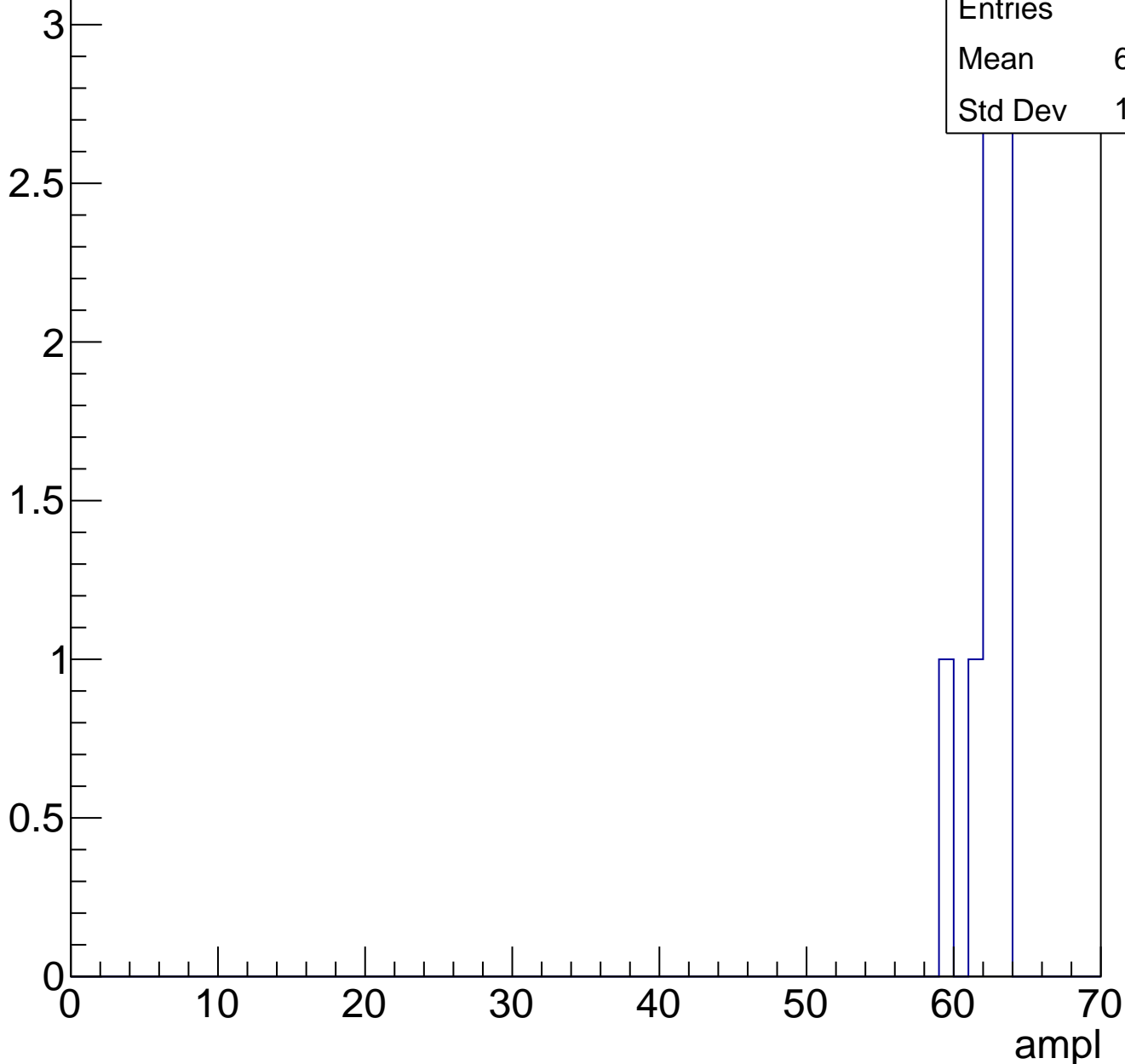
Entries	56
Mean	58.36
Std Dev	8.254



B0L102S, U5-ch93, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch93, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch94, adc0

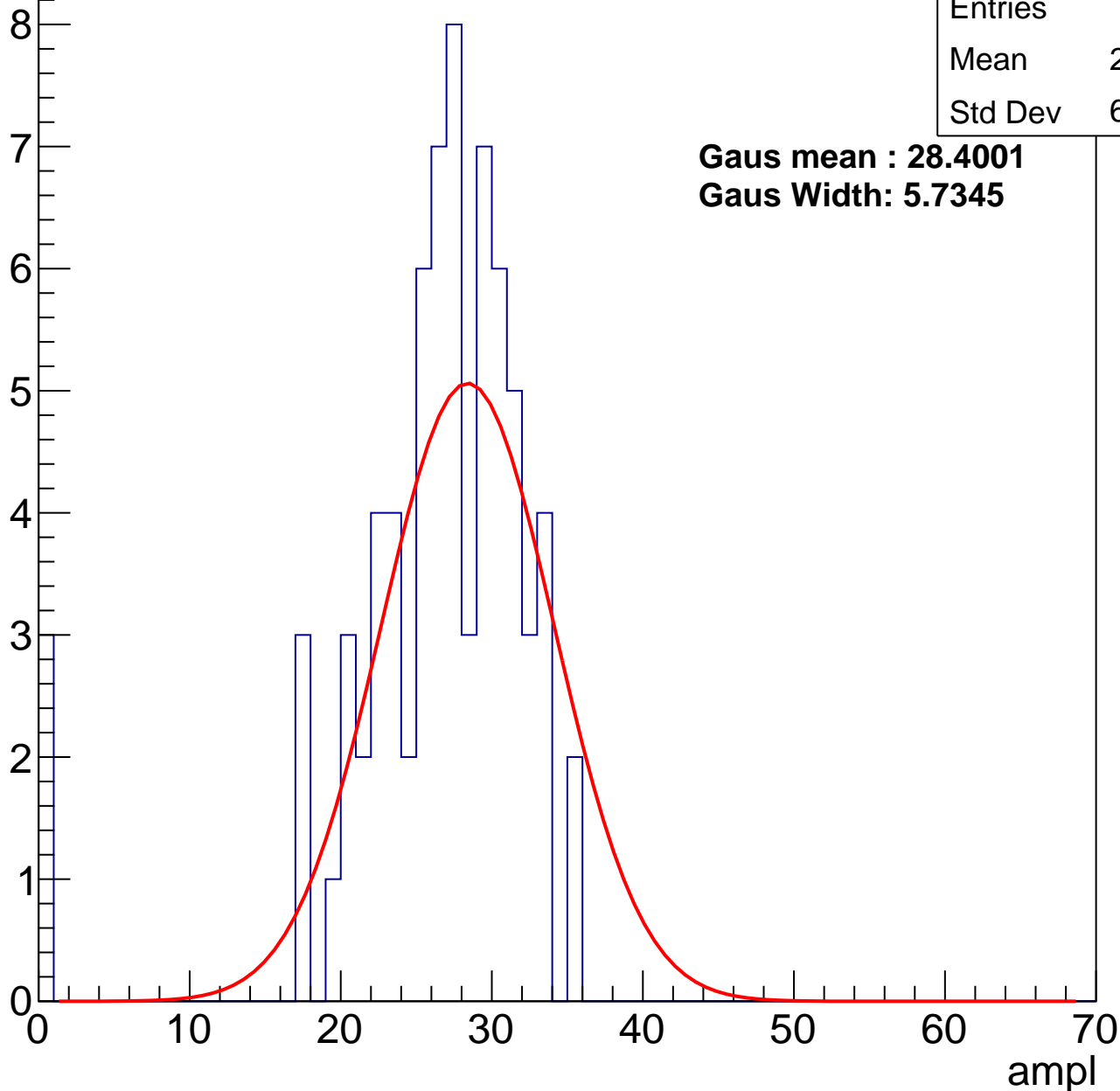
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	25.59
Std Dev	6.774

Gaus mean : 28.4001

Gaus Width: 5.7345



B0L102S, U5-ch94, adc1

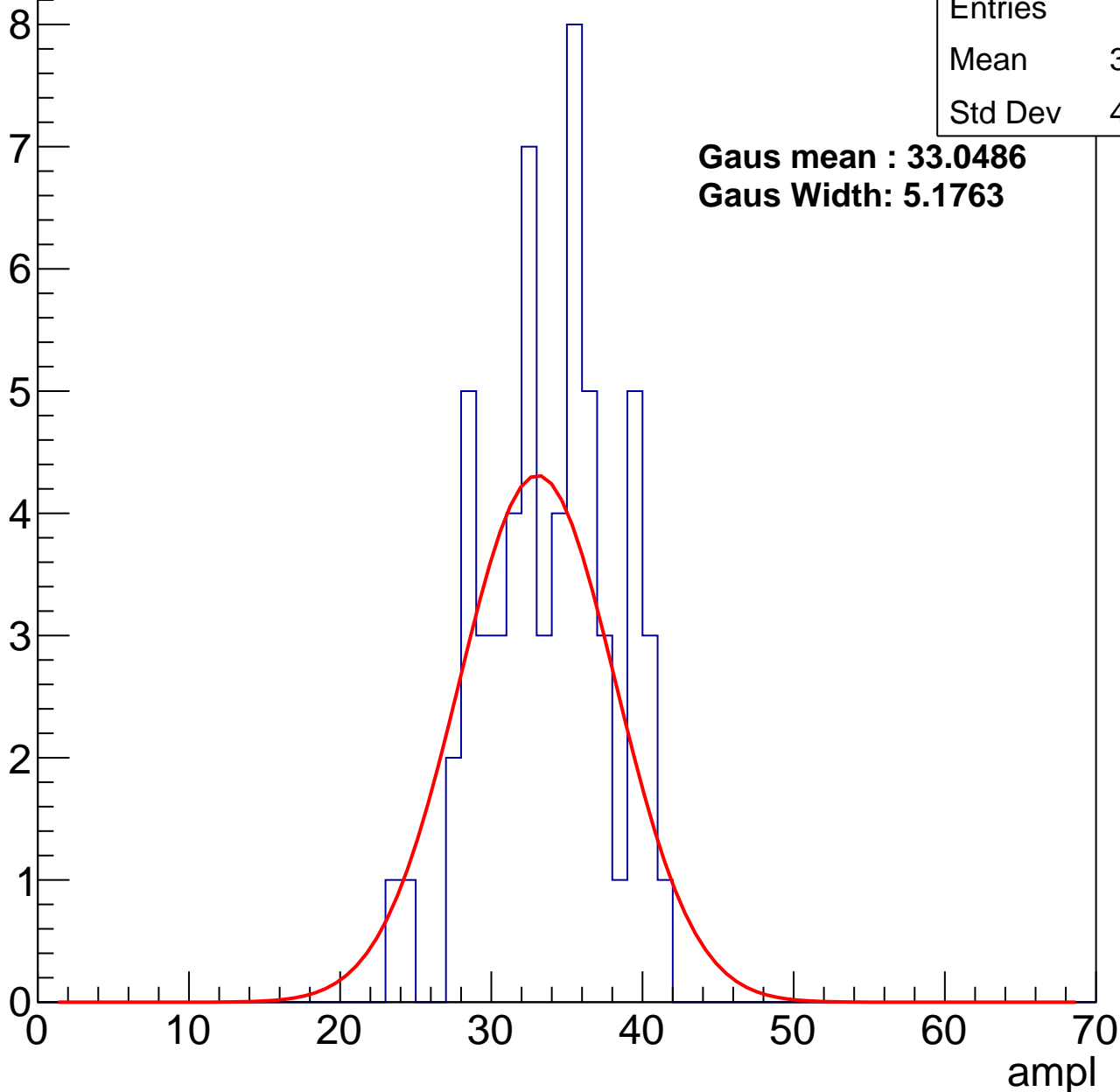
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	33.32
Std Dev	4.147

Gaus mean : 33.0486

Gaus Width: 5.1763



B0L102S, U5-ch94, adc2

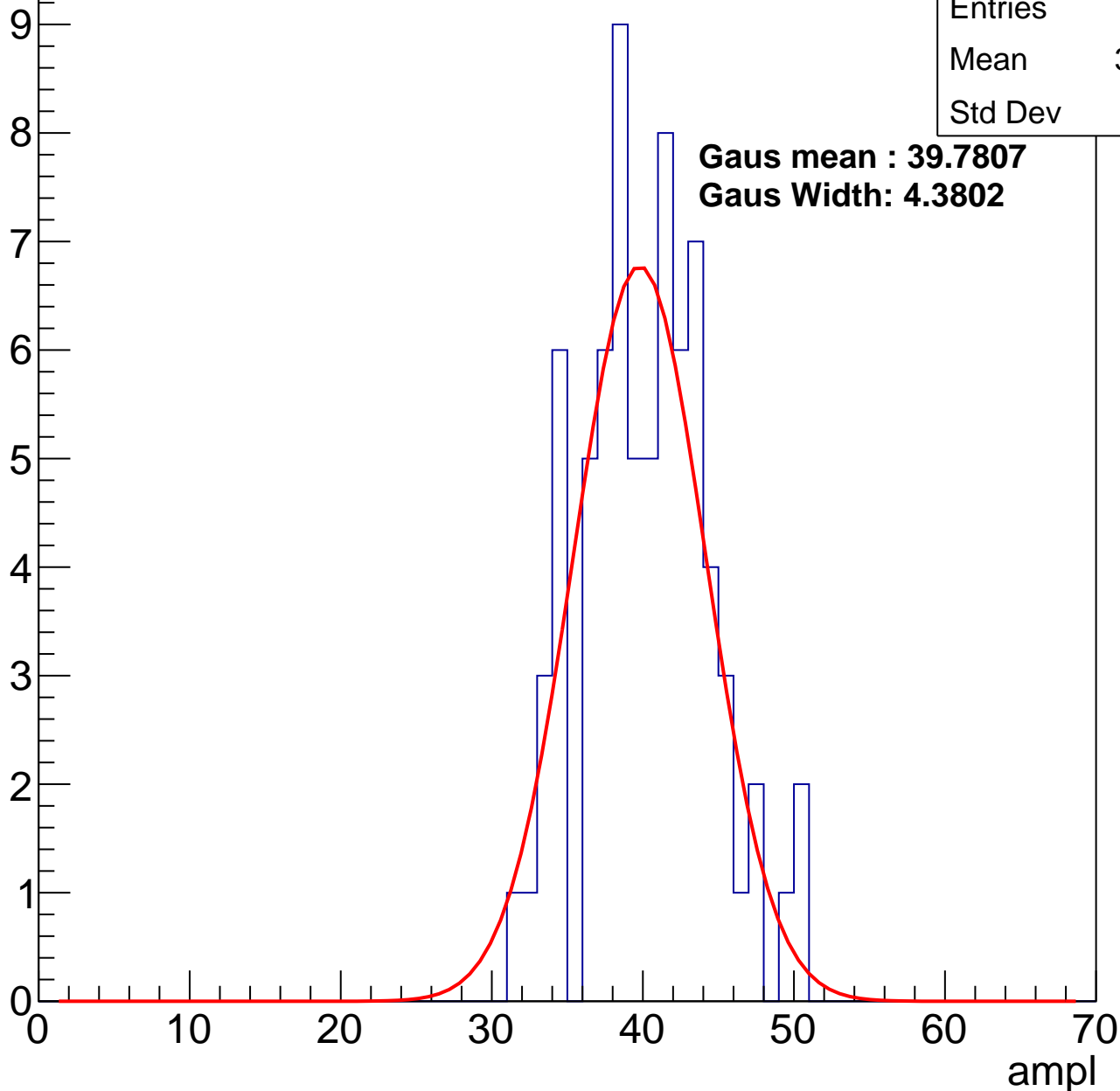
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	39.81
Std Dev	4.21

Gaus mean : 39.7807

Gaus Width: 4.3802

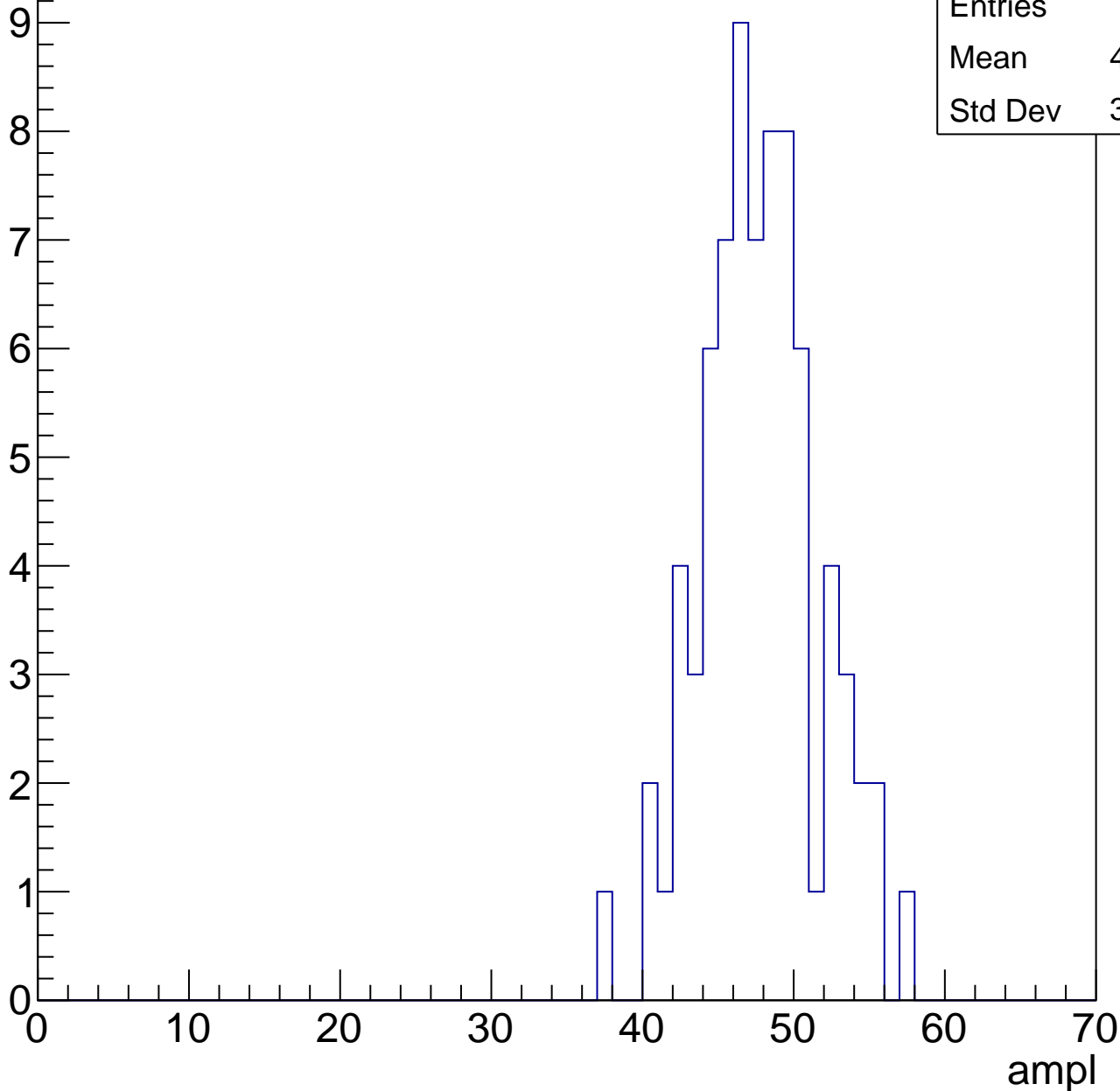


B0L102S, U5-ch94, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	47.28
Std Dev	3.849

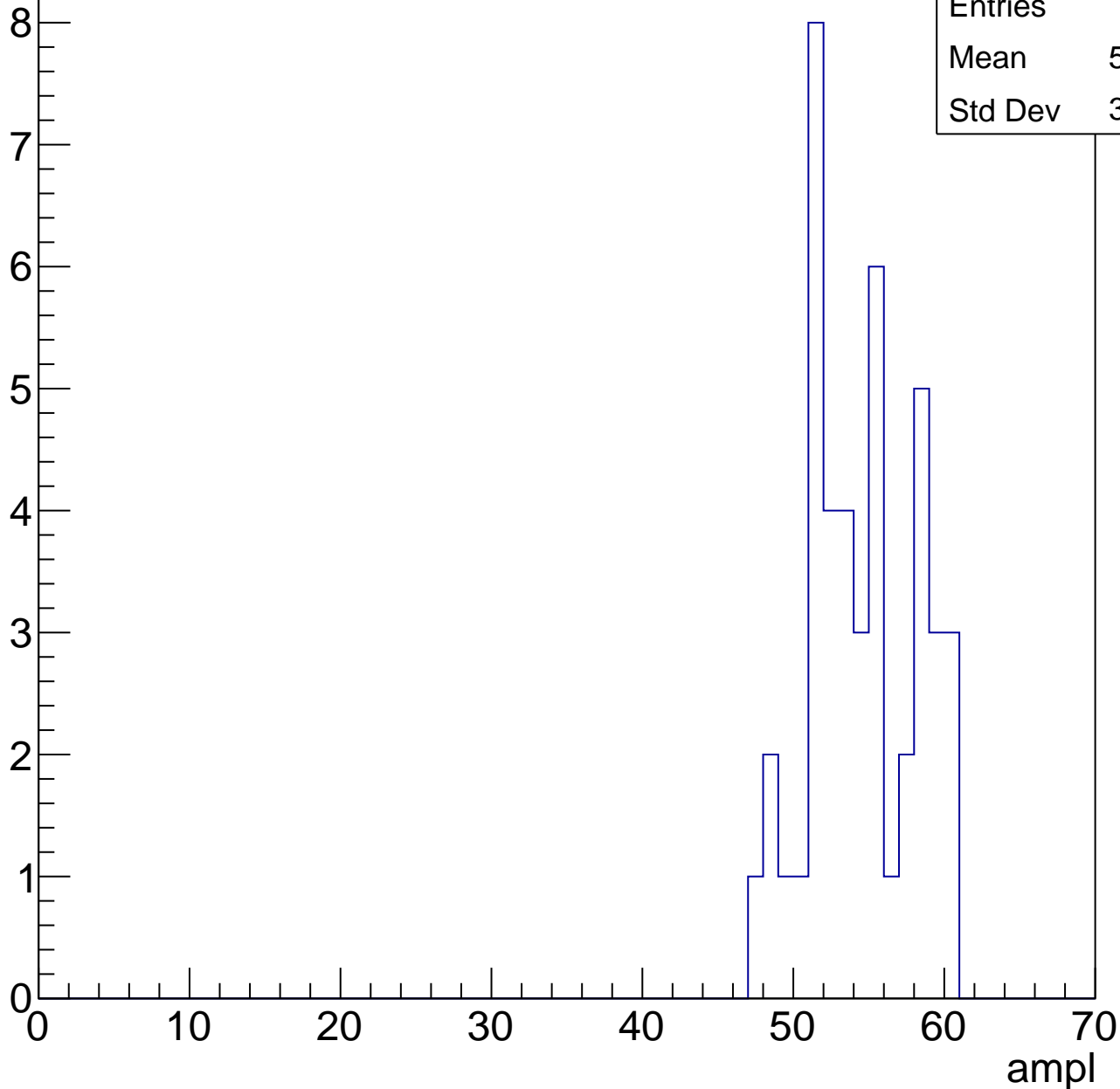


B0L102S, U5-ch94, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	44
Mean	54.07
Std Dev	3.519

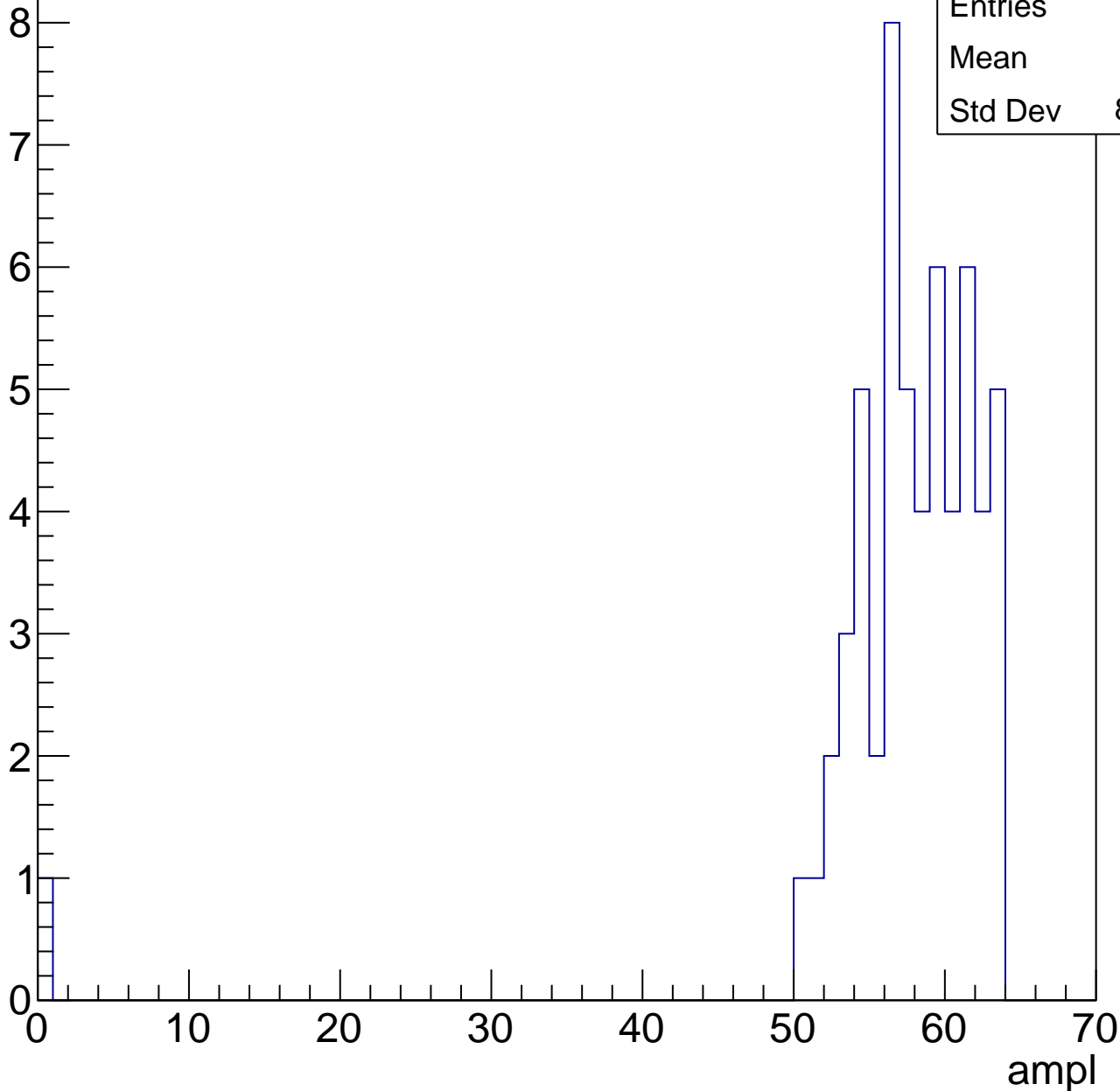


B0L102S, U5-ch94, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	56.7
Std Dev	8.301

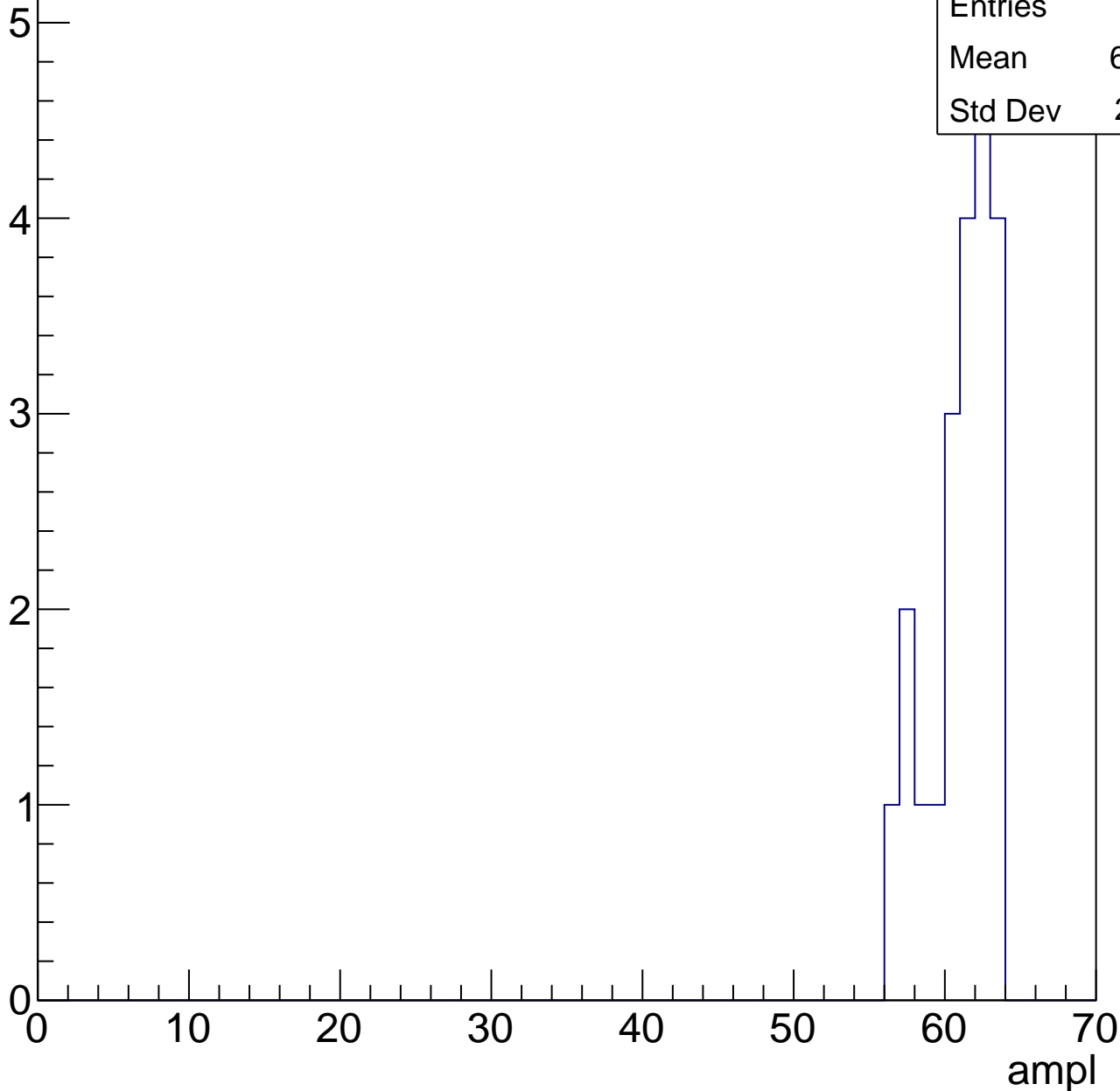


B0L102S, U5-ch94, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	21
Mean	60.62
Std Dev	2.081



B0L102S, U5-ch94, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch95, adc0

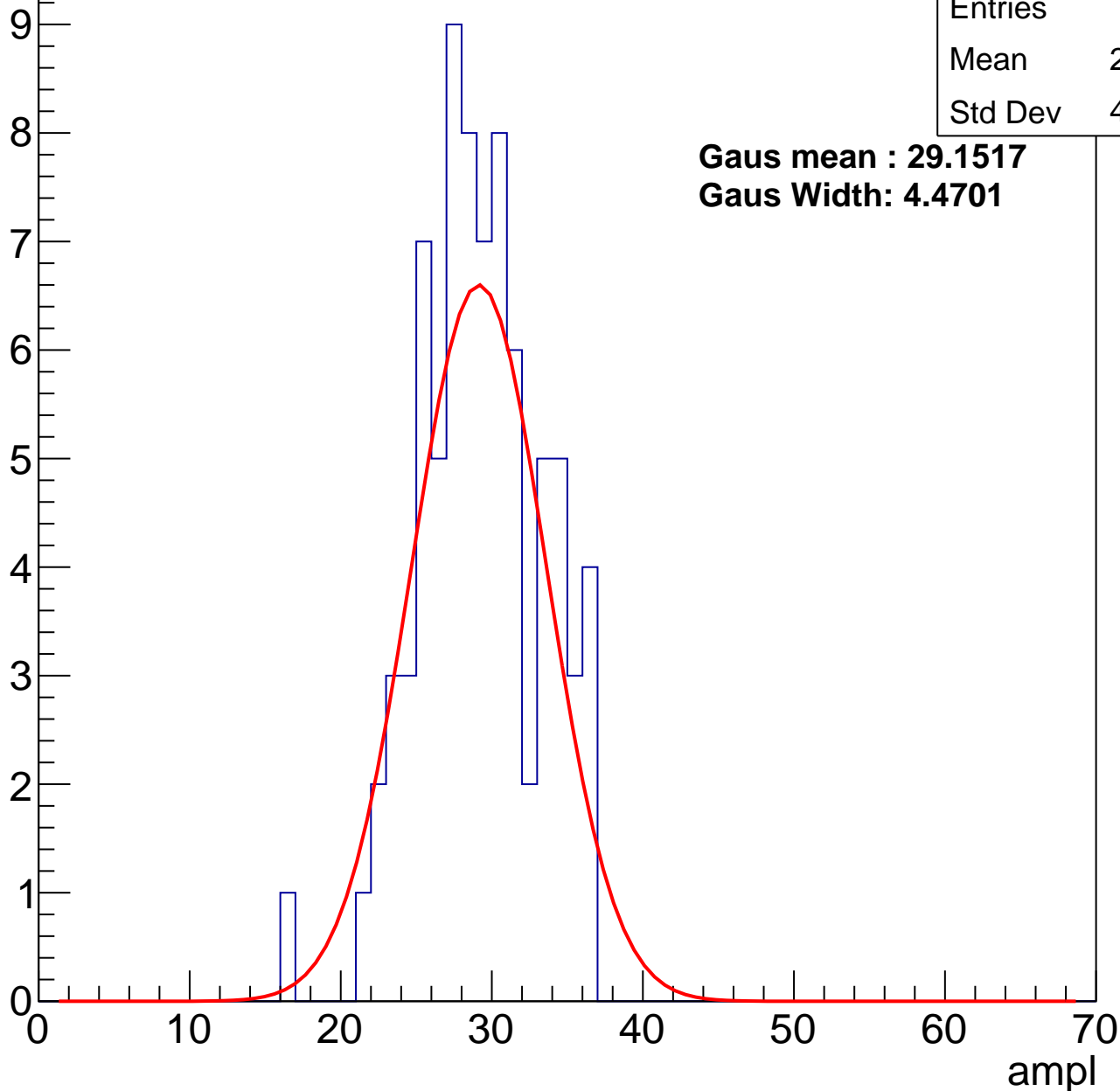
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	28.75
Std Dev	4.008

Gaus mean : 29.1517

Gaus Width: 4.4701



B0L102S, U5-ch95, adc1

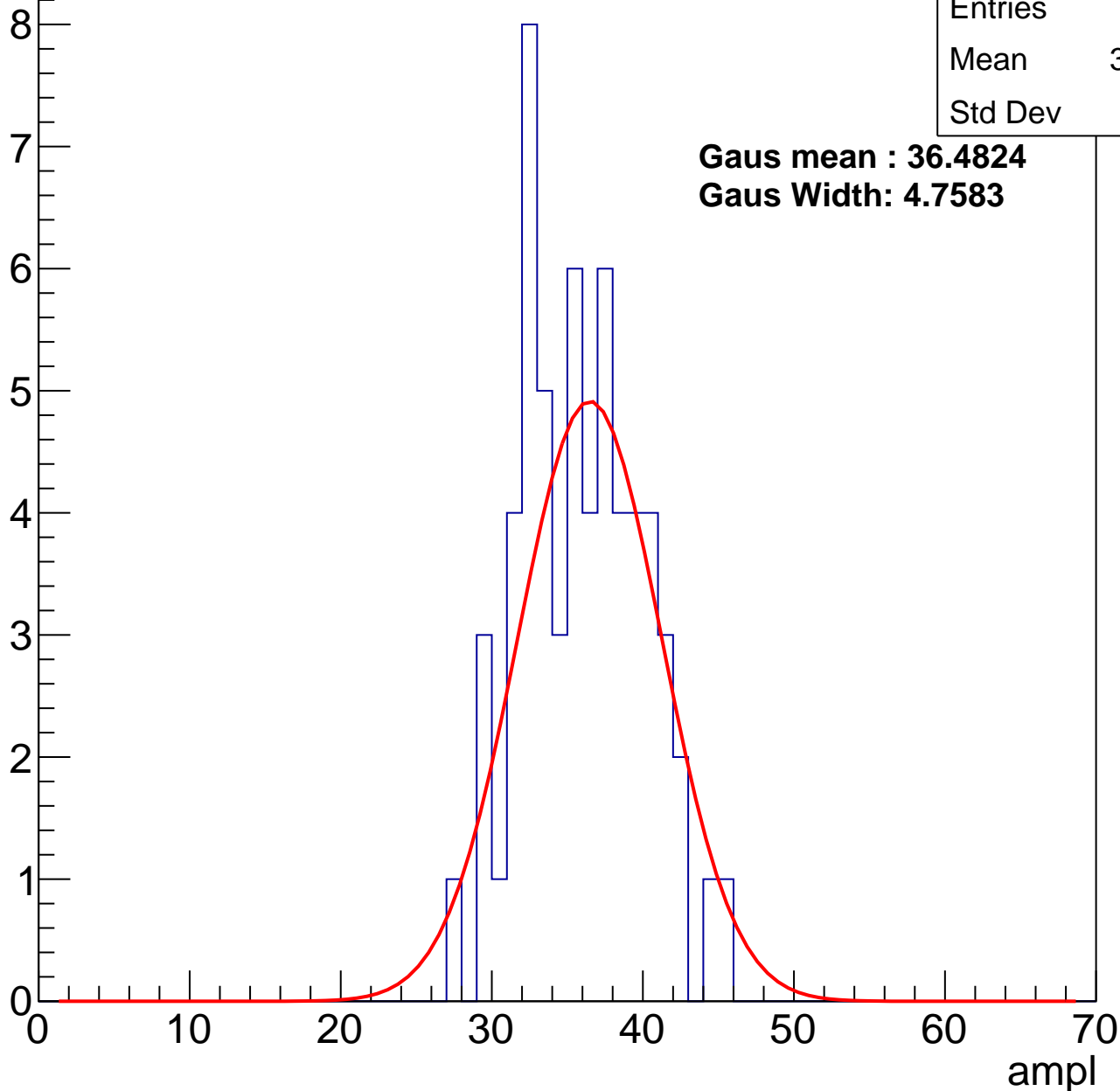
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	35.52
Std Dev	3.99

Gaus mean : 36.4824

Gaus Width: 4.7583



B0L102S, U5-ch95, adc2

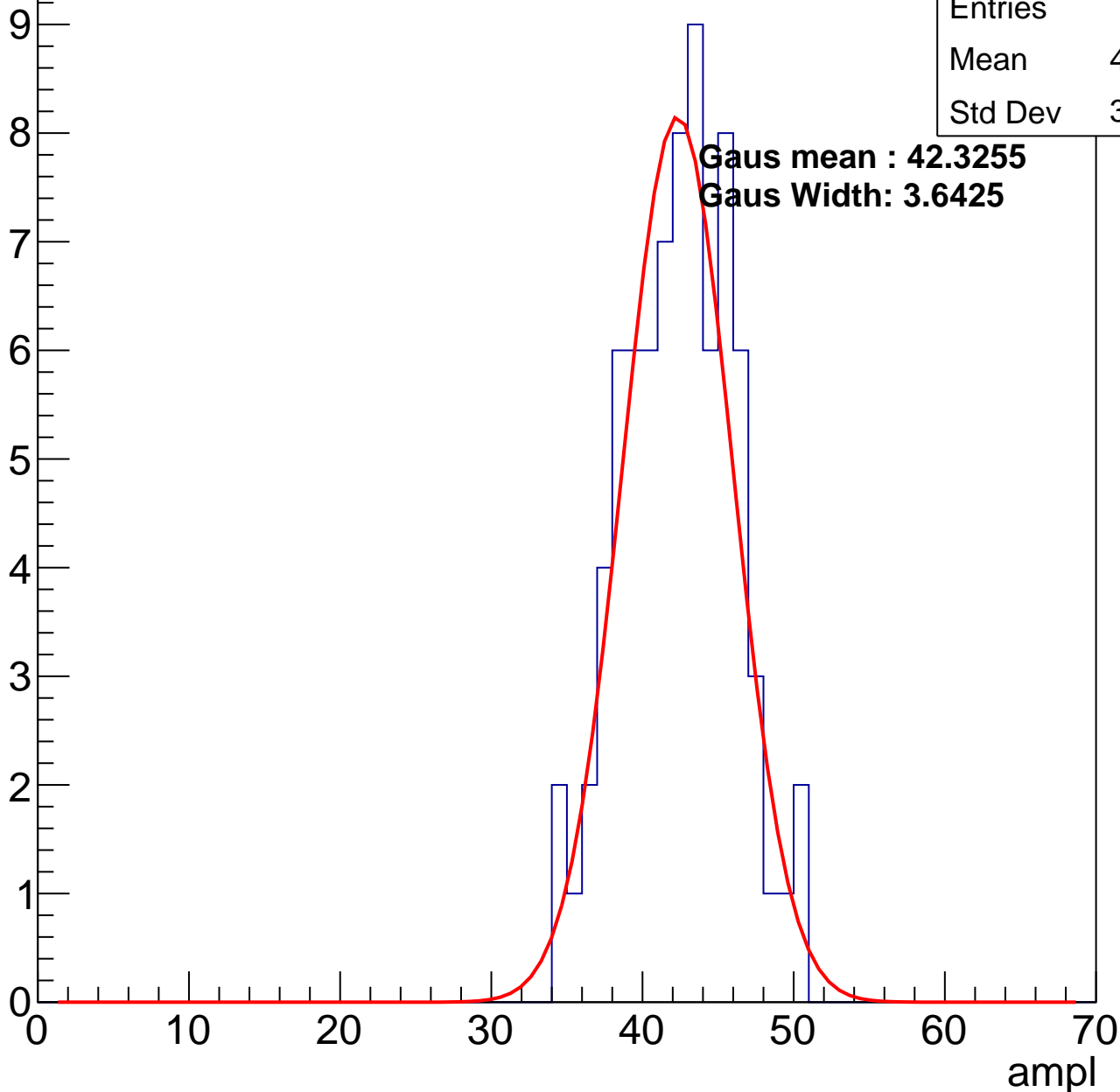
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	41.96
Std Dev	3.614

Gaus mean : 42.3255

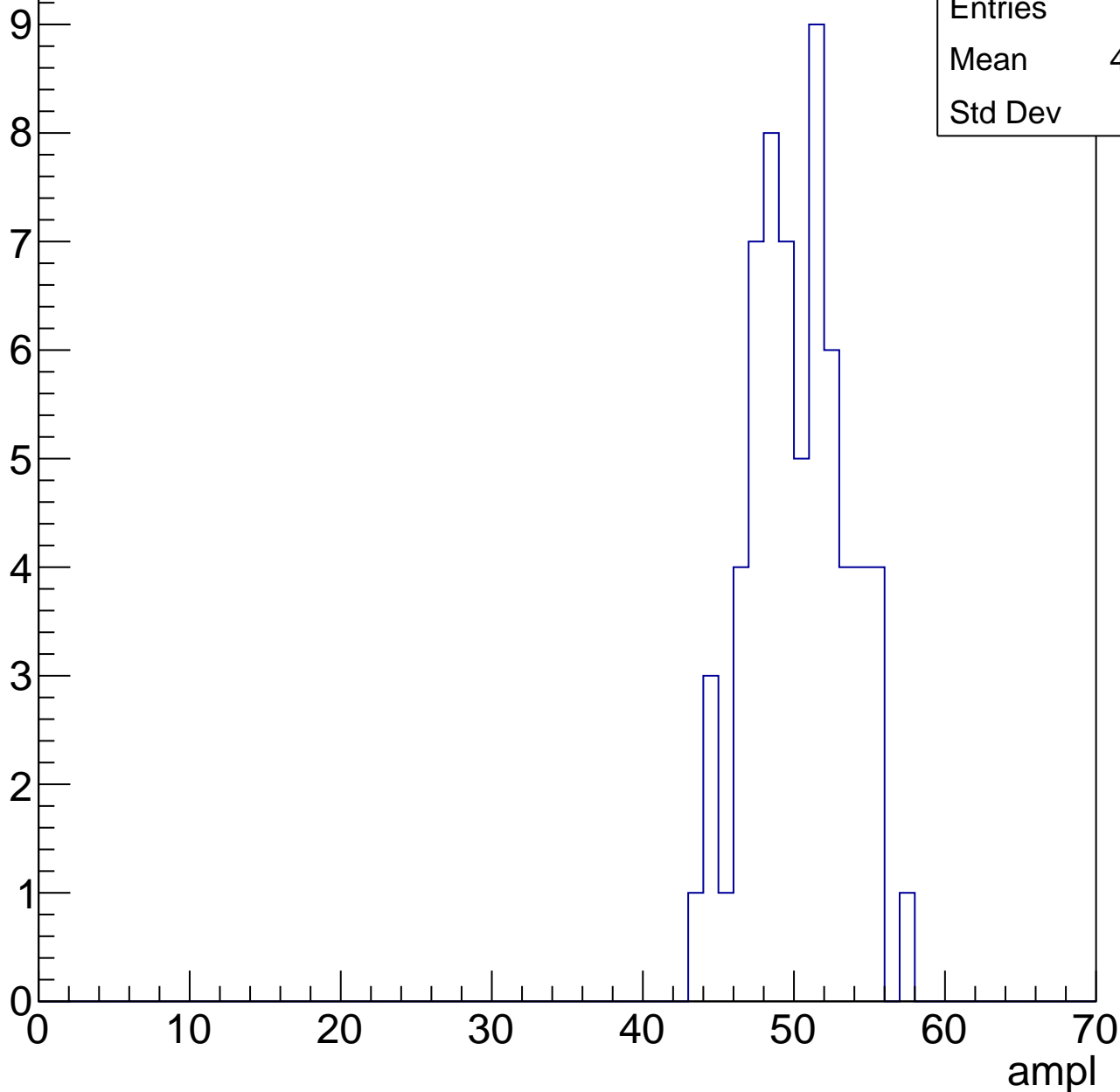
Gaus Width: 3.6425



B0L102S, U5-ch95, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry



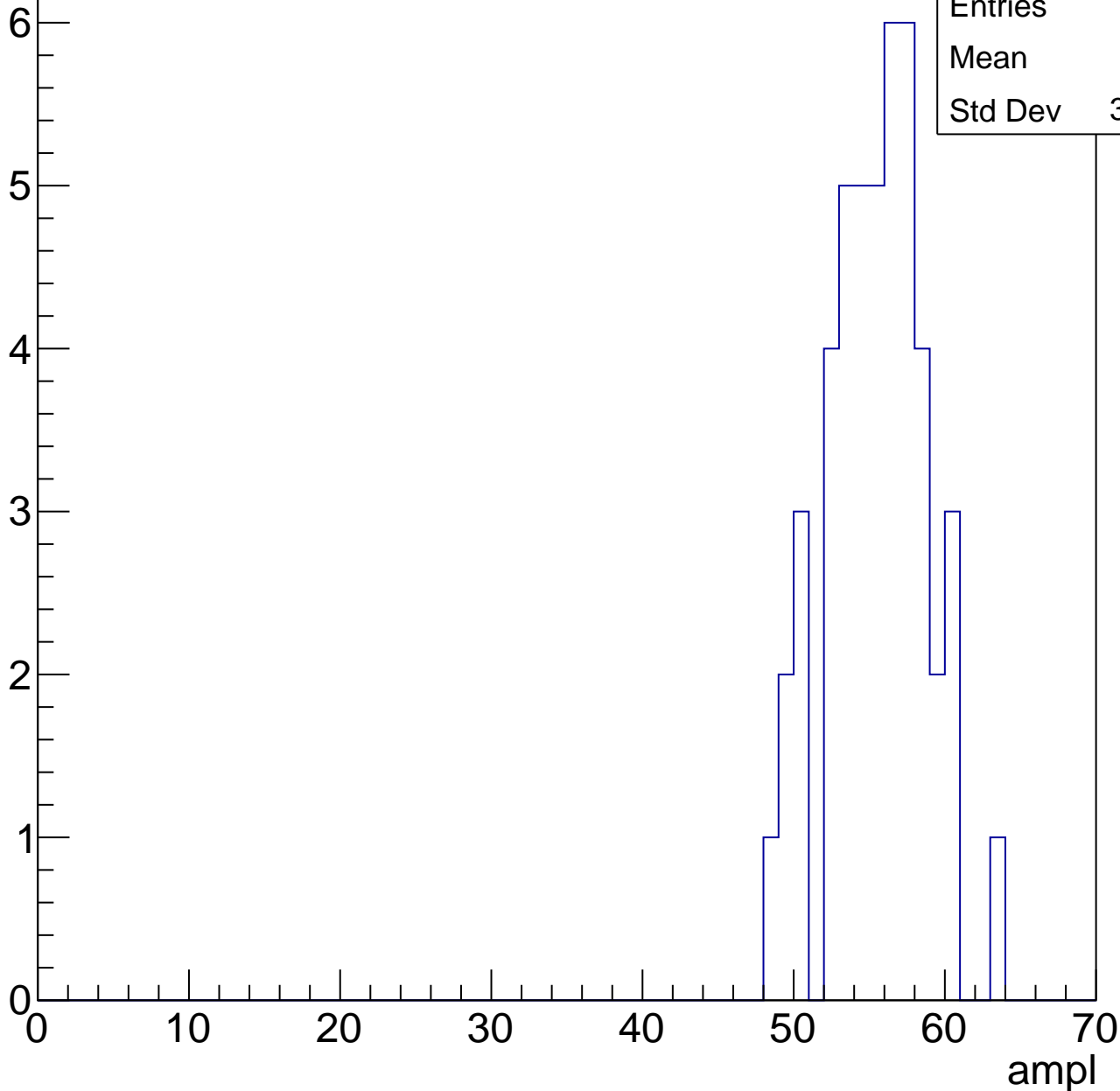
Entries	64
Mean	49.78
Std Dev	3.13

B0L102S, U5-ch95, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	55
Std Dev	3.242

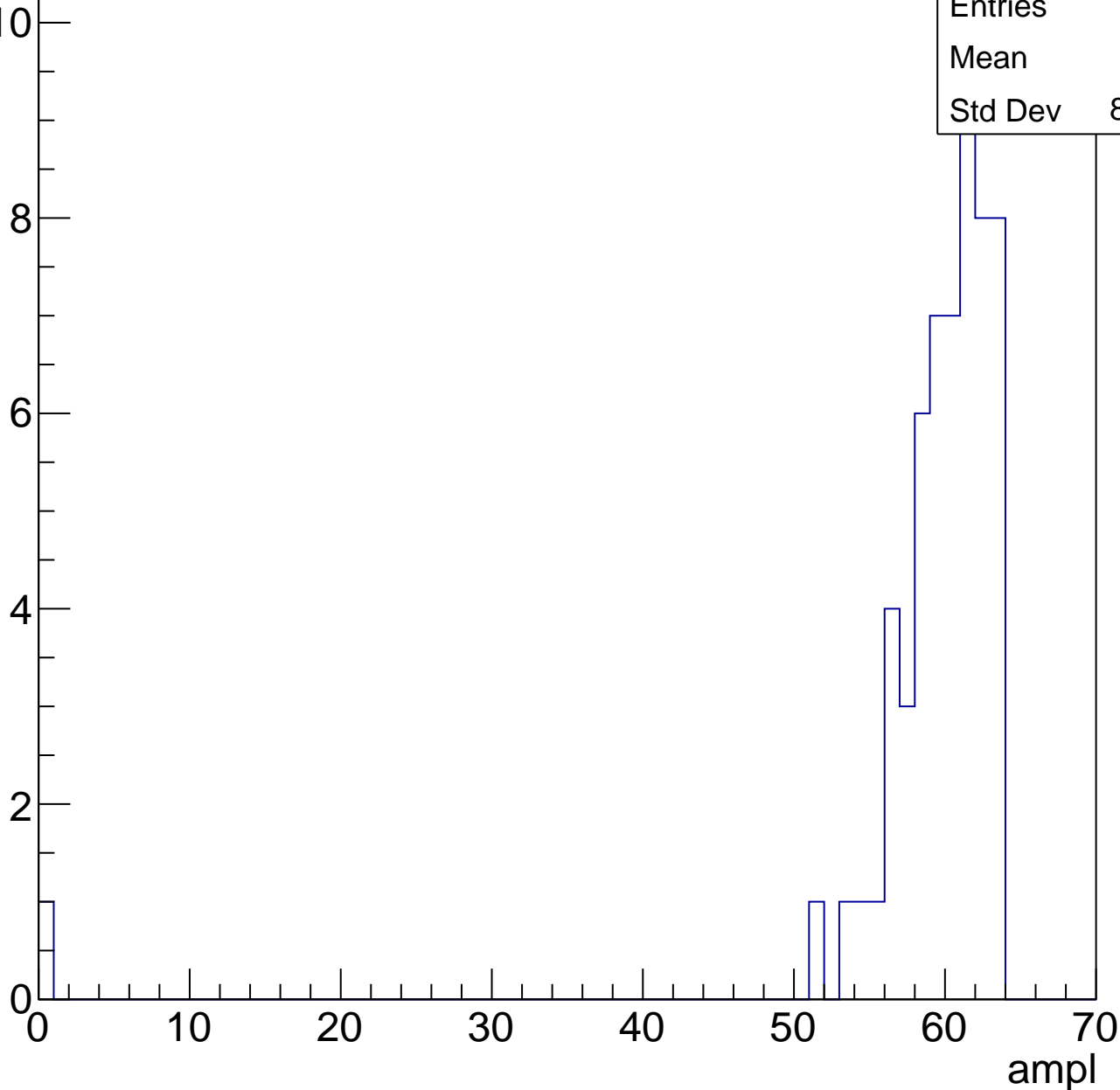


B0L102S, U5-ch95, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

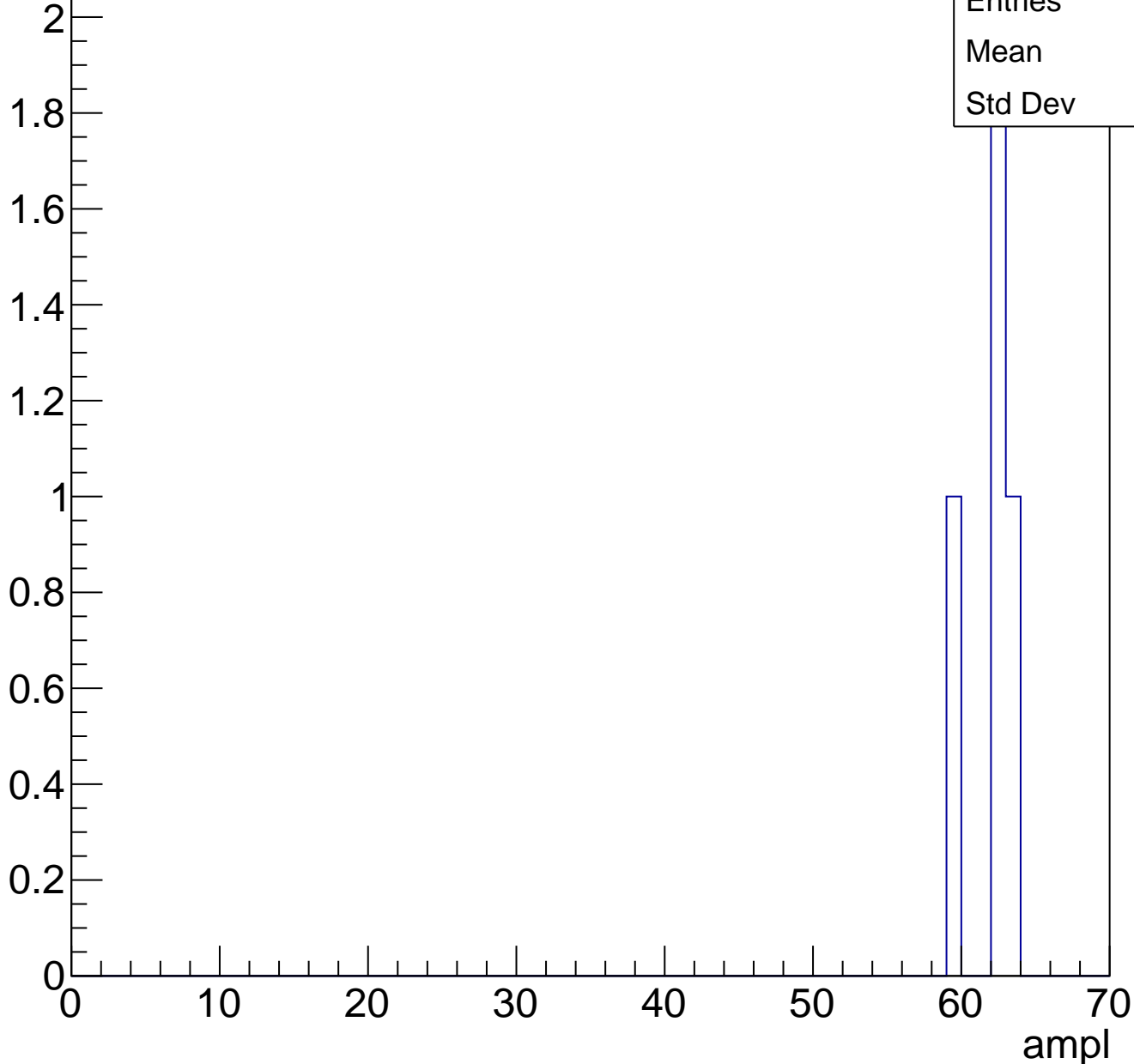
Entries	58
Mean	58.6
Std Dev	8.215



B0L102S, U5-ch95, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch95, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch96, adc0

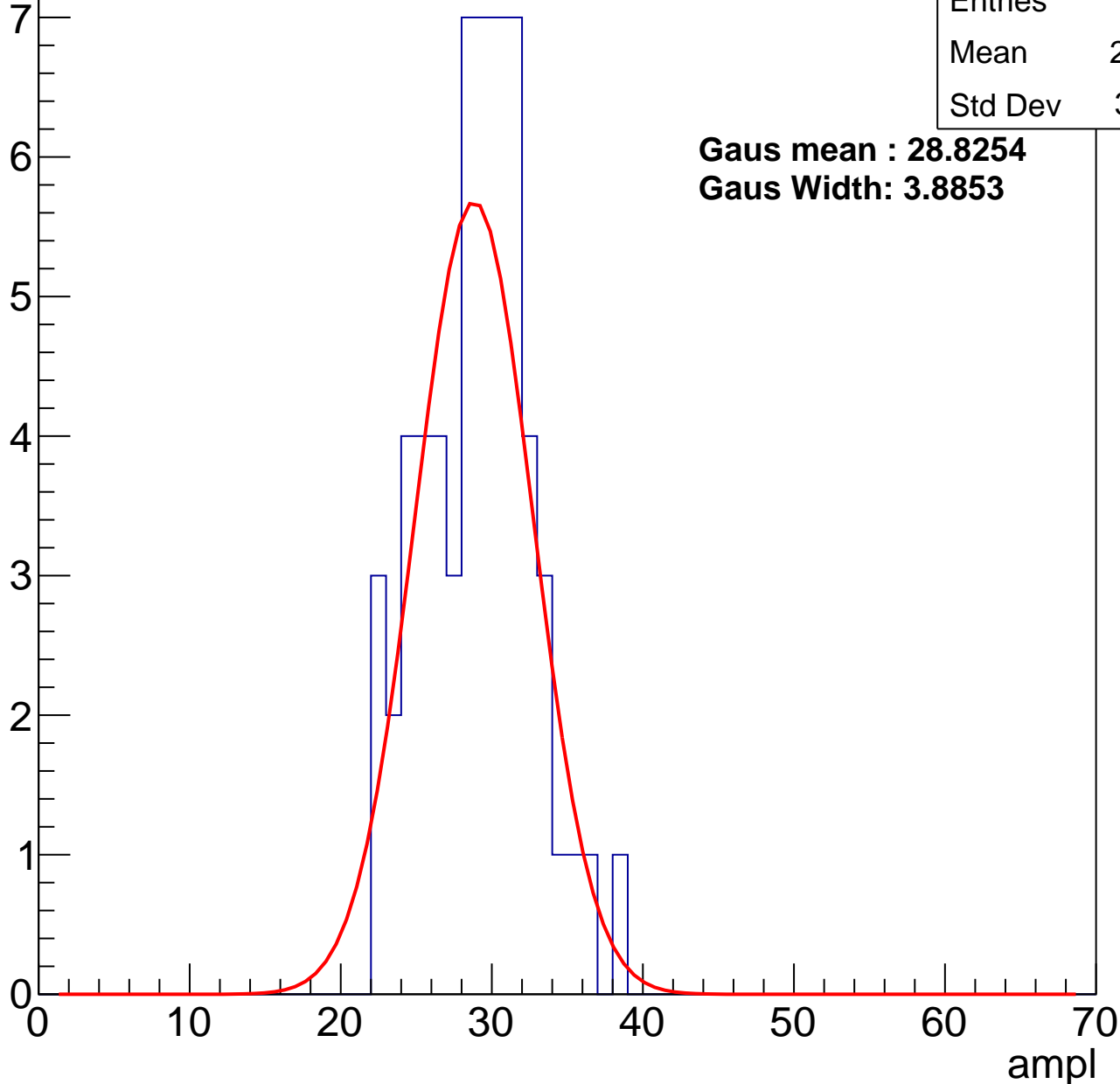
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	59
Mean	28.63
Std Dev	3.541

Gaus mean : 28.8254

Gaus Width: 3.8853



B0L102S, U5-ch96, adc1

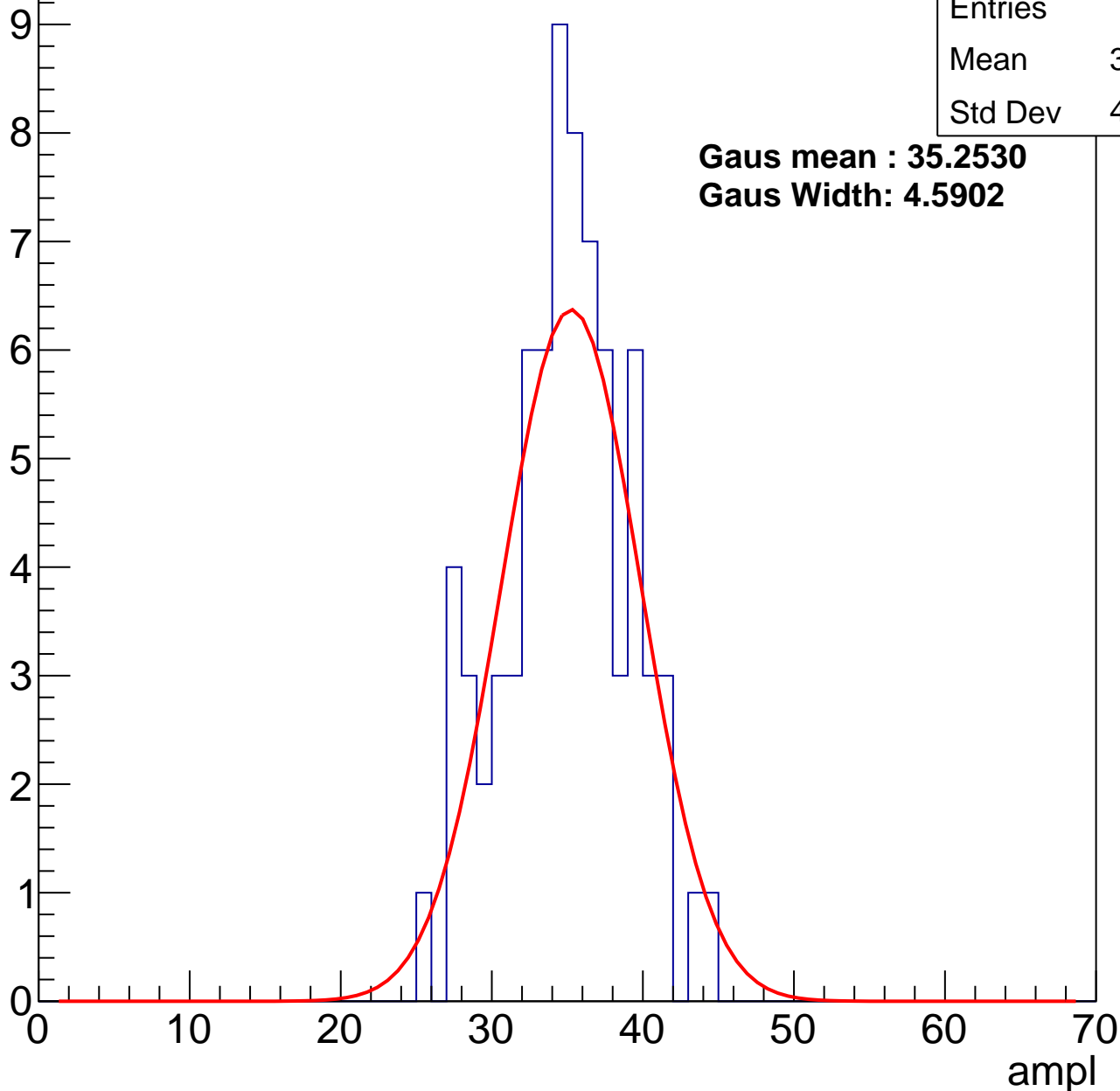
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	34.48
Std Dev	4.077

Gaus mean : 35.2530

Gaus Width: 4.5902

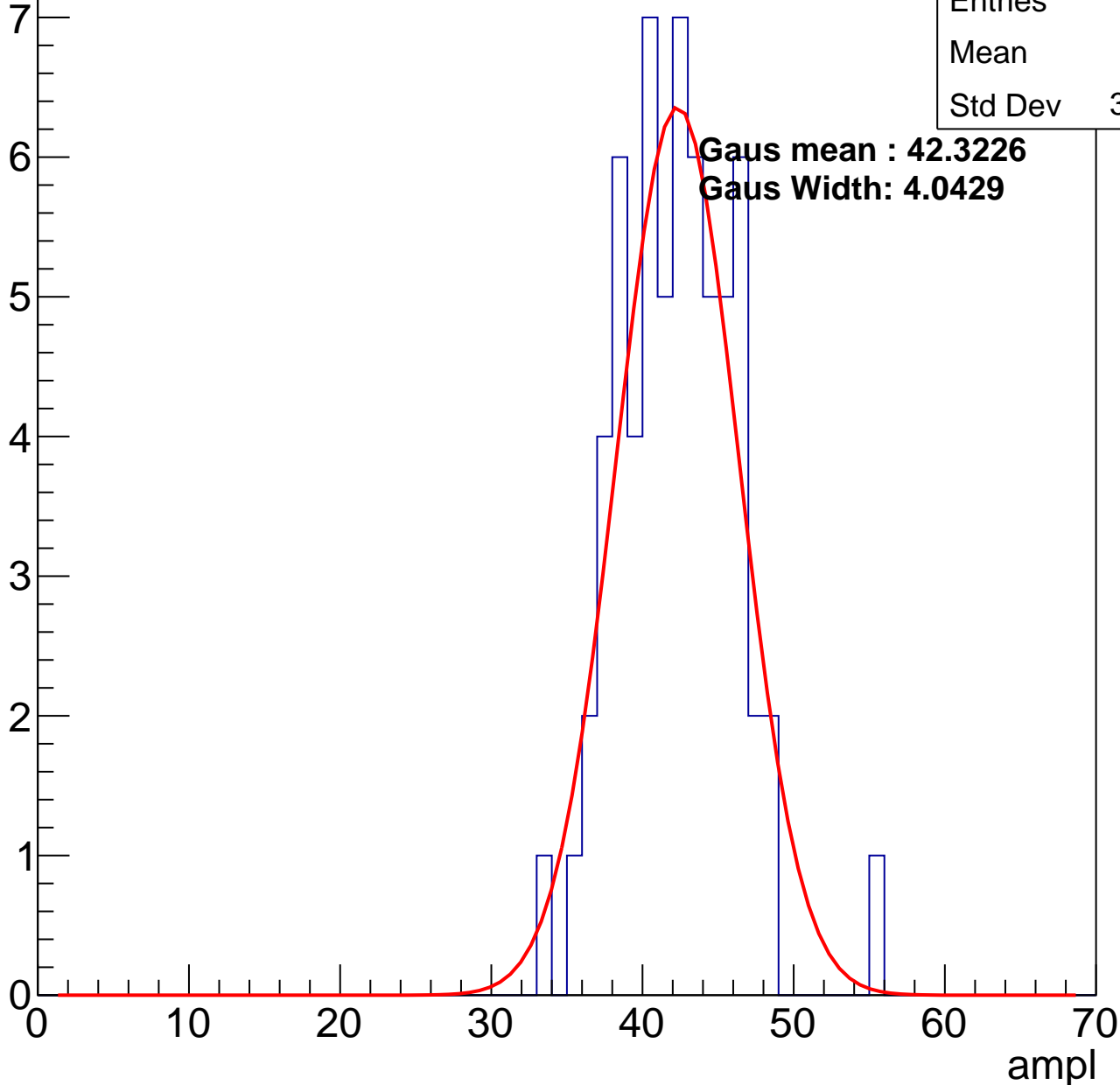


B0L102S, U5-ch96, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	41.8
Std Dev	3.796

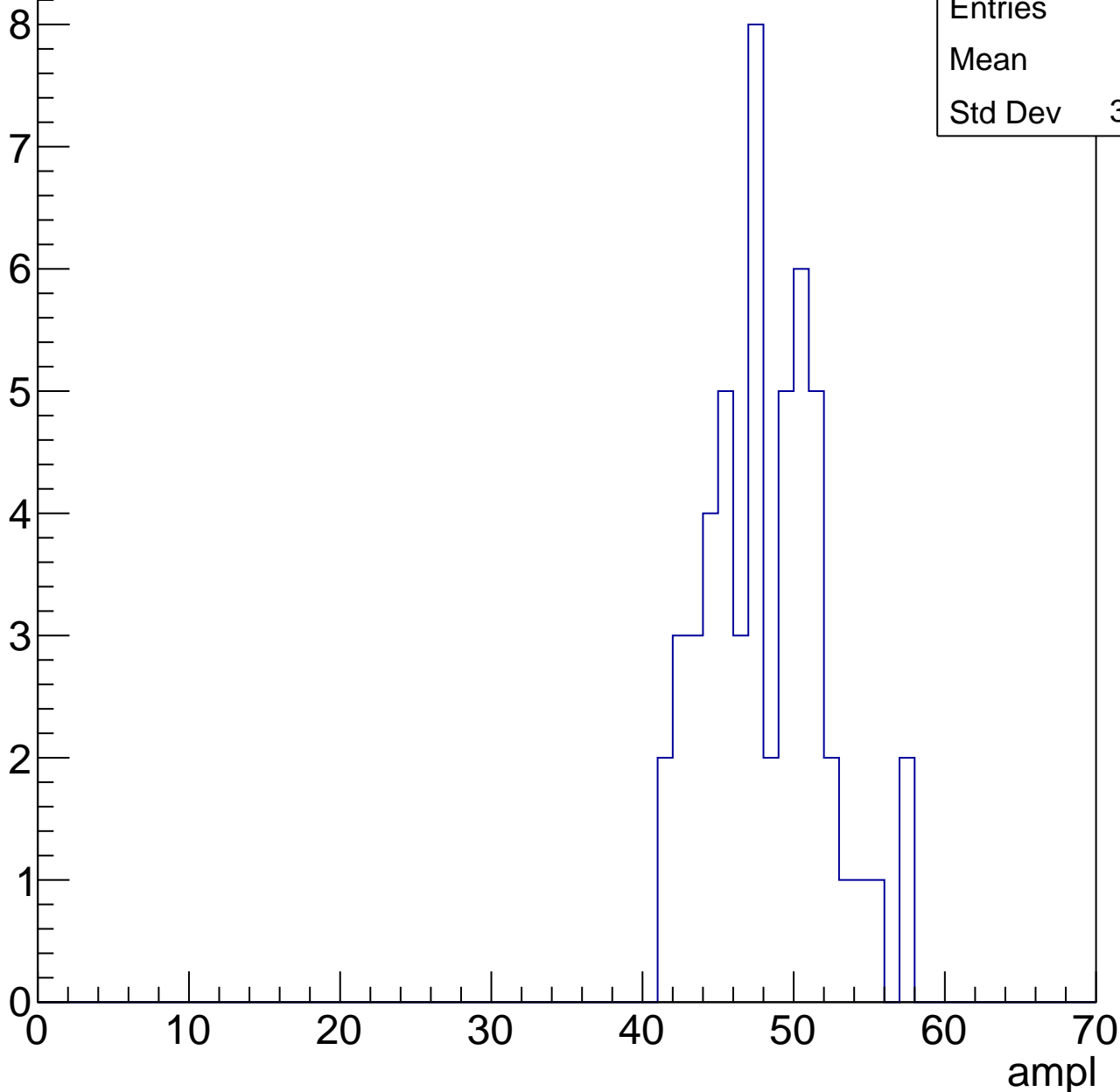


B0L102S, U5-ch96, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	47.7
Std Dev	3.844

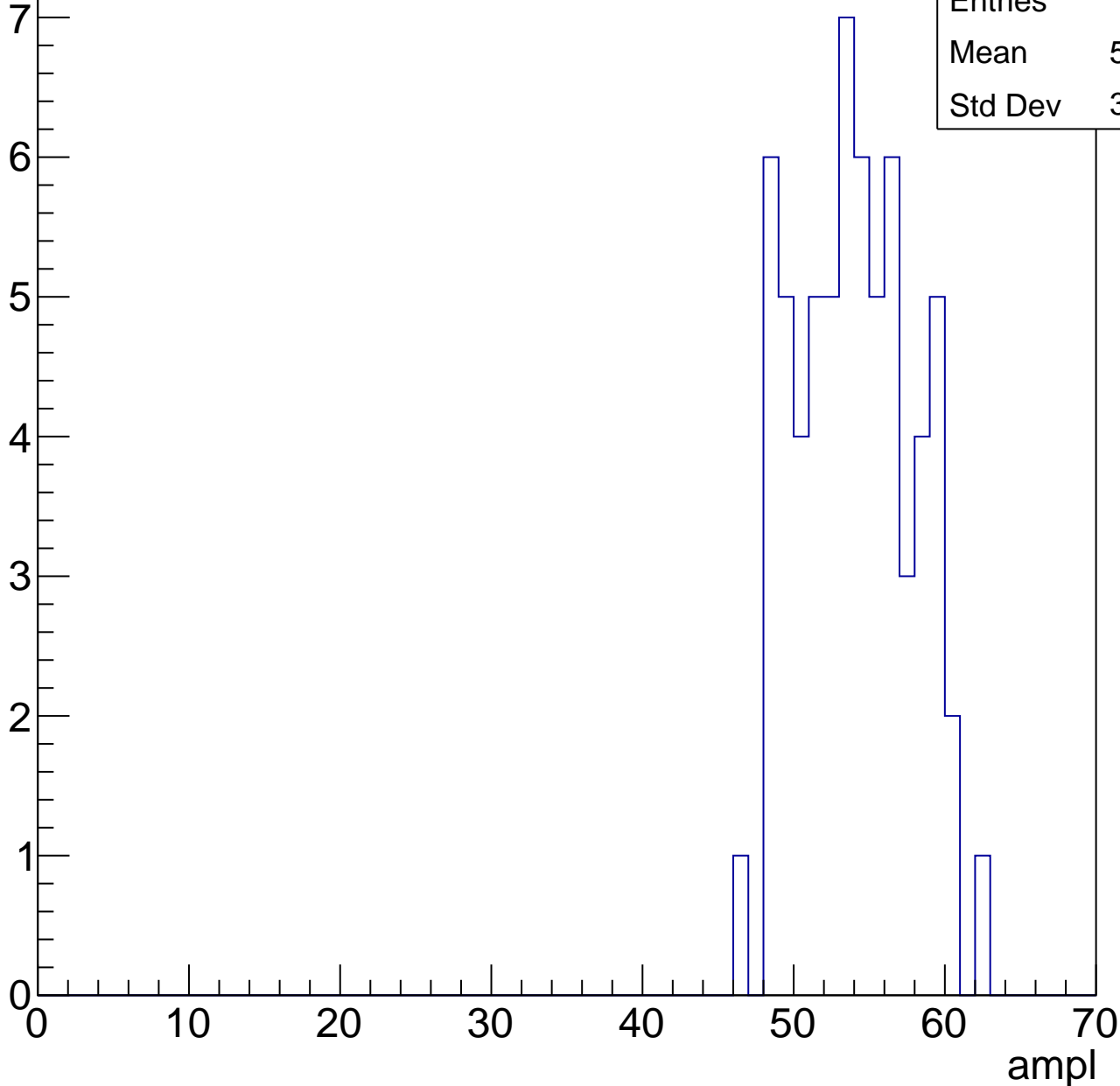


B0L102S, U5-ch96, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	53.54
Std Dev	3.738



B0L102S, U5-ch96, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

7

6

5

4

3

2

1

0

Entries	48
Mean	57.94
Std Dev	2.989

ampl

0

10

20

30

40

50

60

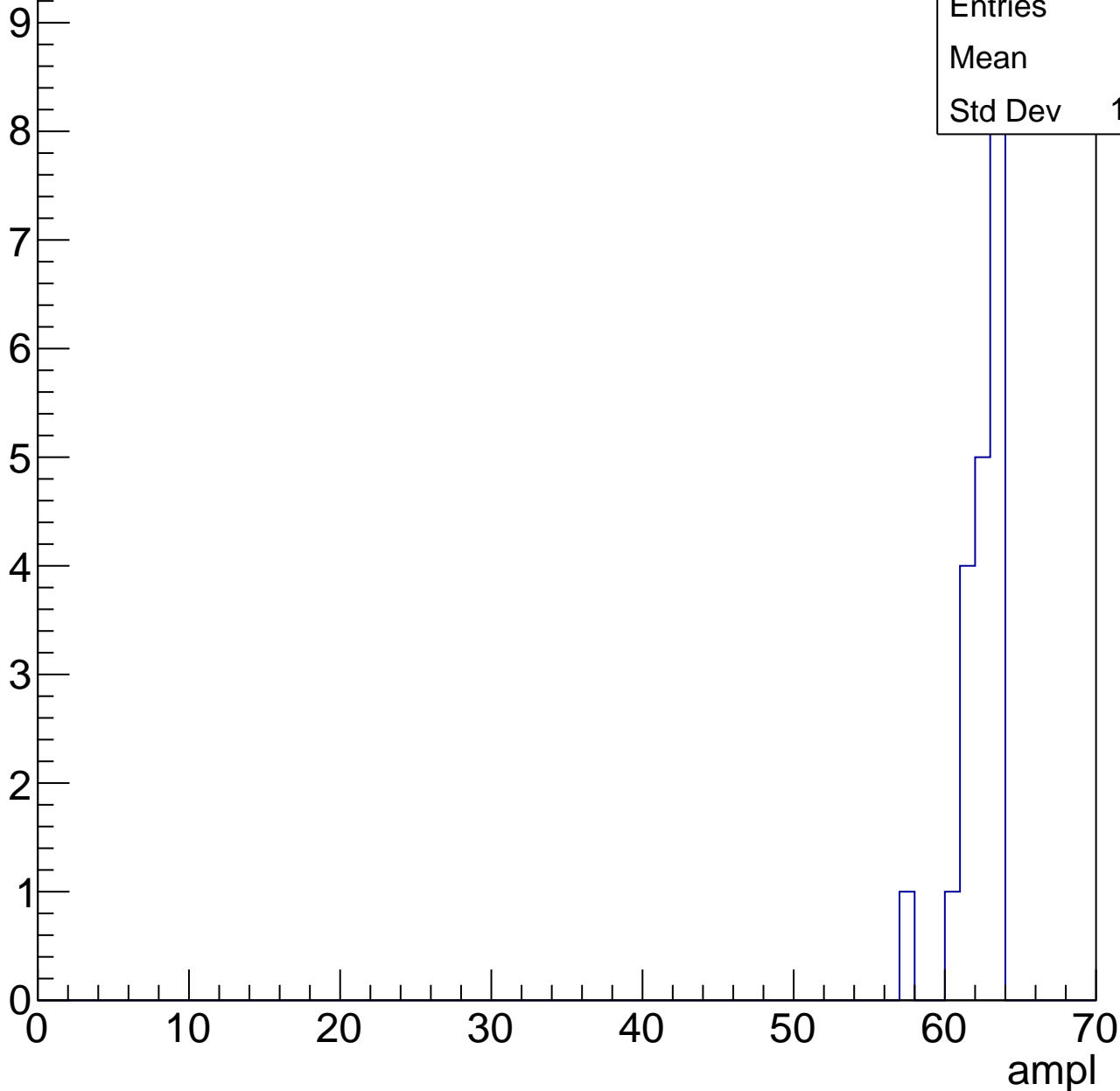
70

B0L102S, U5-ch96, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	20
Mean	61.9
Std Dev	1.446



B0L102S, U5-ch96, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch97, adc0

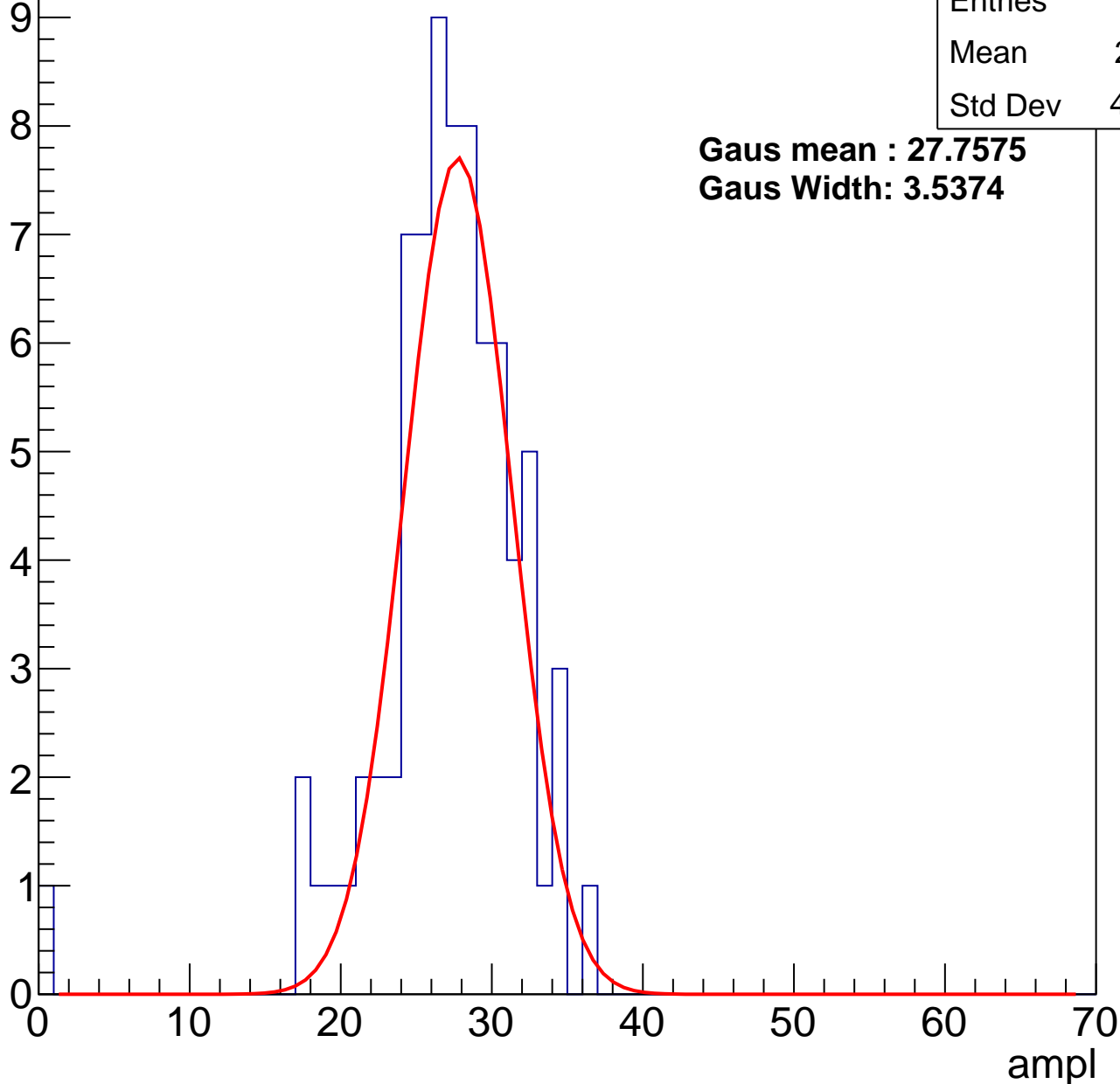
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	26.61
Std Dev	4.973

Gaus mean : 27.7575

Gaus Width: 3.5374



B0L102S, U5-ch97, adc1

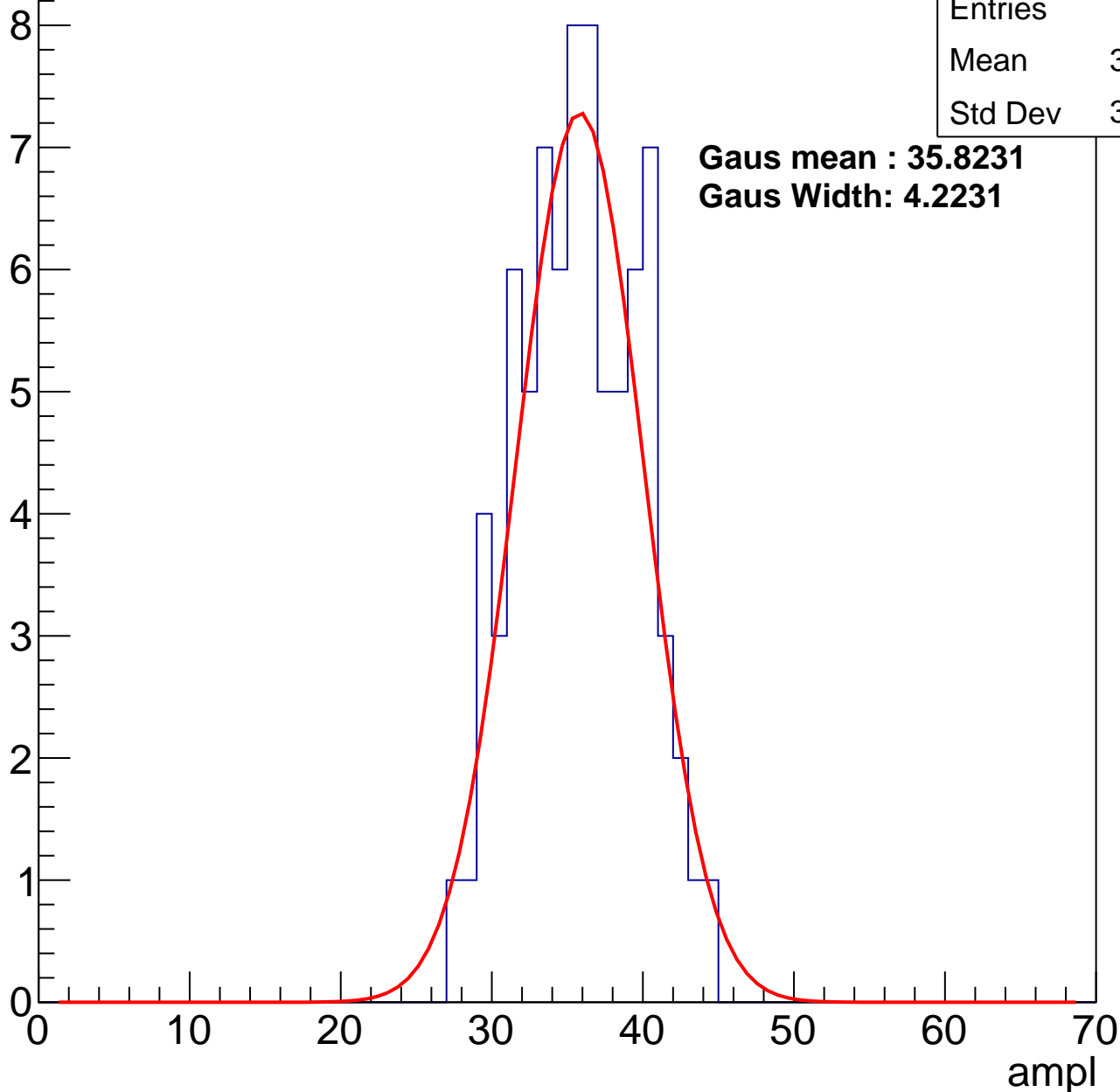
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.35
Std Dev	3.885

Gaus mean : 35.8231

Gaus Width: 4.2231



B0L102S, U5-ch97, adc2

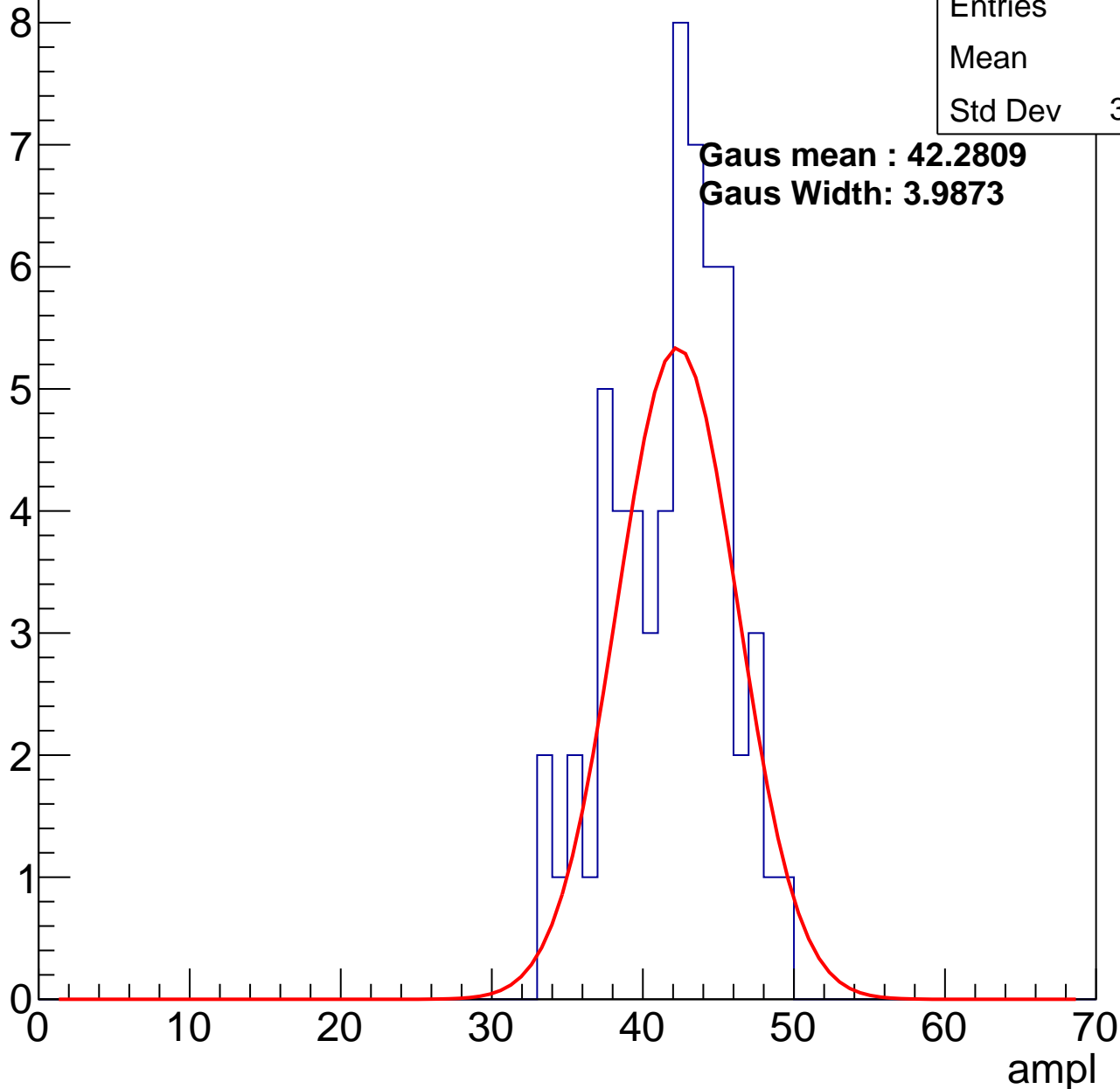
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	41.4
Std Dev	3.778

Gaus mean : 42.2809

Gaus Width: 3.9873

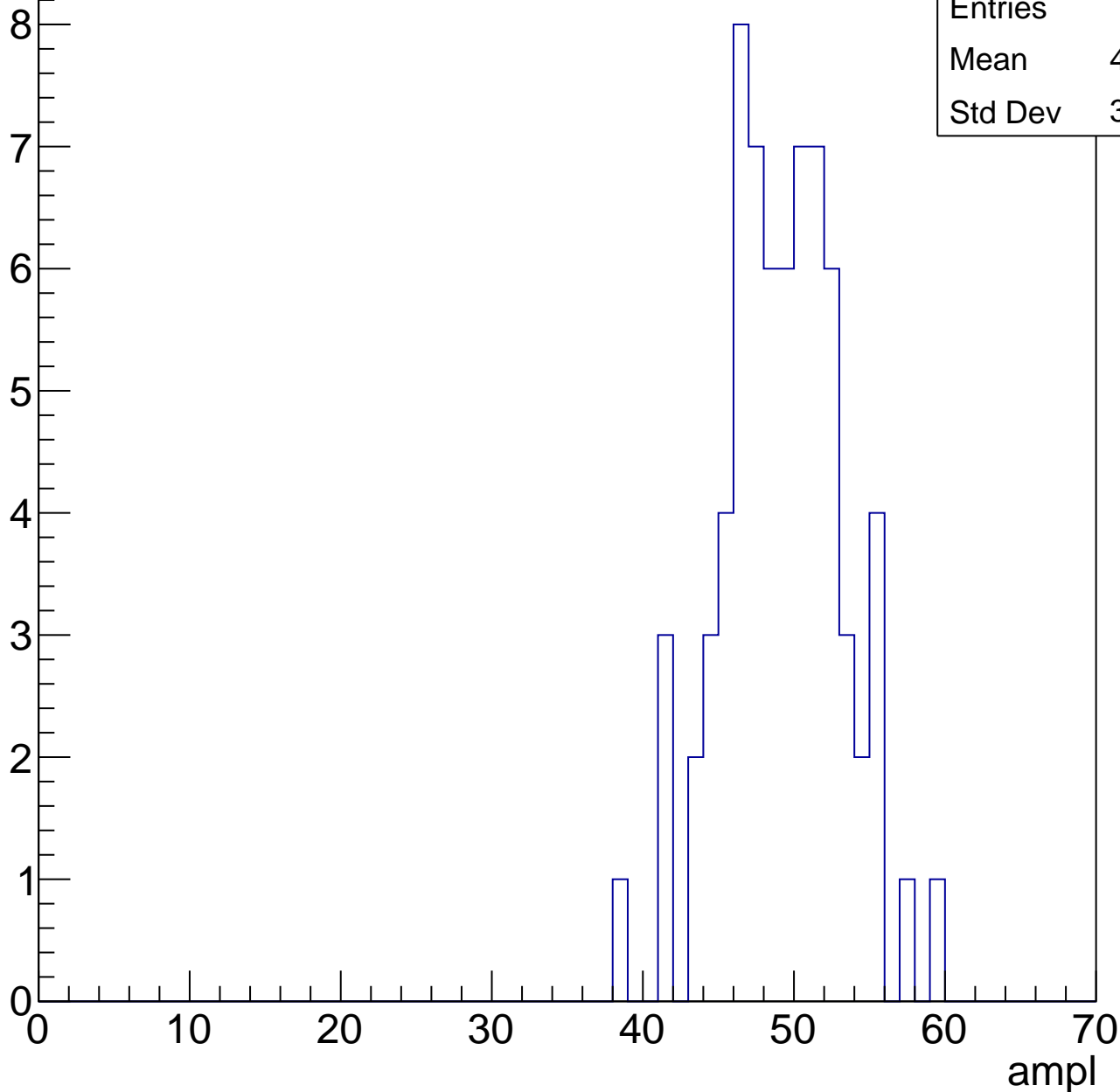


B0L102S, U5-ch97, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

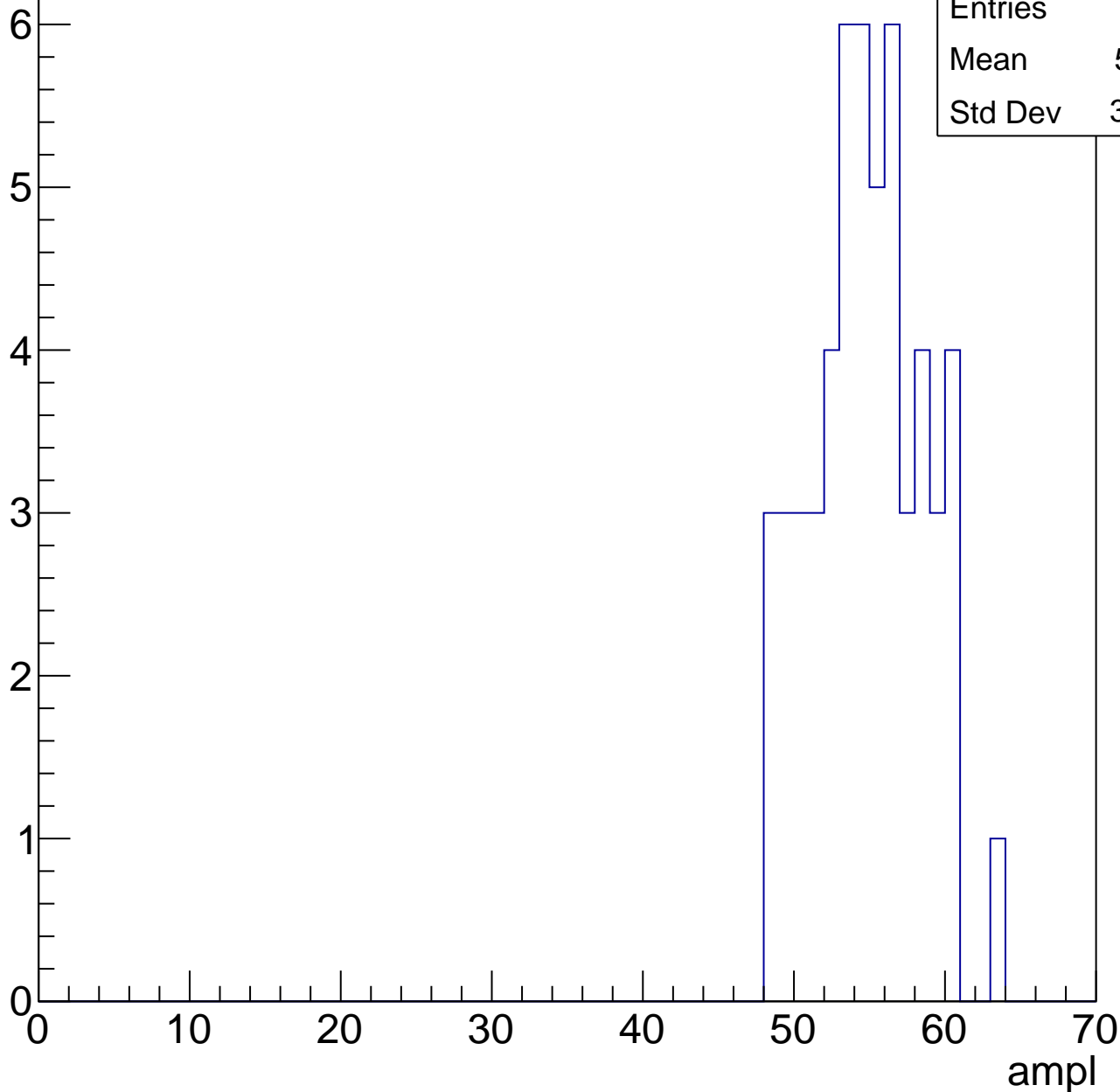
Entries	71
Mean	48.73
Std Dev	3.972



B0L102S, U5-ch97, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry



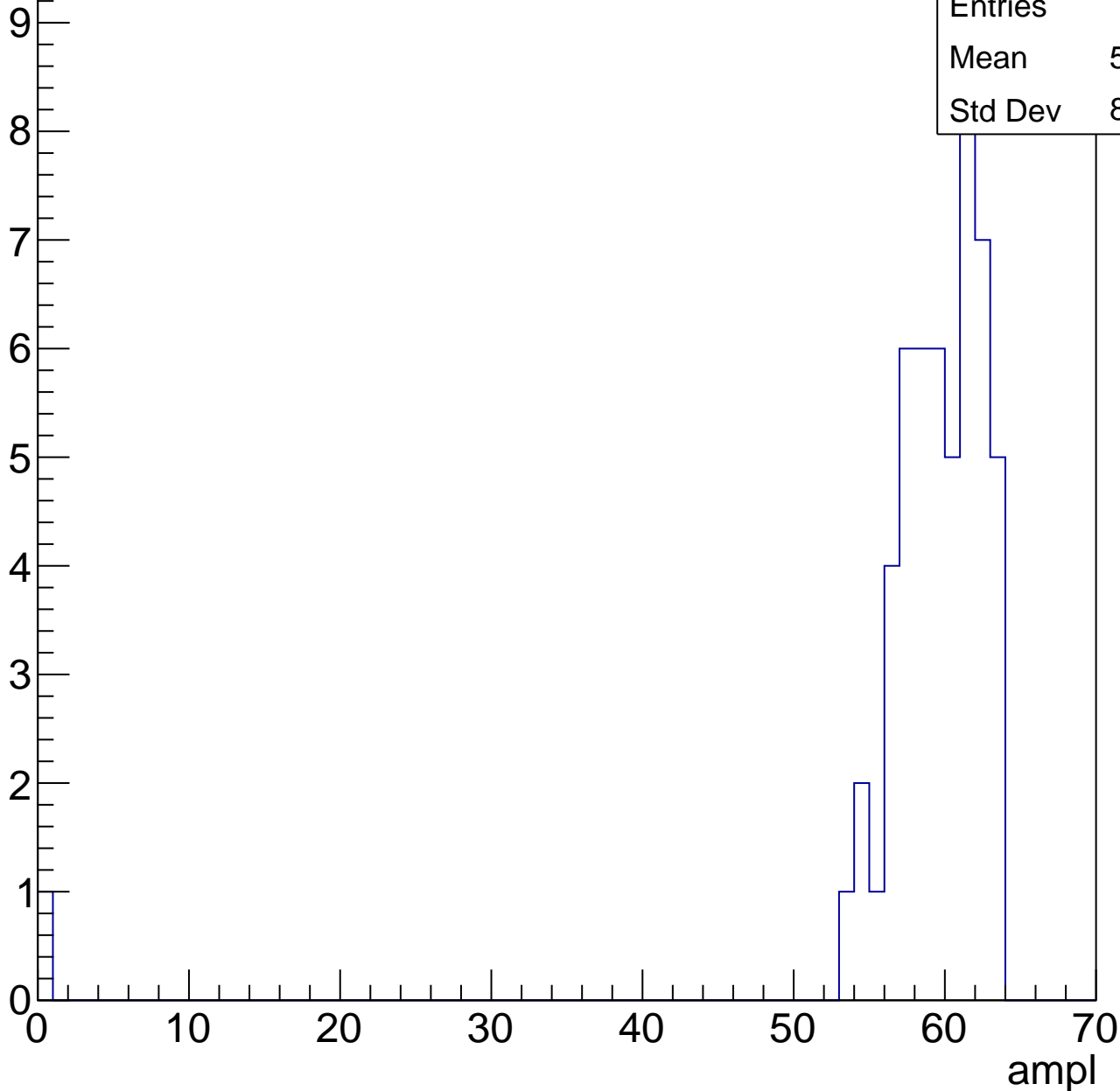
Entries	54
Mean	54.41
Std Dev	3.577

B0L102S, U5-ch97, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	58.15
Std Dev	8.462



B0L102S, U5-ch97, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch97, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch98, adc0

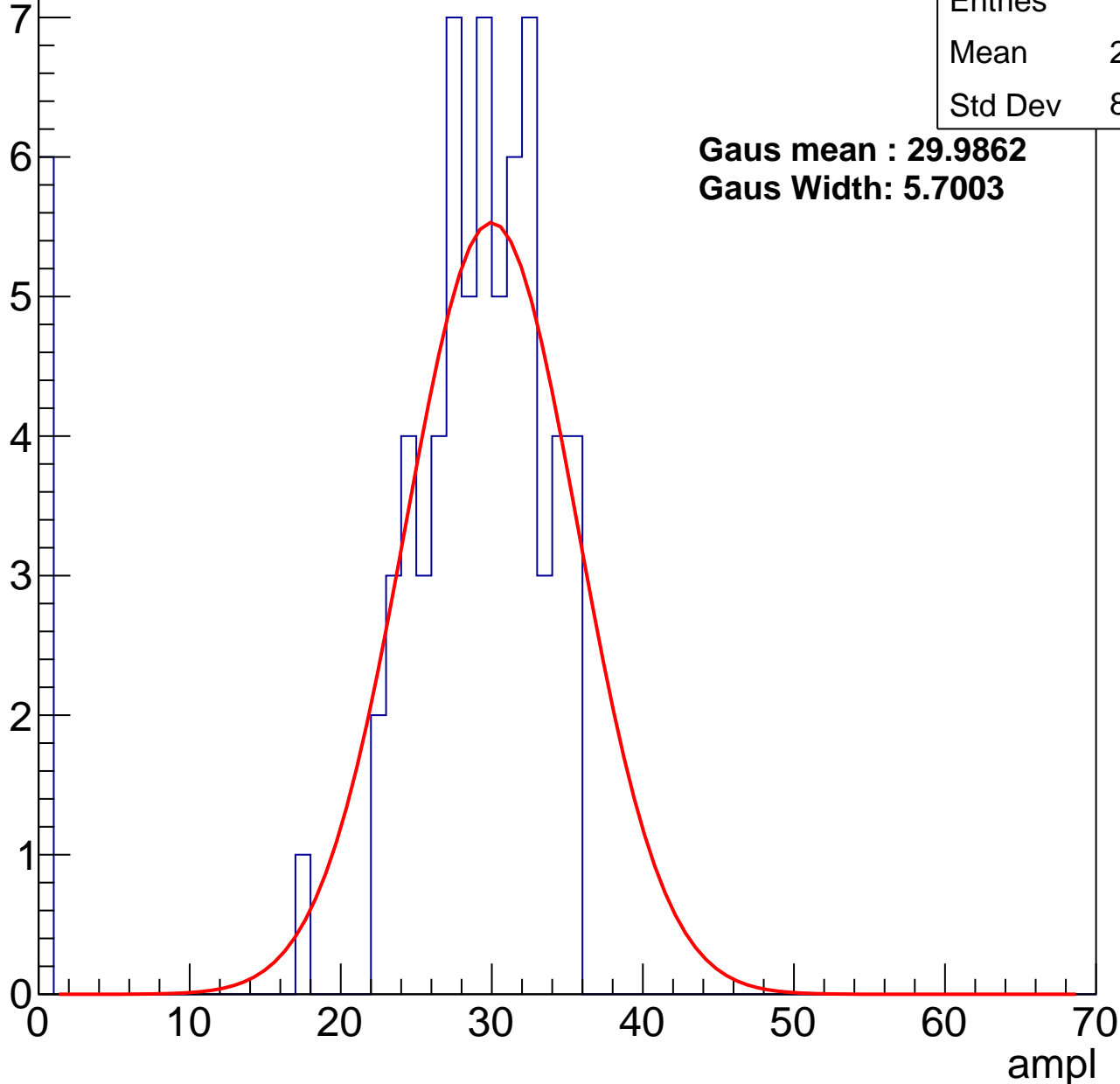
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	26.37
Std Dev	8.805

Gaus mean : 29.9862

Gaus Width: 5.7003



B0L102S, U5-ch98, adc1

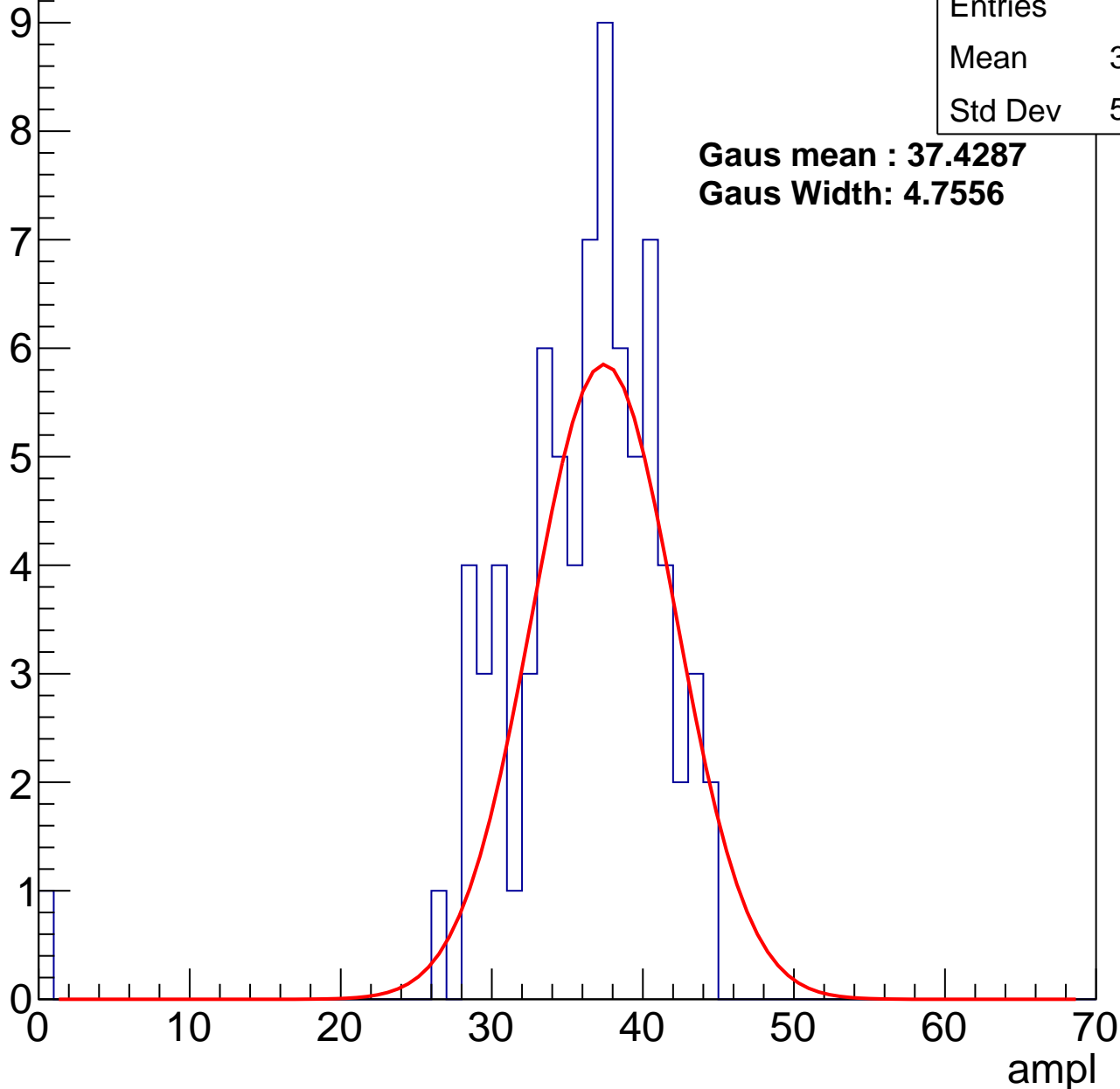
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	35.49
Std Dev	5.934

Gaus mean : 37.4287

Gaus Width: 4.7556



B0L102S, U5-ch98, adc2

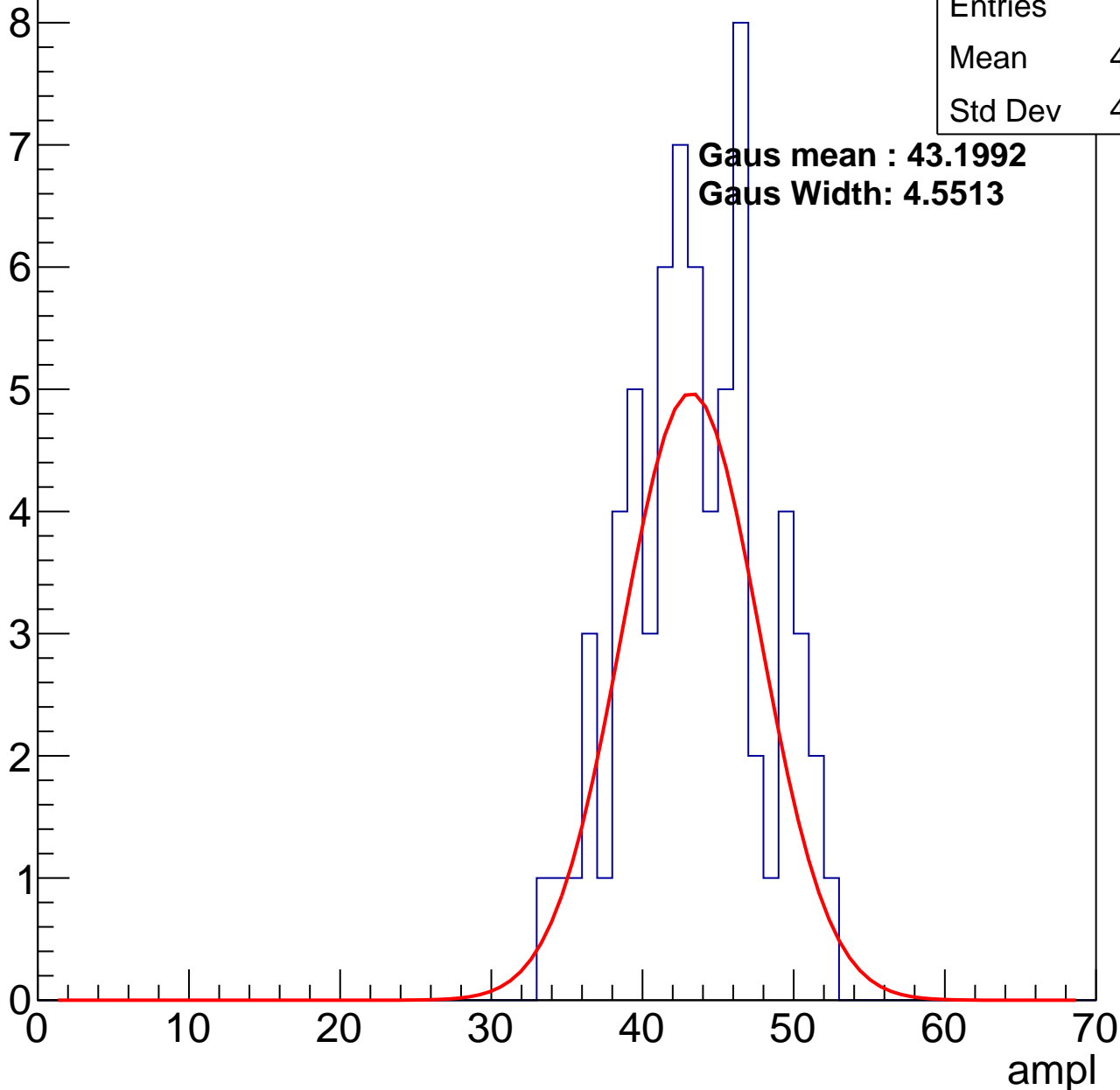
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	42.99
Std Dev	4.404

Gaus mean : 43.1992

Gaus Width: 4.5513

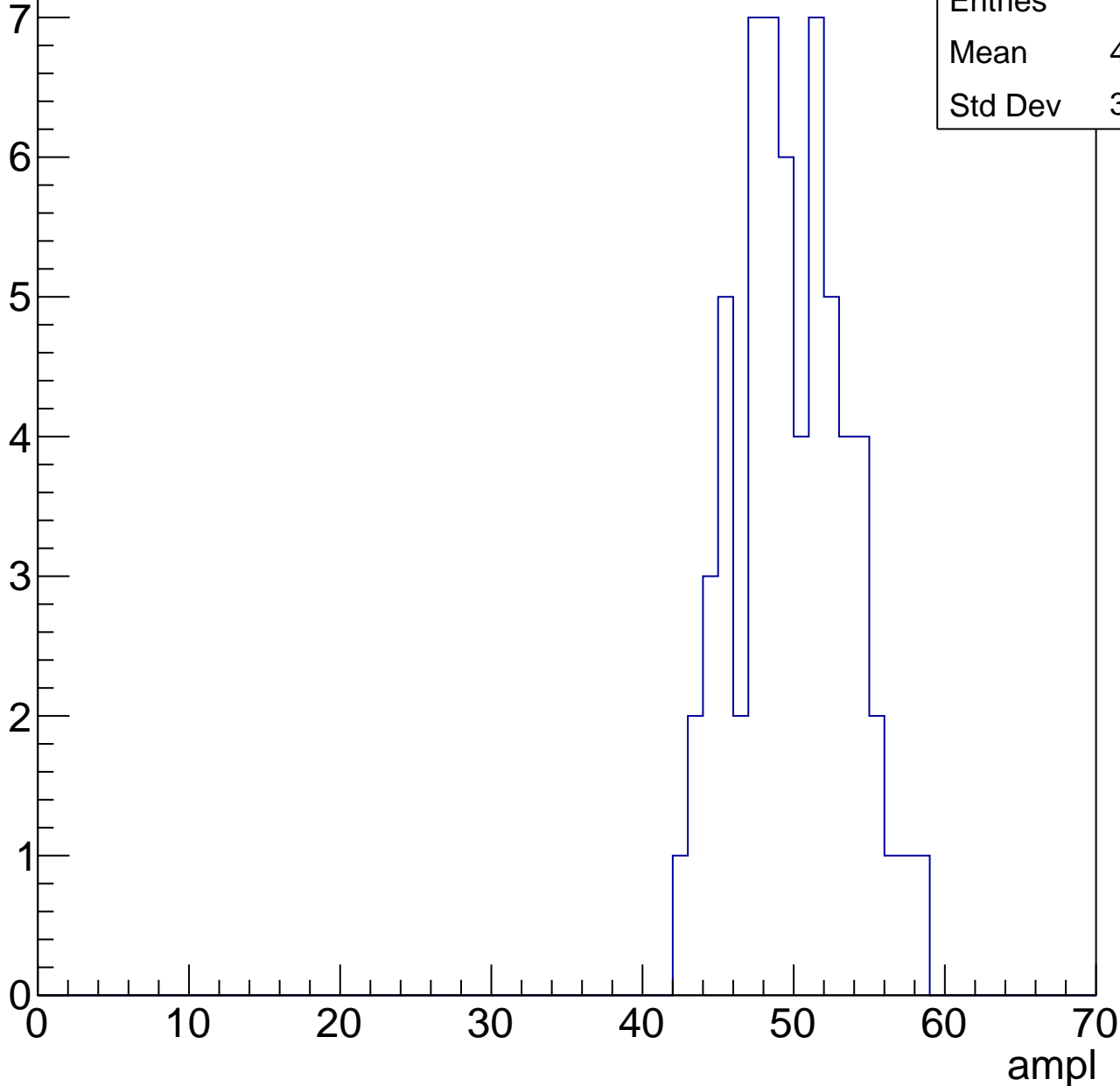


B0L102S, U5-ch98, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	49.39
Std Dev	3.652

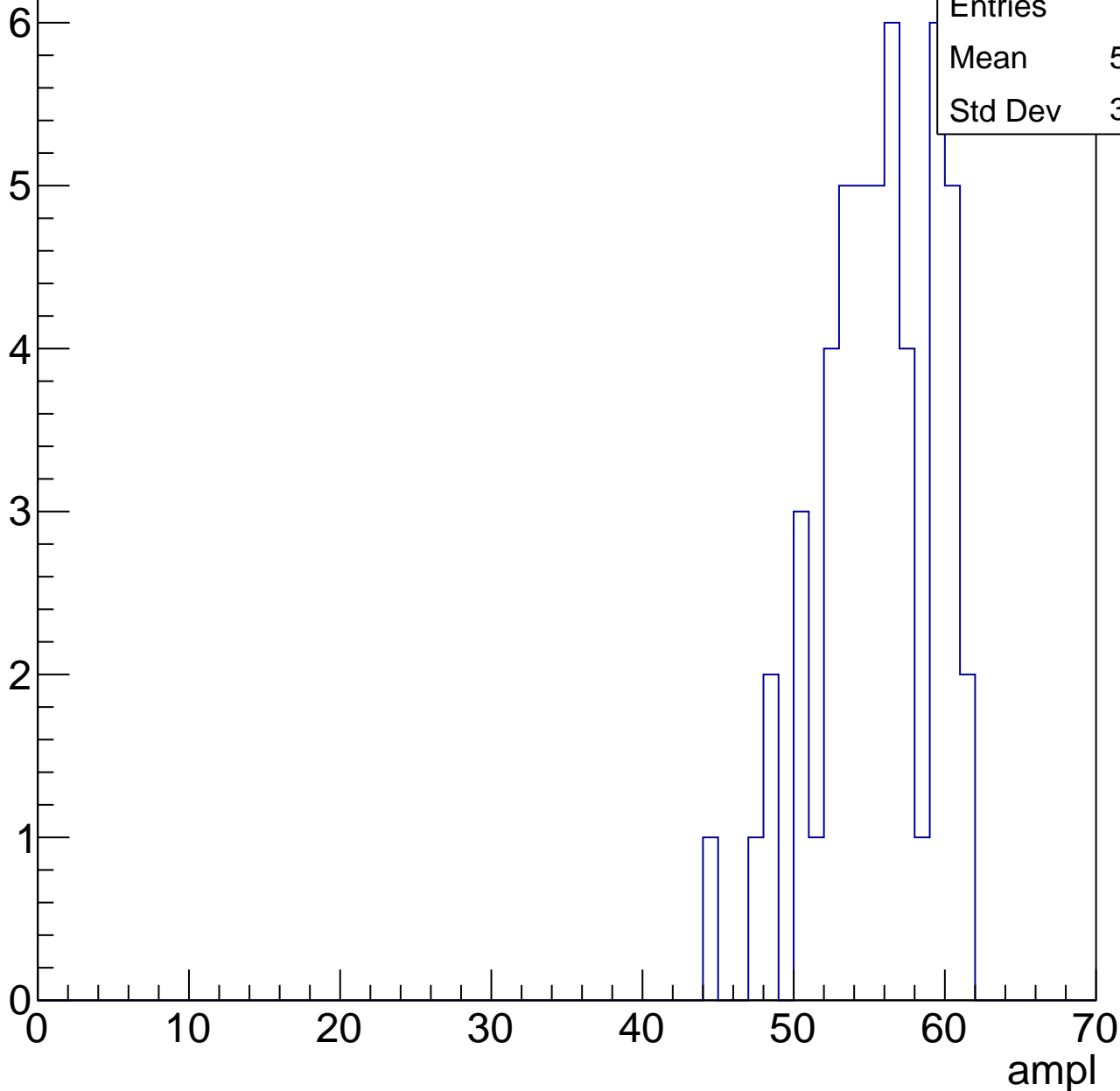


B0L102S, U5-ch98, adc4

calib_packv5_042523_0143.root, FC#12, port B1

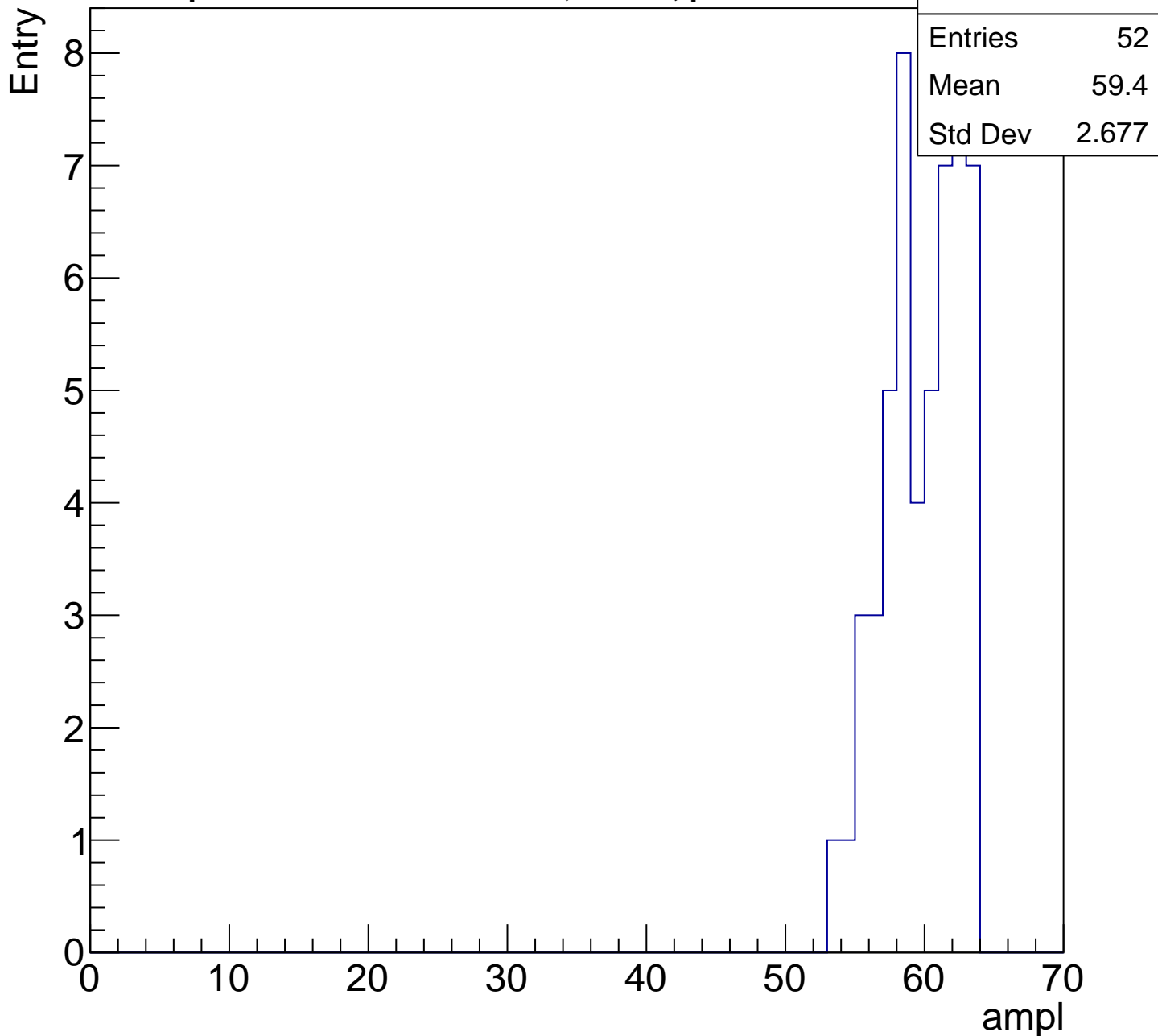
Entry

Entries	51
Mean	54.98
Std Dev	3.858



B0L102S, U5-ch98, adc5

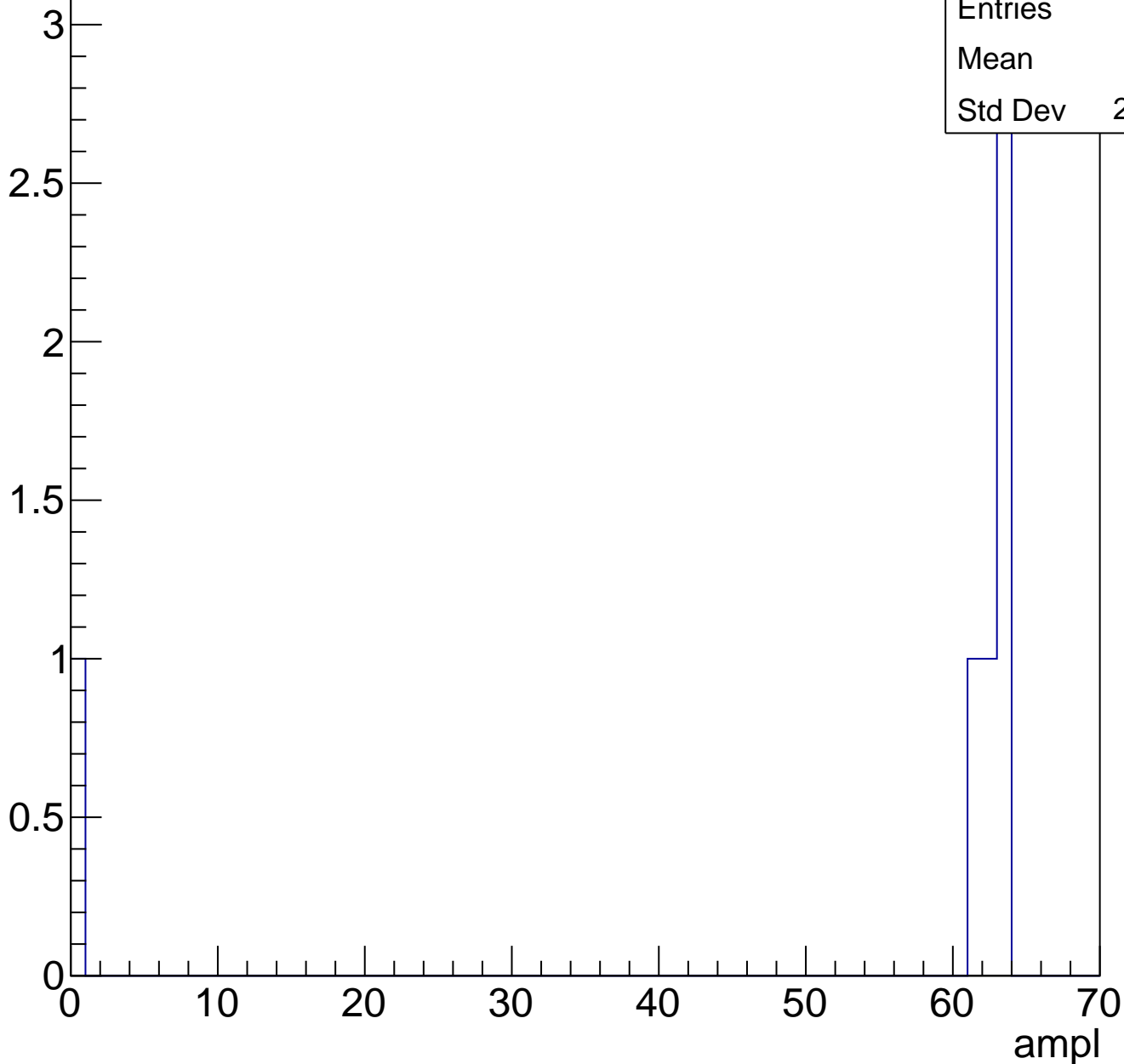
calib_packv5_042523_0143.root, FC#12, port B1



B0L102S, U5-ch98, adc6

calib_packv5_042523_0143.root, FC#12, port B1

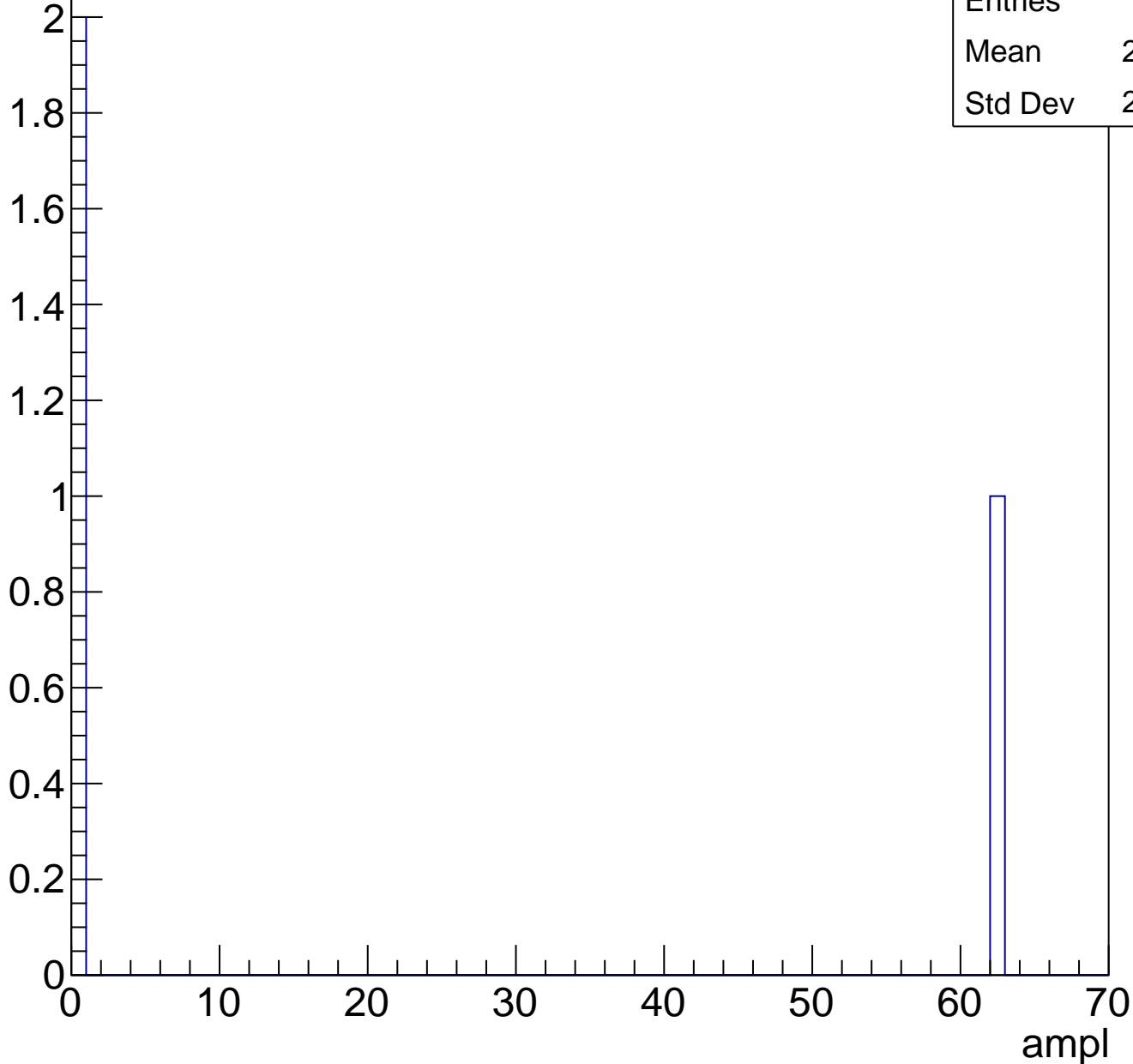
Entry



B0L102S, U5-ch98, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch99, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	66
Mean	28.06
Std Dev	4.713

Gaus mean : 28.7245

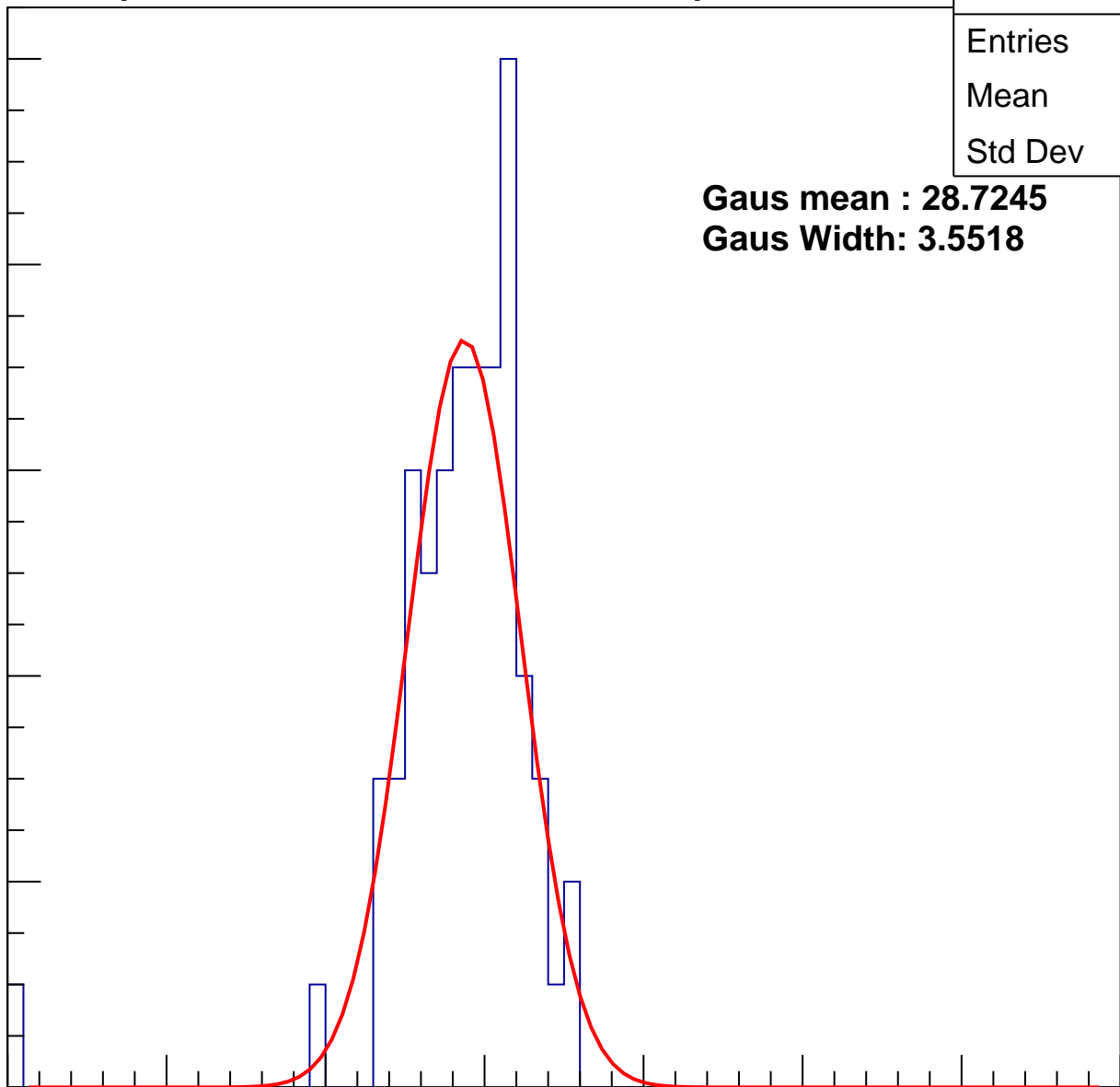
Gaus Width: 3.5518

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch99, adc1

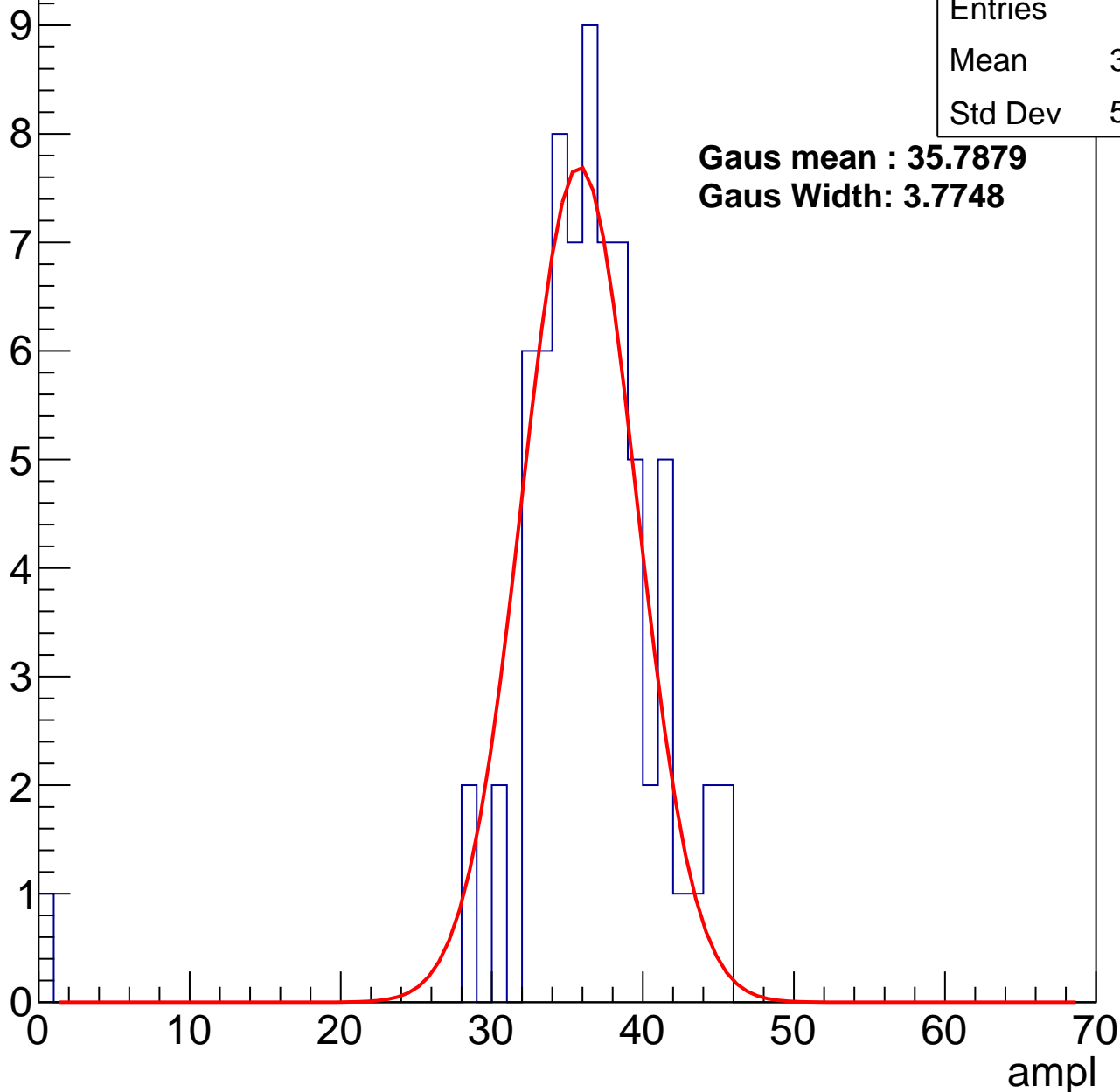
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	35.82
Std Dev	5.607

Gaus mean : 35.7879

Gaus Width: 3.7748



B0L102S, U5-ch99, adc2

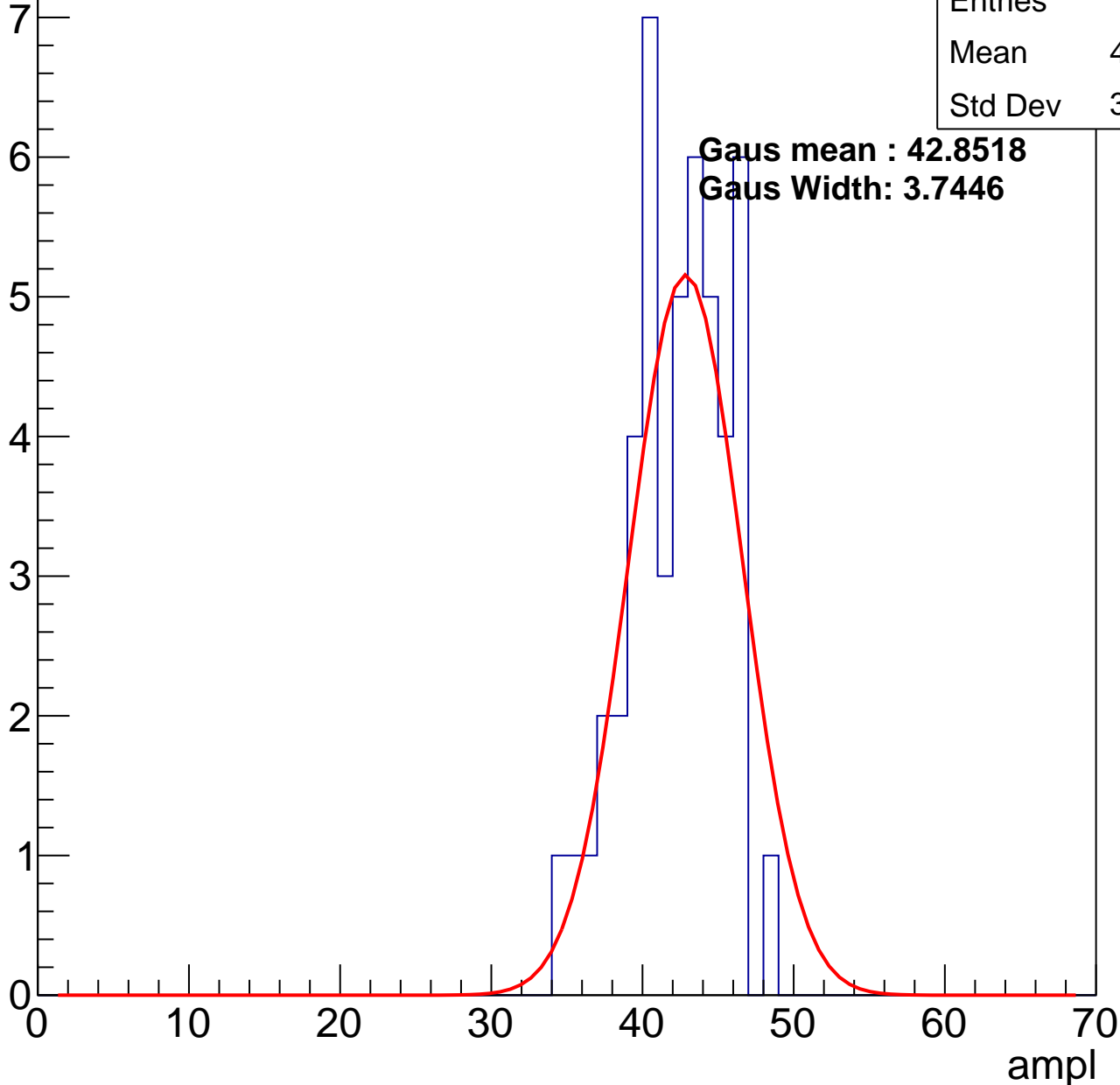
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	41.79
Std Dev	3.195

Gaus mean : 42.8518

Gaus Width: 3.7446



B0L102S, U5-ch99, adc3

calib_packv5_042523_0143.root, FC#12, port B1

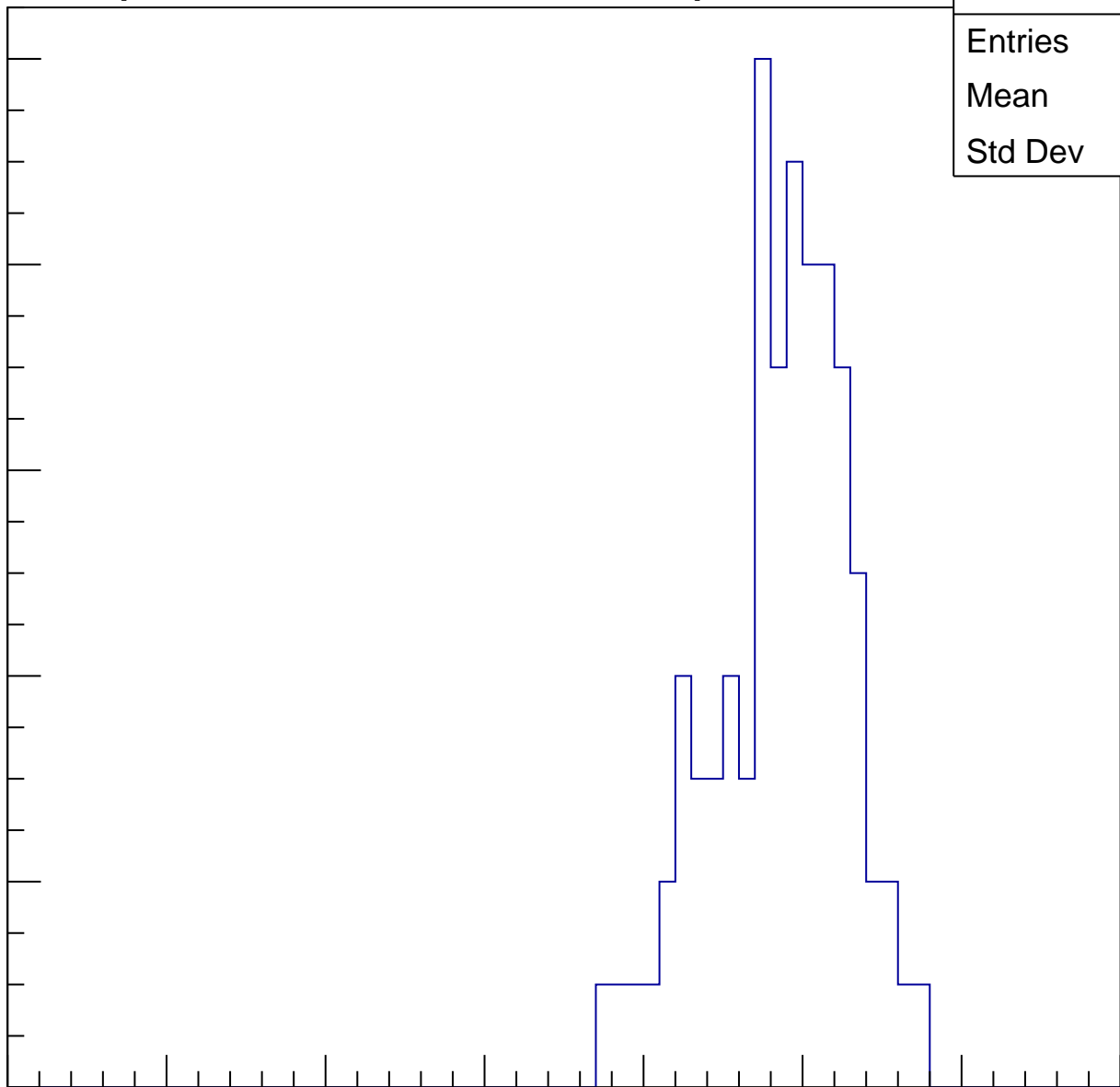
Entries	83
Mean	48.17
Std Dev	4.182

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

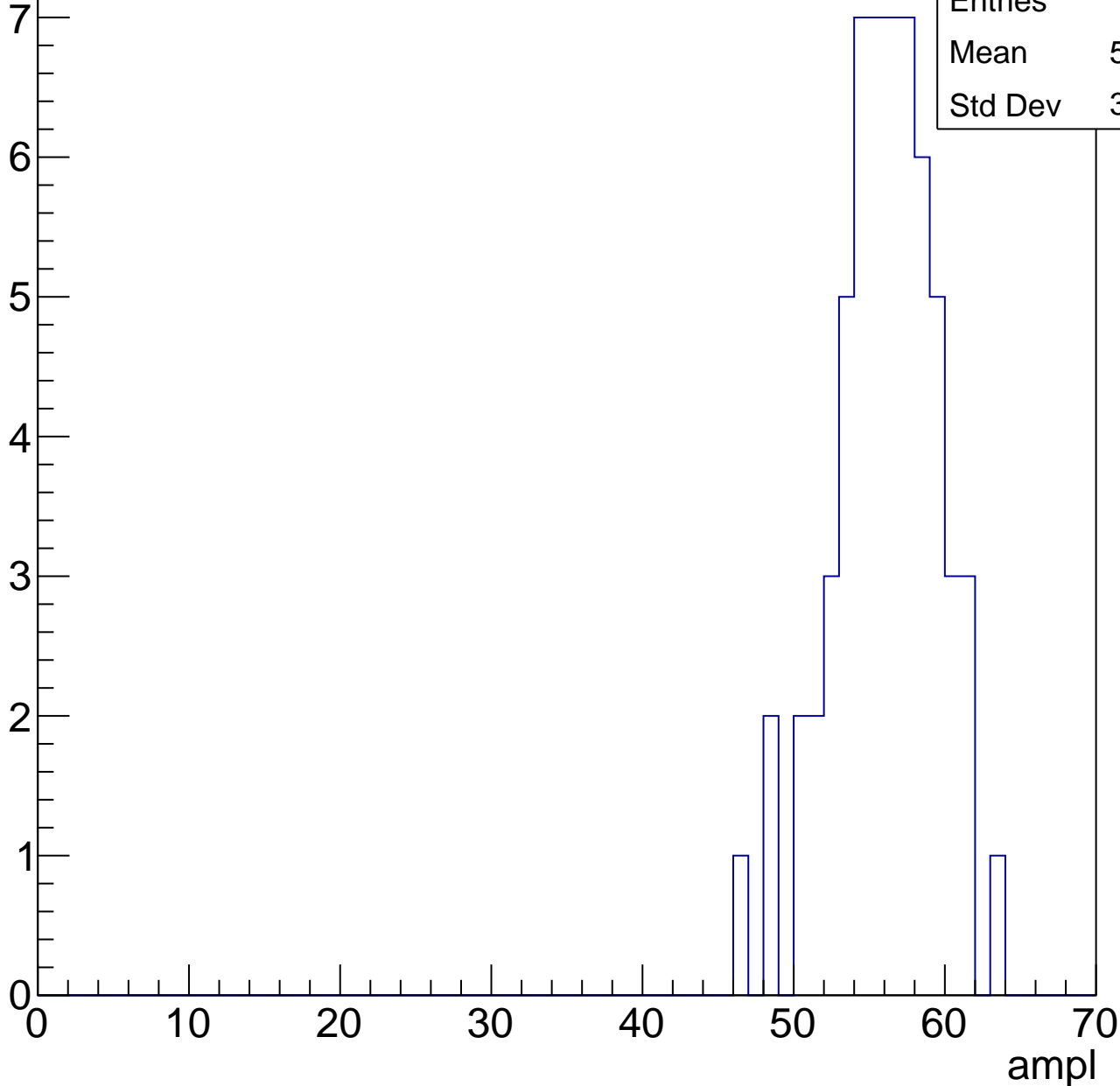


B0L102S, U5-ch99, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.54
Std Dev	3.419



B0L102S, U5-ch99, adc5

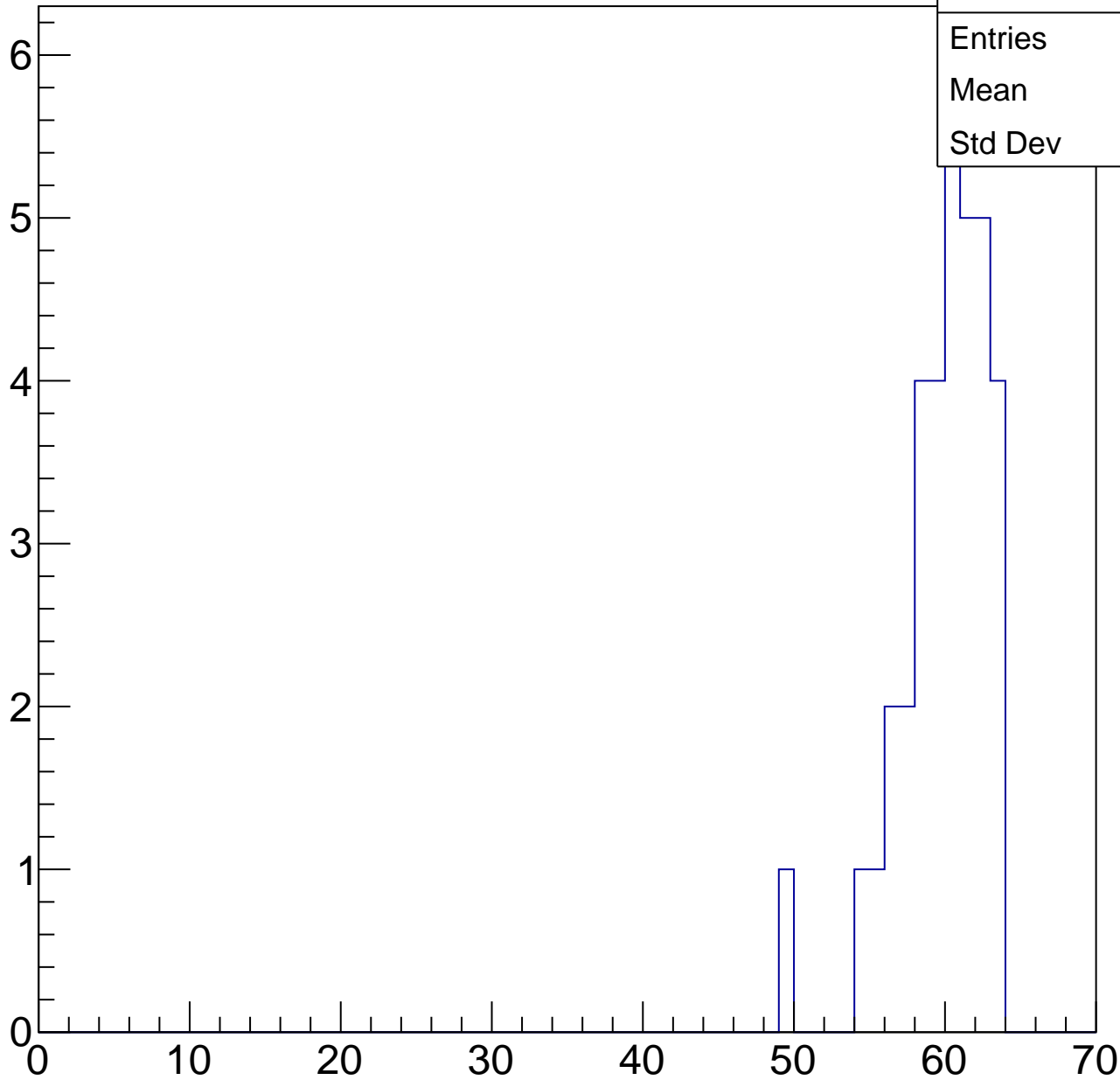
calib_packv5_042523_0143.root, FC#12, port B1

Entry

6
5
4
3
2
1
0

Entries	35
Mean	59.4
Std Dev	2.93

ampl

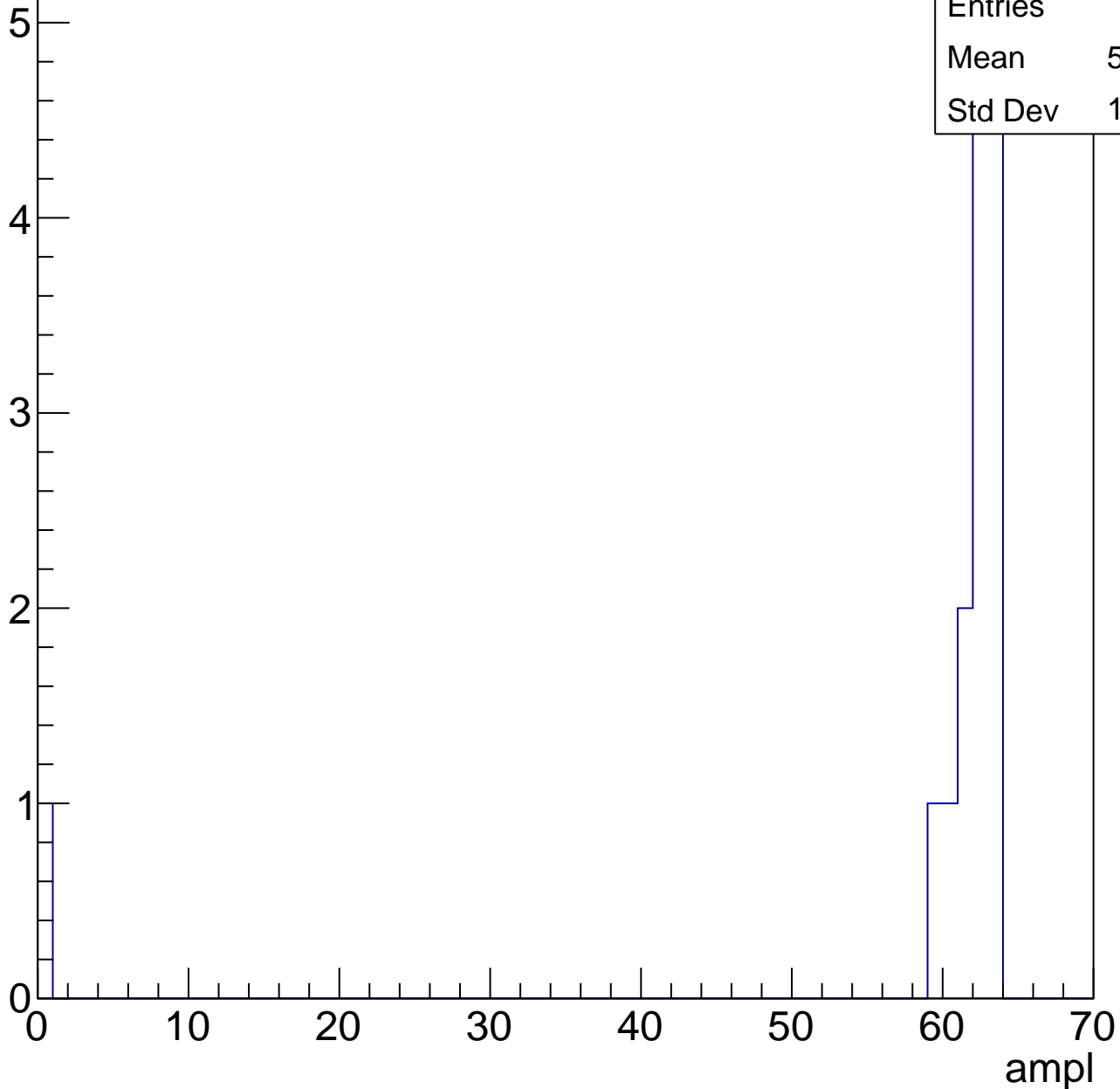


B0L102S, U5-ch99, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	15
Mean	57.73
Std Dev	15.47



B0L102S, U5-ch99, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch100, adc0

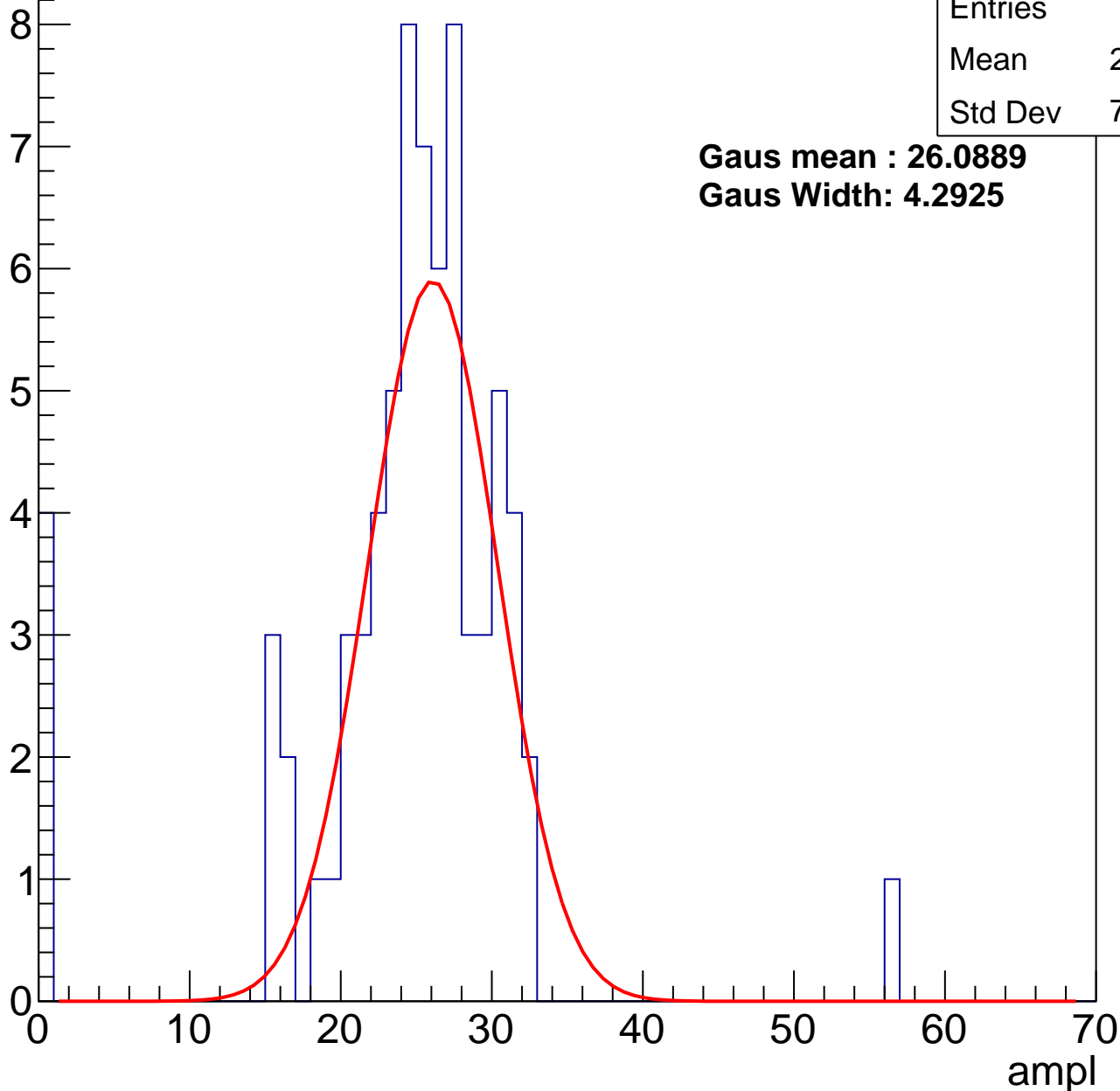
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	23.89
Std Dev	7.925

Gaus mean : 26.0889

Gaus Width: 4.2925



B0L102S, U5-ch100, adc1

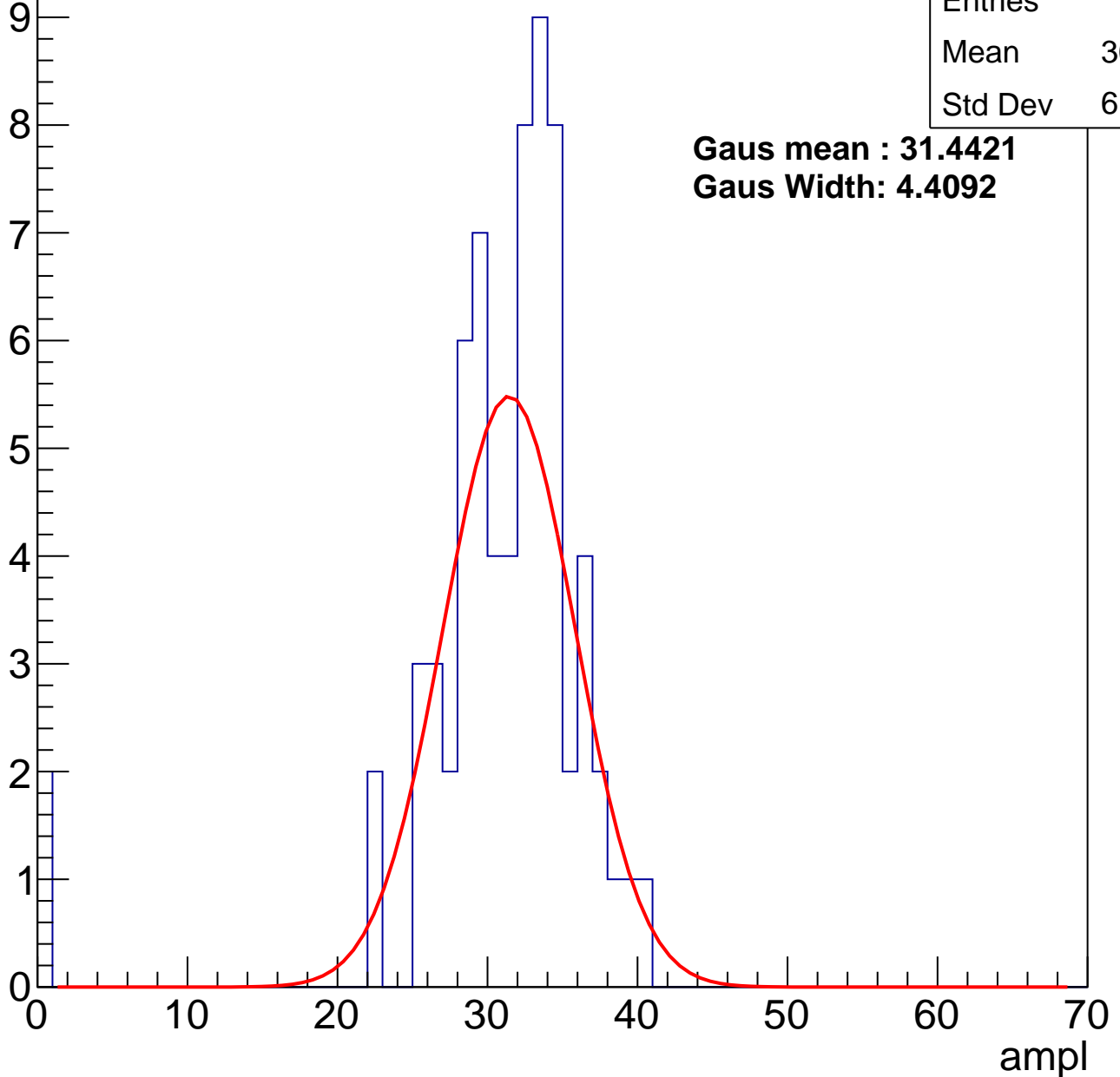
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	30.38
Std Dev	6.452

Gaus mean : 31.4421

Gaus Width: 4.4092



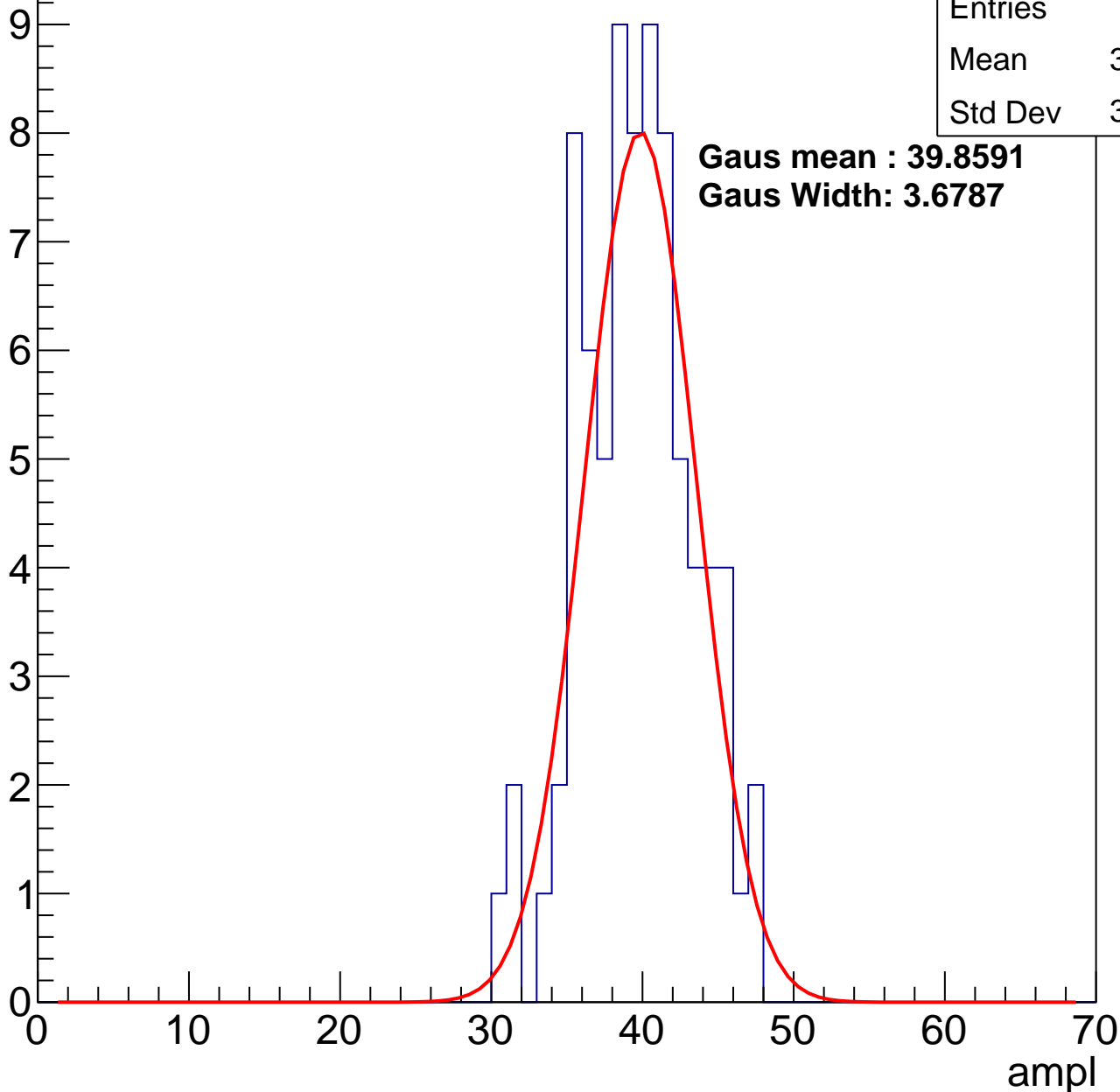
B0L102S, U5-ch100, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	39.16
Std Dev	3.689

Gaus mean : 39.8591
Gaus Width: 3.6787

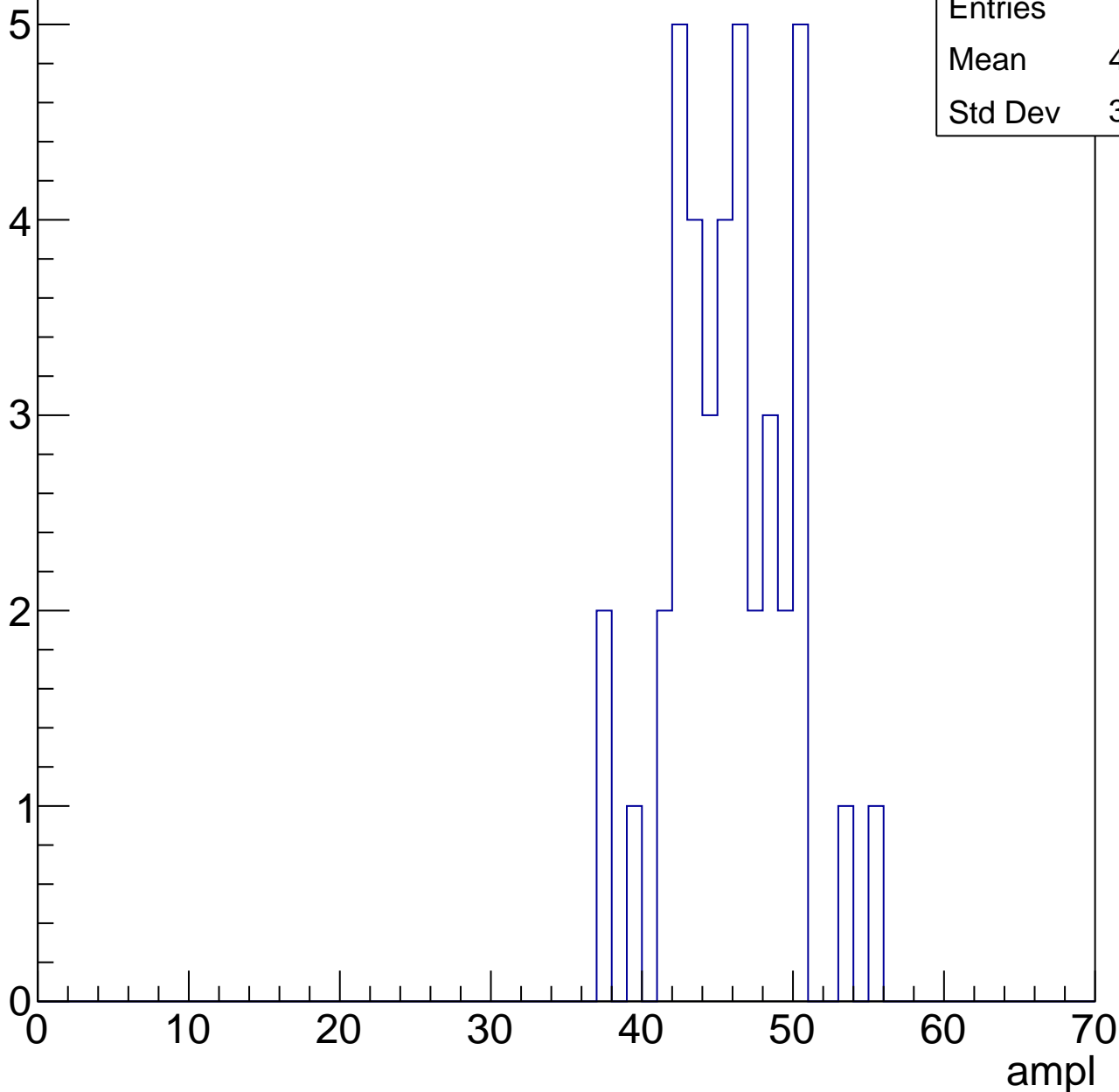


B0L102S, U5-ch100, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

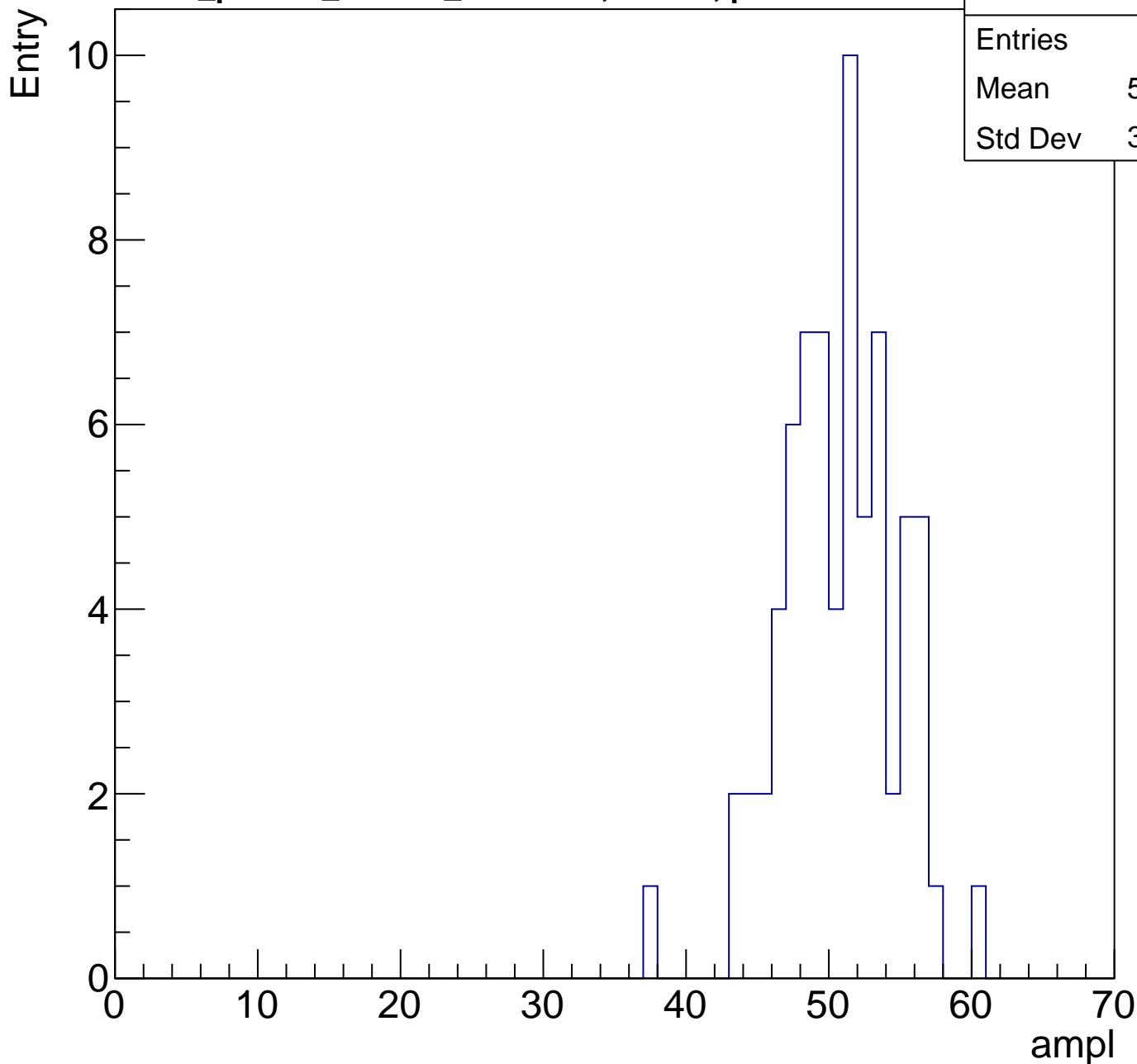
Entries	40
Mean	45.33
Std Dev	3.927



B0L102S, U5-ch100, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	71
Mean	50.24
Std Dev	3.966

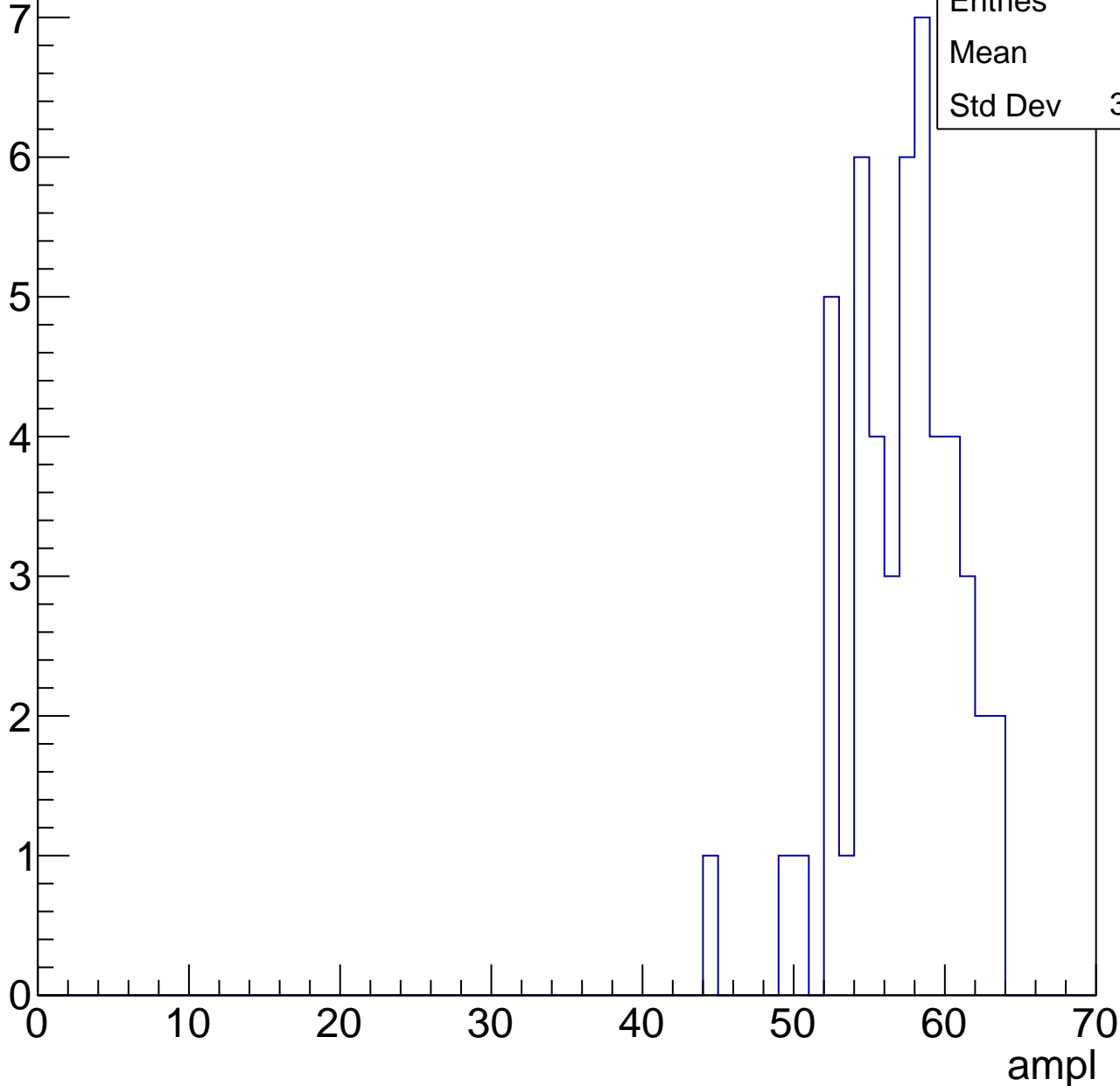


B0L102S, U5-ch100, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	50
Mean	56.5
Std Dev	3.775

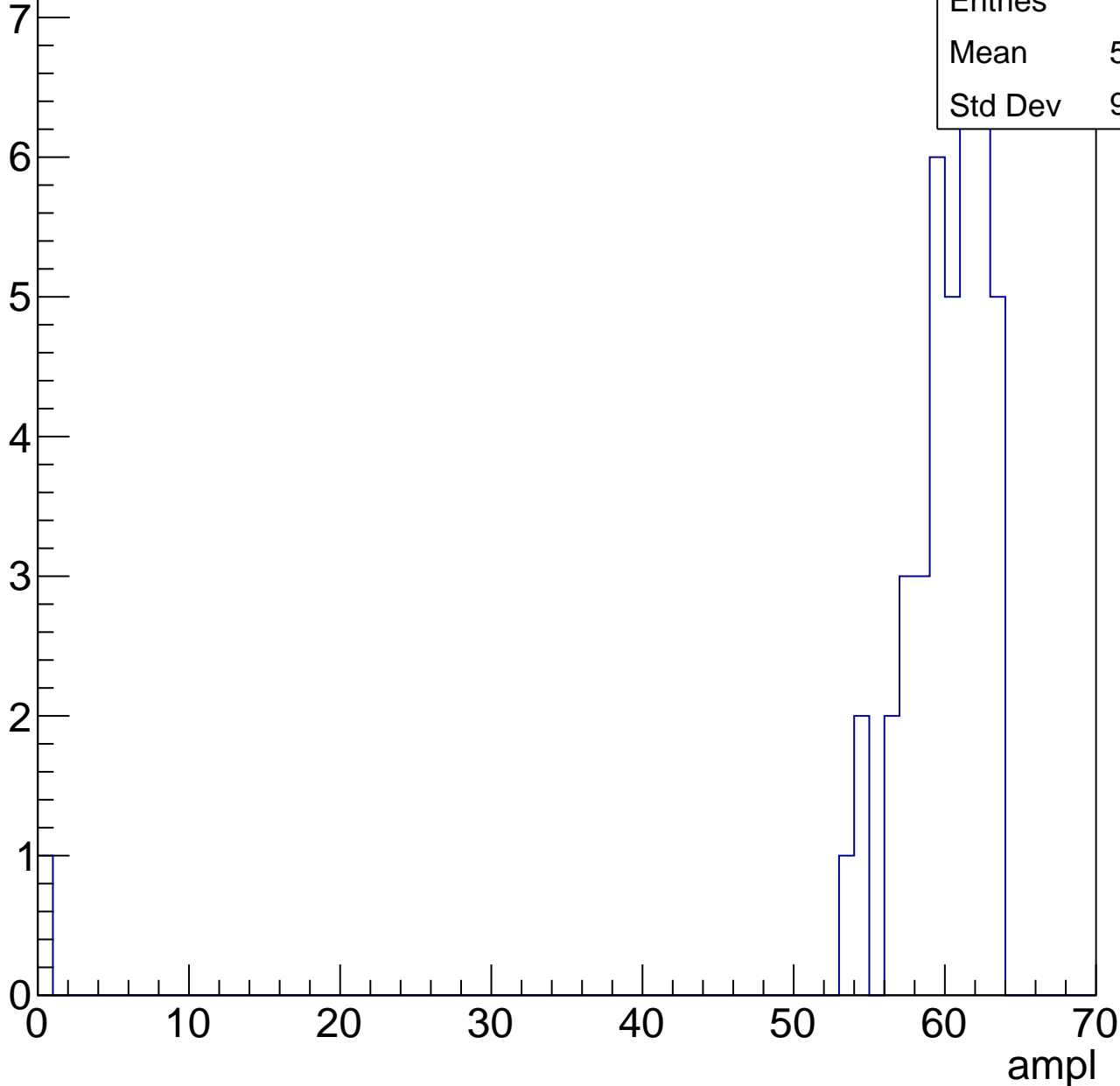


B0L102S, U5-ch100, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

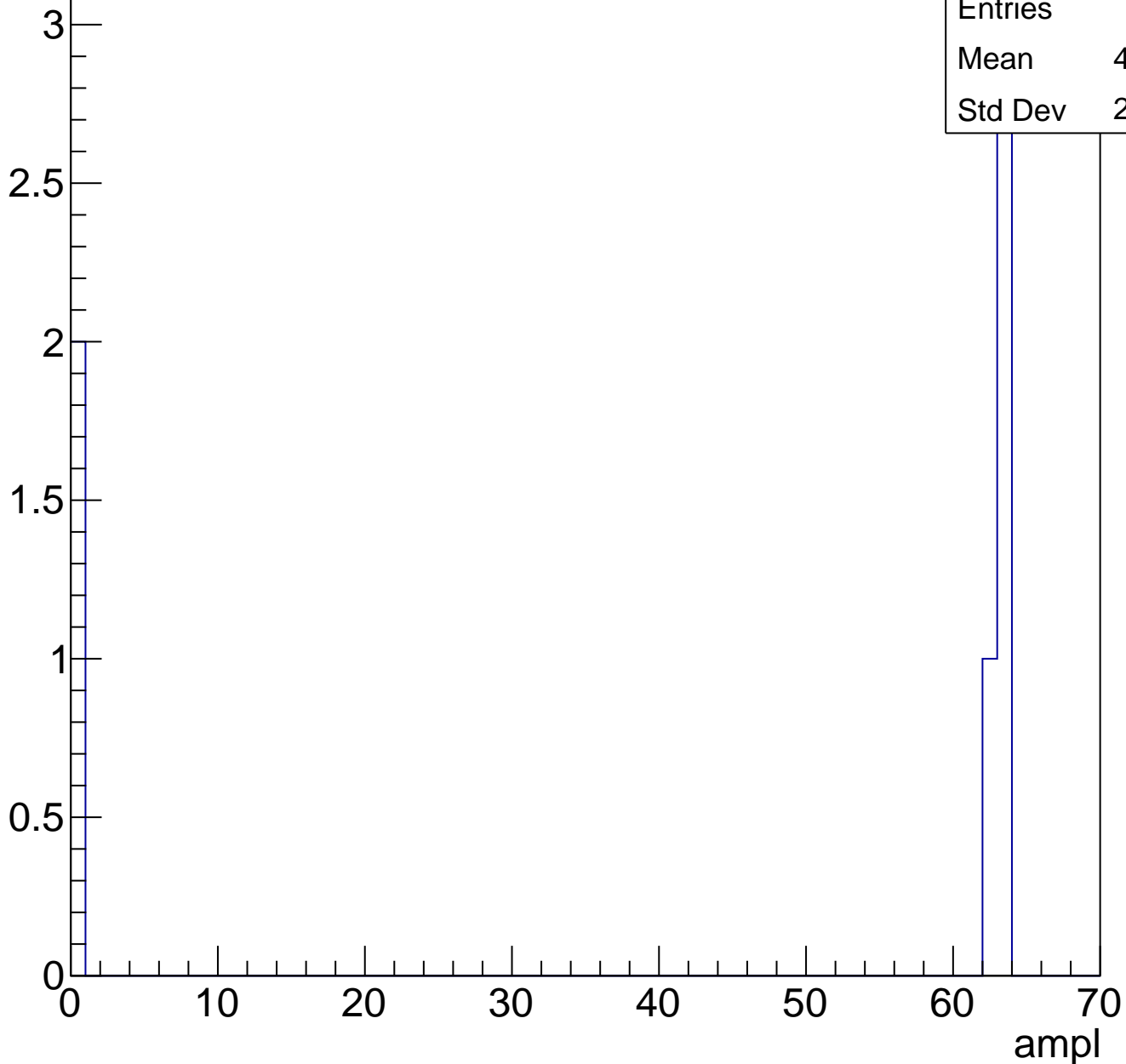
Entries	42
Mean	58.29
Std Dev	9.455



B0L102S, U5-ch100, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch101, adc0

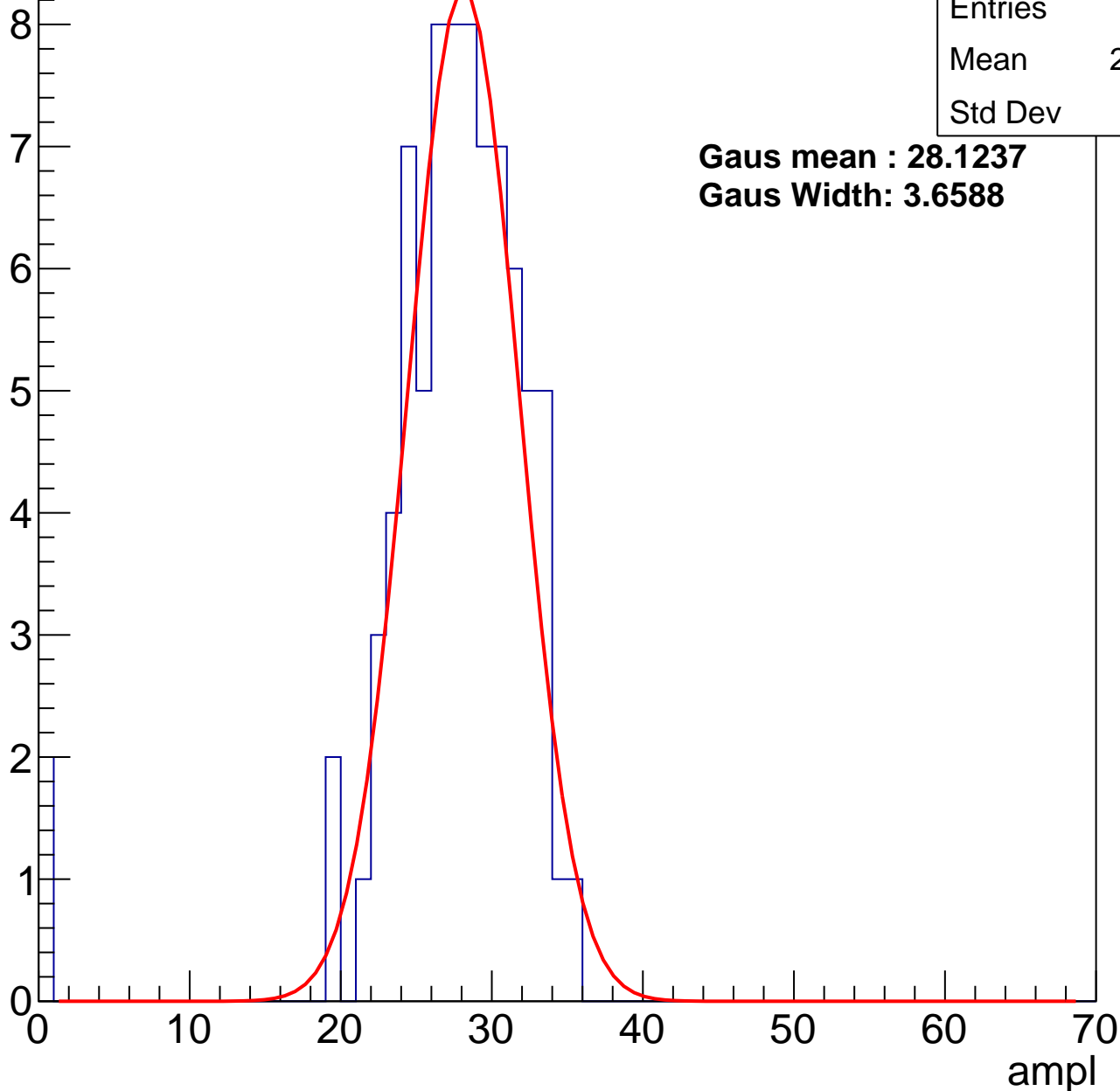
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	80
Mean	26.89
Std Dev	5.55

Gaus mean : 28.1237

Gaus Width: 3.6588



B0L102S, U5-ch101, adc1

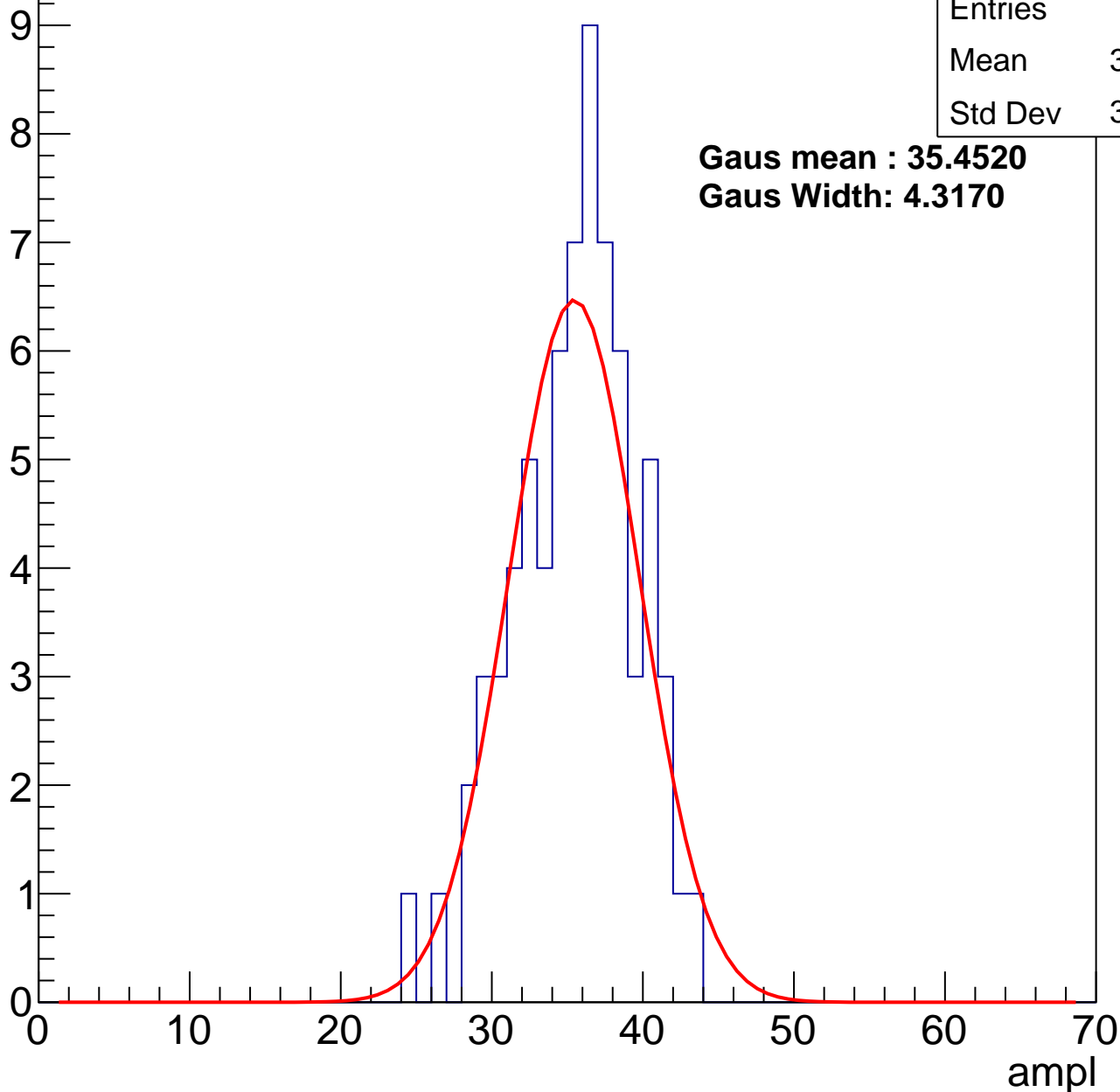
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	34.99
Std Dev	3.934

Gaus mean : 35.4520

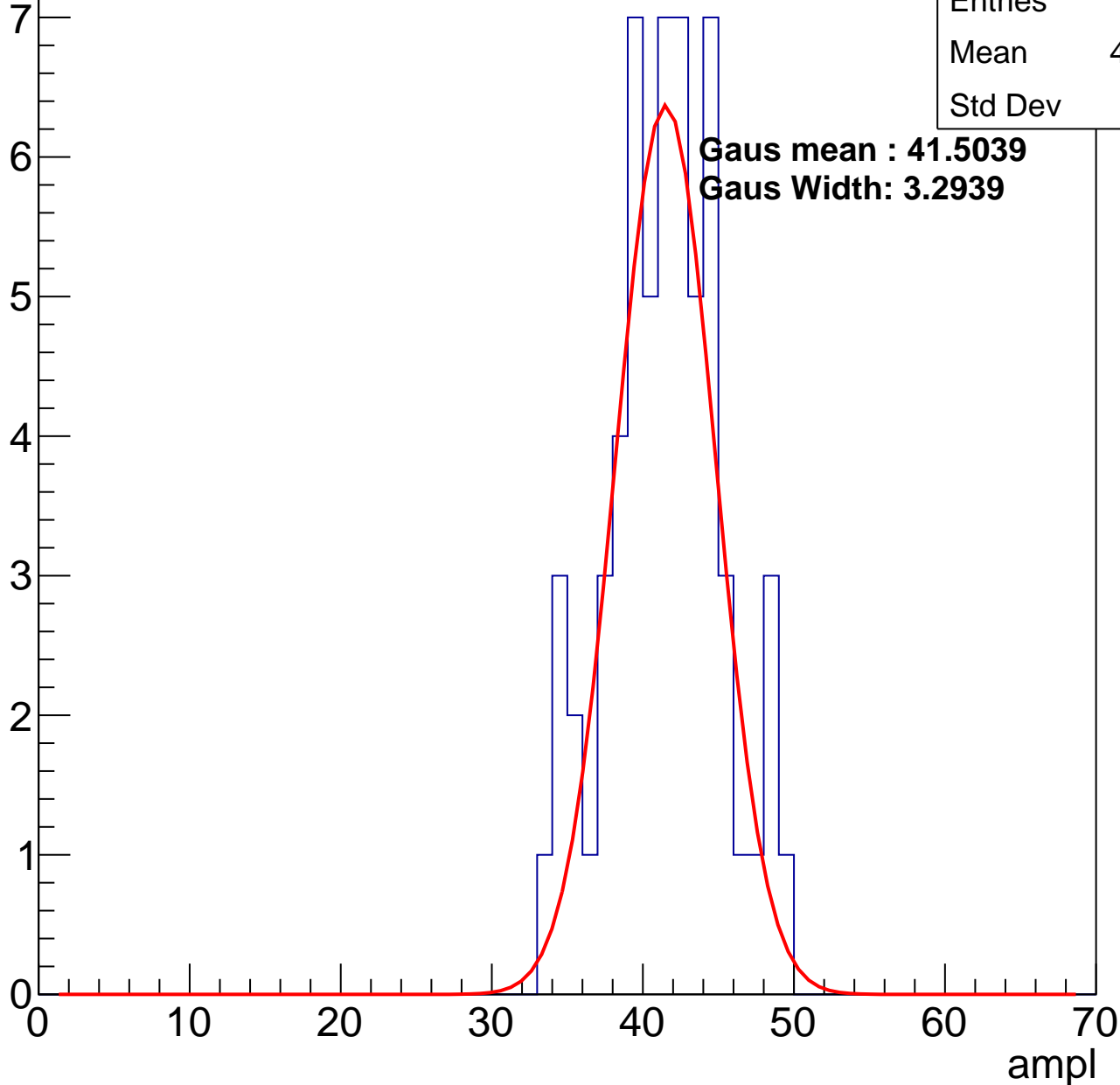
Gaus Width: 4.3170



B0L102S, U5-ch101, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

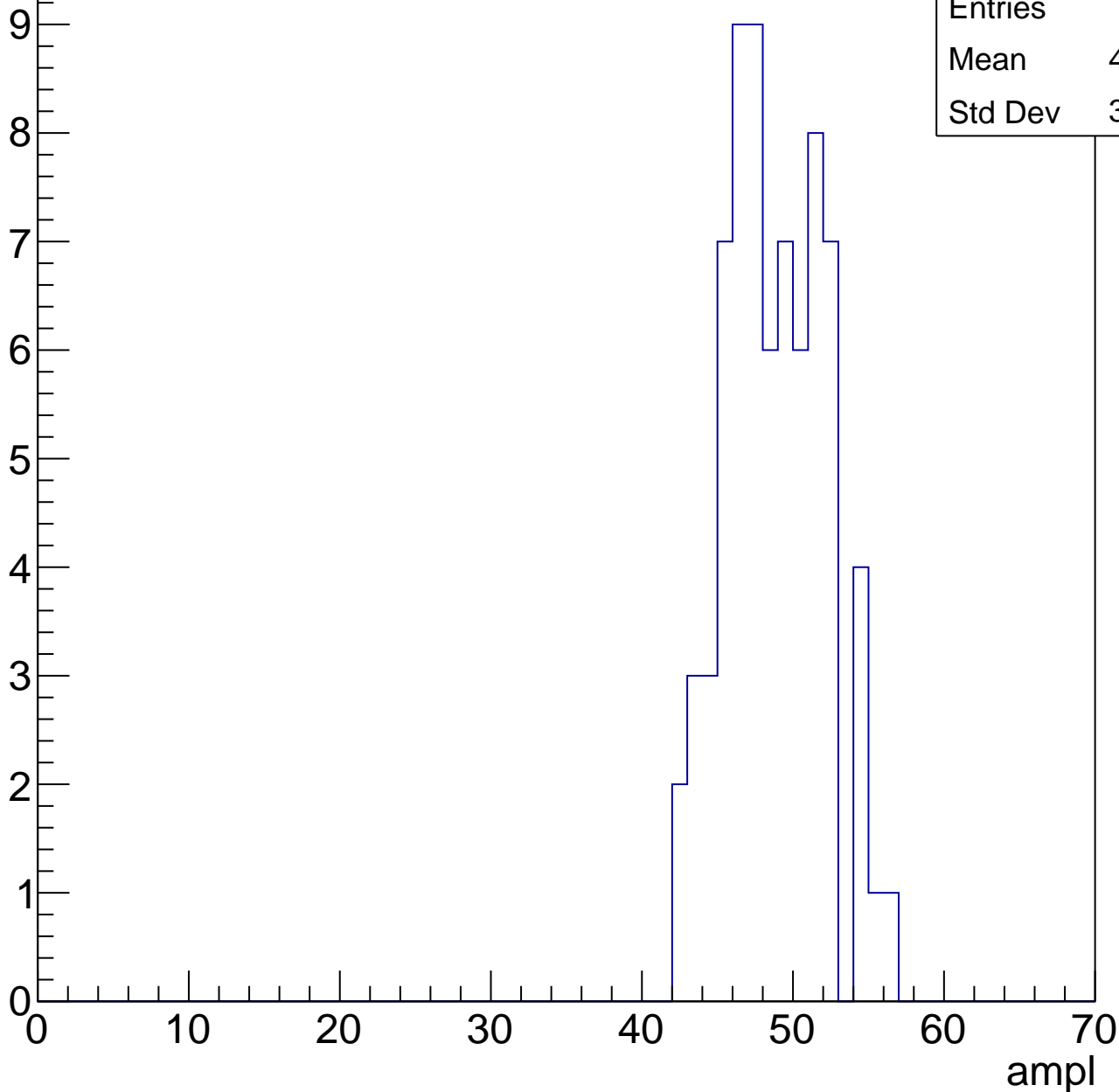


B0L102S, U5-ch101, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	48.32
Std Dev	3.243

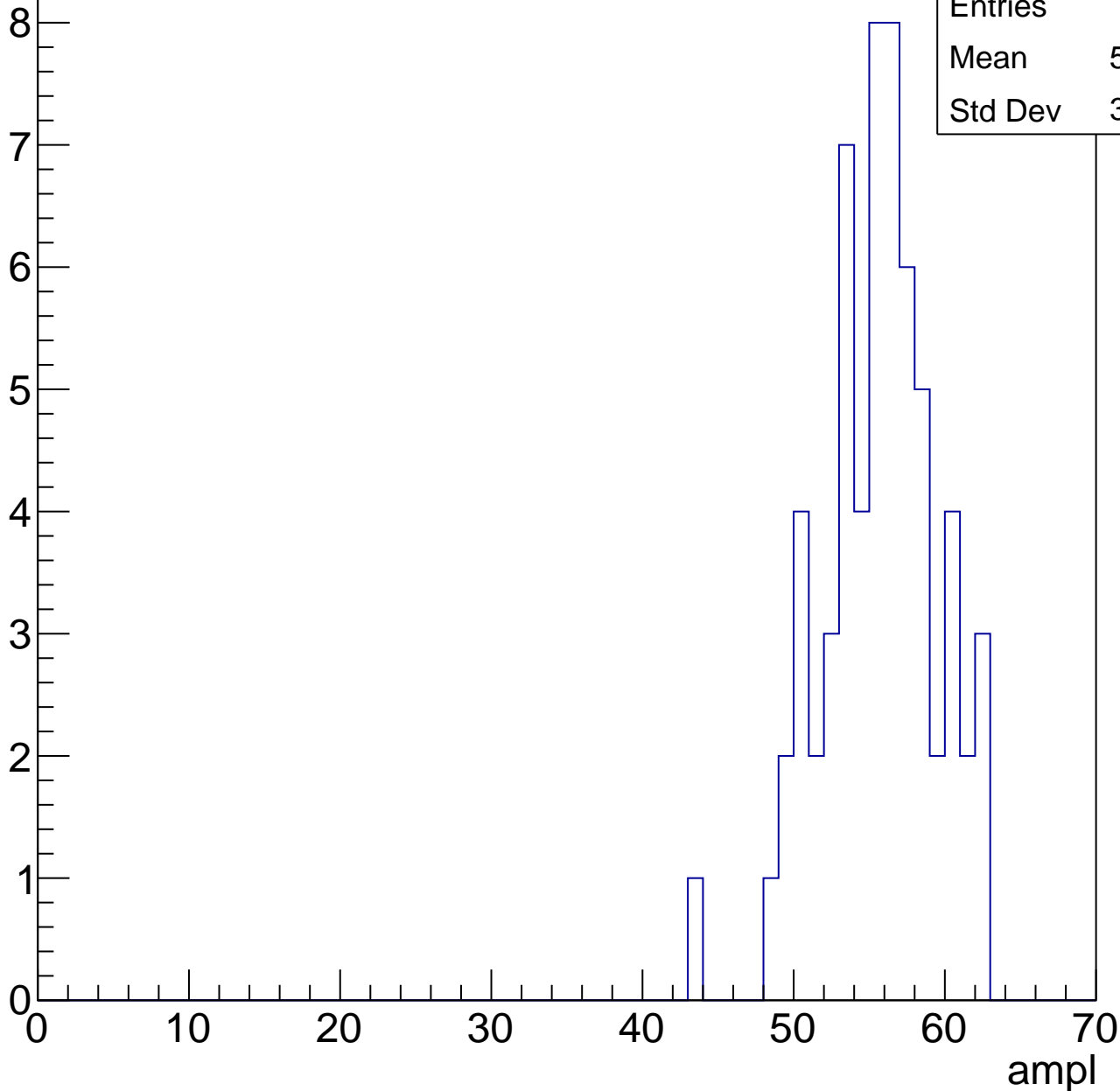


B0L102S, U5-ch101, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	55.16
Std Dev	3.773

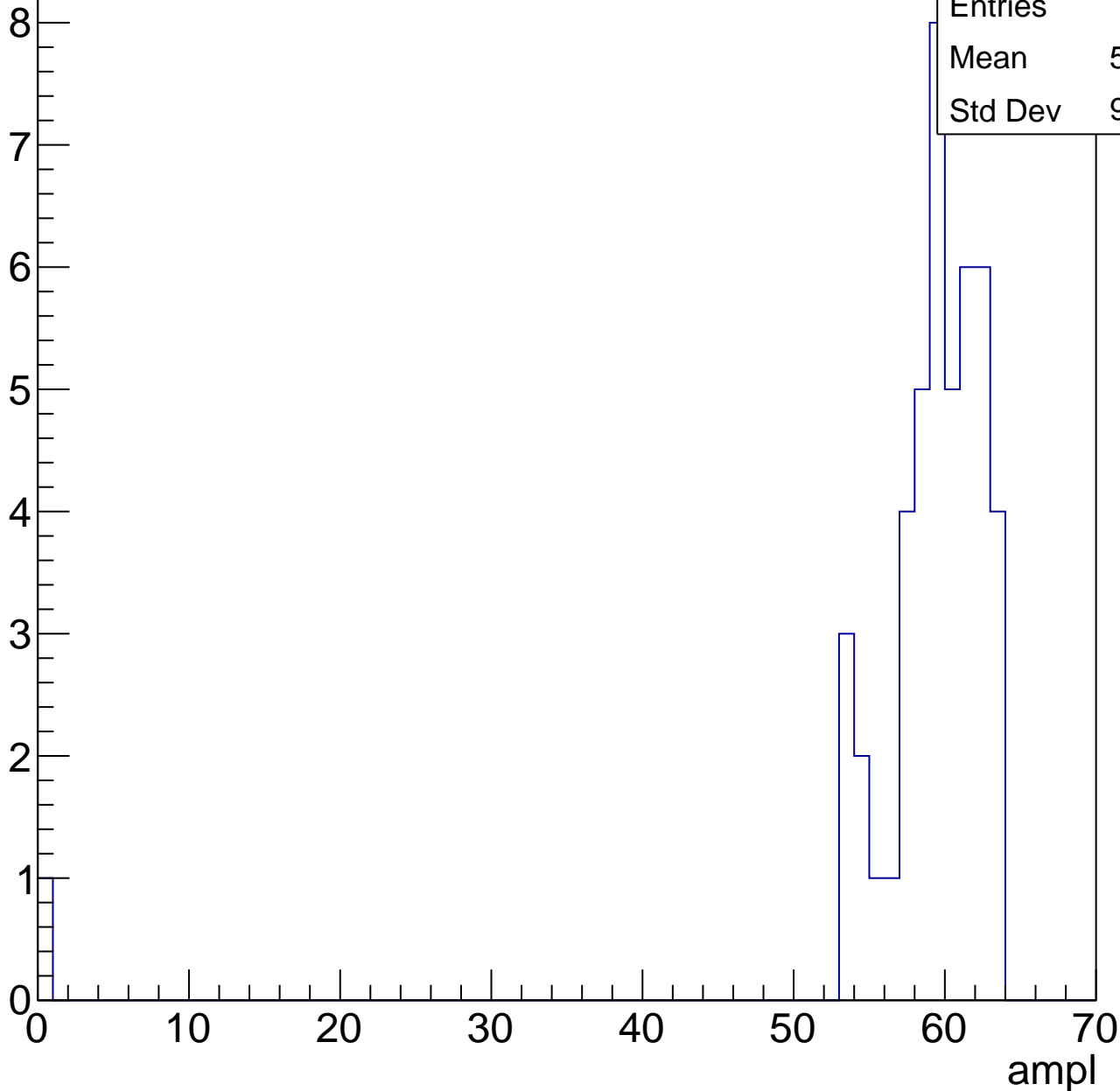


B0L102S, U5-ch101, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	57.78
Std Dev	9.046

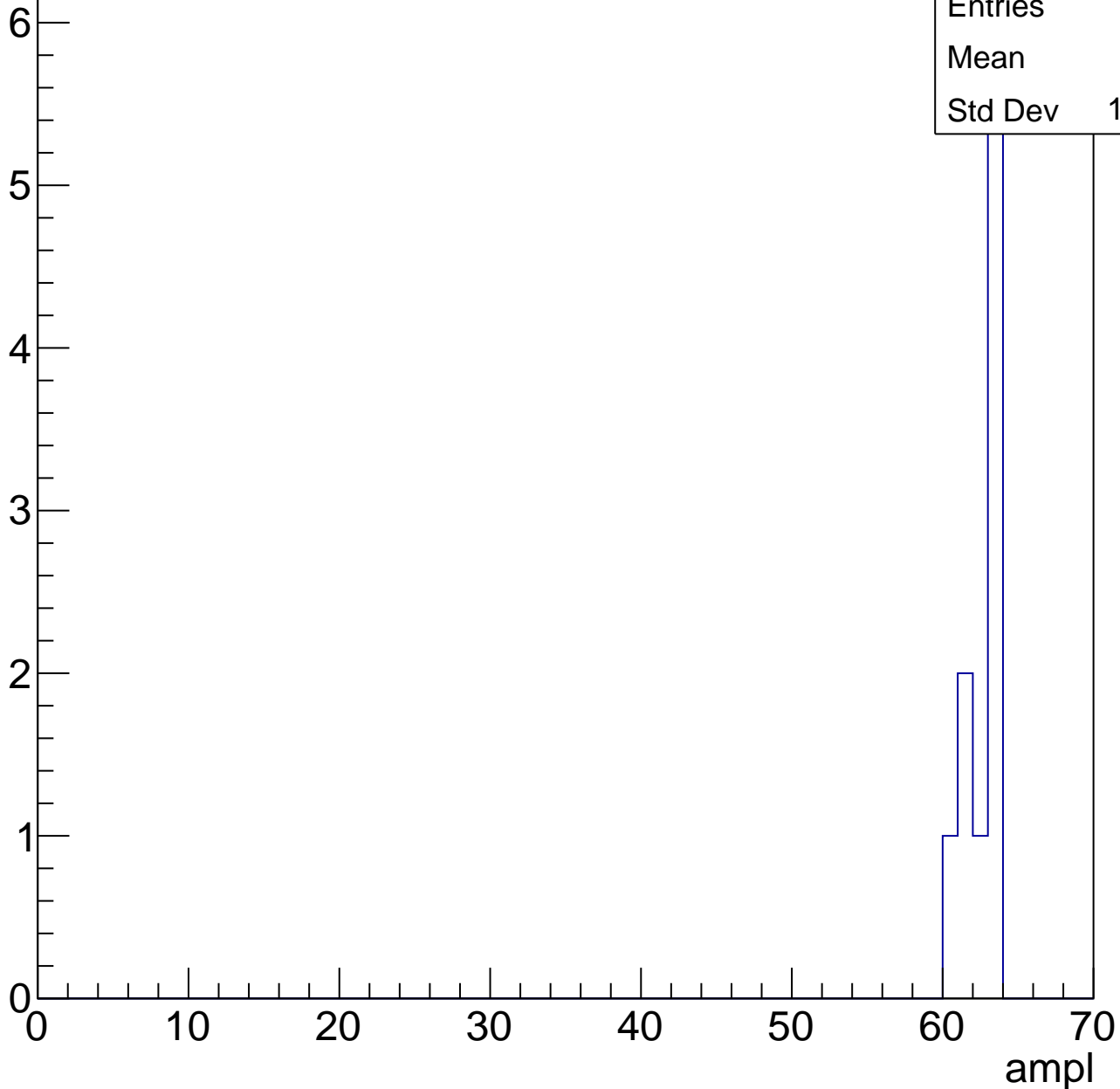


B0L102S, U5-ch101, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	10
Mean	62.2
Std Dev	1.077



B0L102S, U5-ch101, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch102, adc0

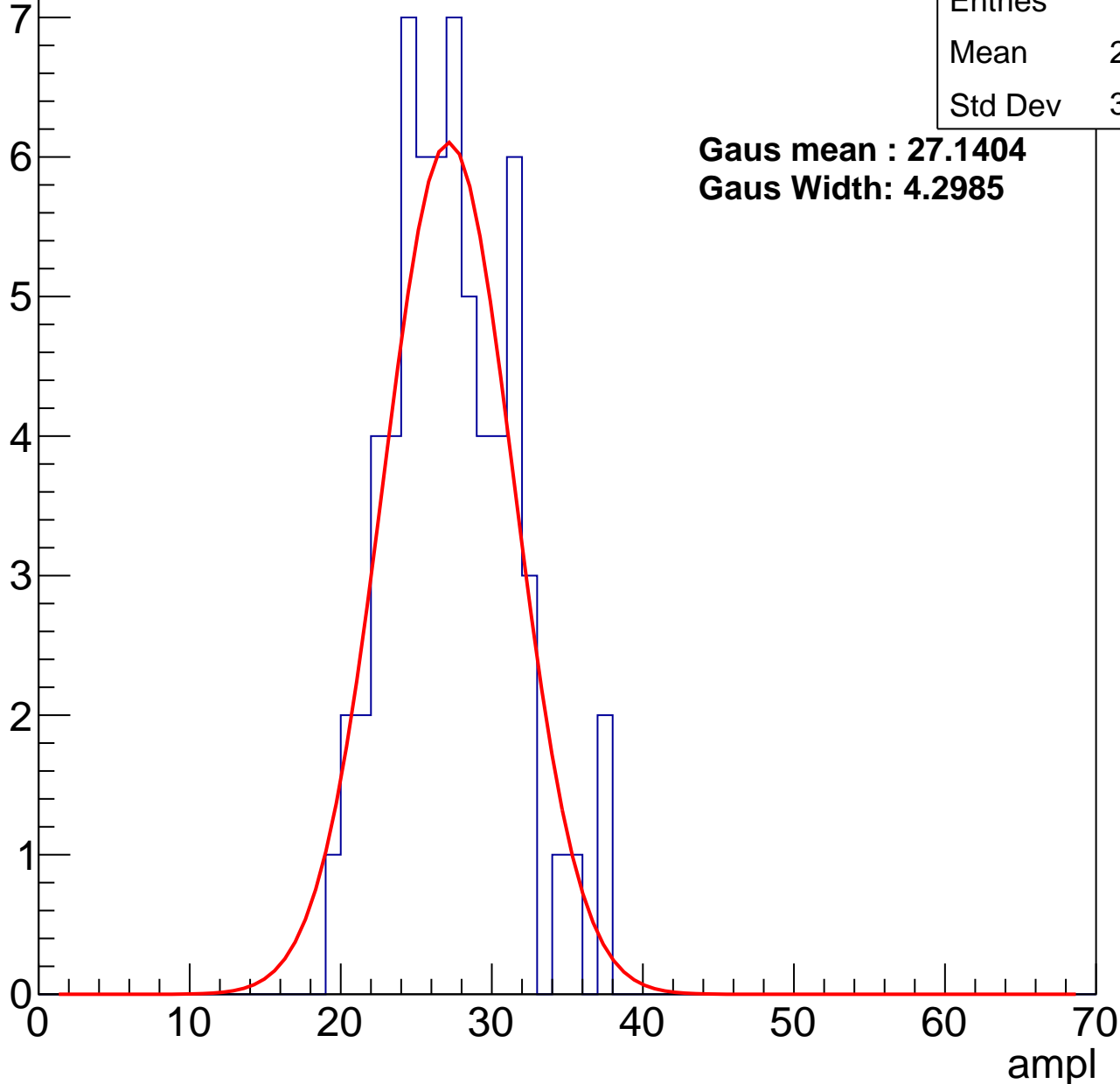
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	26.85
Std Dev	3.982

Gaus mean : 27.1404

Gaus Width: 4.2985



B0L102S, U5-ch102, adc1

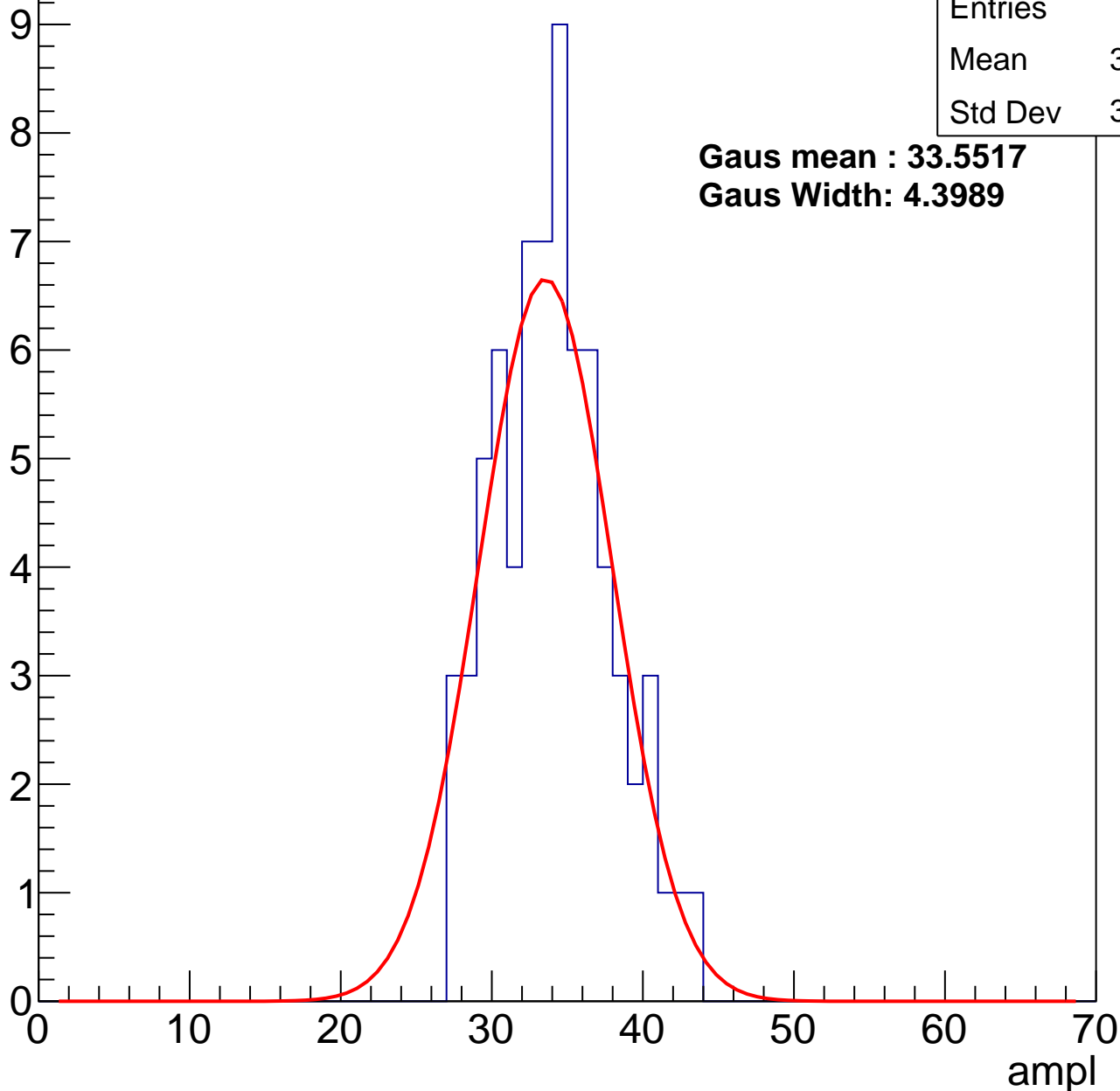
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	33.62
Std Dev	3.758

Gaus mean : 33.5517

Gaus Width: 4.3989



B0L102S, U5-ch102, adc2

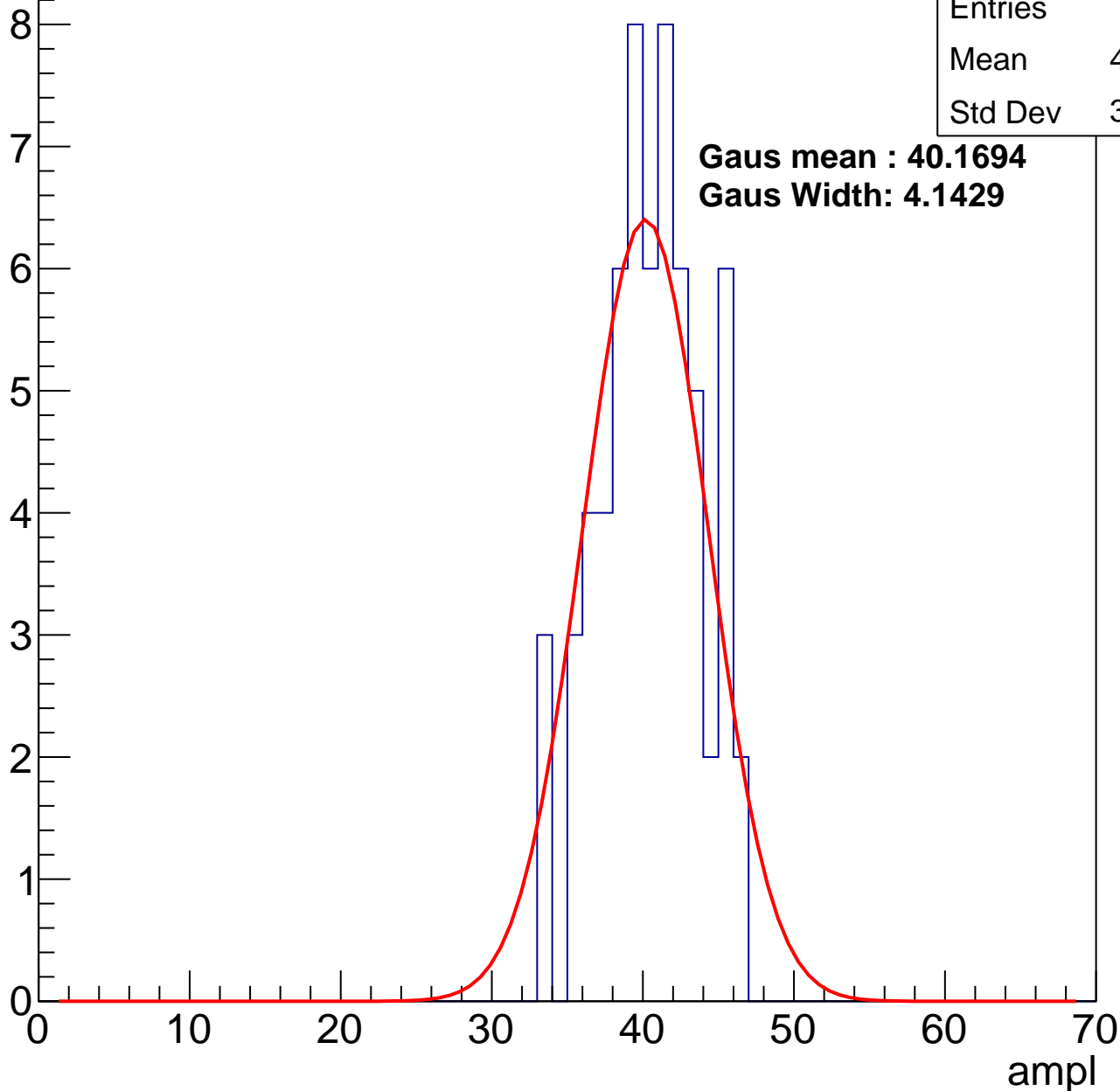
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	40.02
Std Dev	3.297

Gaus mean : 40.1694

Gaus Width: 4.1429



B0L102S, U5-ch102, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	76
Mean	47.37
Std Dev	3.594

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

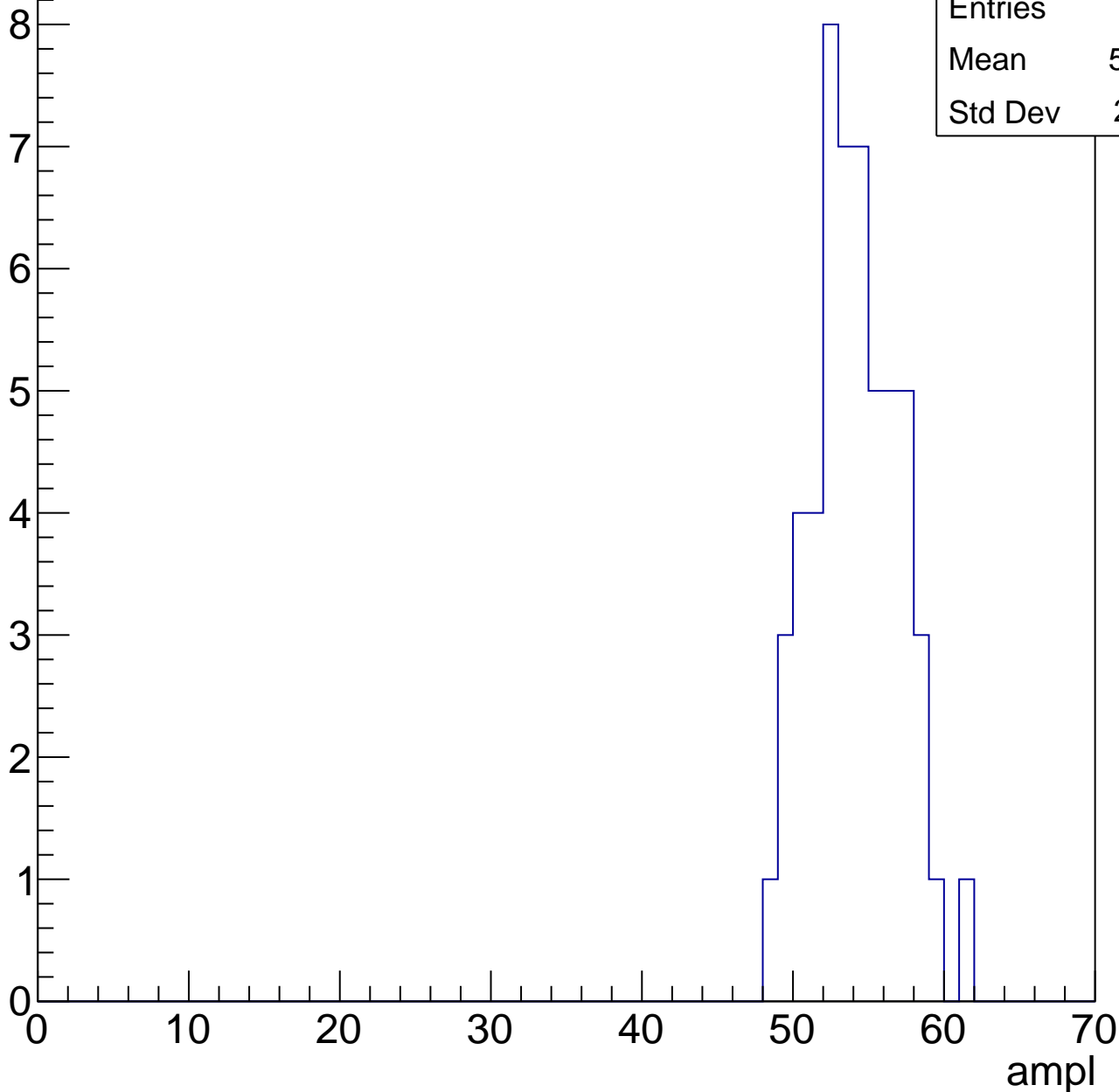
70

B0L102S, U5-ch102, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	53.67
Std Dev	2.841



B0L102S, U5-ch102, adc5

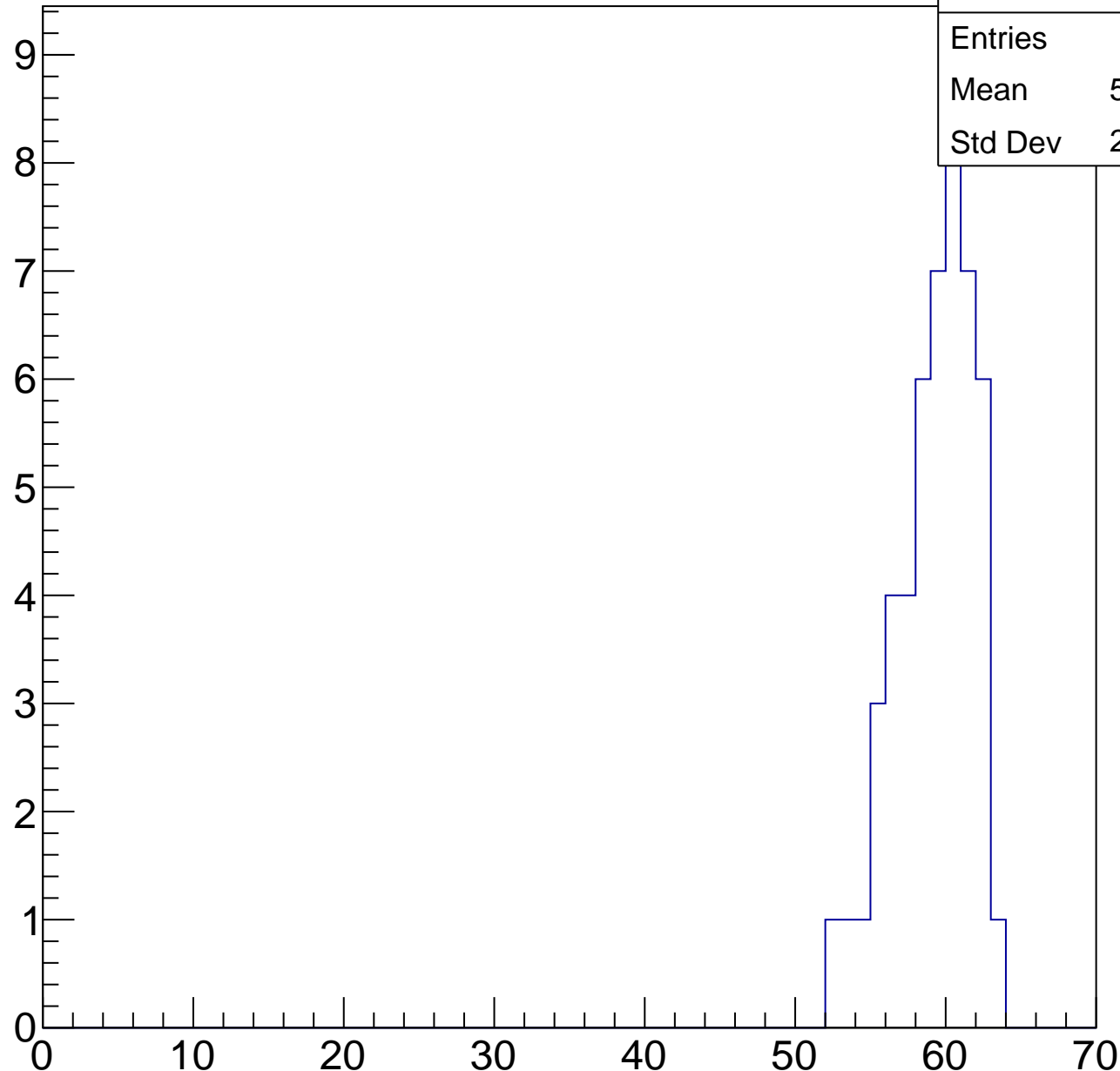
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.78
Std Dev	2.532

ampl

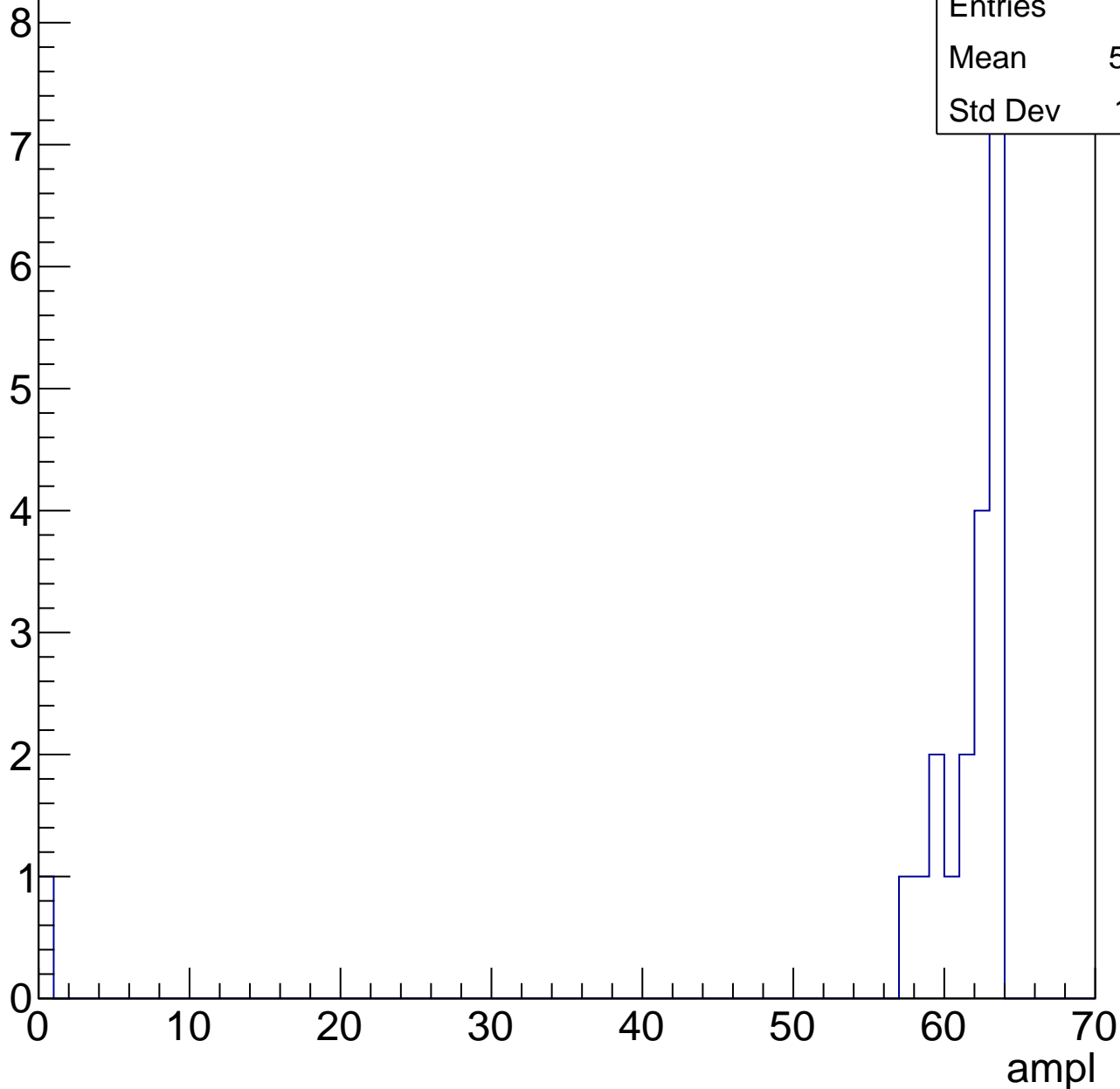


B0L102S, U5-ch102, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

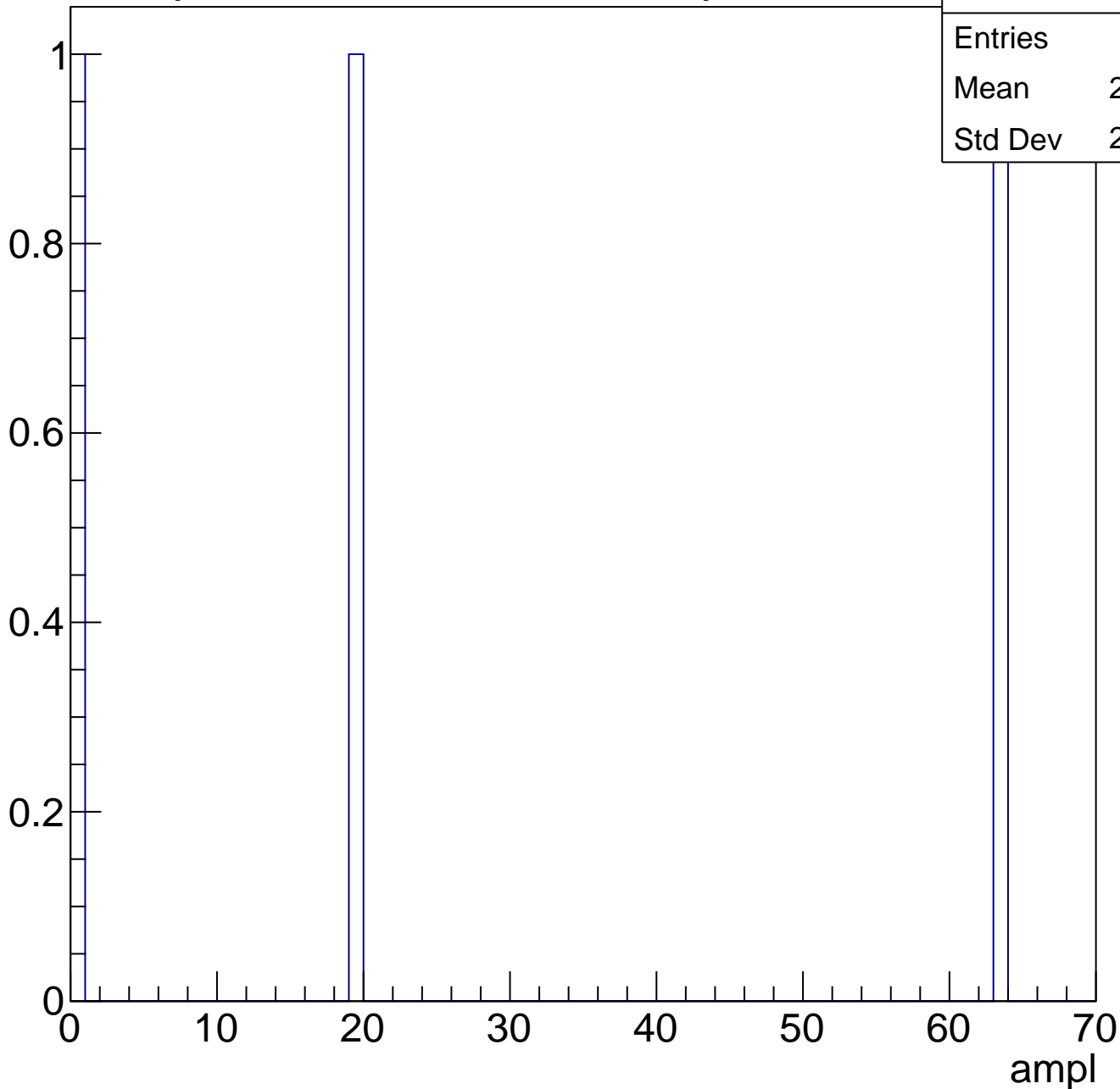
Entries	20
Mean	58.35
Std Dev	13.51



B0L102S, U5-ch102, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B0L102S, U5-ch103, adc0

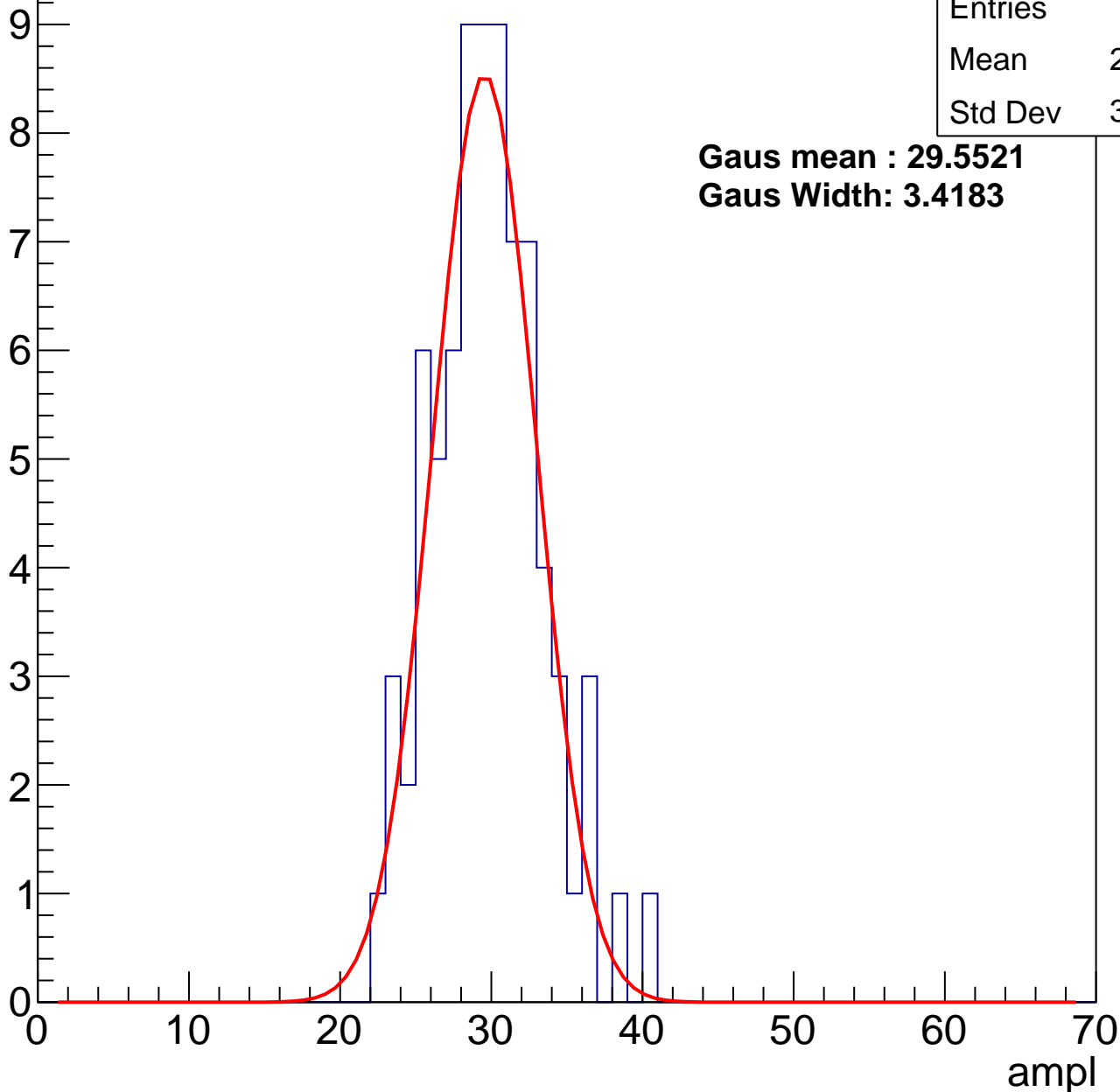
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	29.35
Std Dev	3.596

Gaus mean : 29.5521

Gaus Width: 3.4183



B0L102S, U5-ch103, adc1

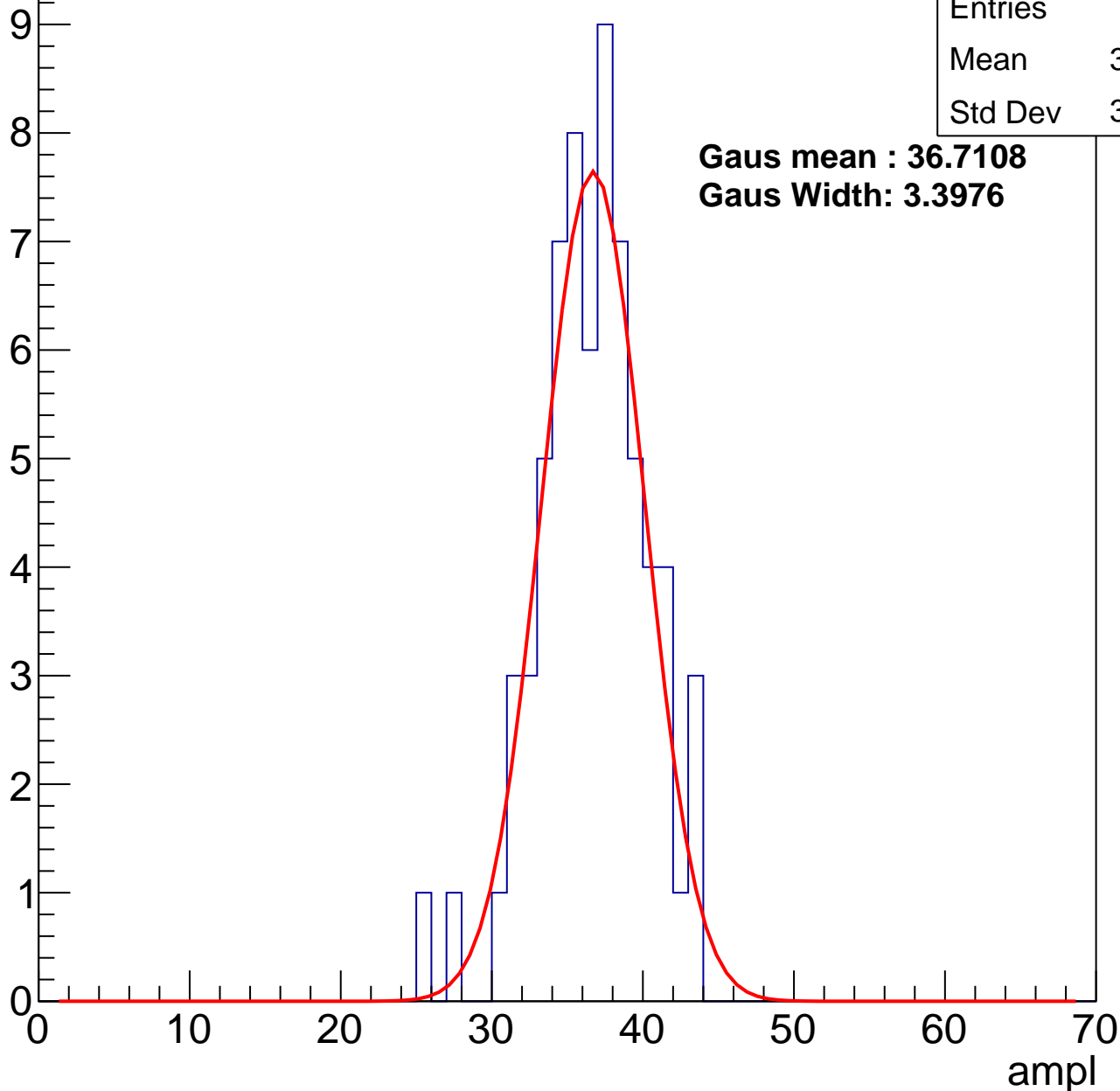
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	36.16
Std Dev	3.575

Gaus mean : 36.7108

Gaus Width: 3.3976



B0L102S, U5-ch103, adc2

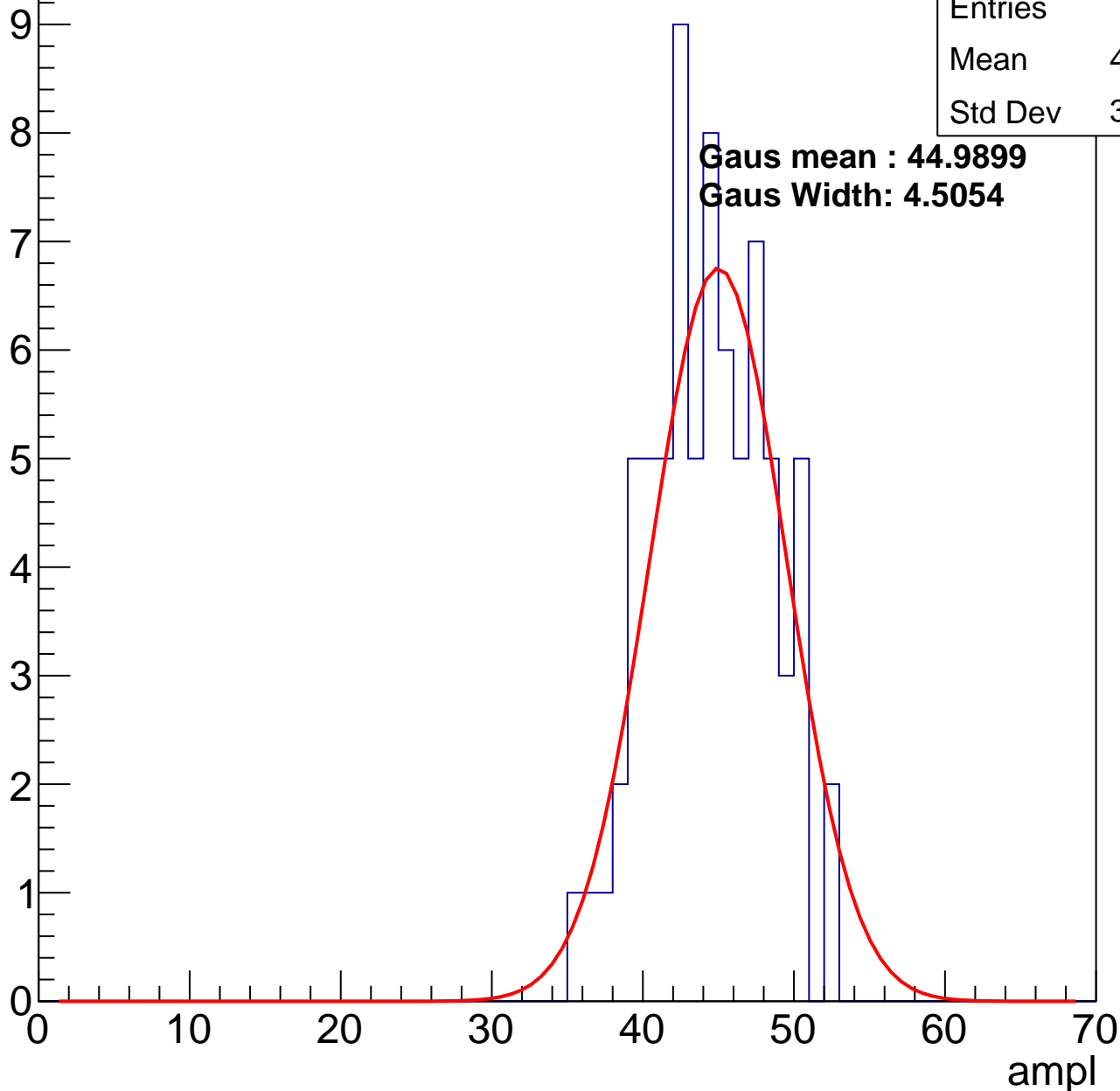
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	43.99
Std Dev	3.845

Gaus mean : 44.9899

Gaus Width: 4.5054

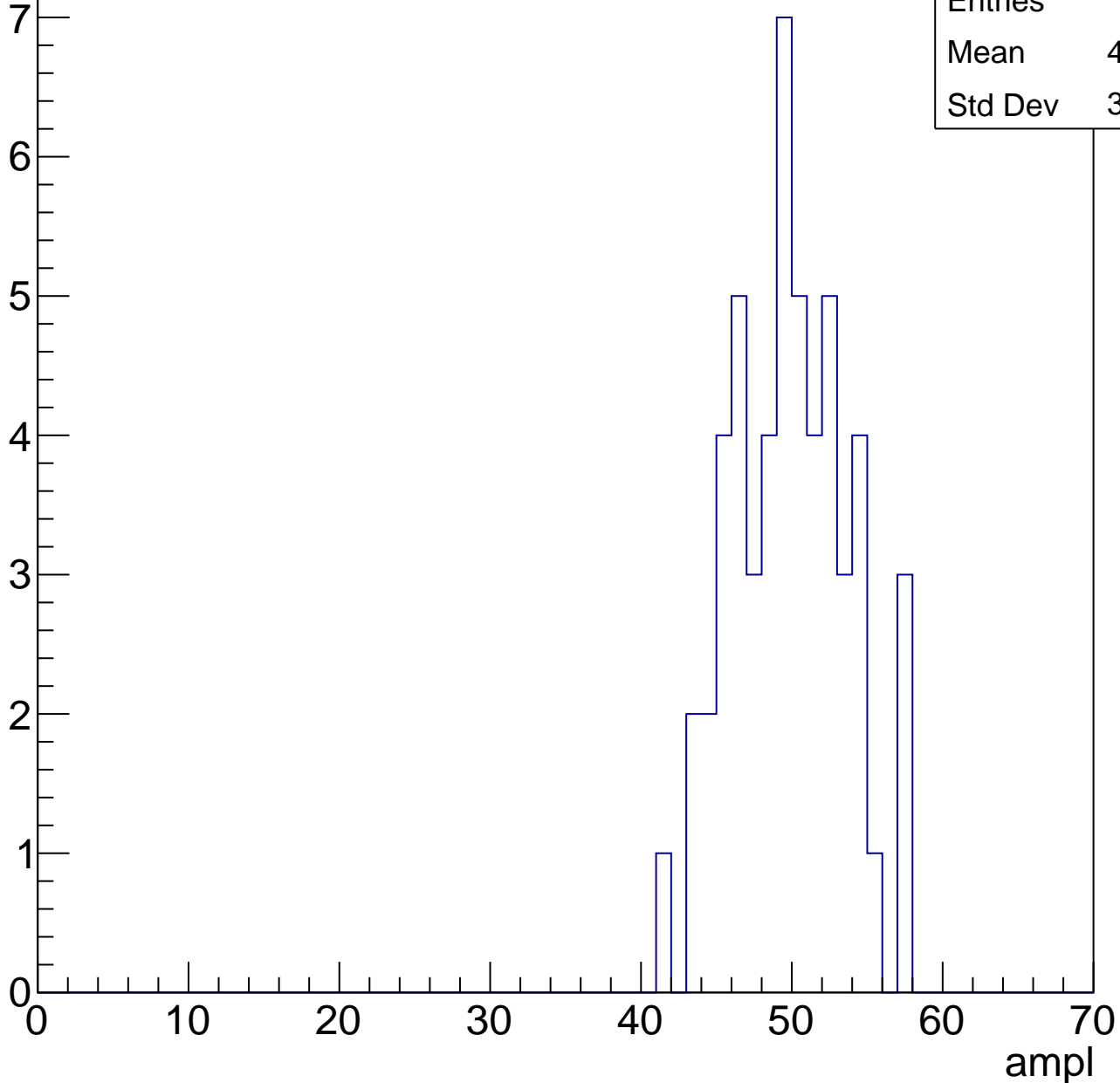


B0L102S, U5-ch103, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	49.36
Std Dev	3.752

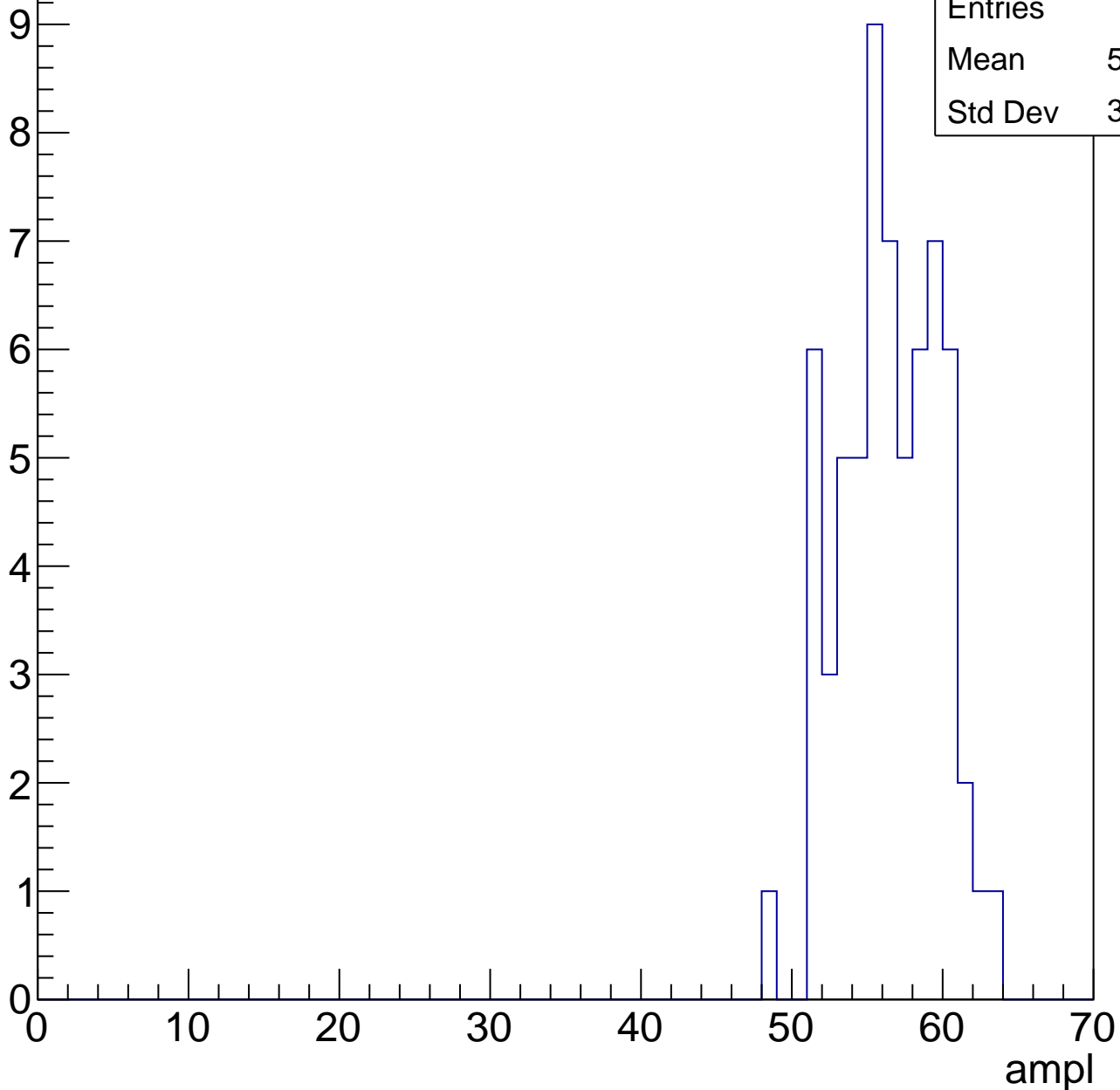


B0L102S, U5-ch103, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	56.02
Std Dev	3.209

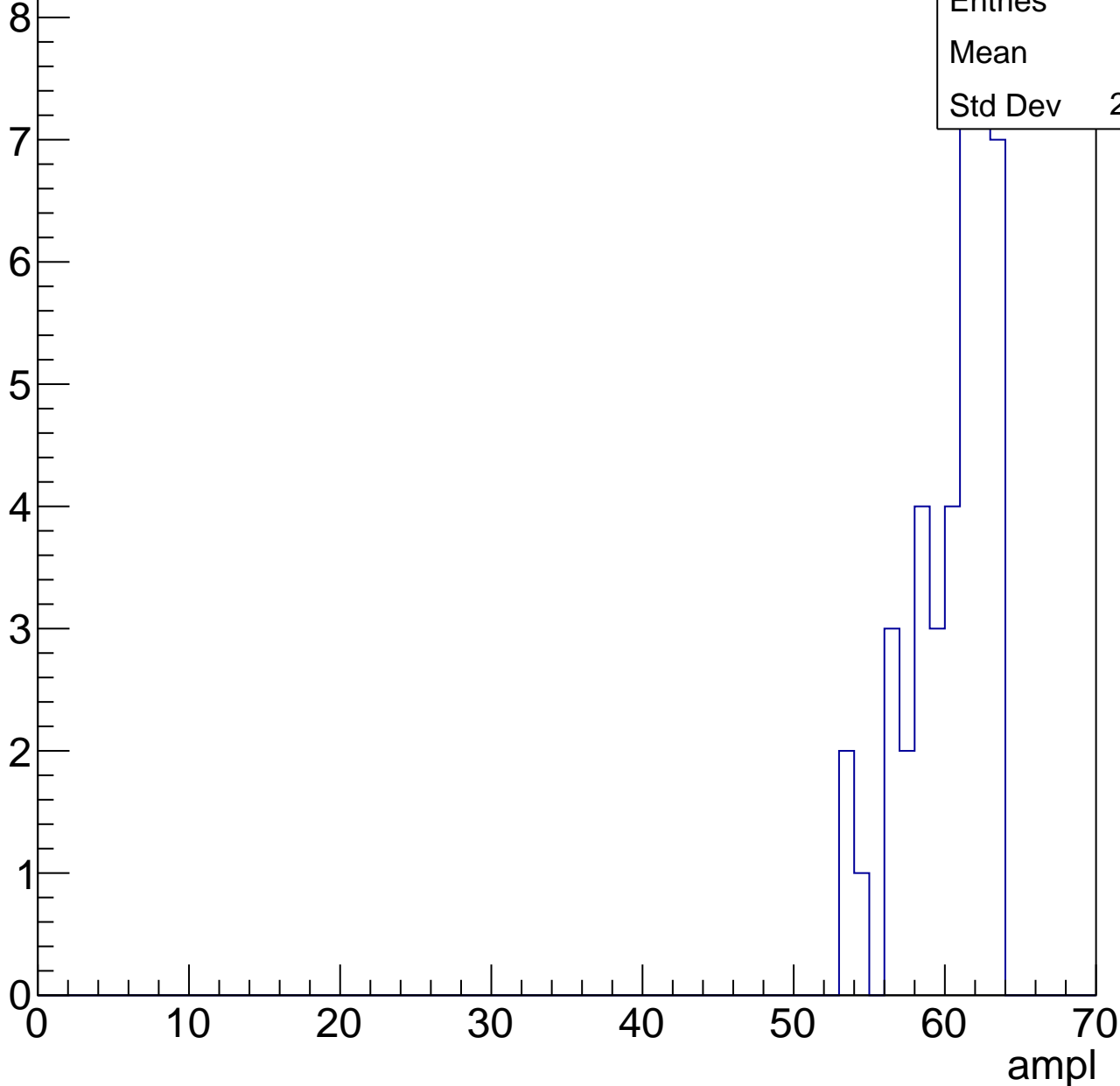


B0L102S, U5-ch103, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	59.9
Std Dev	2.767



B0L102S, U5-ch103, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch103, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch104, adc0

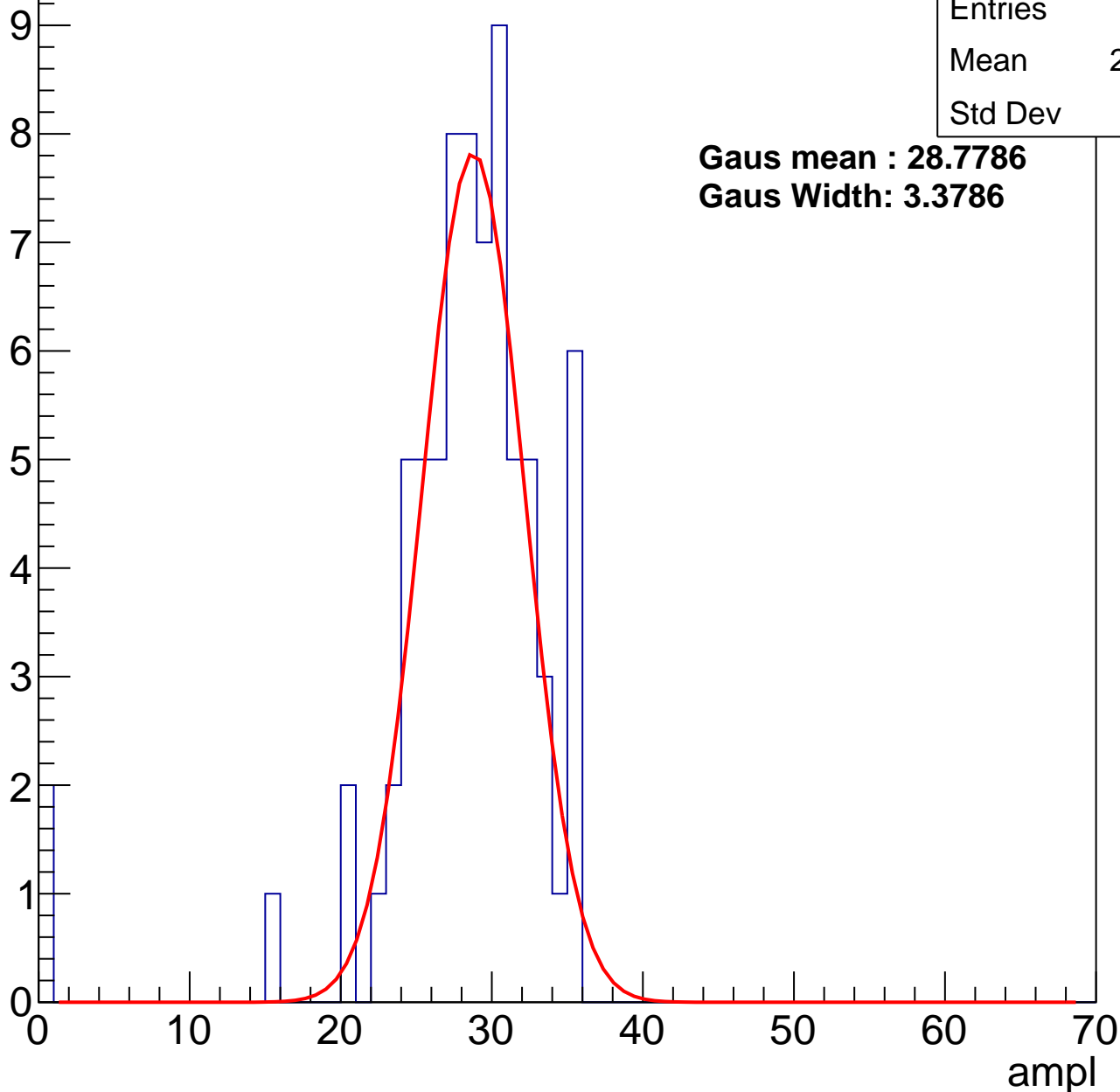
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	27.59
Std Dev	5.96

Gaus mean : 28.7786

Gaus Width: 3.3786



B0L102S, U5-ch104, adc1

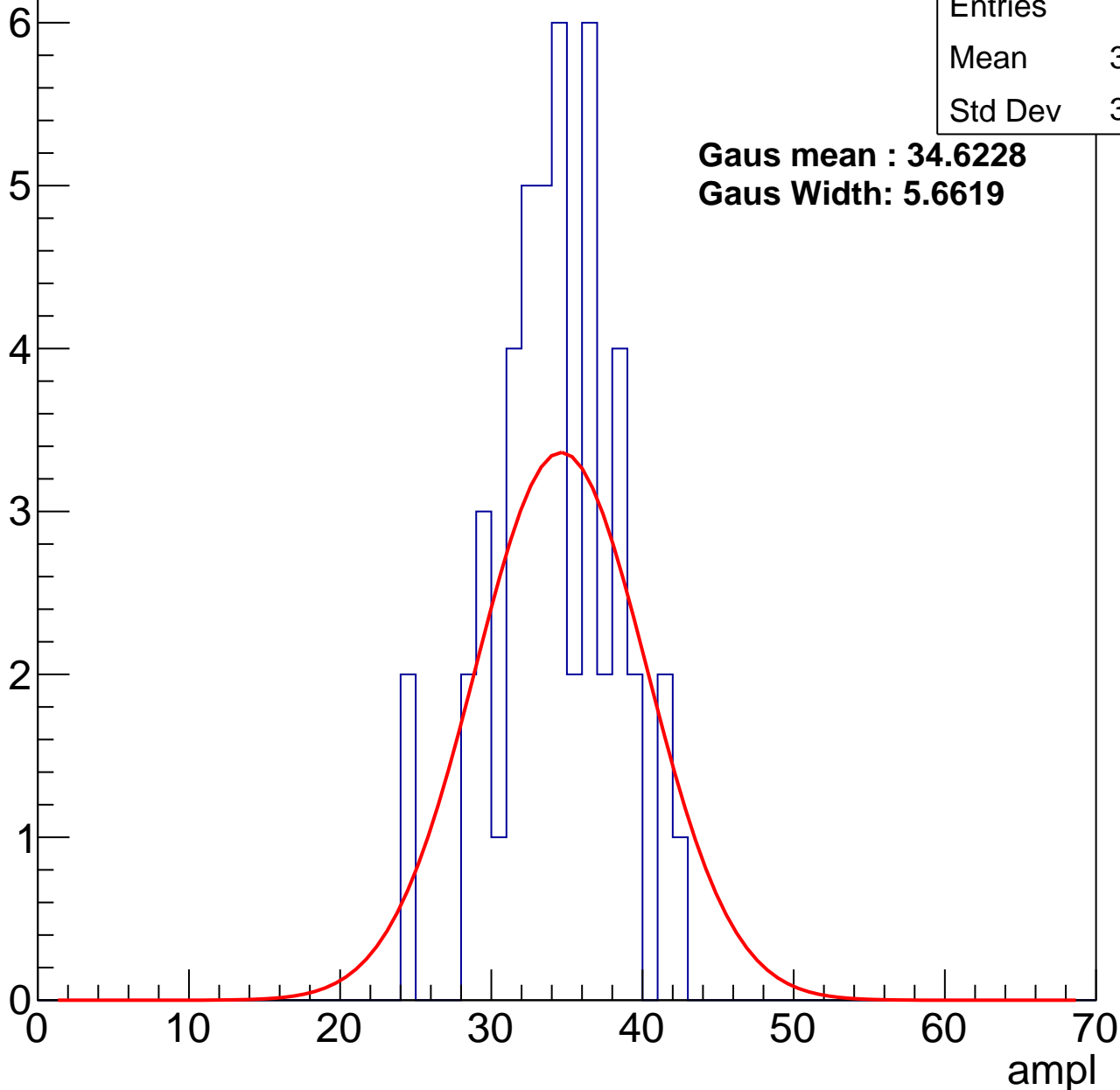
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	47
Mean	33.79
Std Dev	3.973

Gaus mean : 34.6228

Gaus Width: 5.6619



B0L102S, U5-ch104, adc2

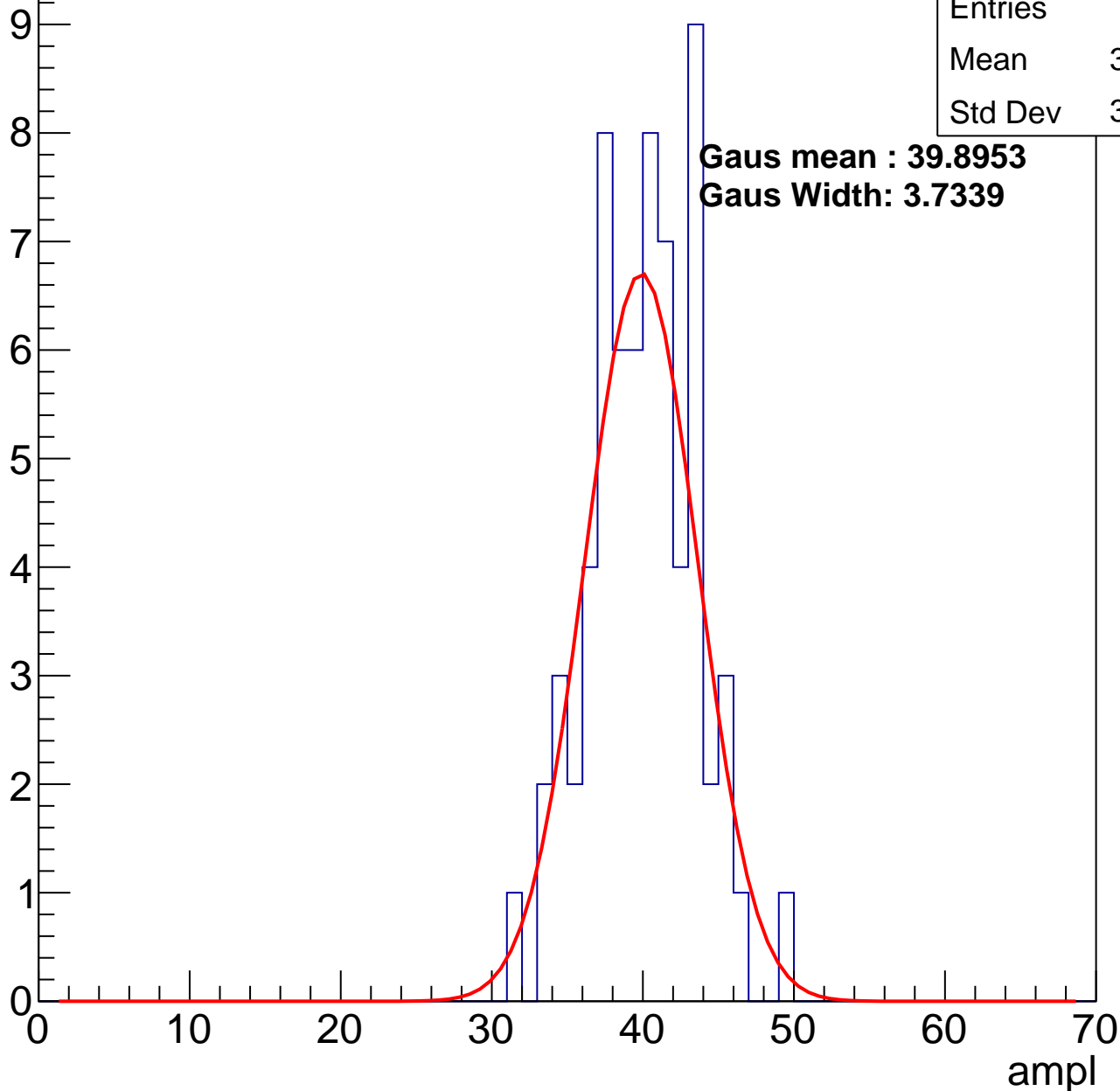
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	39.57
Std Dev	3.495

Gaus mean : 39.8953

Gaus Width: 3.7339

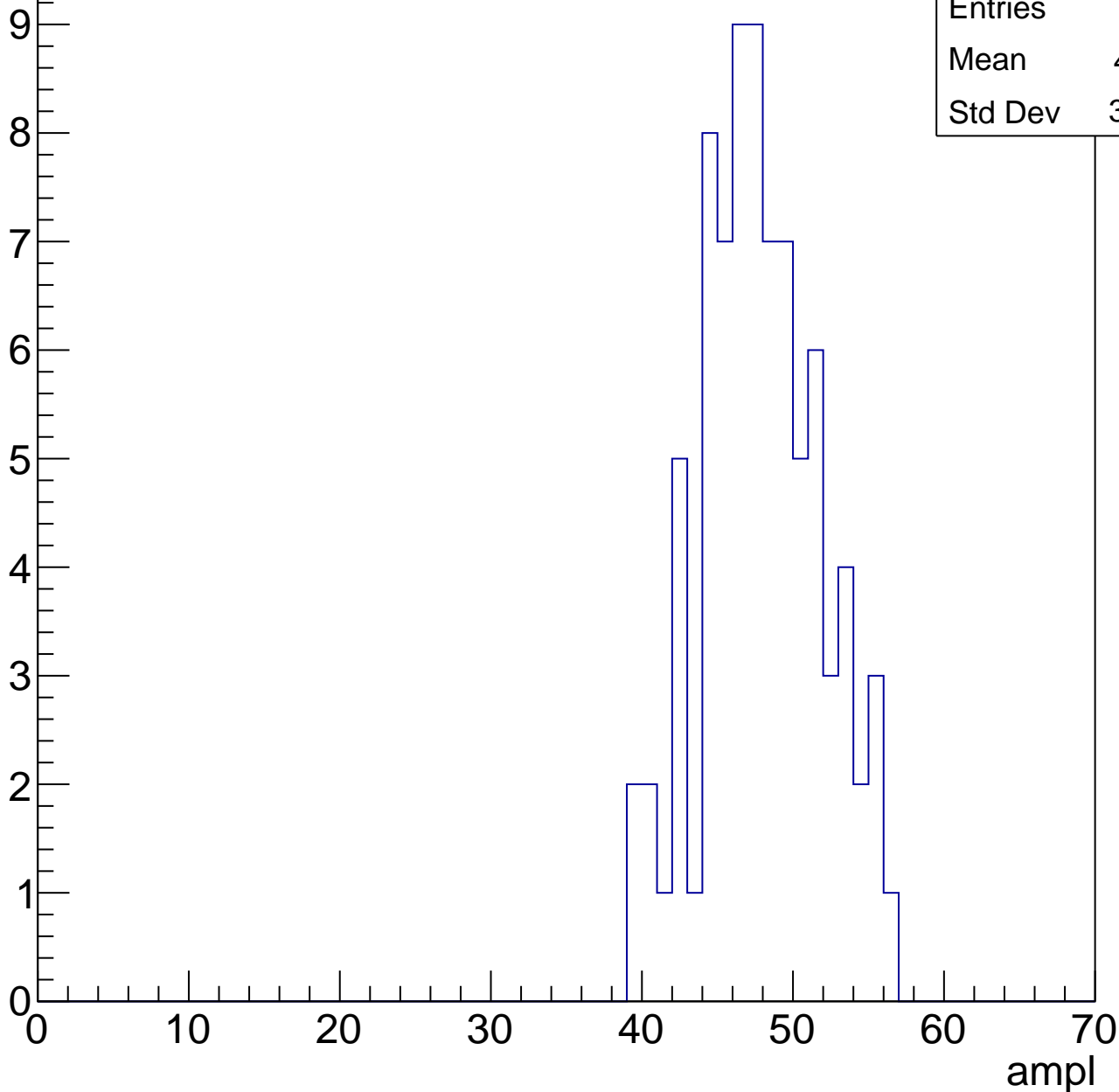


B0L102S, U5-ch104, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	82
Mean	47.41
Std Dev	3.926

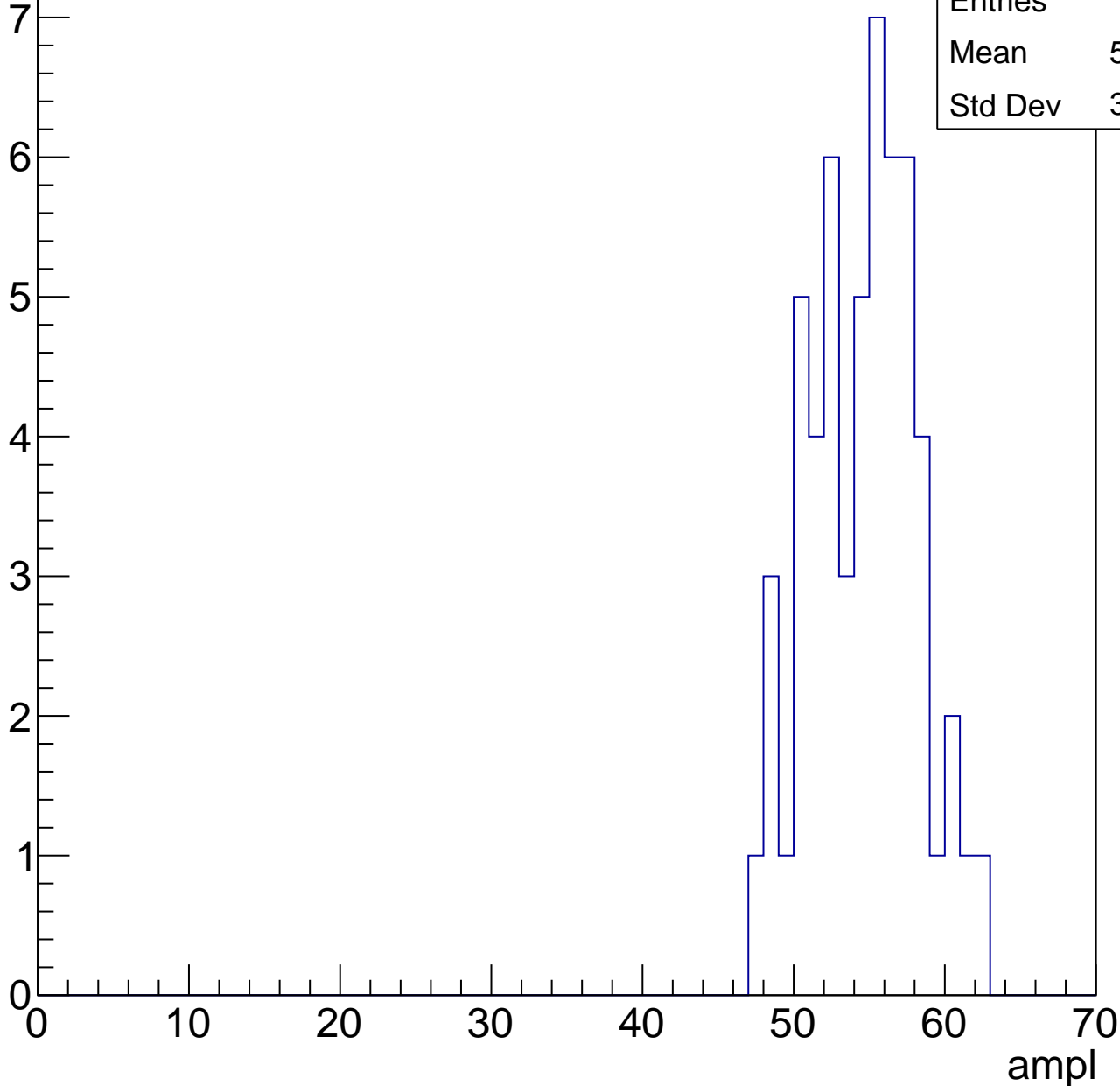


B0L102S, U5-ch104, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	54.14
Std Dev	3.492



B0L102S, U5-ch104, adc5

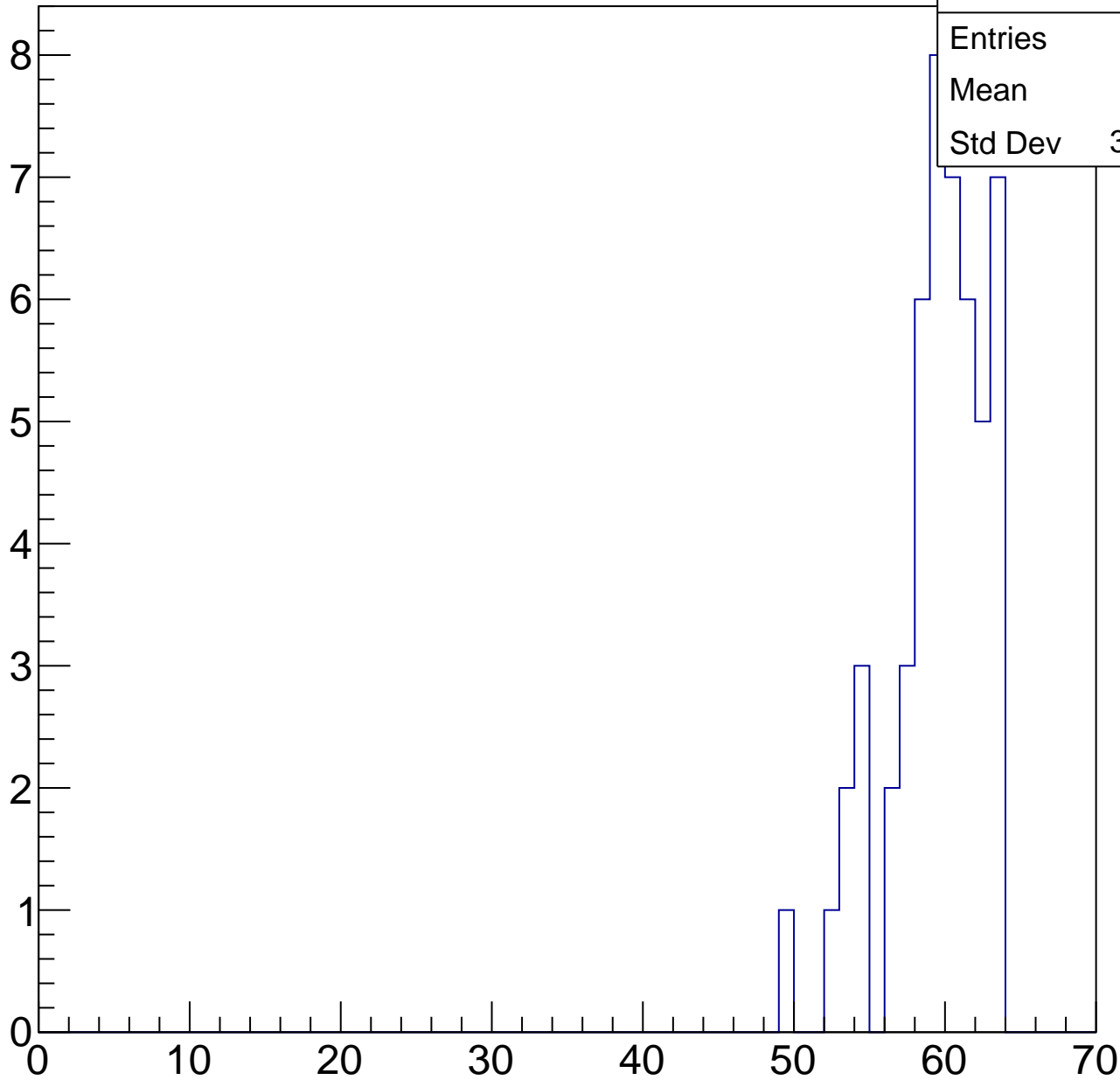
calib_packv5_042523_0143.root, FC#12, port B1

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59
Std Dev	3.193

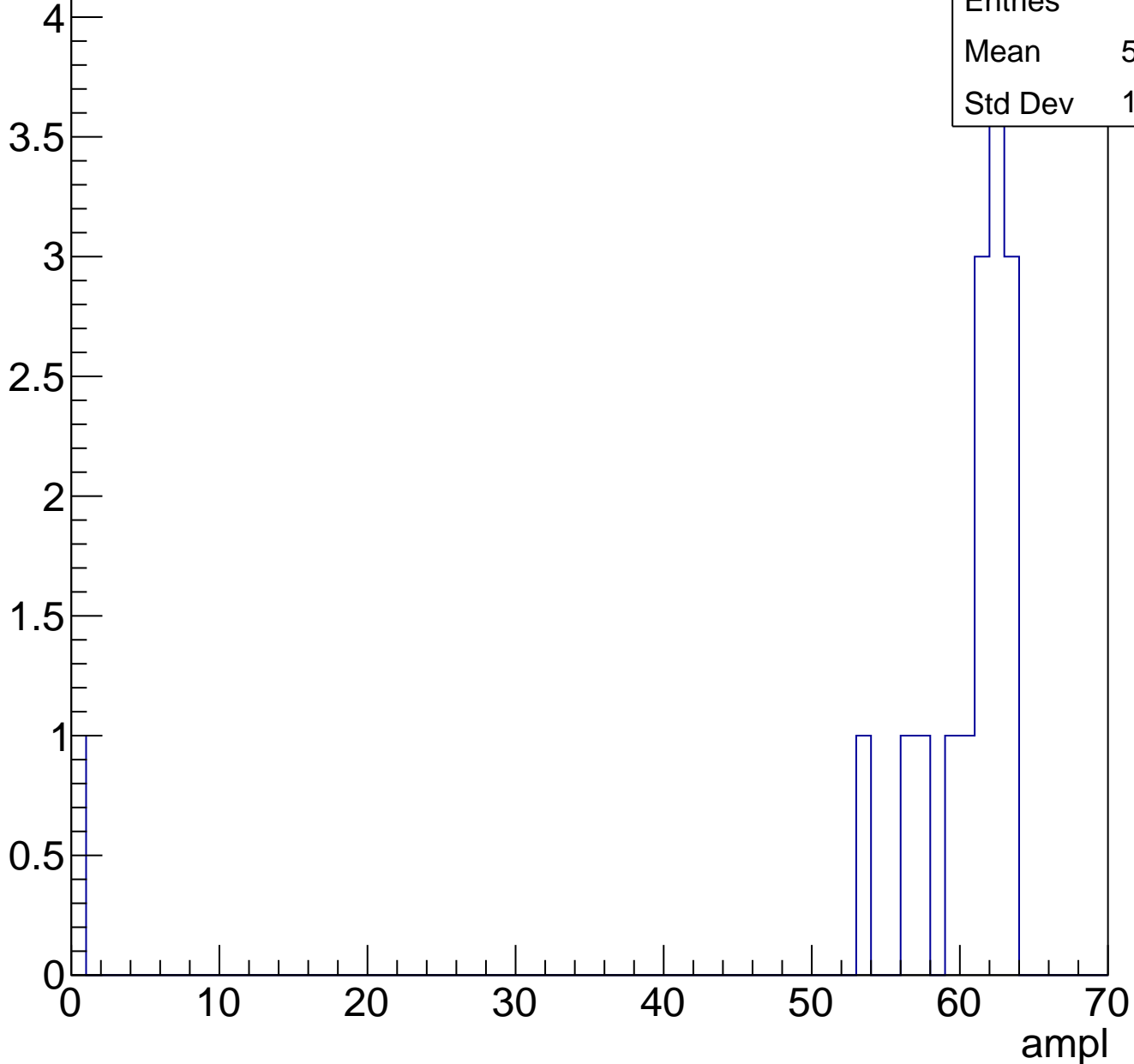
ampl



B0L102S, U5-ch104, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	16
Mean	56.56
Std Dev	14.86

B0L102S, U5-ch104, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch105, adc0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	87
Mean	28.13
Std Dev	5.599

Gaus mean : 29.0683

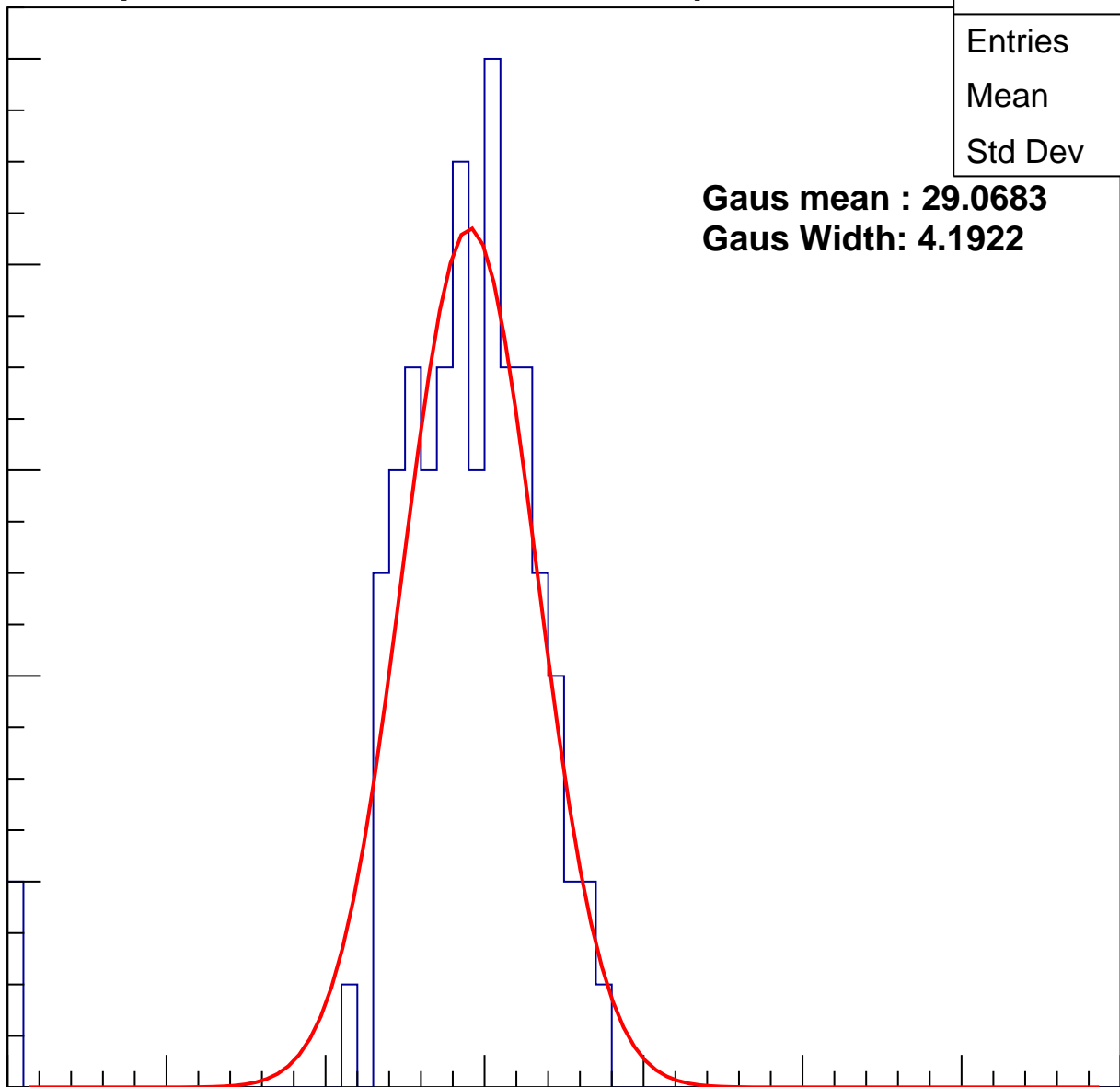
Gaus Width: 4.1922

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch105, adc1

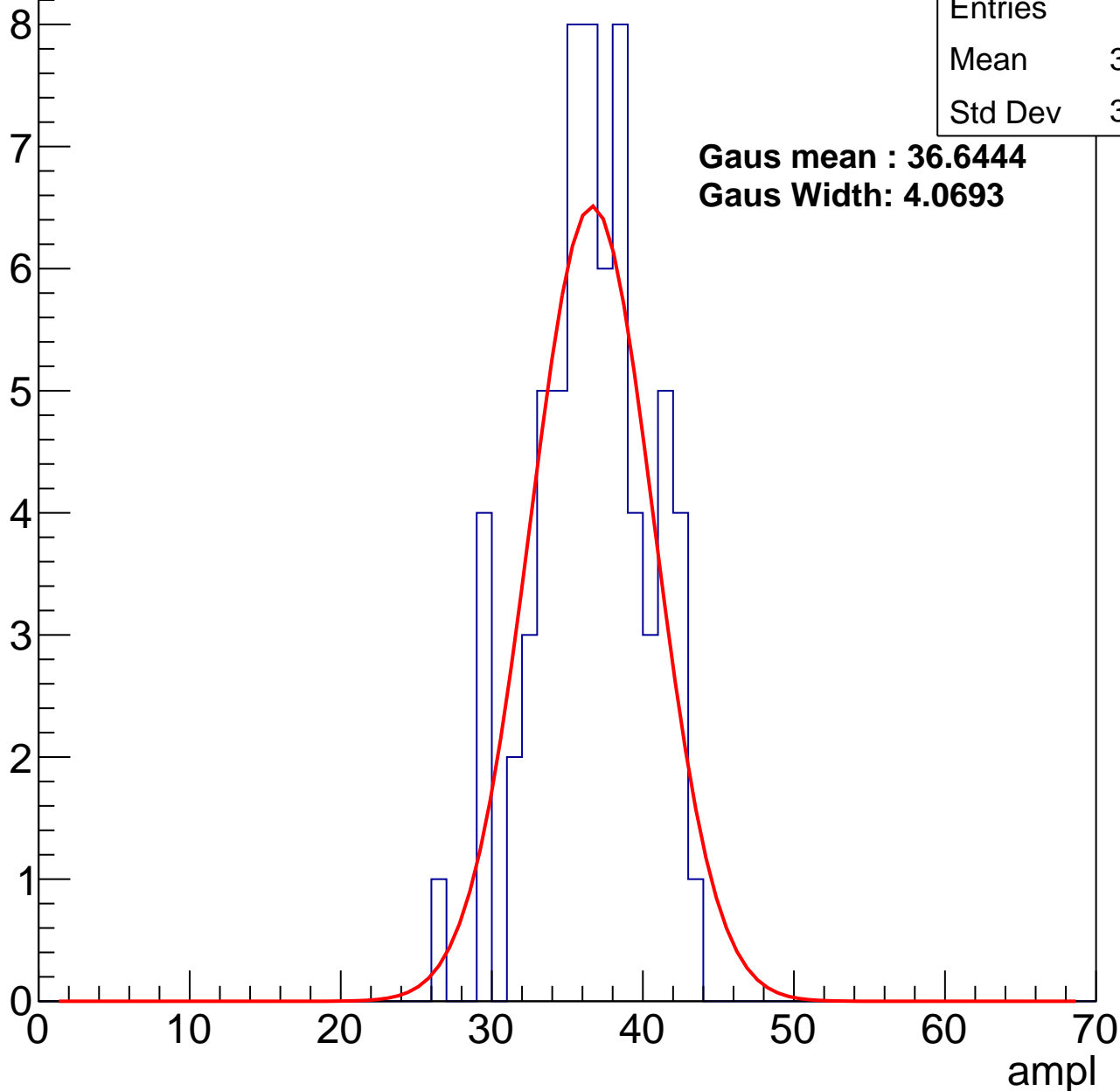
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	36.13
Std Dev	3.677

Gaus mean : 36.6444

Gaus Width: 4.0693



B0L102S, U5-ch105, adc2

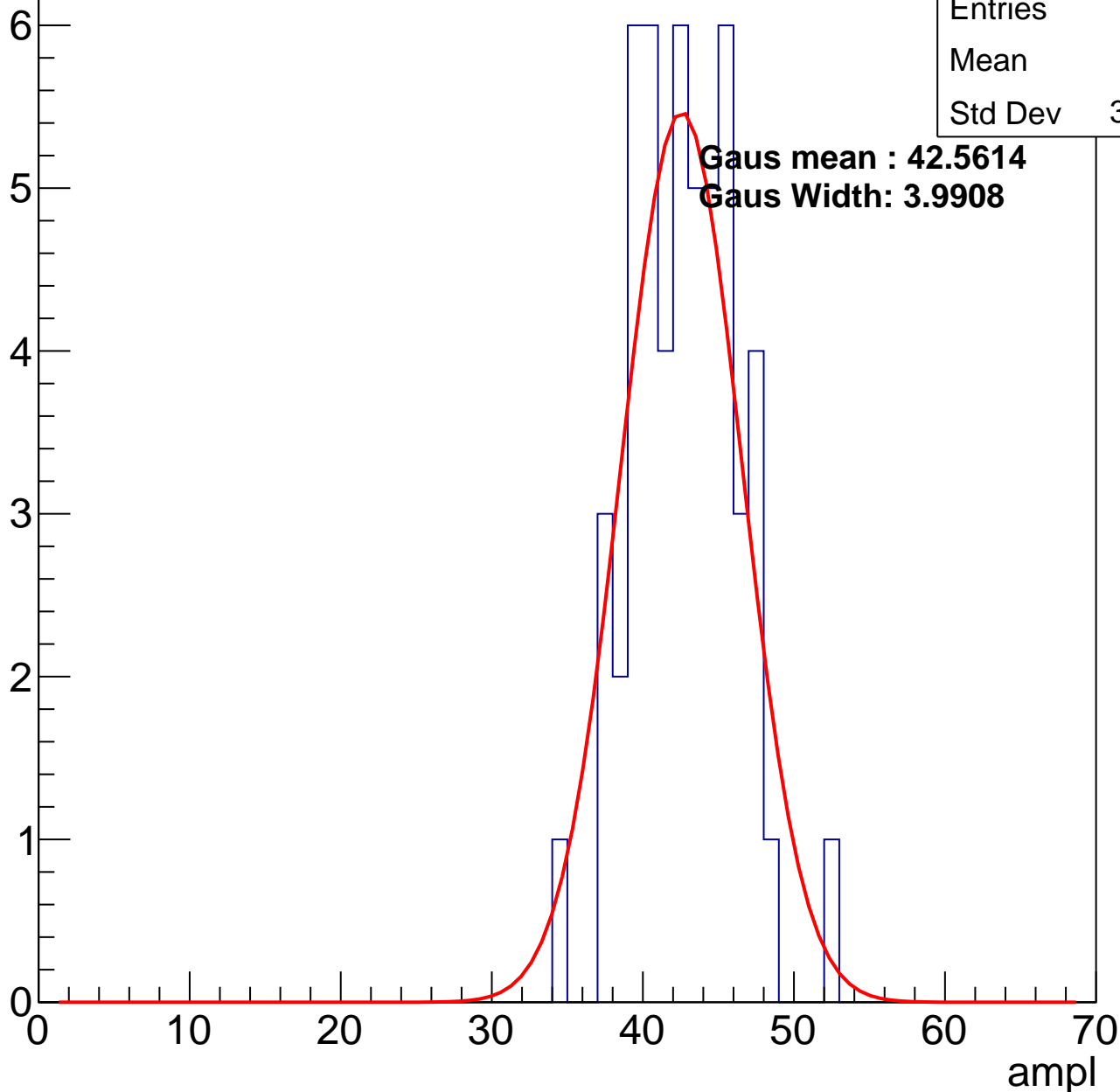
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	42.3
Std Dev	3.396

Gaus mean : 42.5614

Gaus Width: 3.9908

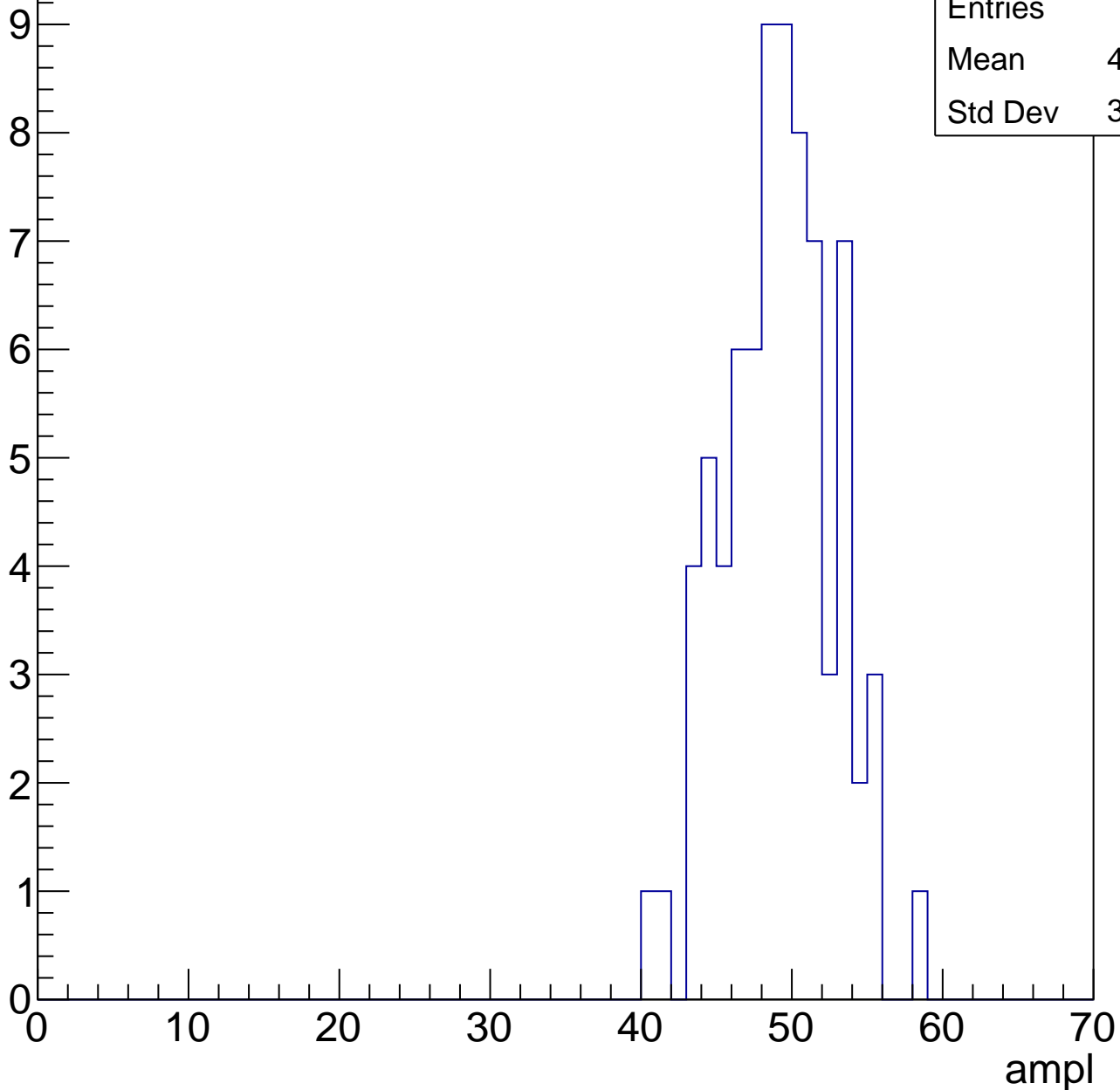


B0L102S, U5-ch105, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	48.67
Std Dev	3.574

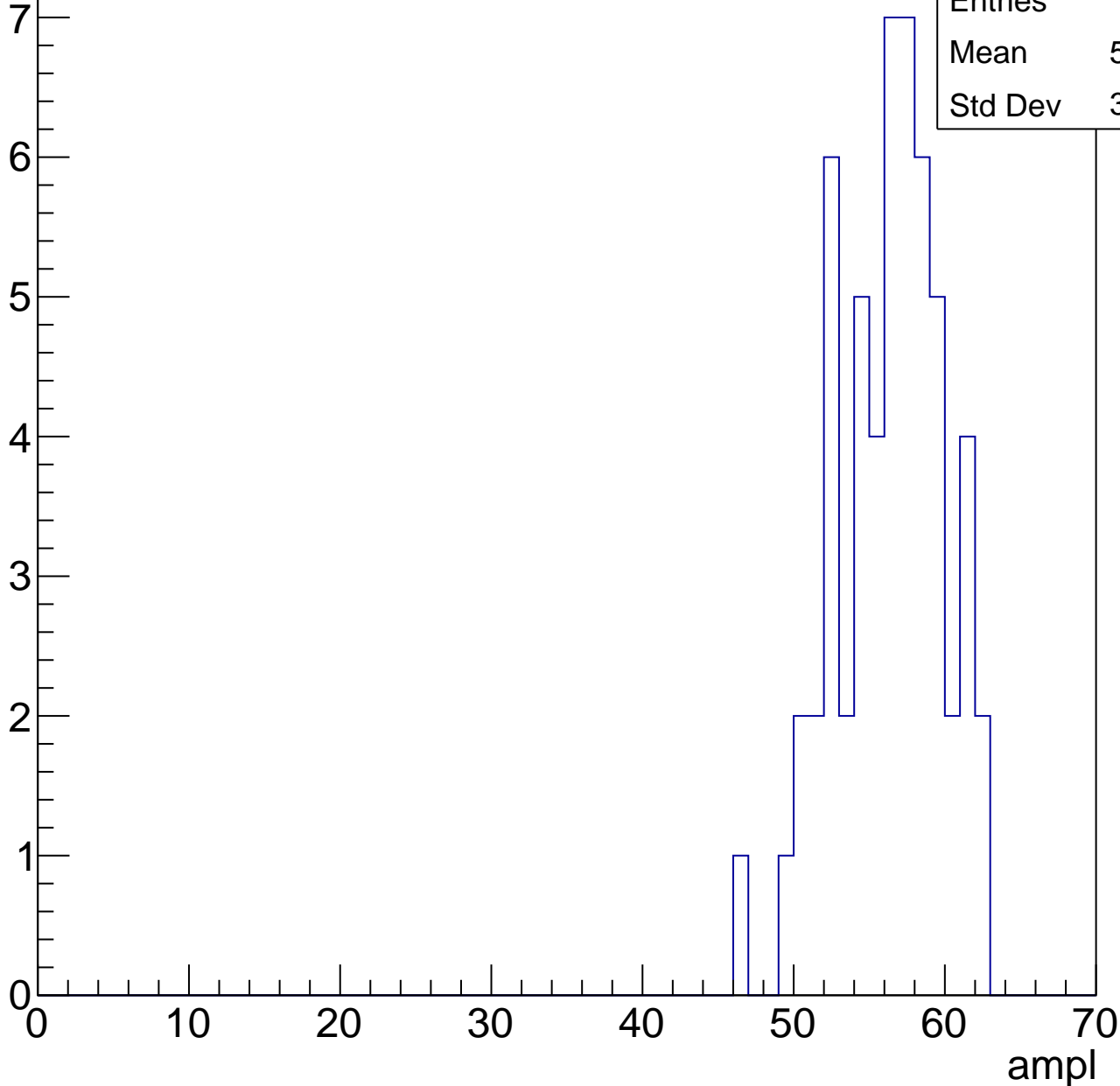


B0L102S, U5-ch105, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

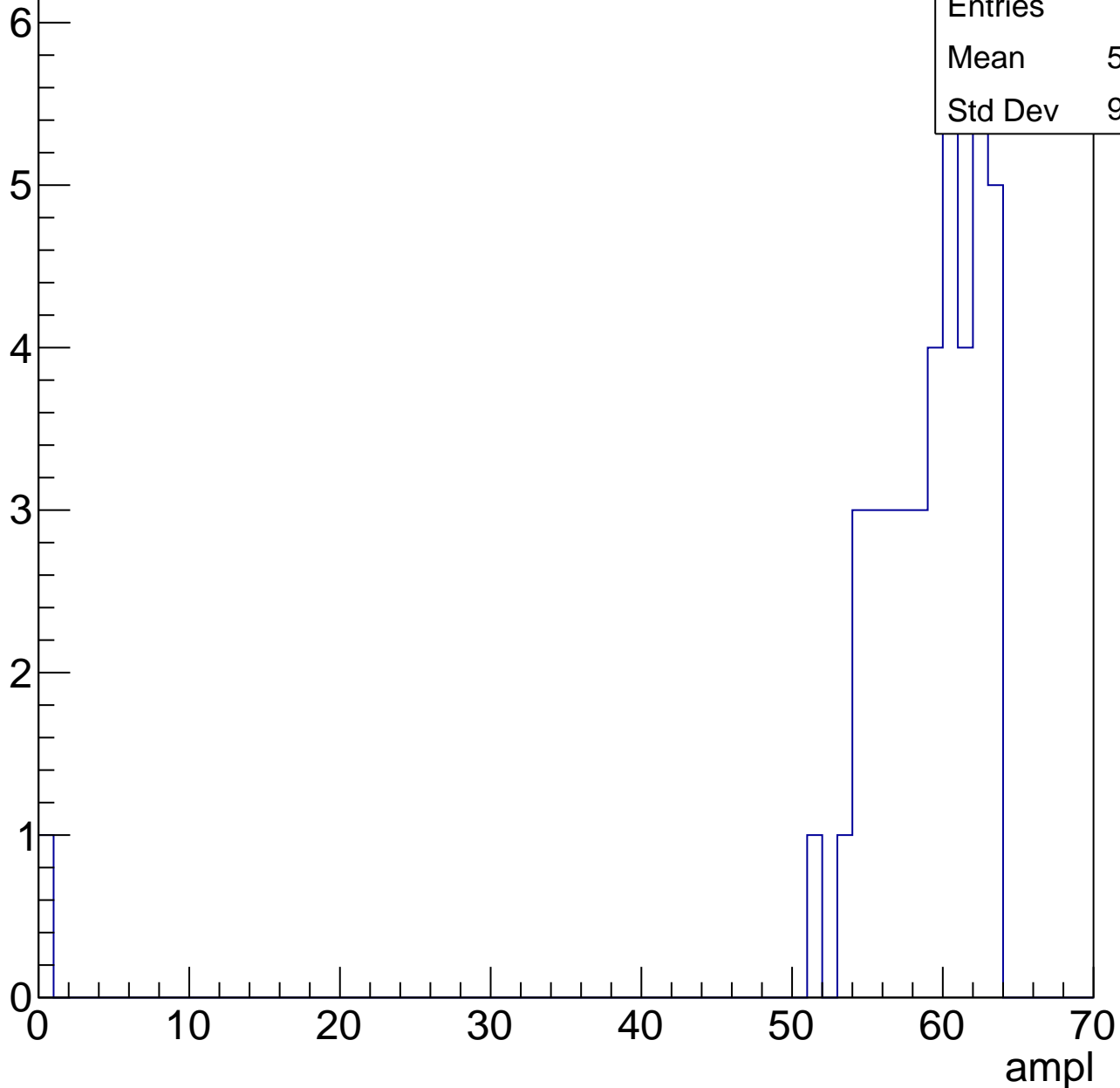
Entries	56
Mean	55.84
Std Dev	3.504



B0L102S, U5-ch105, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

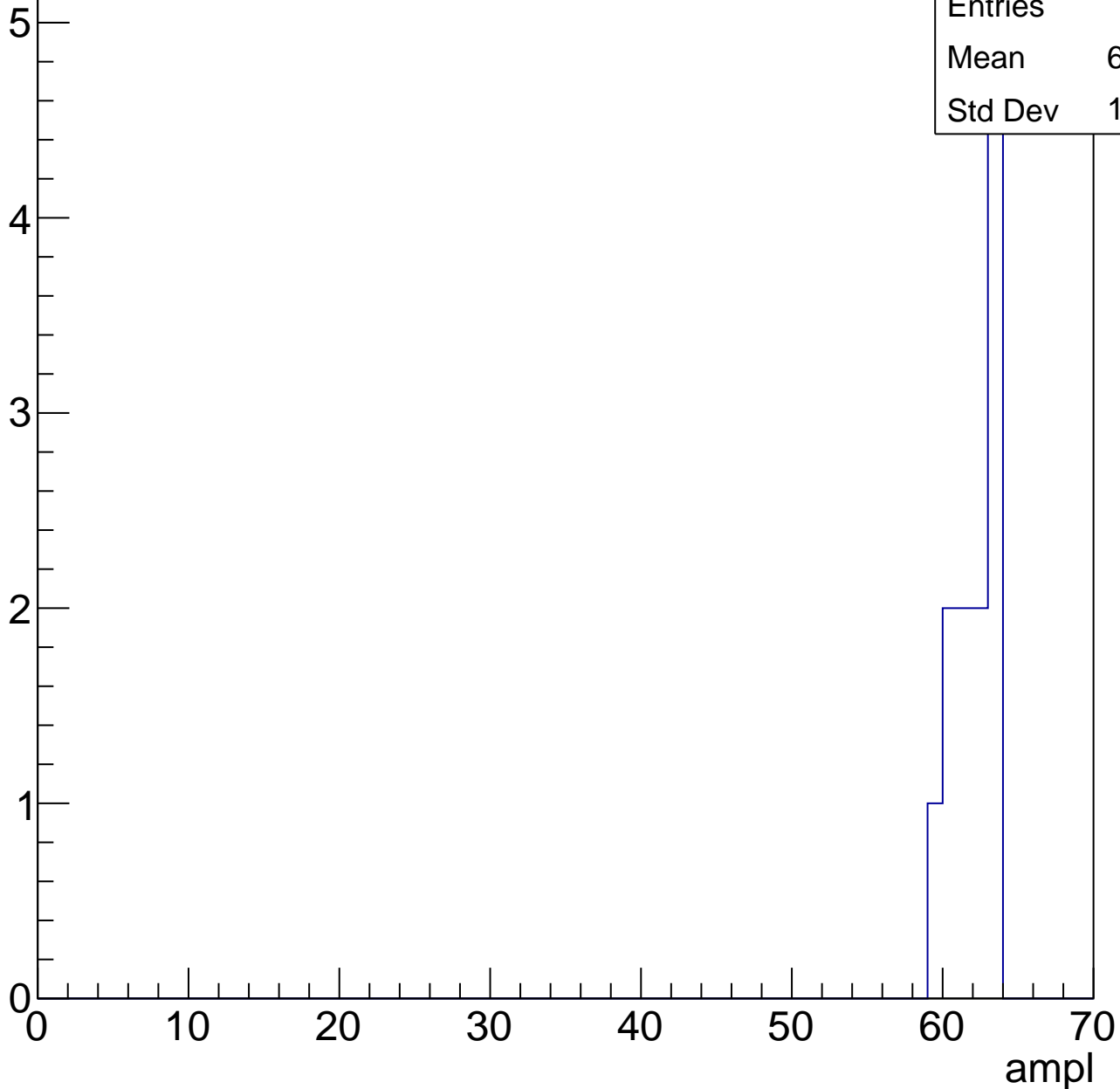


B0L102S, U5-ch105, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	12
Mean	61.67
Std Dev	1.374



B0L102S, U5-ch105, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch106, adc0

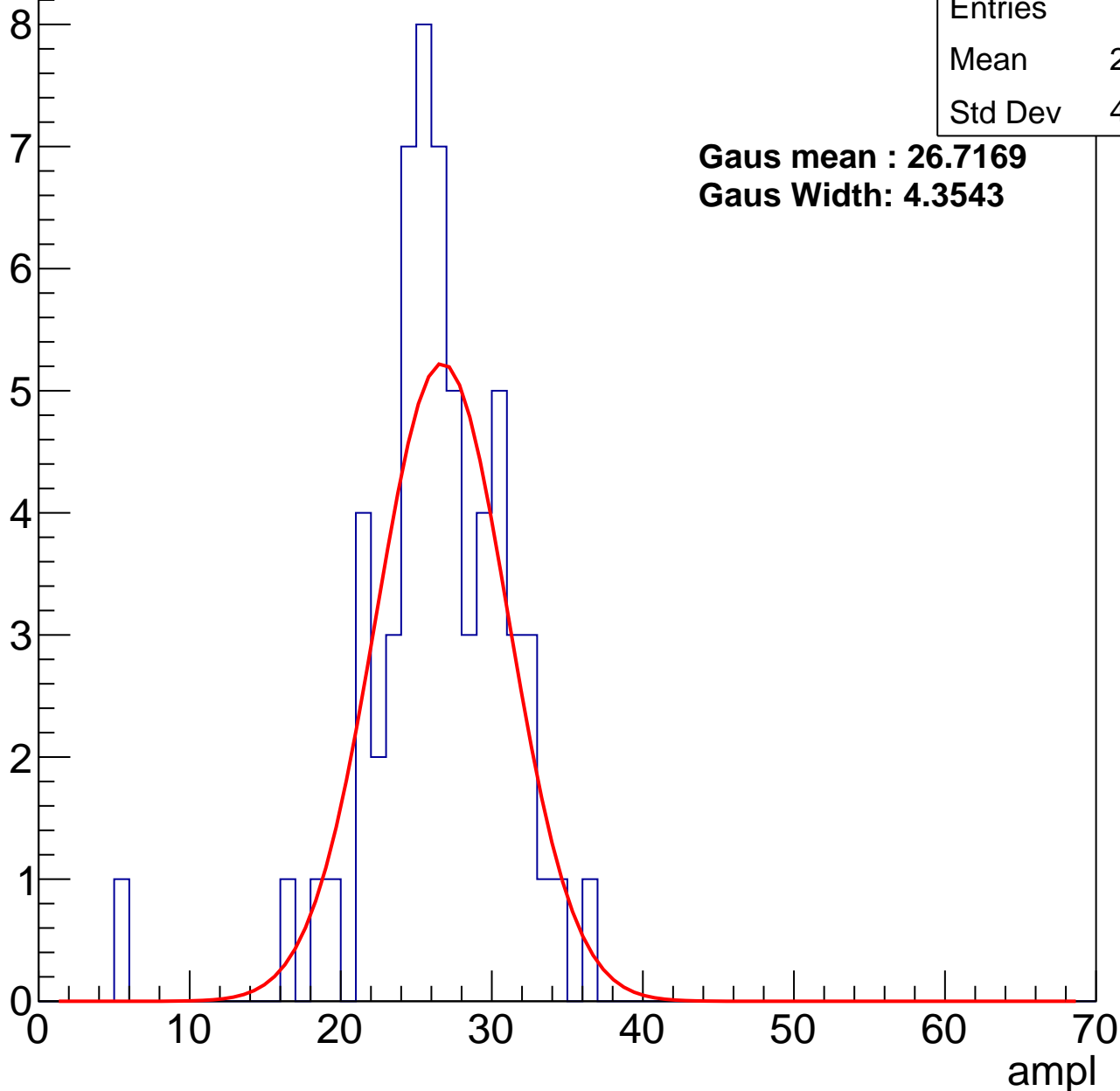
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	25.93
Std Dev	4.752

Gaus mean : 26.7169

Gaus Width: 4.3543



B0L102S, U5-ch106, adc1

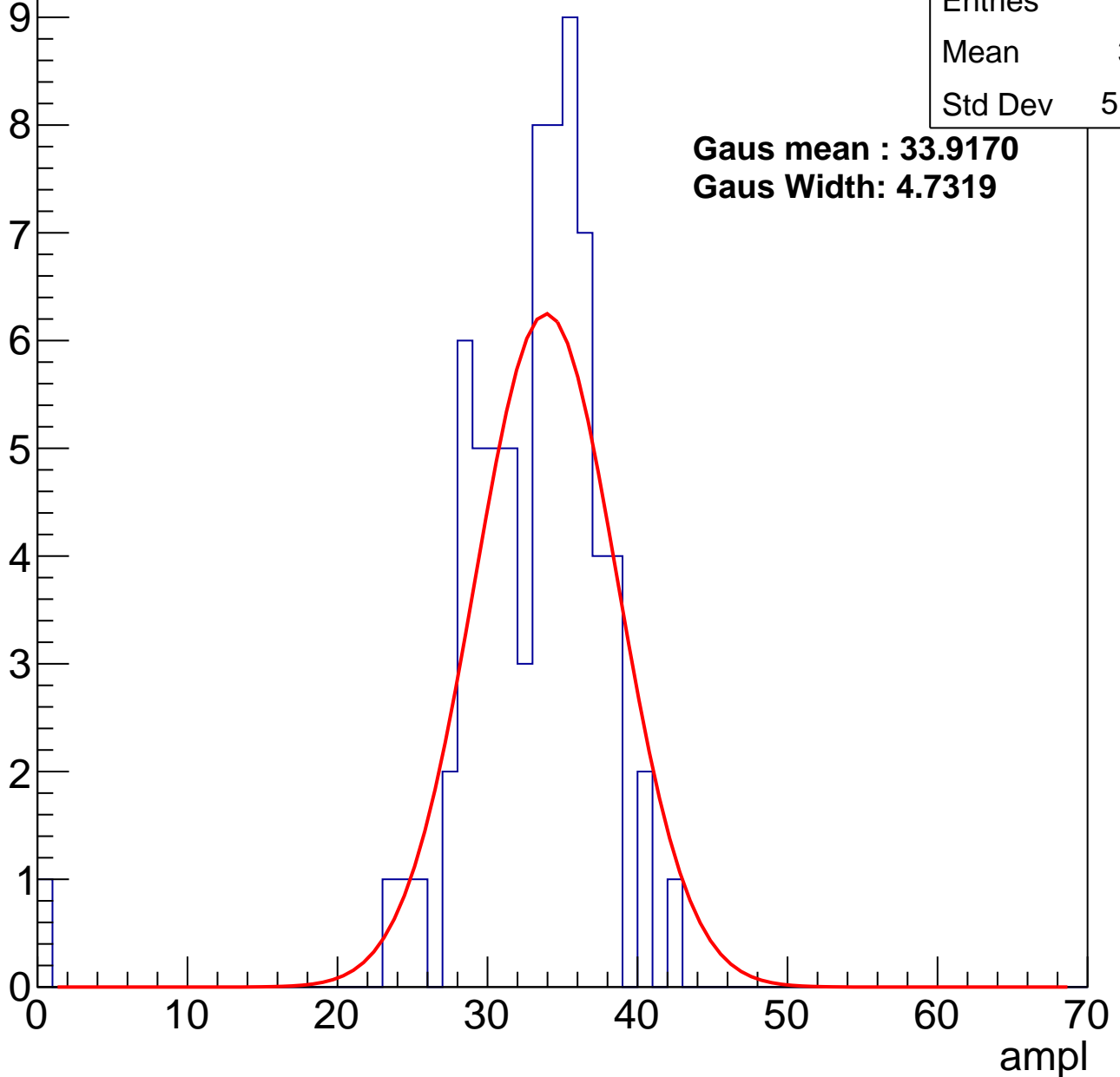
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	32.4
Std Dev	5.398

Gaus mean : 33.9170

Gaus Width: 4.7319



B0L102S, U5-ch106, adc2

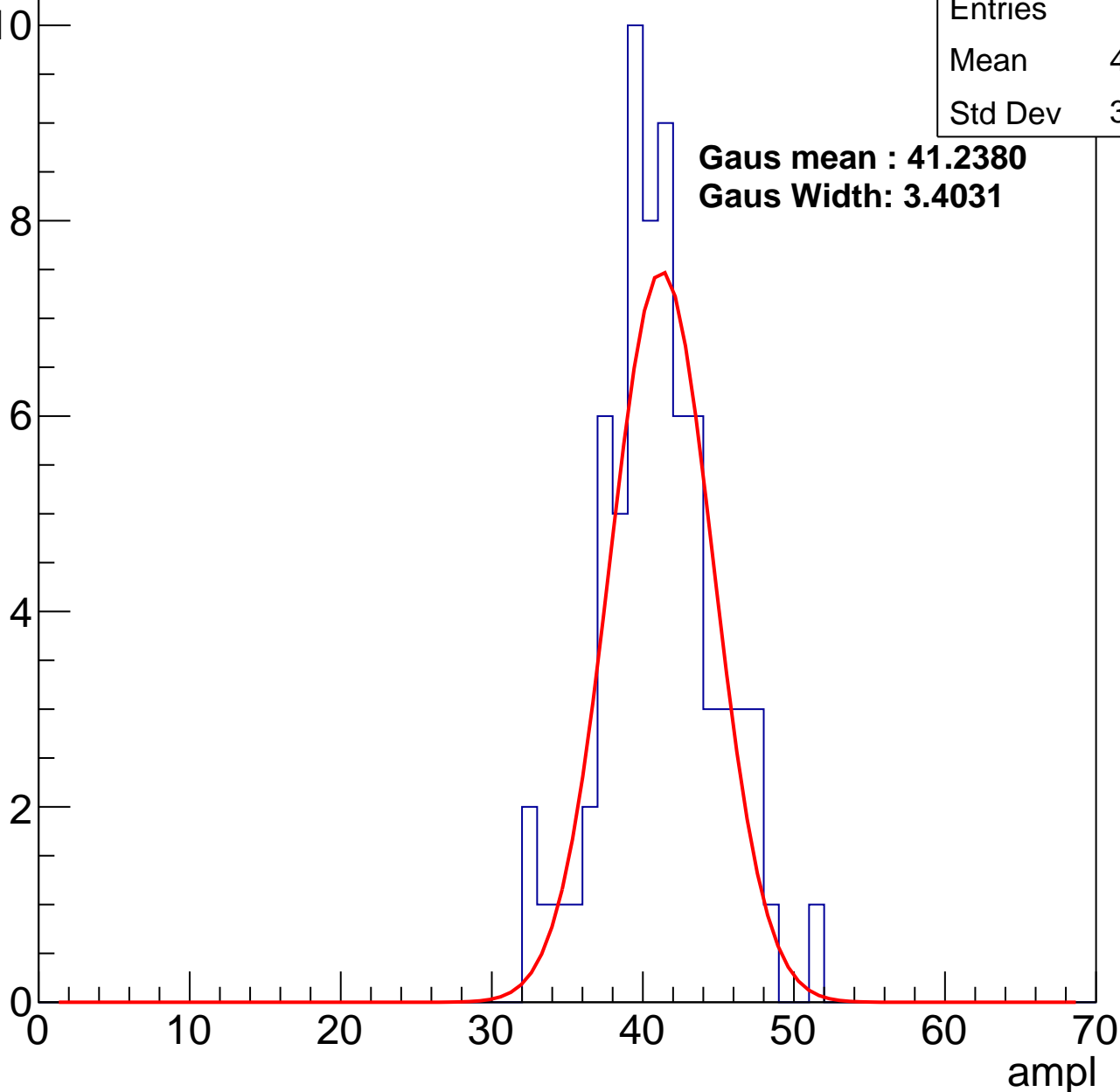
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	40.62
Std Dev	3.709

Gaus mean : 41.2380

Gaus Width: 3.4031

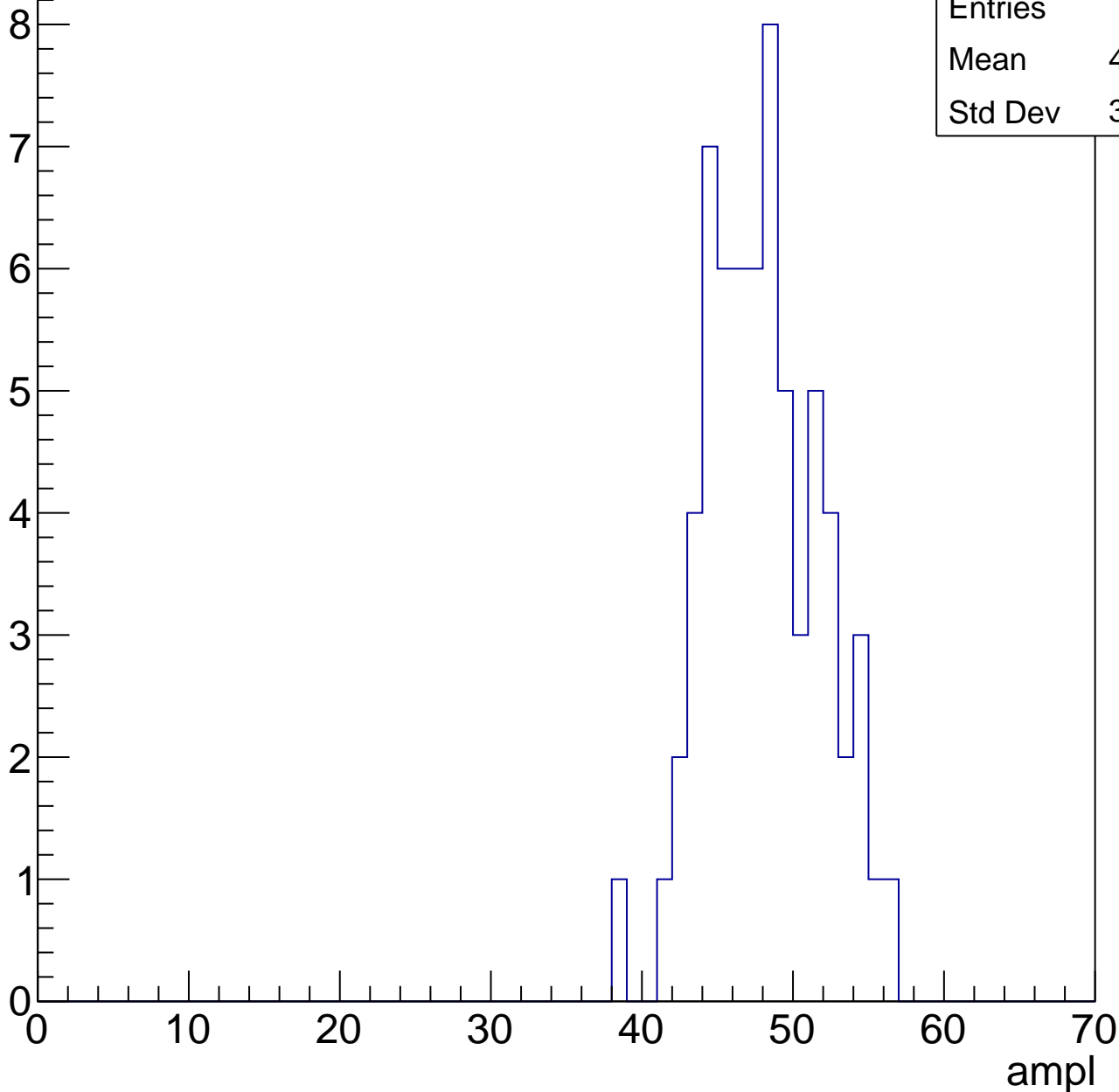


B0L102S, U5-ch106, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	47.57
Std Dev	3.725

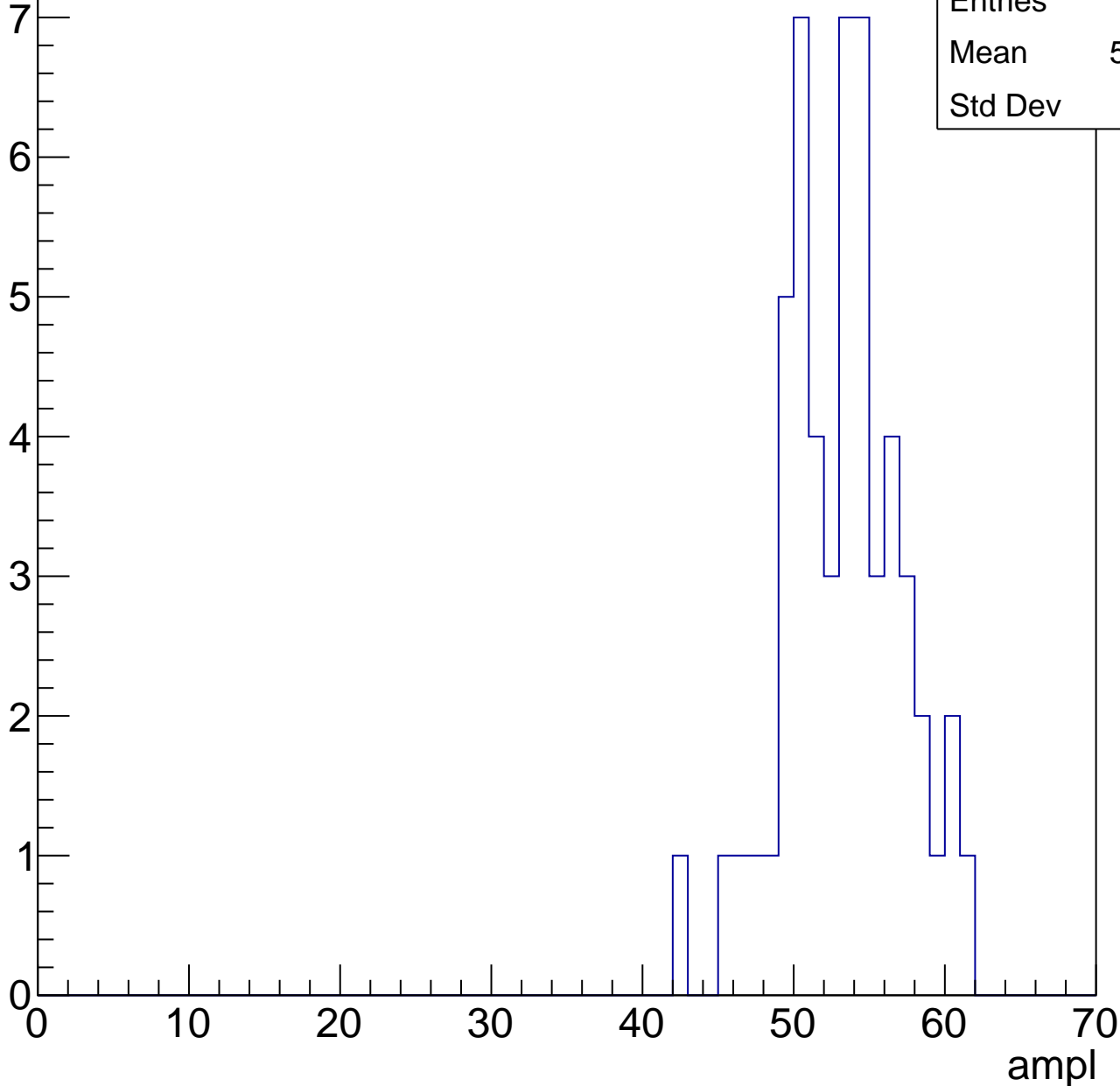


B0L102S, U5-ch106, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

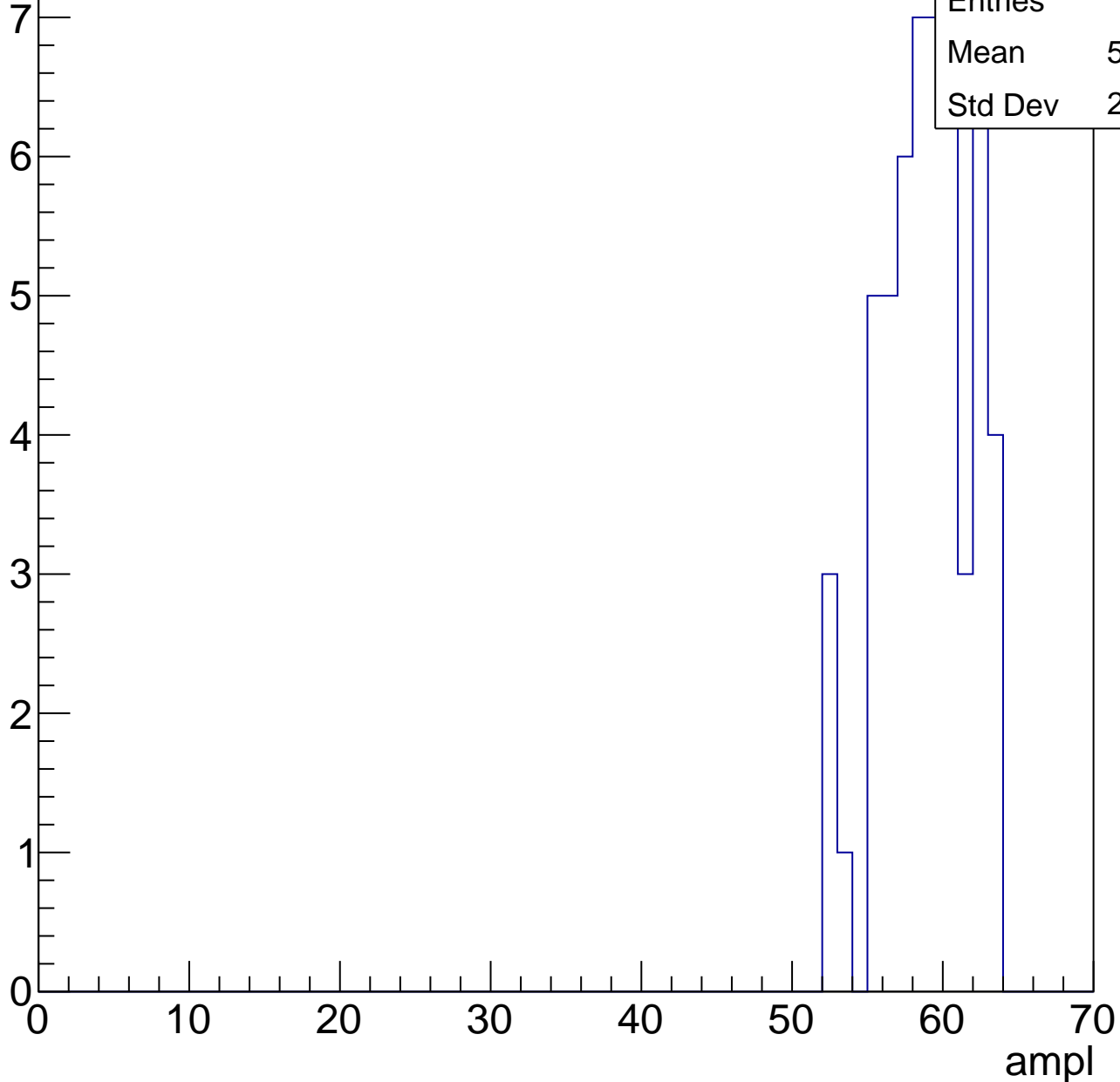
Entries	54
Mean	52.74
Std Dev	3.86



B0L102S, U5-ch106, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

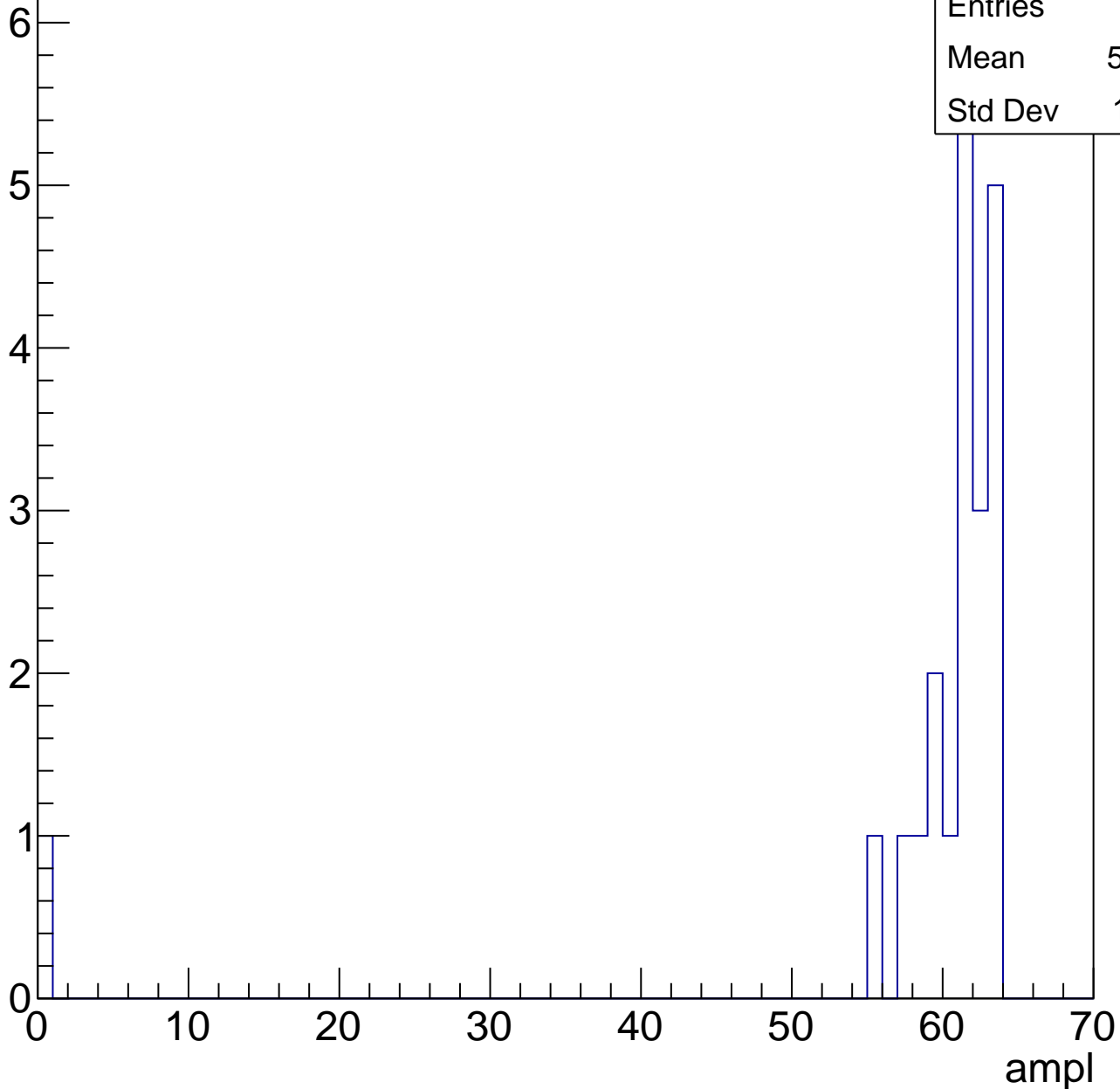


B0L102S, U5-ch106, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	21
Mean	57.86
Std Dev	13.11



B0L102S, U5-ch106, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	63
Std Dev	0

B0L102S, U5-ch107, adc0

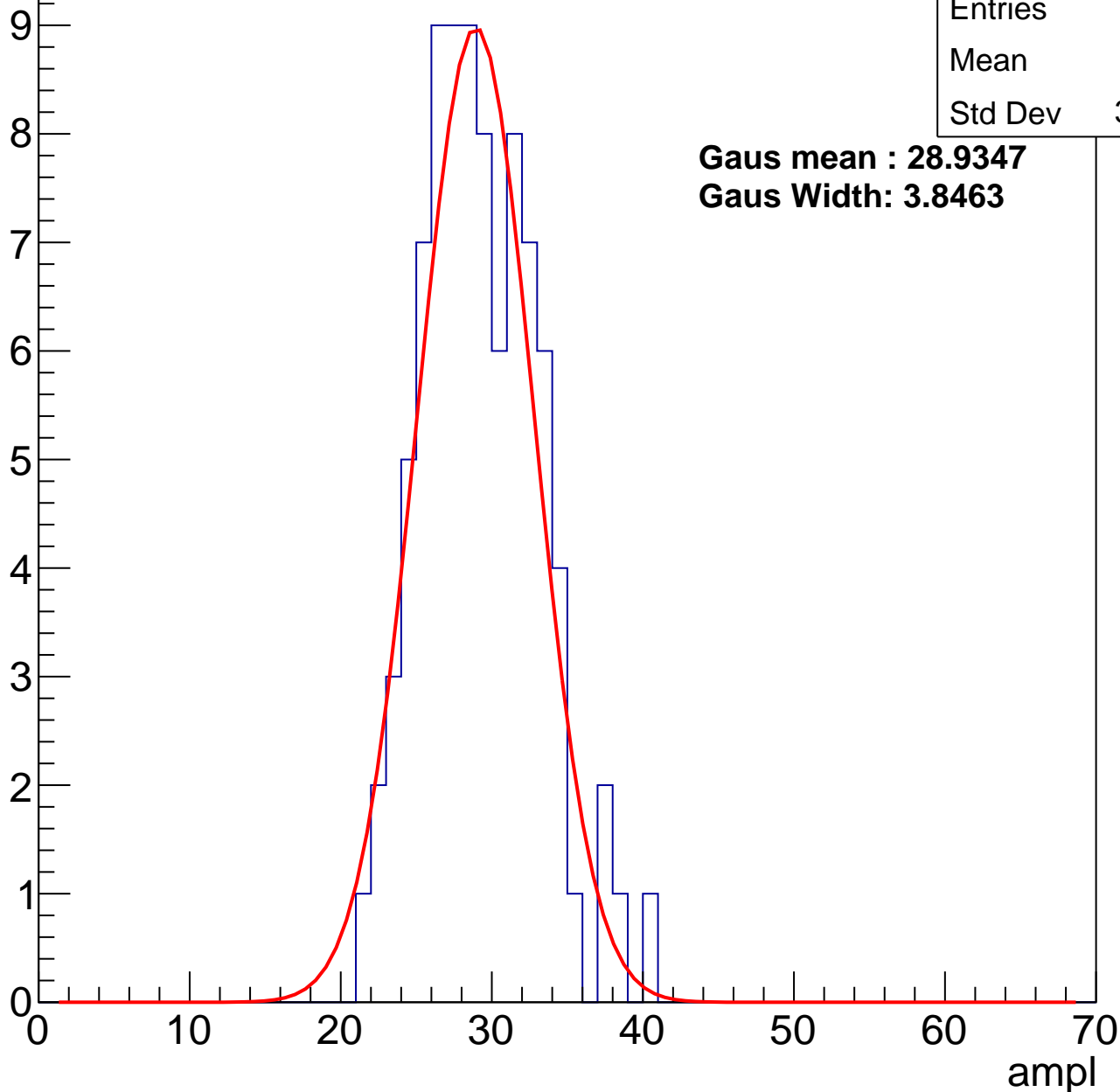
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	28.8
Std Dev	3.811

Gaus mean : 28.9347

Gaus Width: 3.8463



B0L102S, U5-ch107, adc1

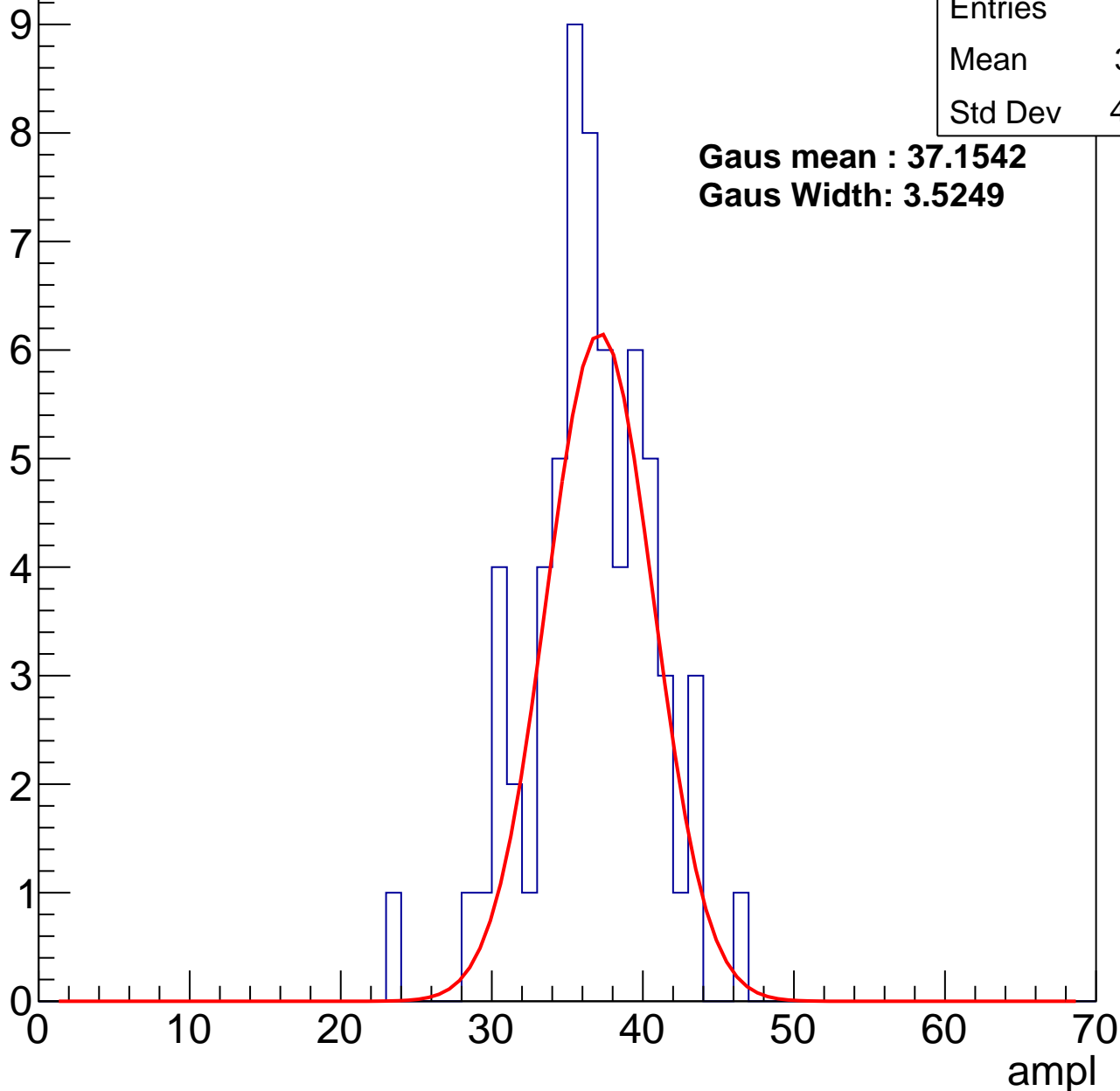
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	36.11
Std Dev	4.058

Gaus mean : 37.1542

Gaus Width: 3.5249



B0L102S, U5-ch107, adc2

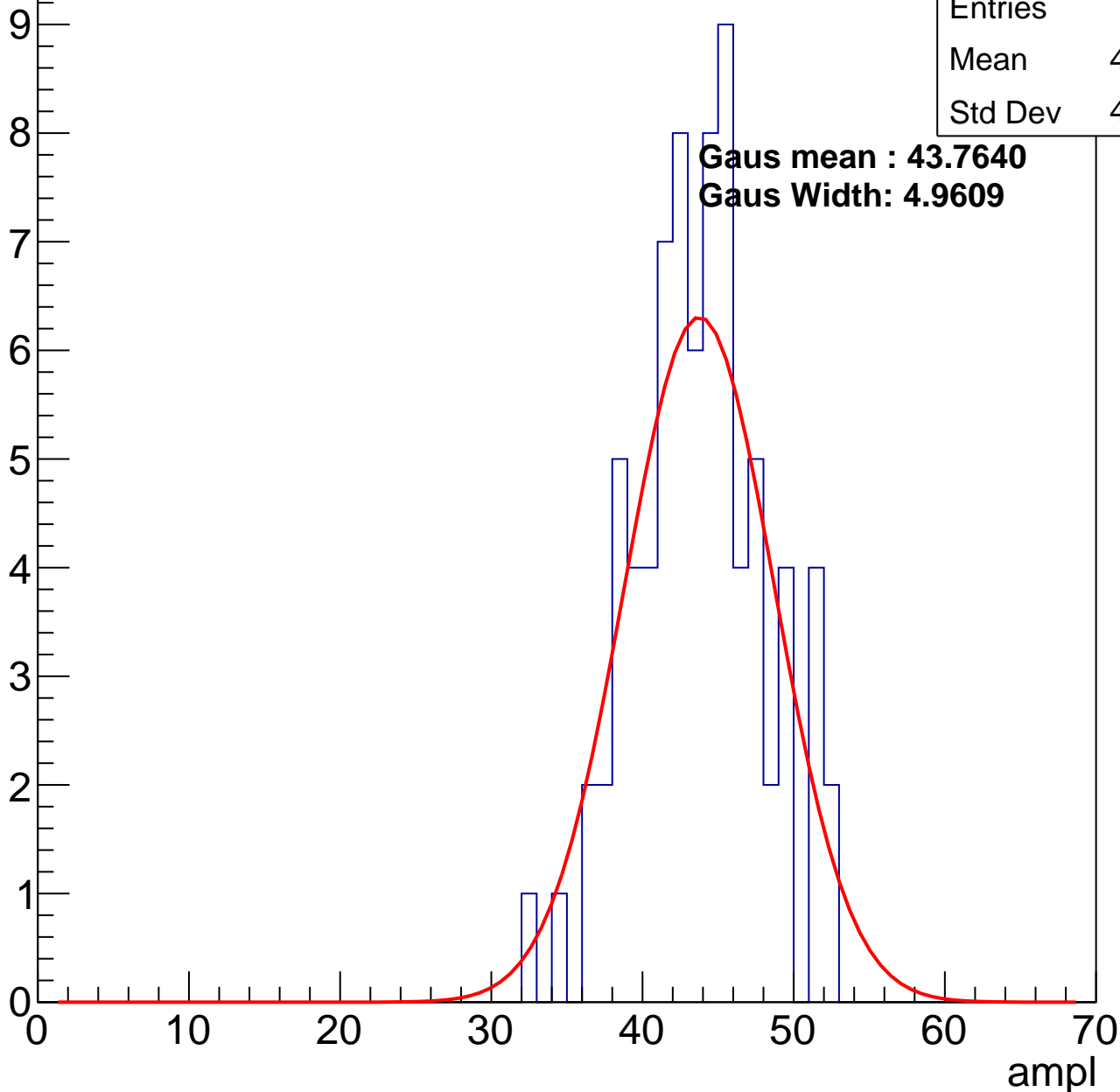
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	43.27
Std Dev	4.236

Gaus mean : 43.7640

Gaus Width: 4.9609

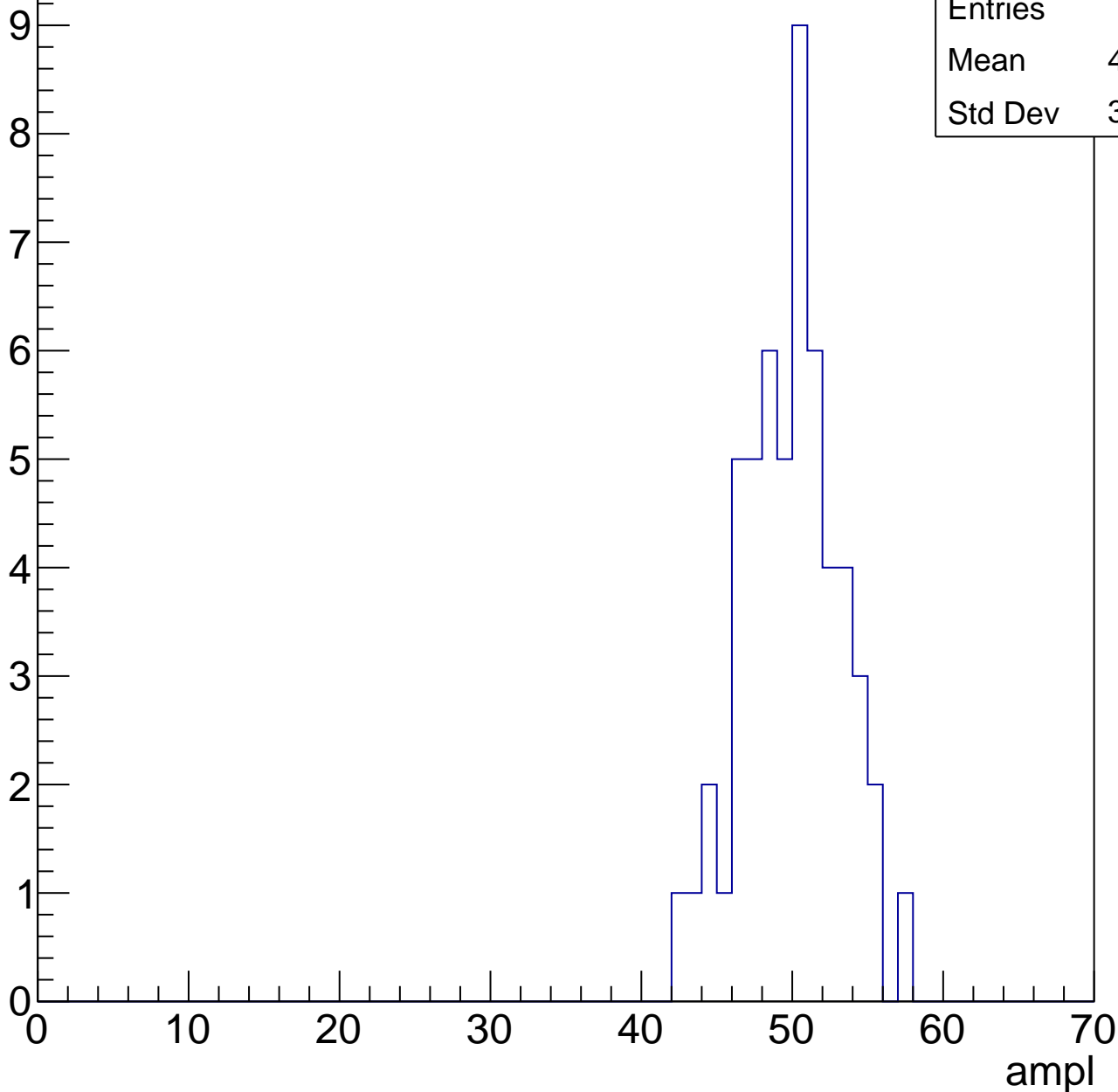


B0L102S, U5-ch107, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	49.47
Std Dev	3.173

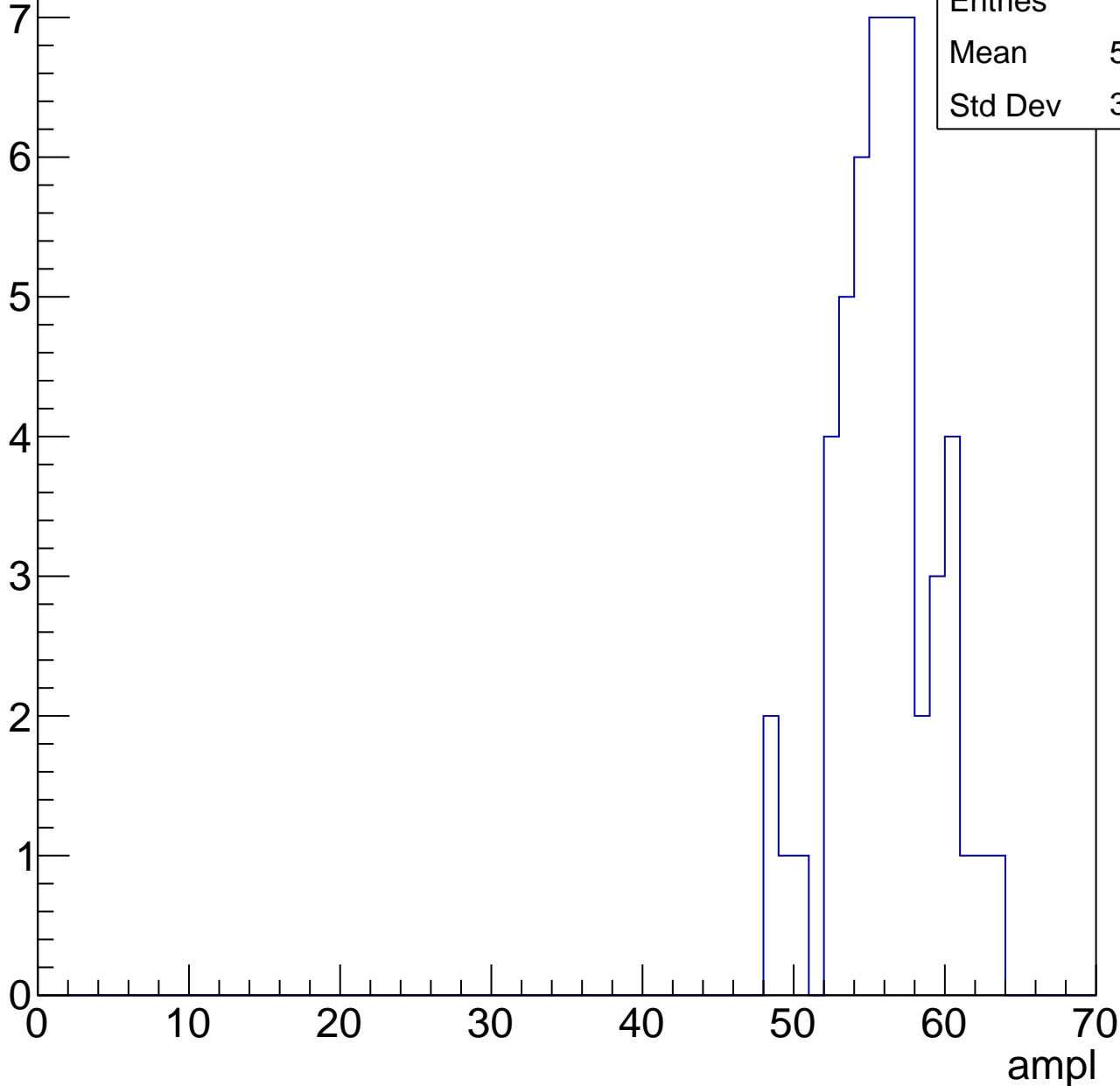


B0L102S, U5-ch107, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	55.52
Std Dev	3.273



B0L102S, U5-ch107, adc5

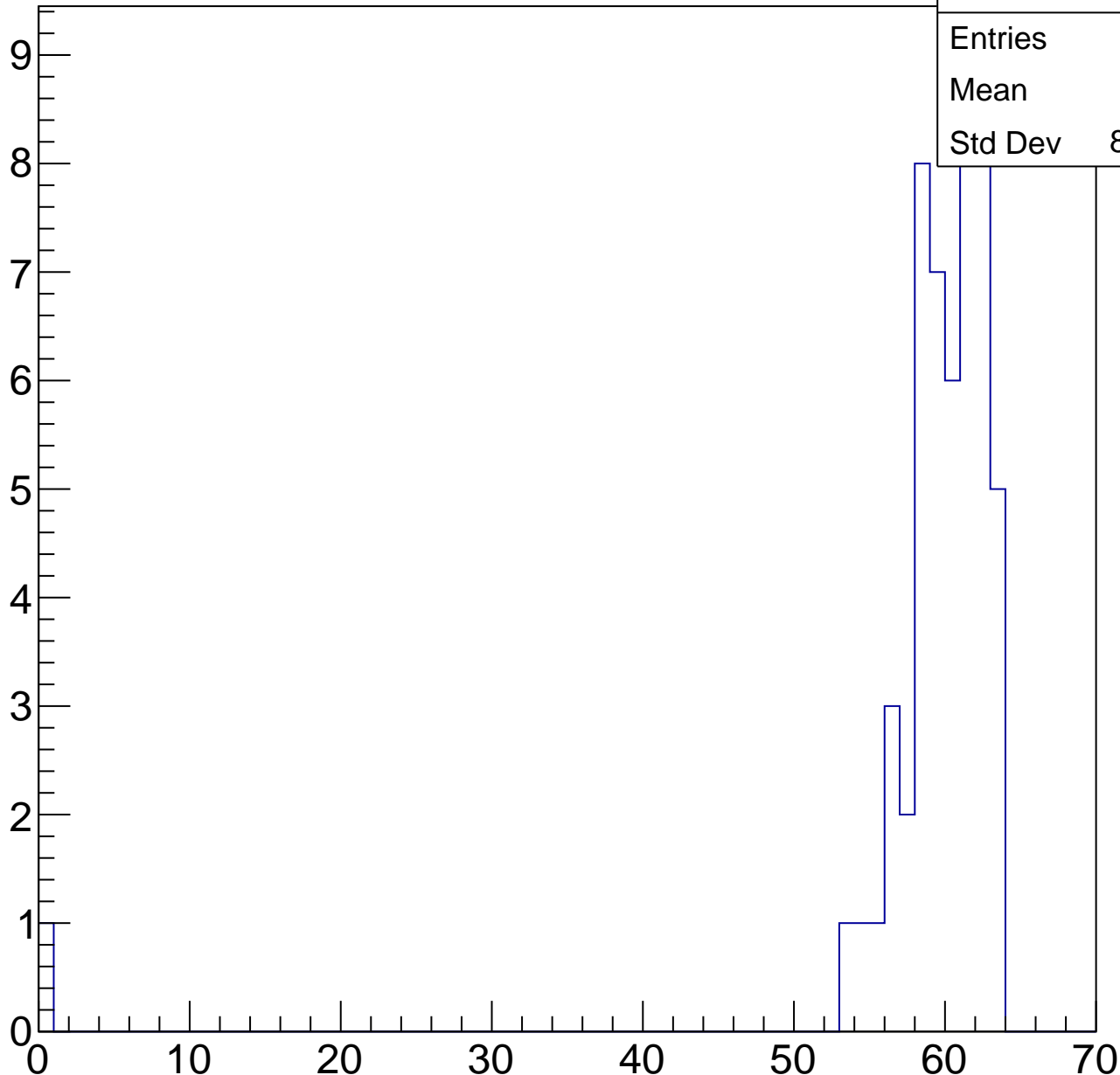
calib_packv5_042523_0143.root, FC#12, port B1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.5
Std Dev	8.534

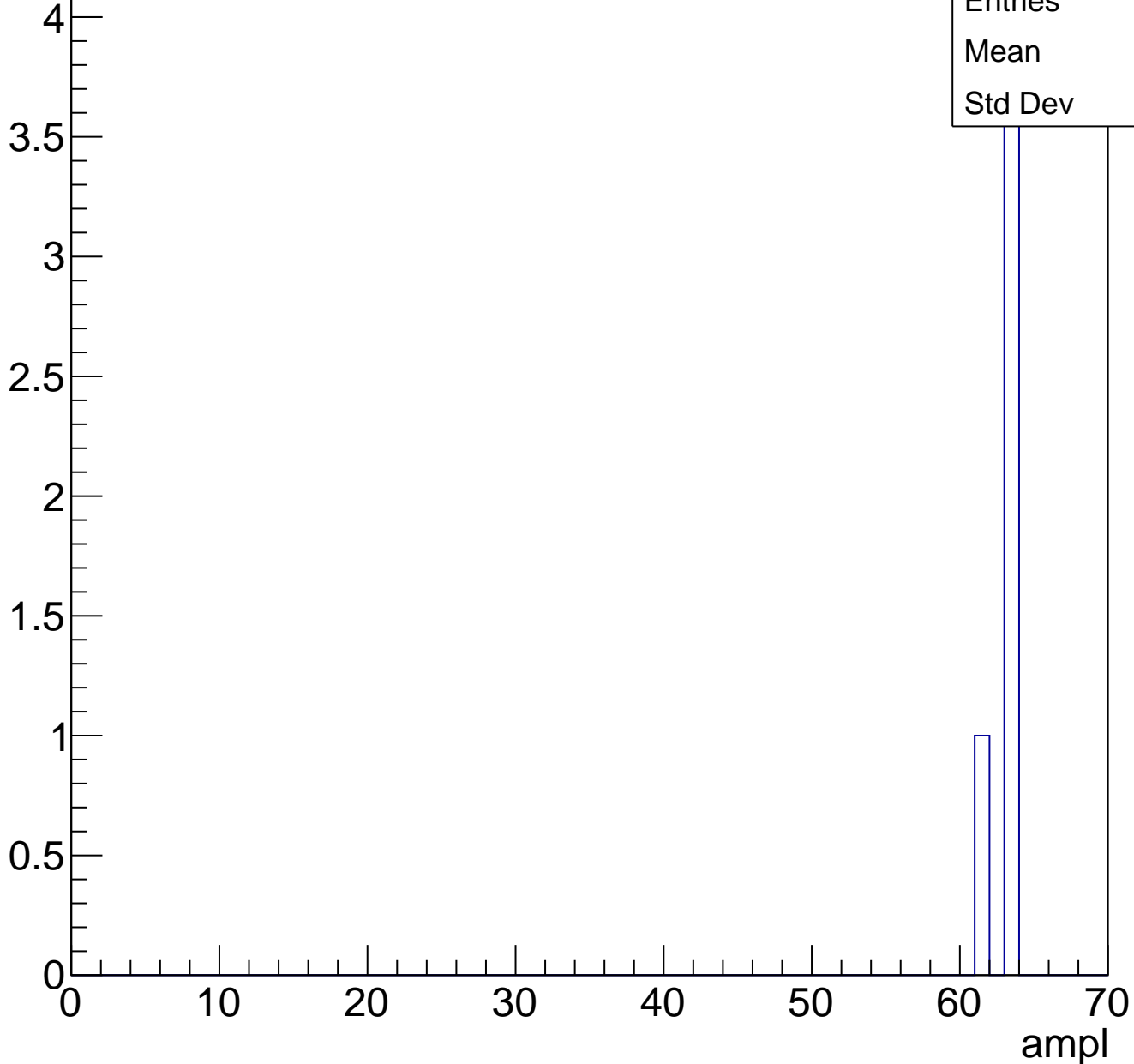
ampl



B0L102S, U5-ch107, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch107, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch108, adc0

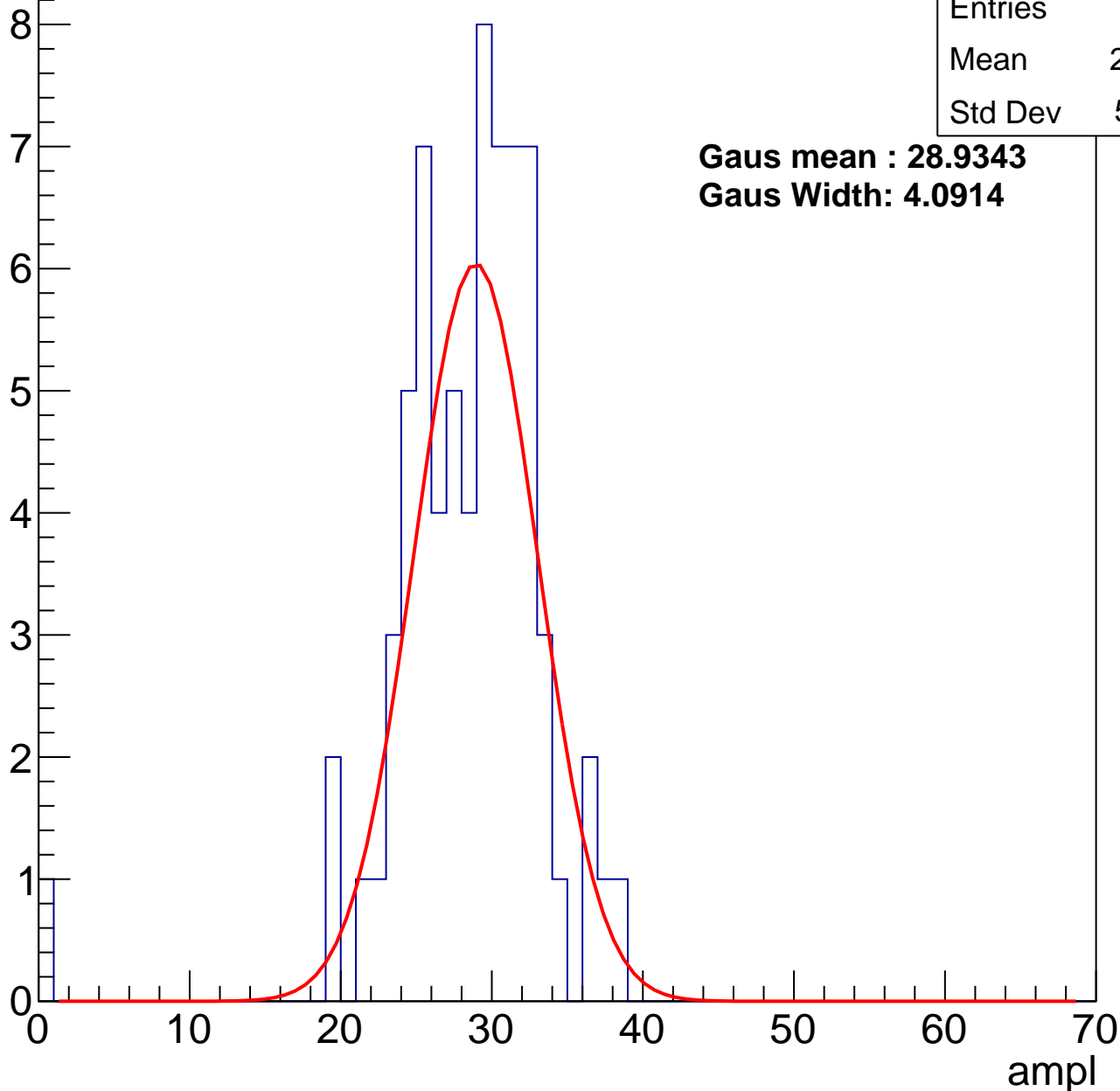
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	27.99
Std Dev	5.211

Gaus mean : 28.9343

Gaus Width: 4.0914



B0L102S, U5-ch108, adc1

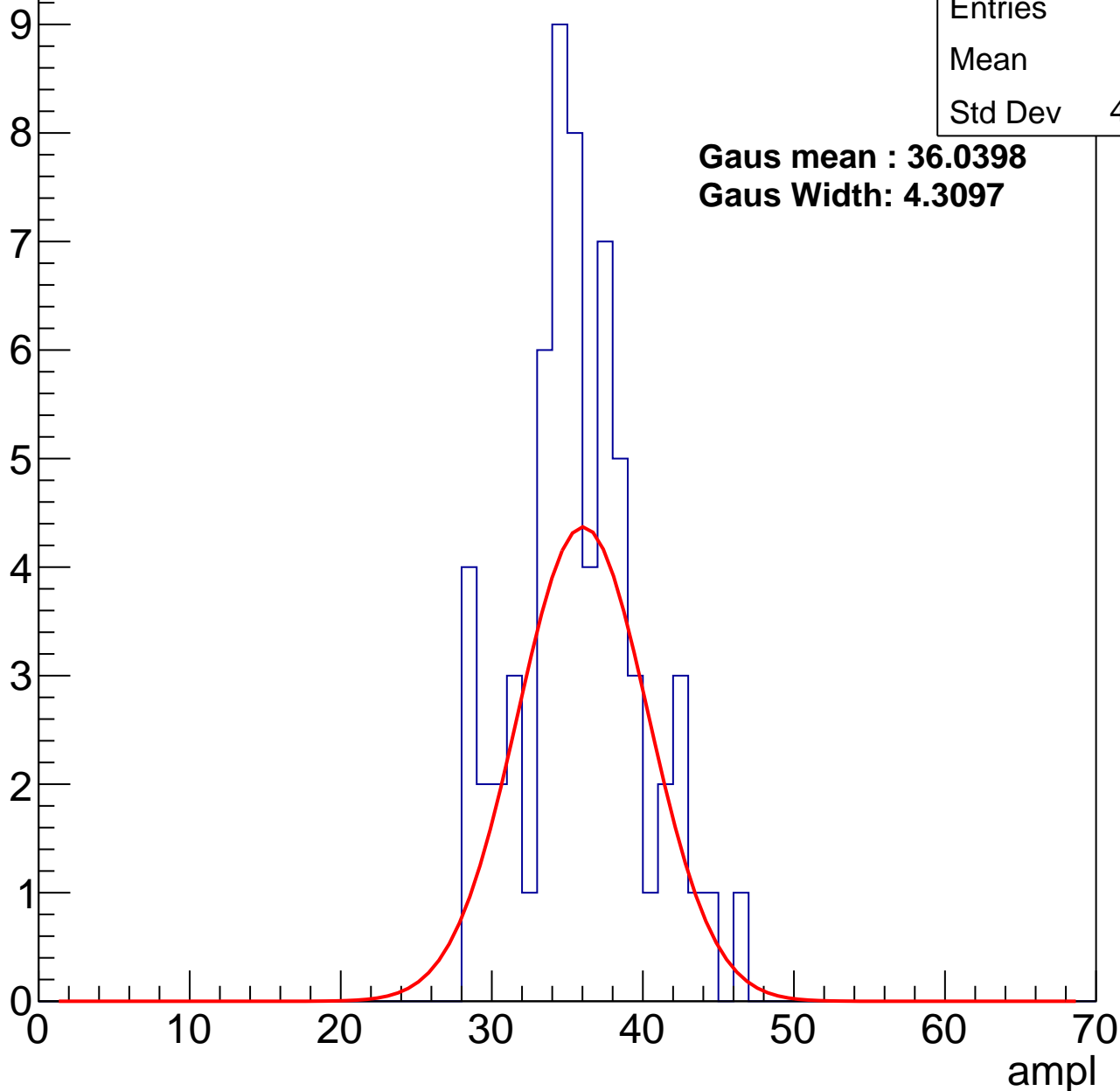
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	35.4
Std Dev	4.049

Gaus mean : 36.0398

Gaus Width: 4.3097

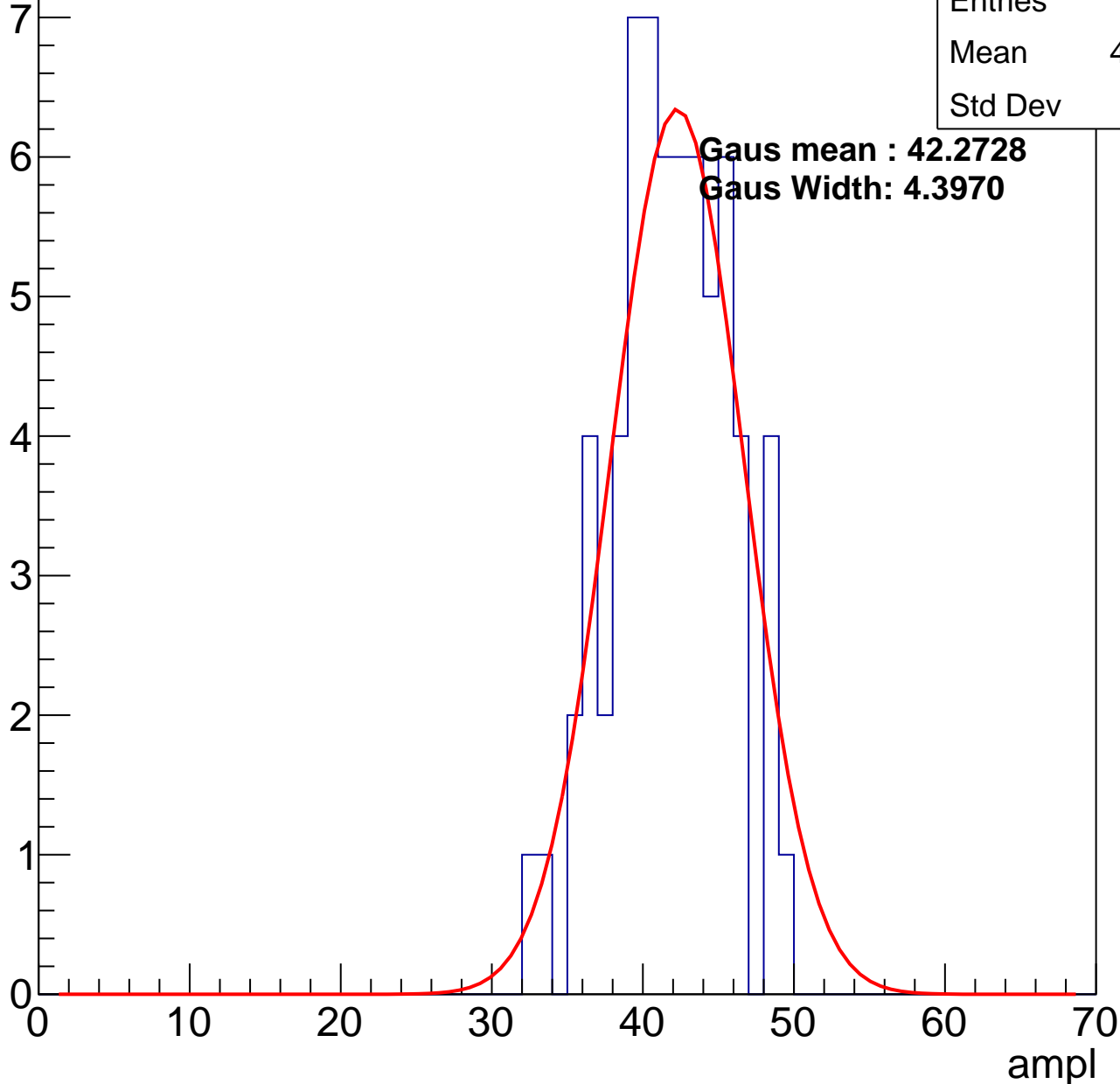


B0L102S, U5-ch108, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	41.35
Std Dev	3.78

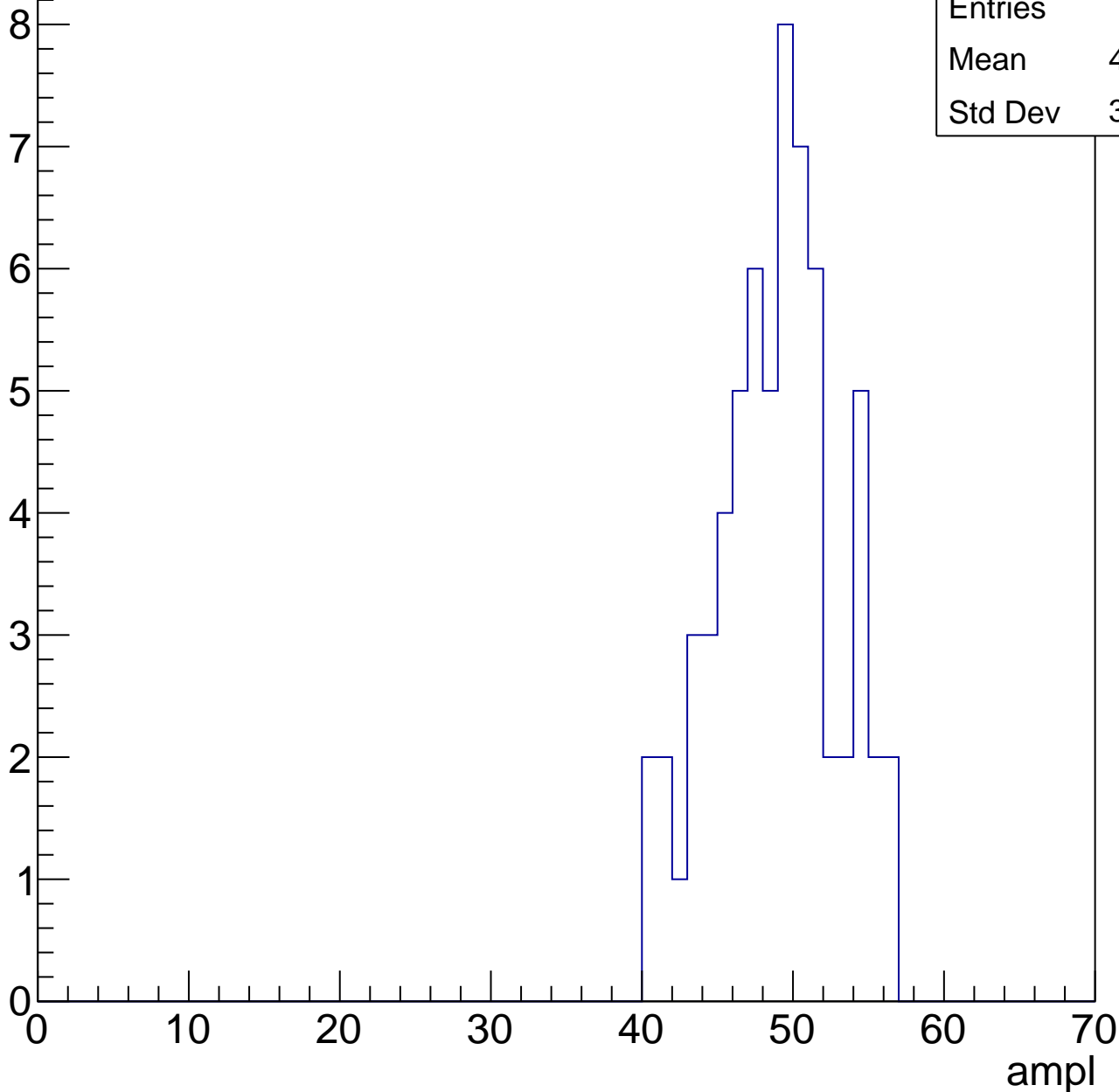


B0L102S, U5-ch108, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	48.42
Std Dev	3.949

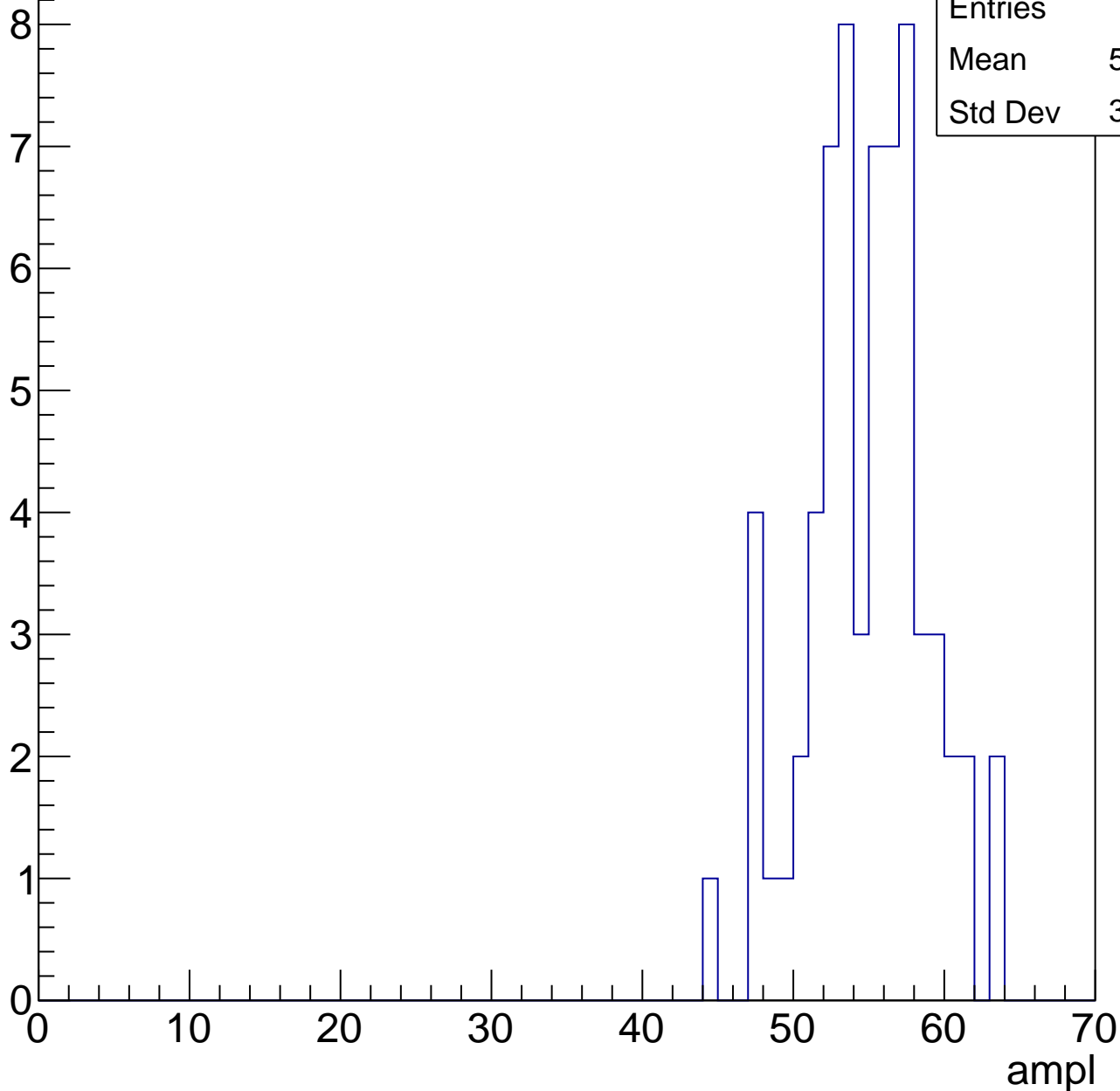


B0L102S, U5-ch108, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	54.38
Std Dev	3.948

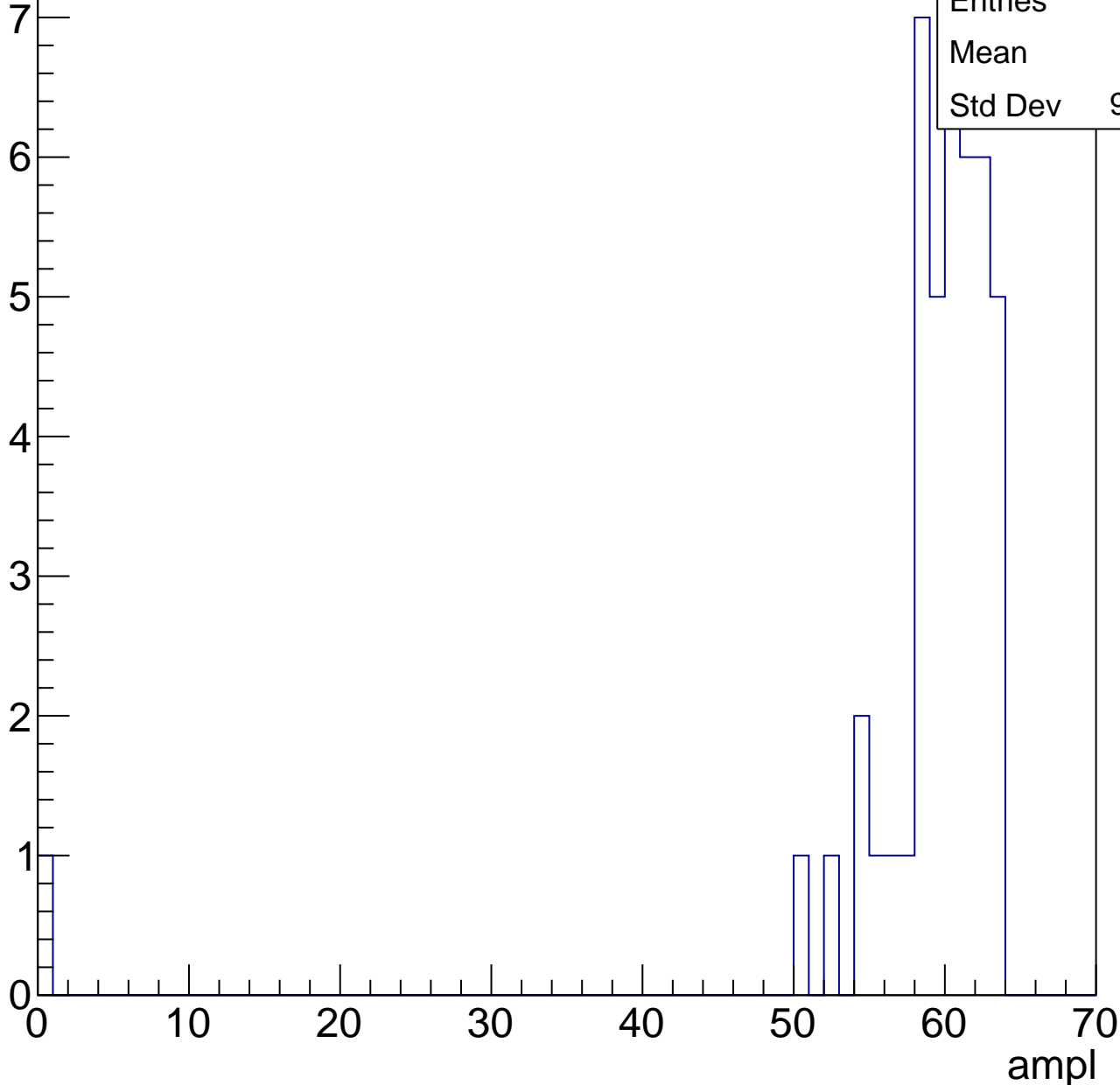


B0L102S, U5-ch108, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

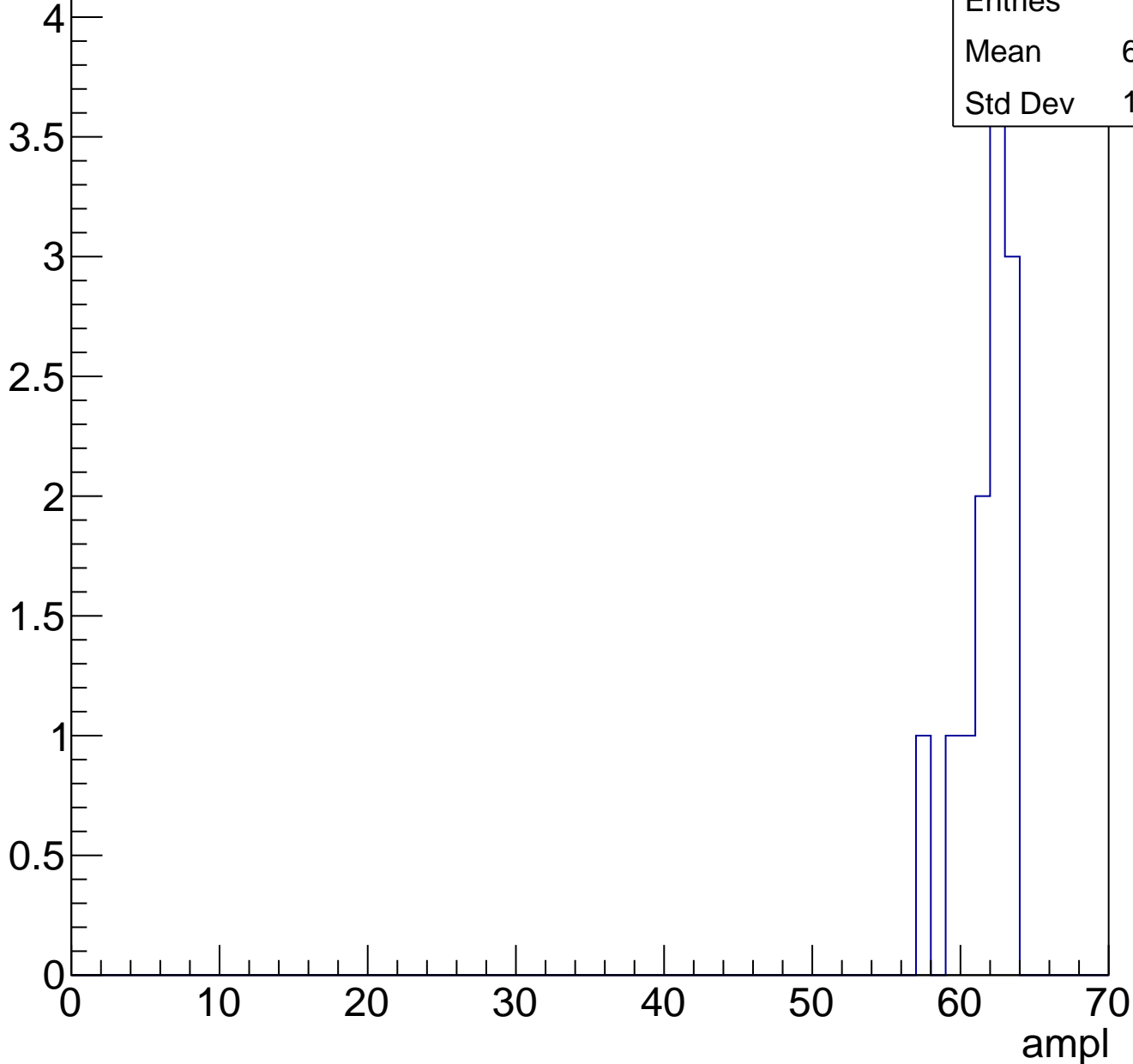
Entries	44
Mean	58
Std Dev	9.315



B0L102S, U5-ch108, adc6

calib_packv5_042523_0143.root, FC#12, port B1

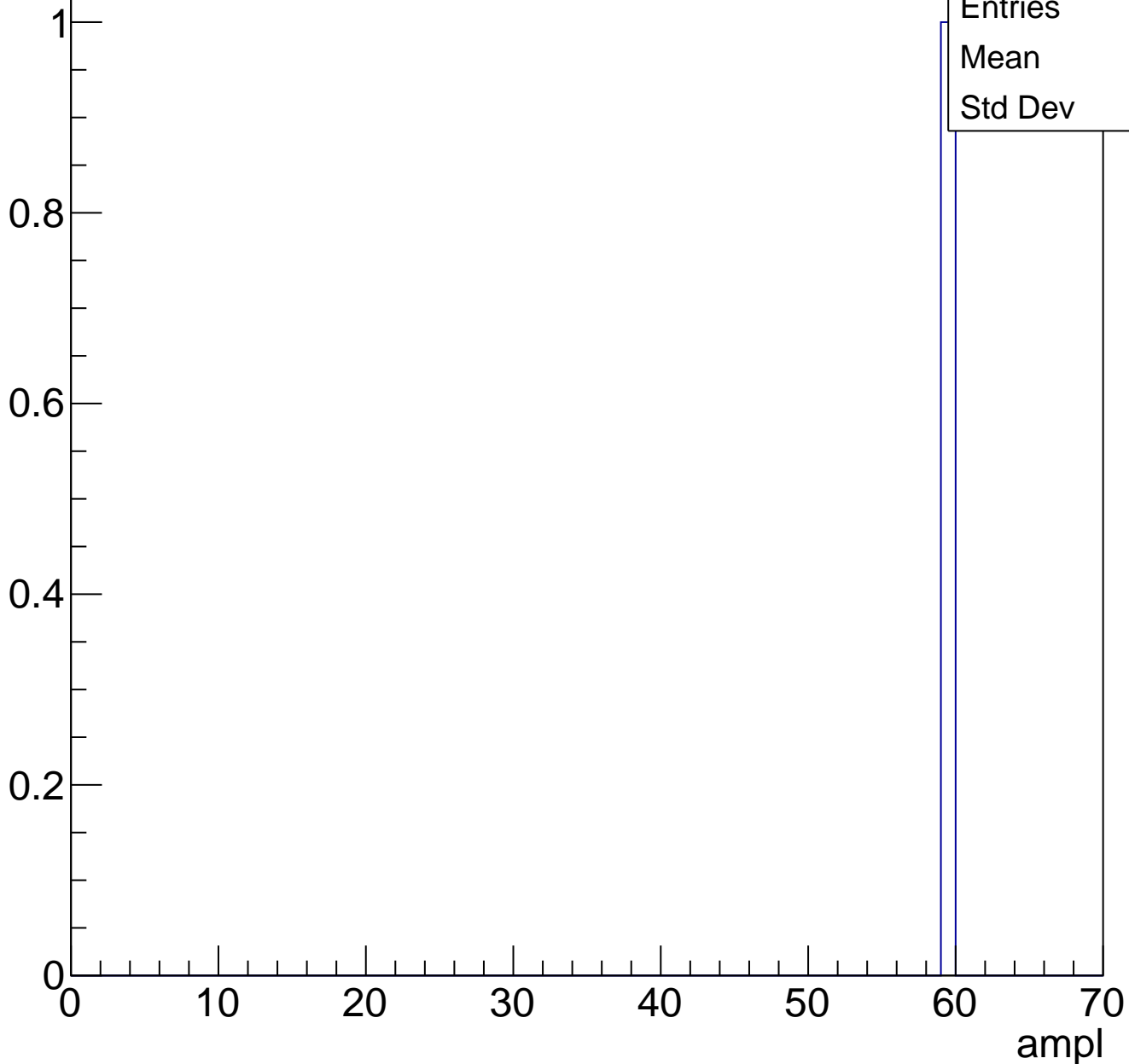
Entry



B0L102S, U5-ch108, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch109, adc0

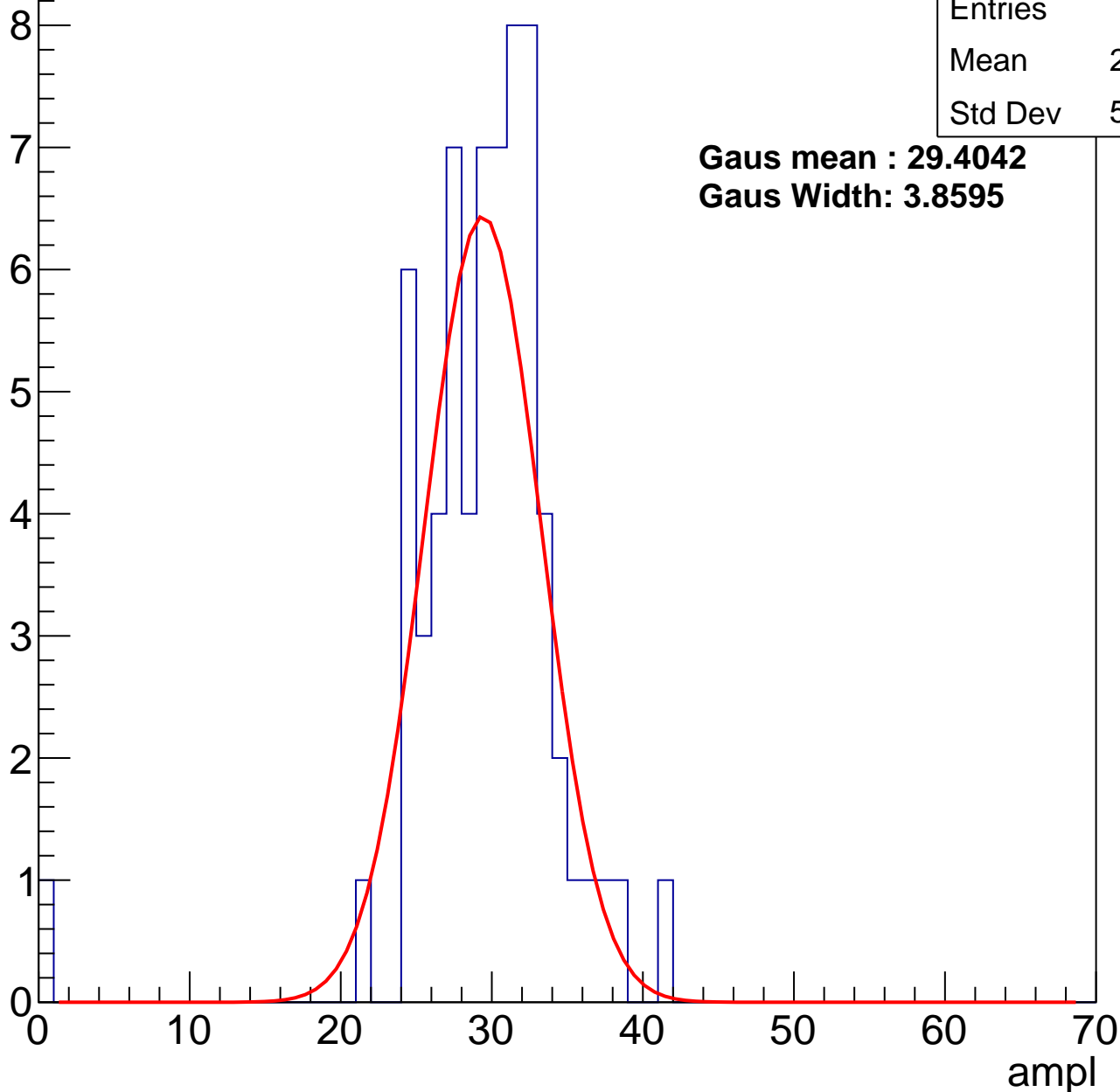
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	29.09
Std Dev	5.139

Gaus mean : 29.4042

Gaus Width: 3.8595



B0L102S, U5-ch109, adc1

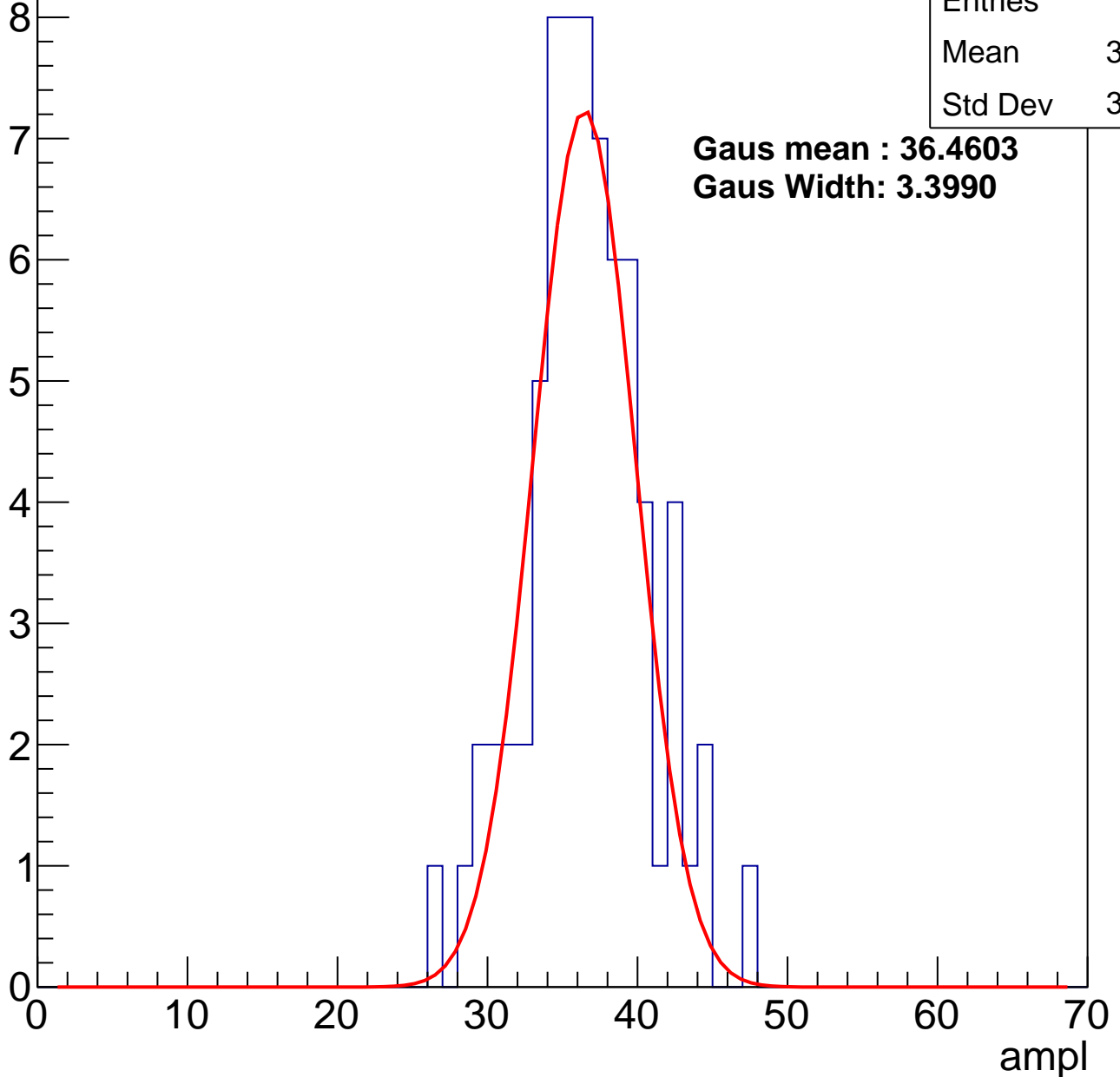
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	36.21
Std Dev	3.961

Gaus mean : 36.4603

Gaus Width: 3.3990



B0L102S, U5-ch109, adc2

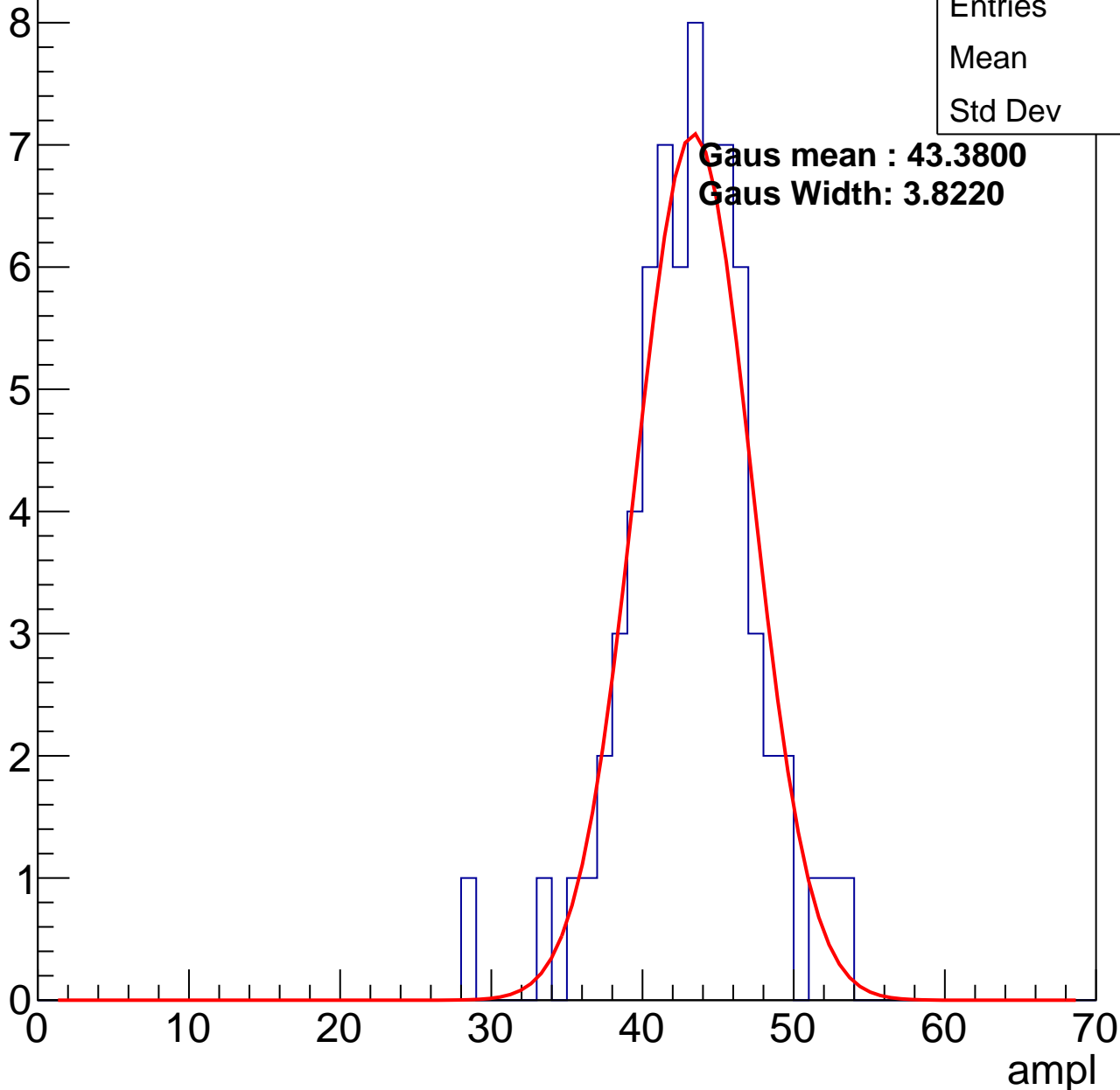
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	42.7
Std Dev	4.2

Gaus mean : 43.3800

Gaus Width: 3.8220

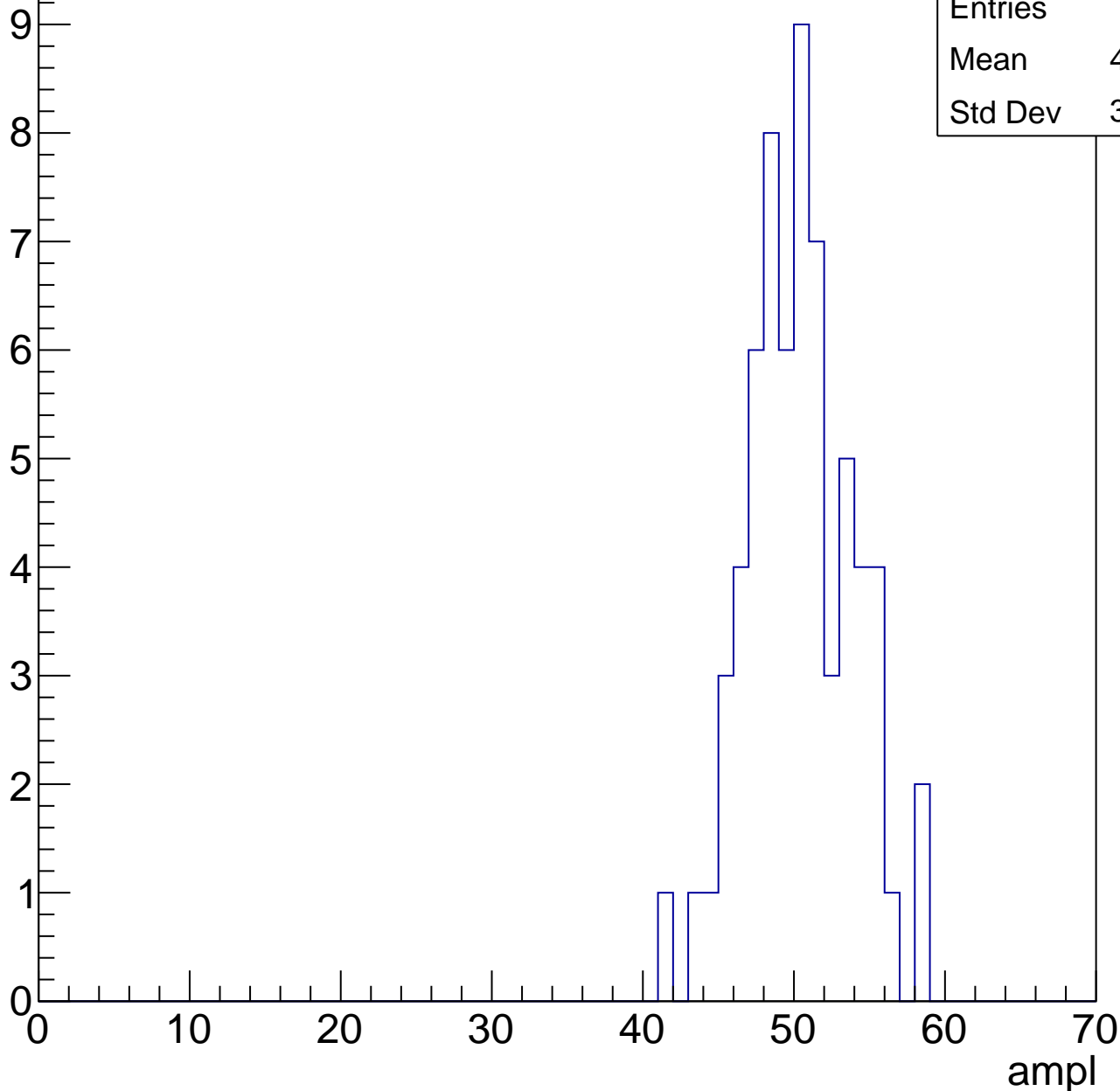


B0L102S, U5-ch109, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	49.89
Std Dev	3.482

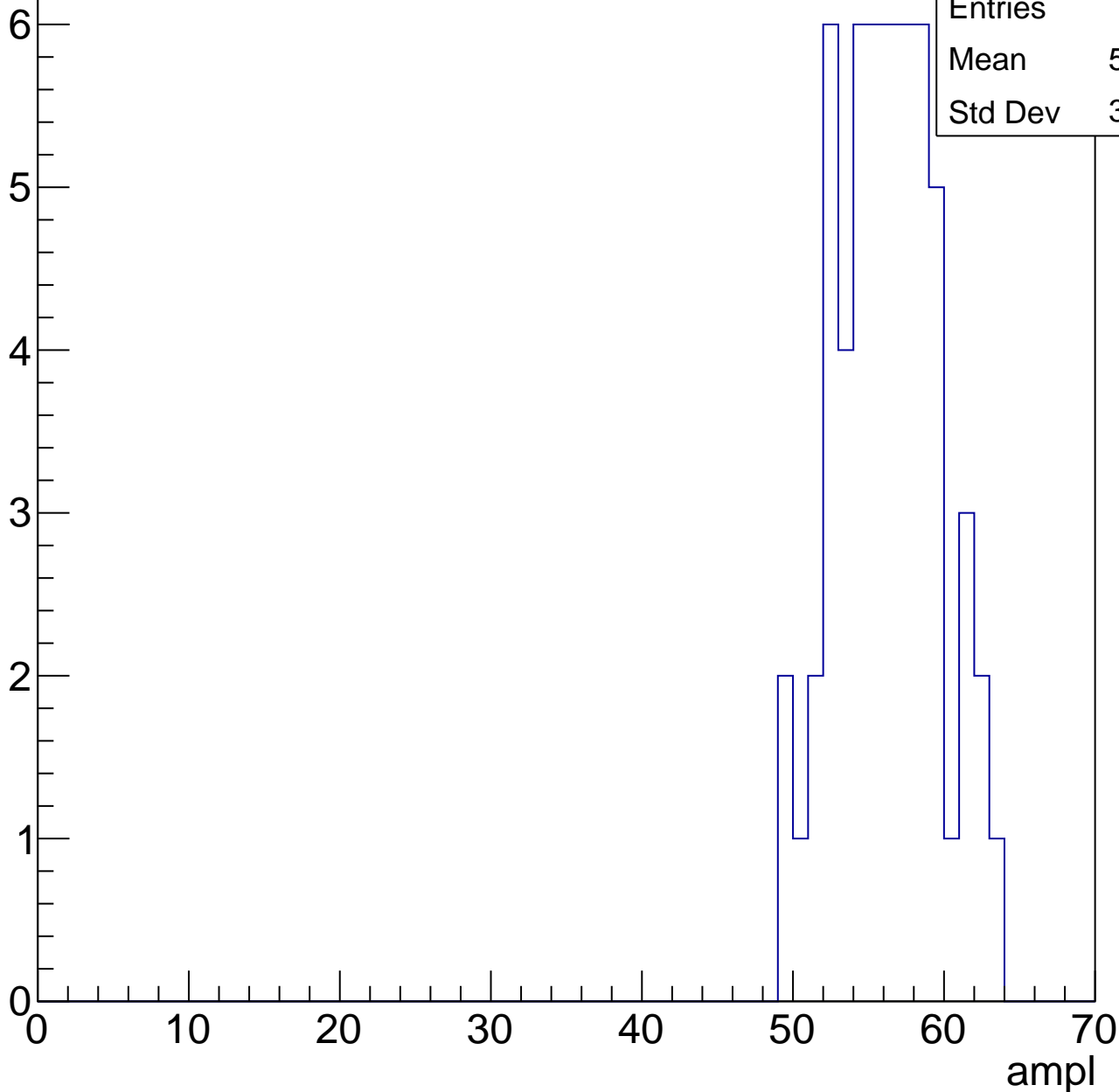


B0L102S, U5-ch109, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

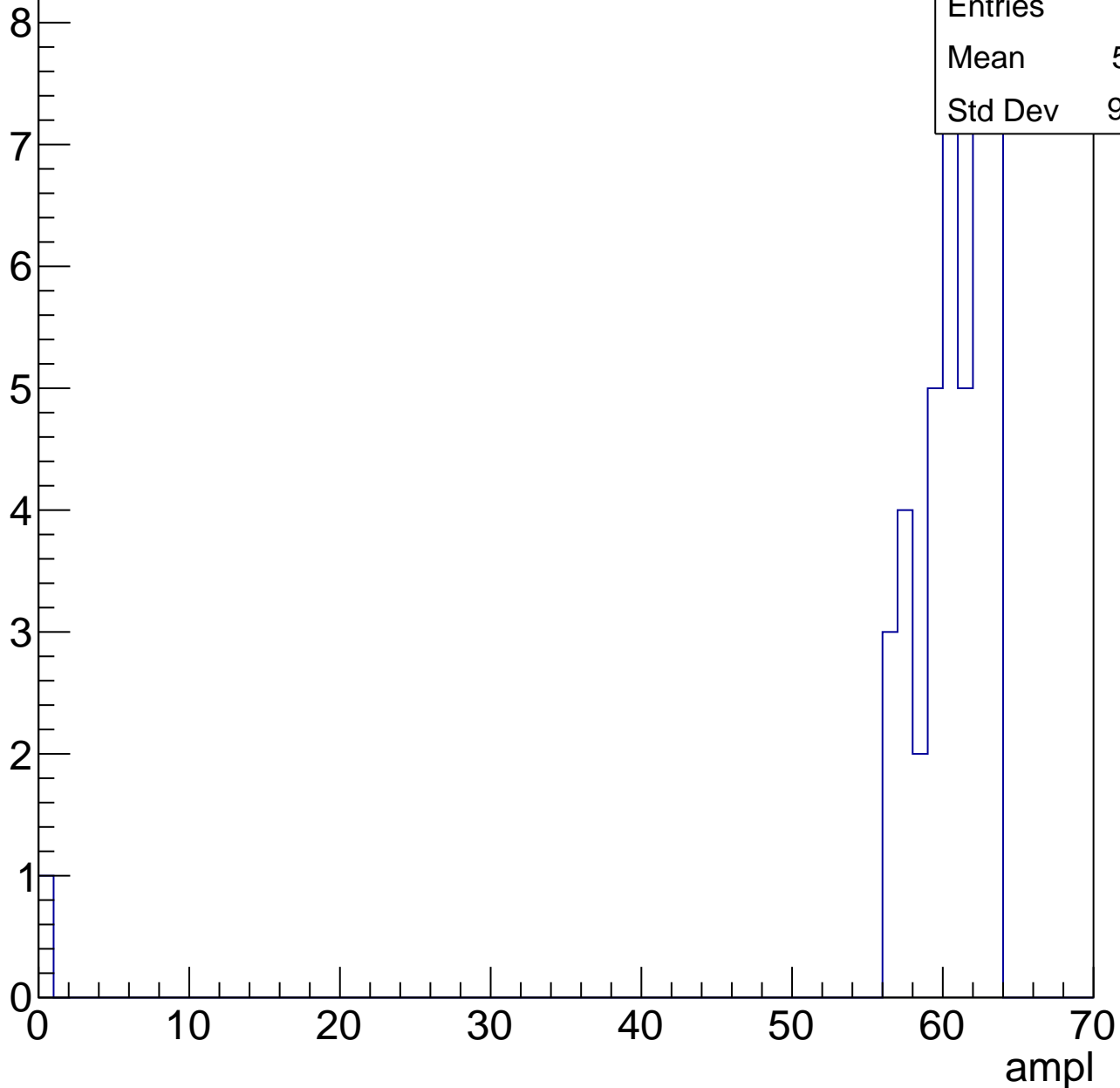
Entries	57
Mean	55.77
Std Dev	3.325



B0L102S, U5-ch109, adc5

calib_packv5_042523_0143.root, FC#12, port B1

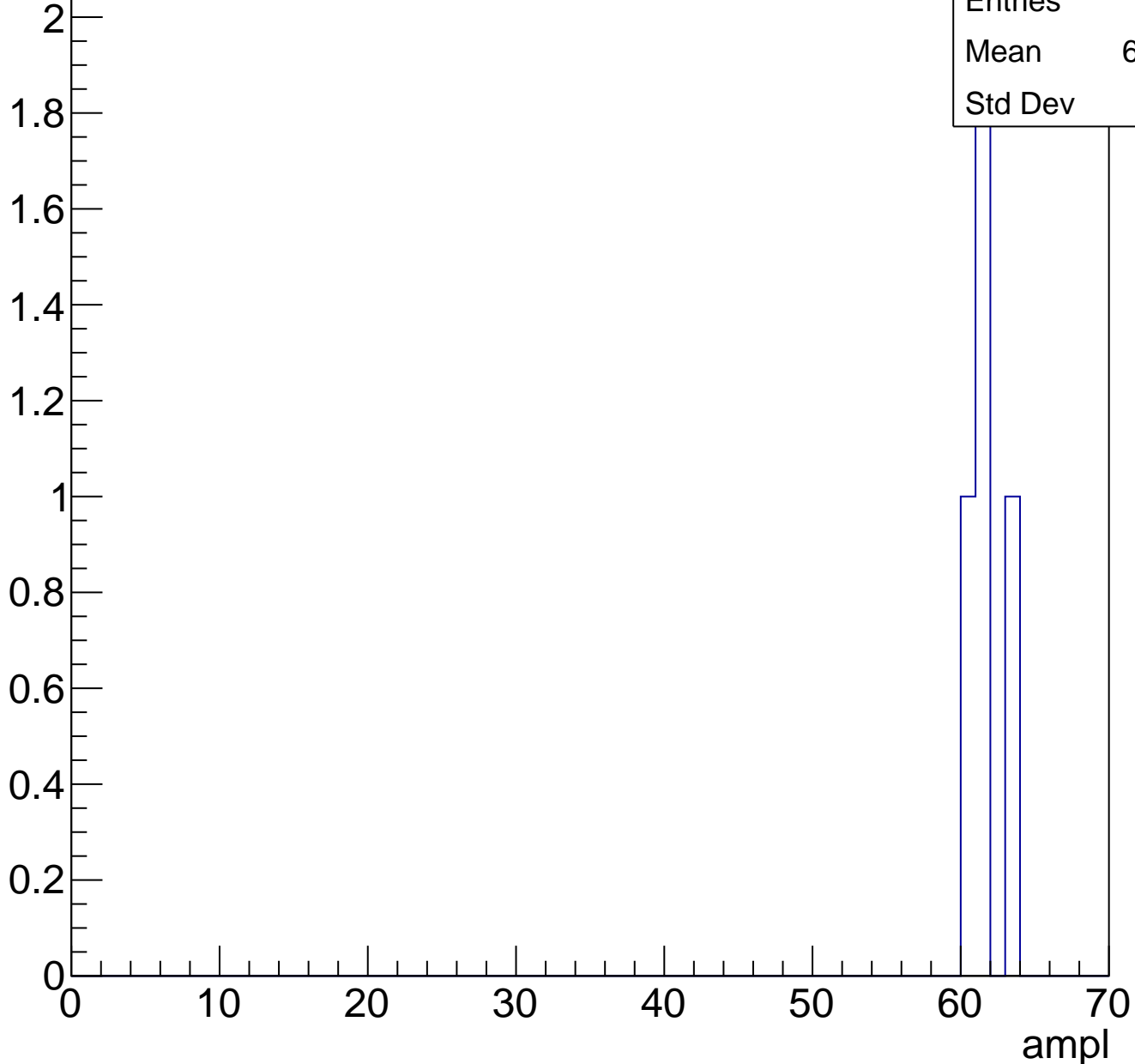
Entry



B0L102S, U5-ch109, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch109, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch110, adc0

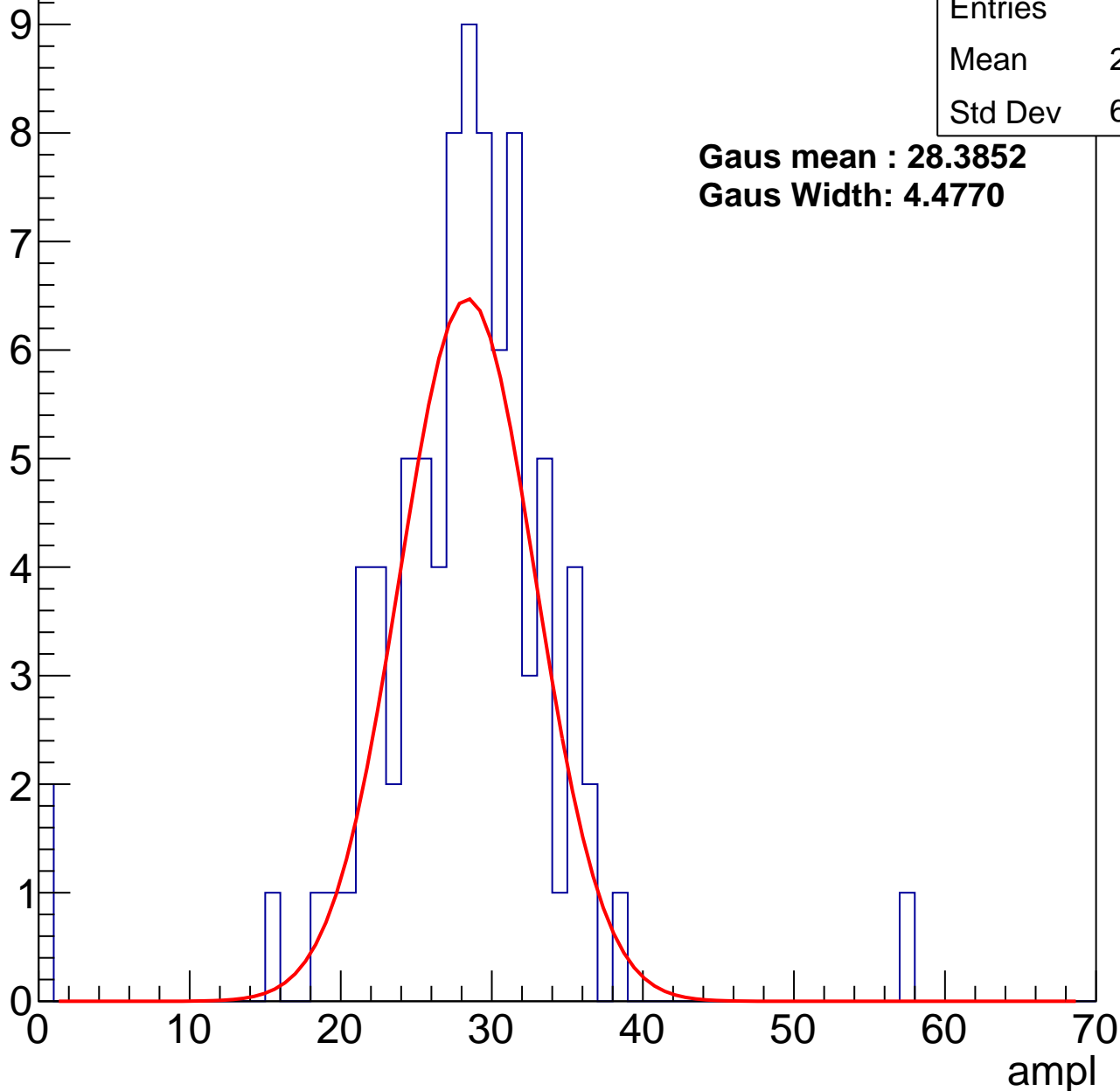
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	86
Mean	27.55
Std Dev	6.889

Gaus mean : 28.3852

Gaus Width: 4.4770



B0L102S, U5-ch110, adc1

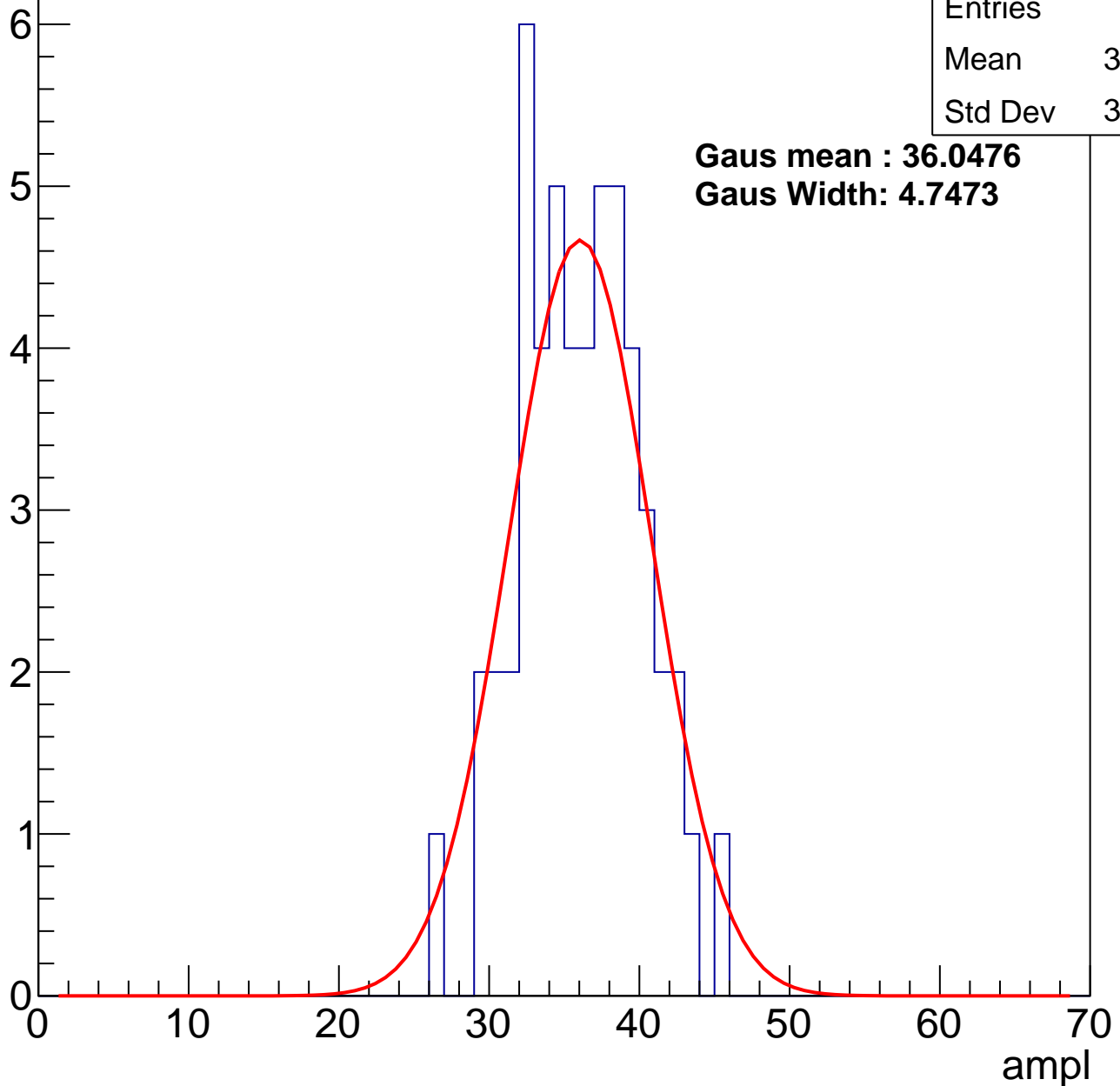
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	35.64
Std Dev	3.958

Gaus mean : 36.0476

Gaus Width: 4.7473



B0L102S, U5-ch110, adc2

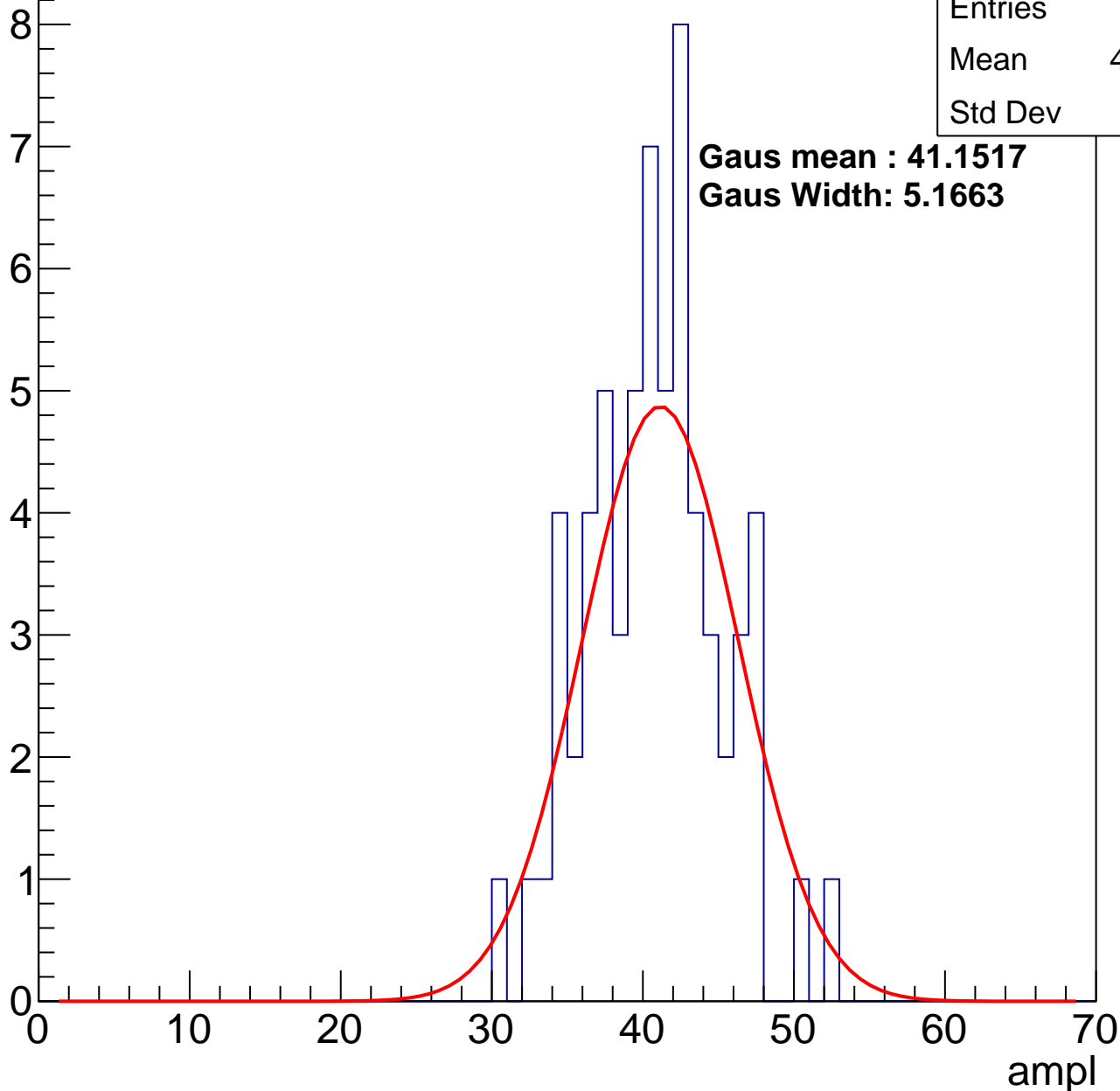
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	40.34
Std Dev	4.41

Gaus mean : 41.1517

Gaus Width: 5.1663

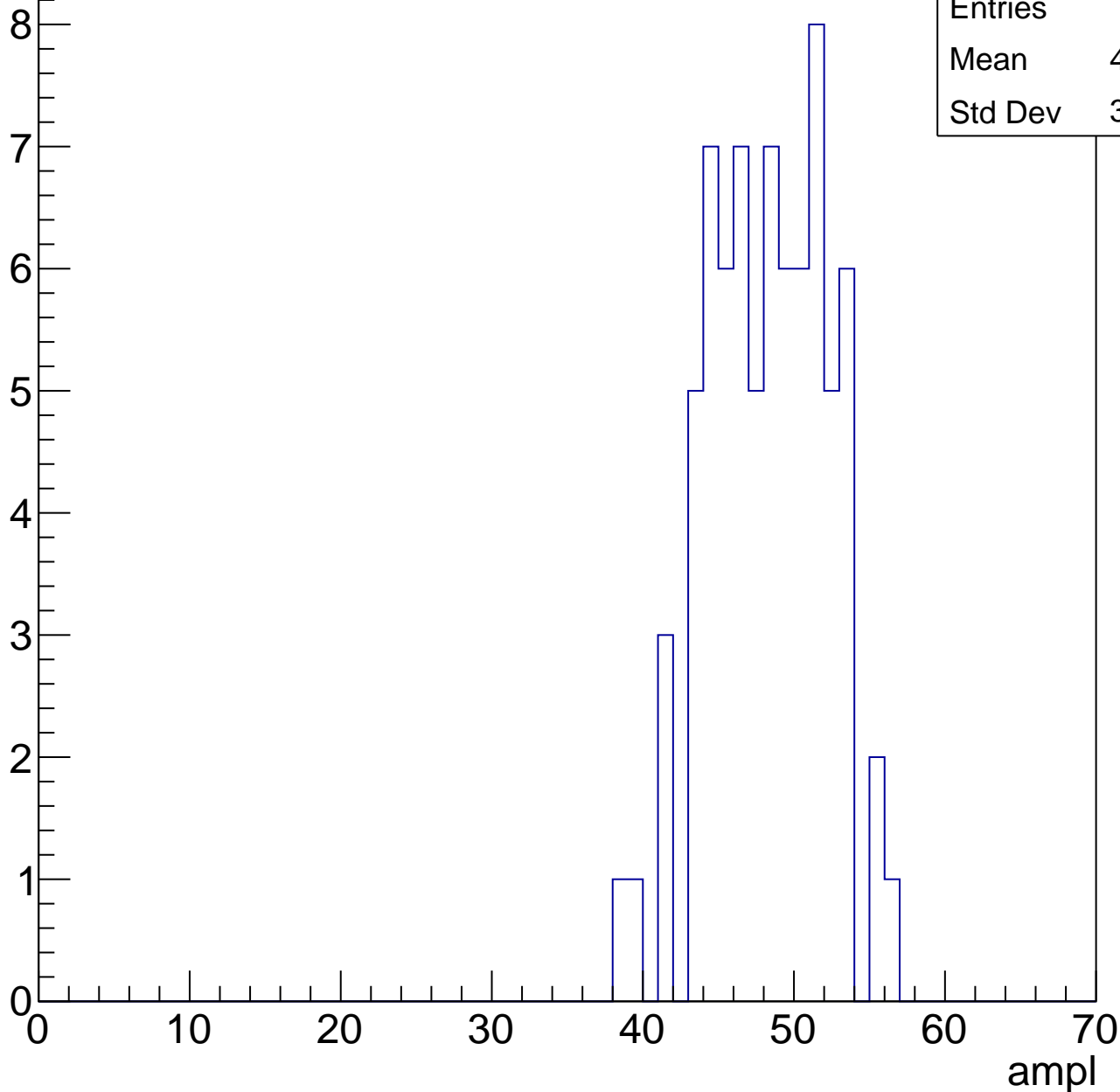


B0L102S, U5-ch110, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	76
Mean	47.79
Std Dev	3.877

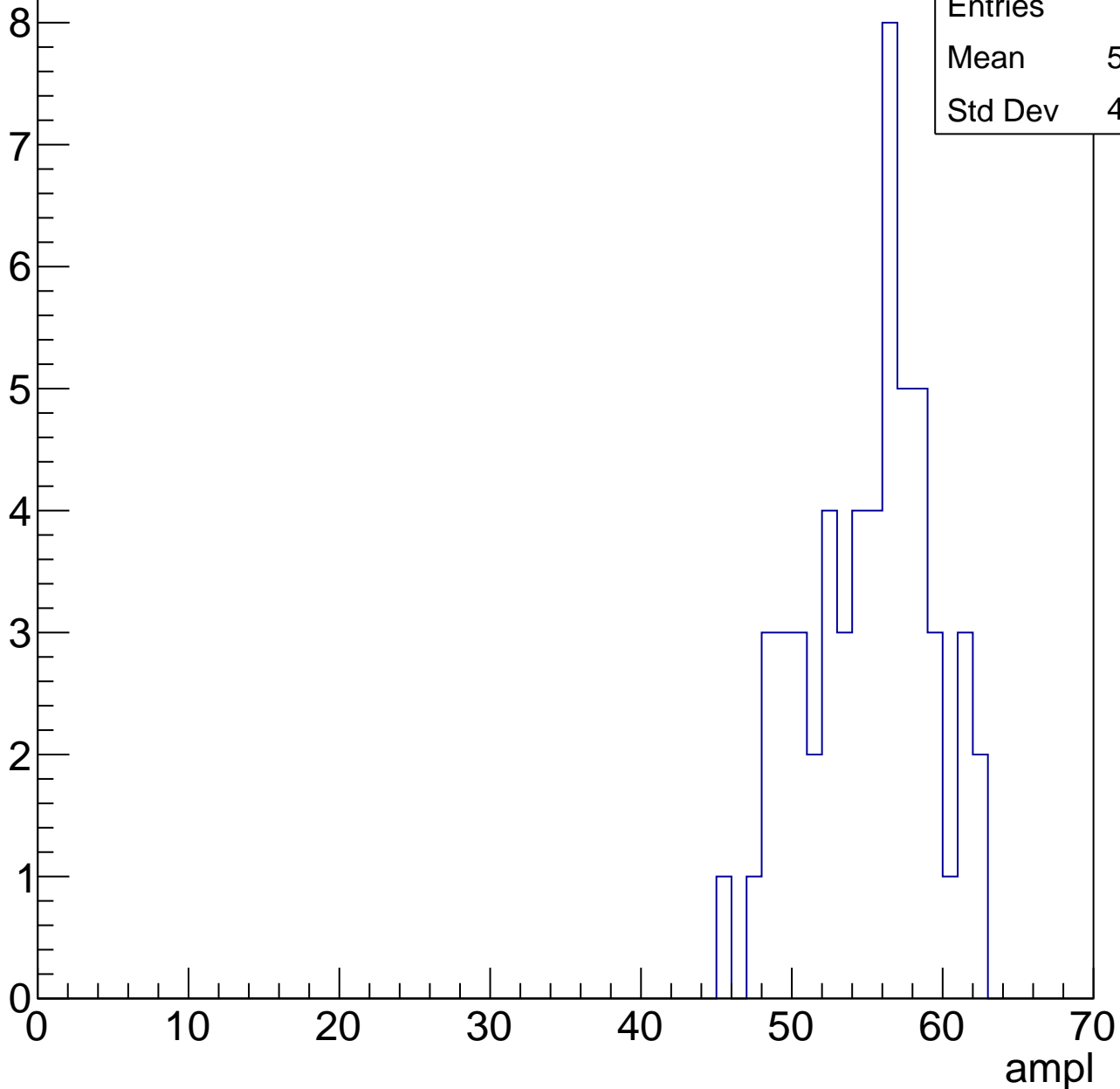


B0L102S, U5-ch110, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	54.64
Std Dev	4.096

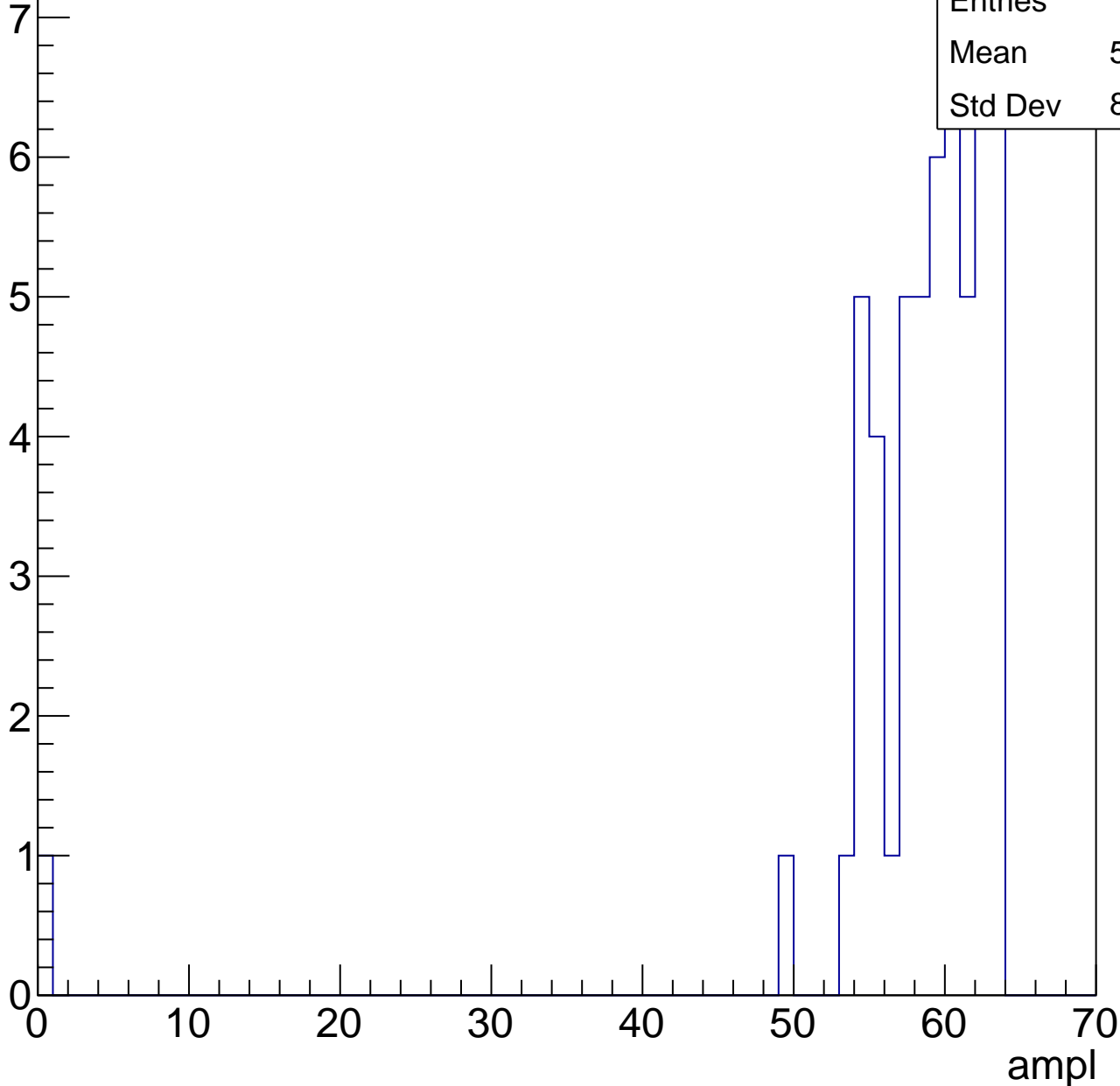


B0L102S, U5-ch110, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

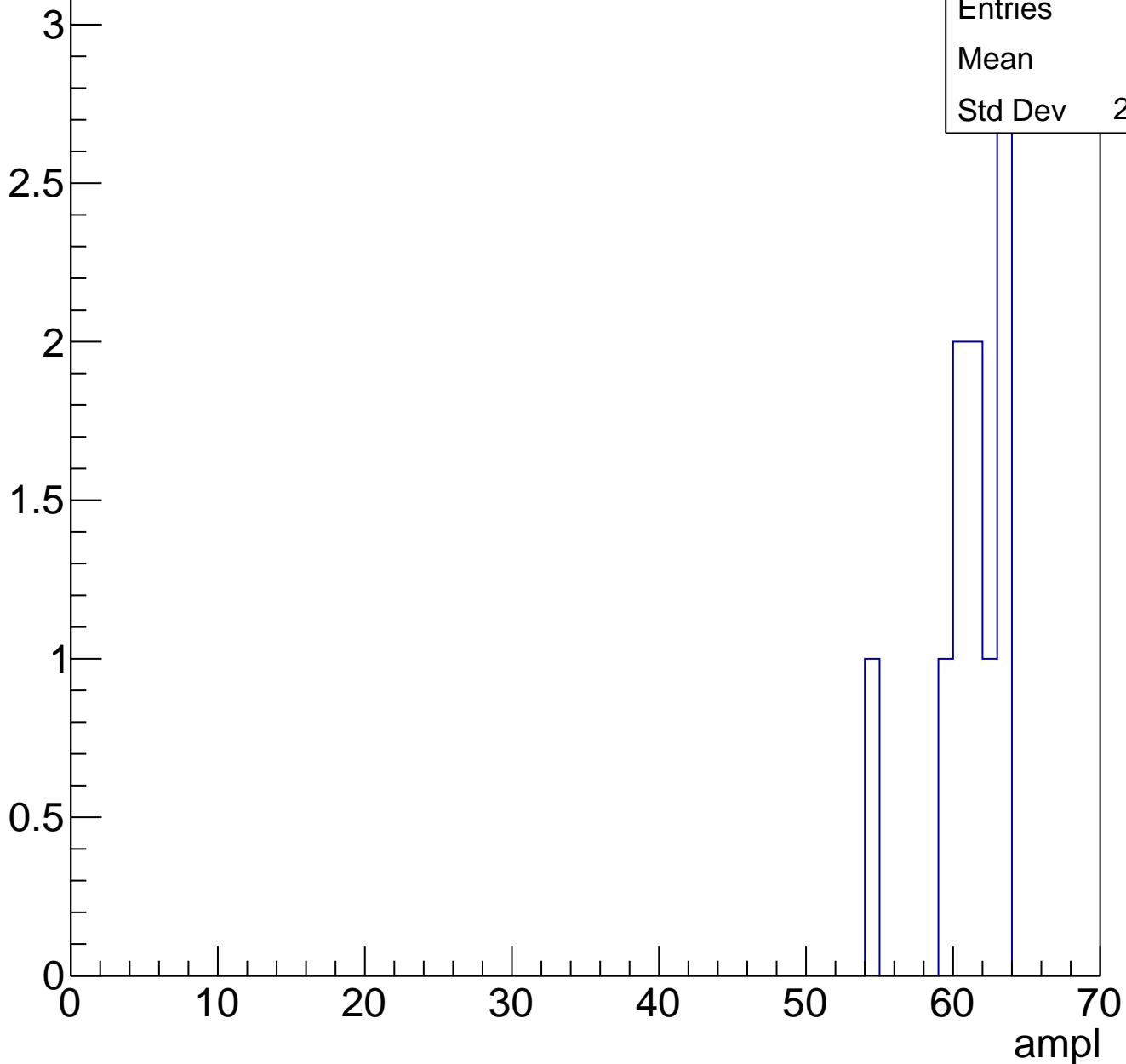
Entries	55
Mean	57.76
Std Dev	8.483



B0L102S, U5-ch110, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch110, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch111, adc0

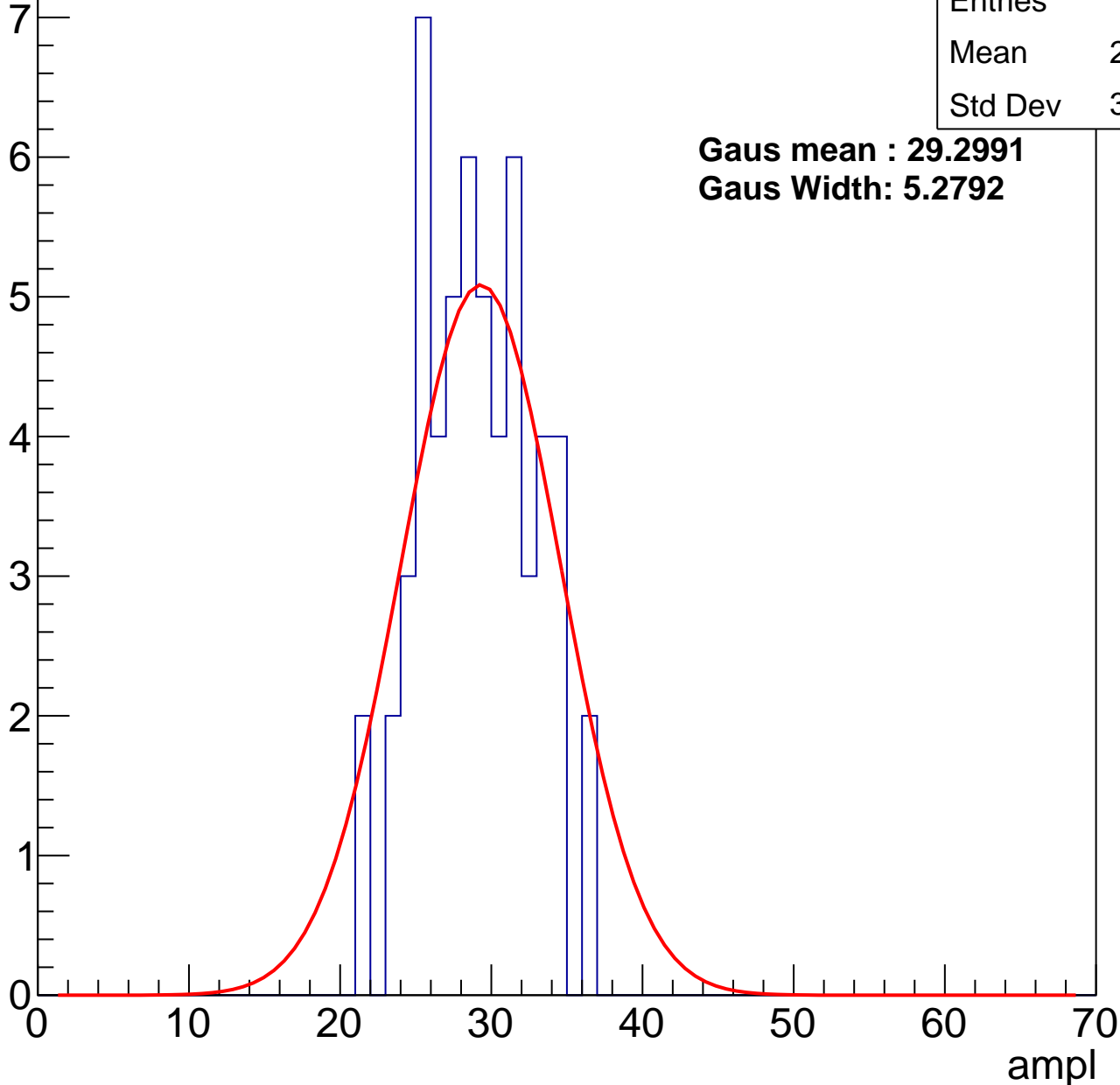
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	28.58
Std Dev	3.642

Gaus mean : 29.2991

Gaus Width: 5.2792



B0L102S, U5-ch111, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	99
Mean	35.09
Std Dev	7.56

Gaus mean : 36.5090

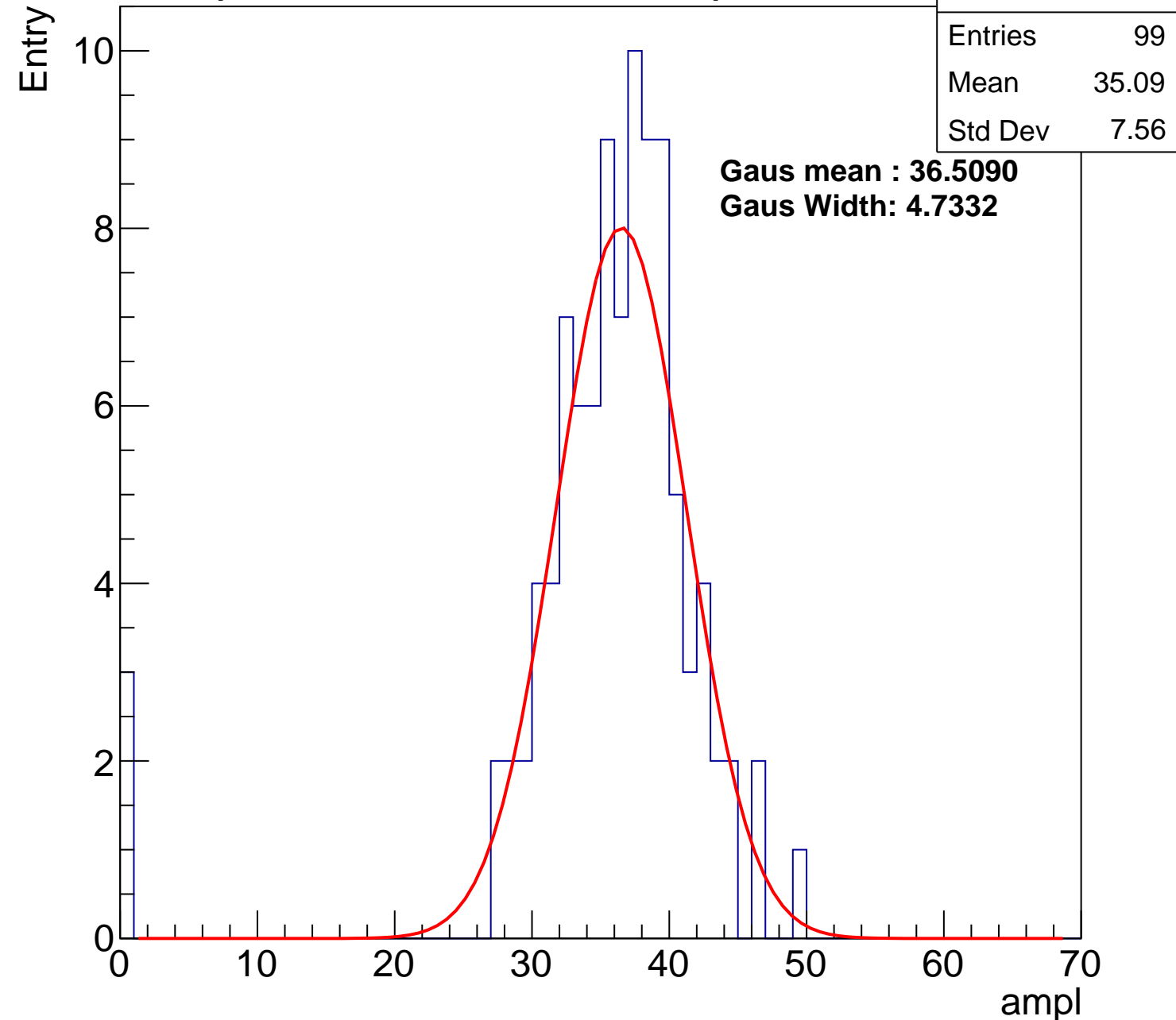
Gaus Width: 4.7332

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U5-ch111, adc2

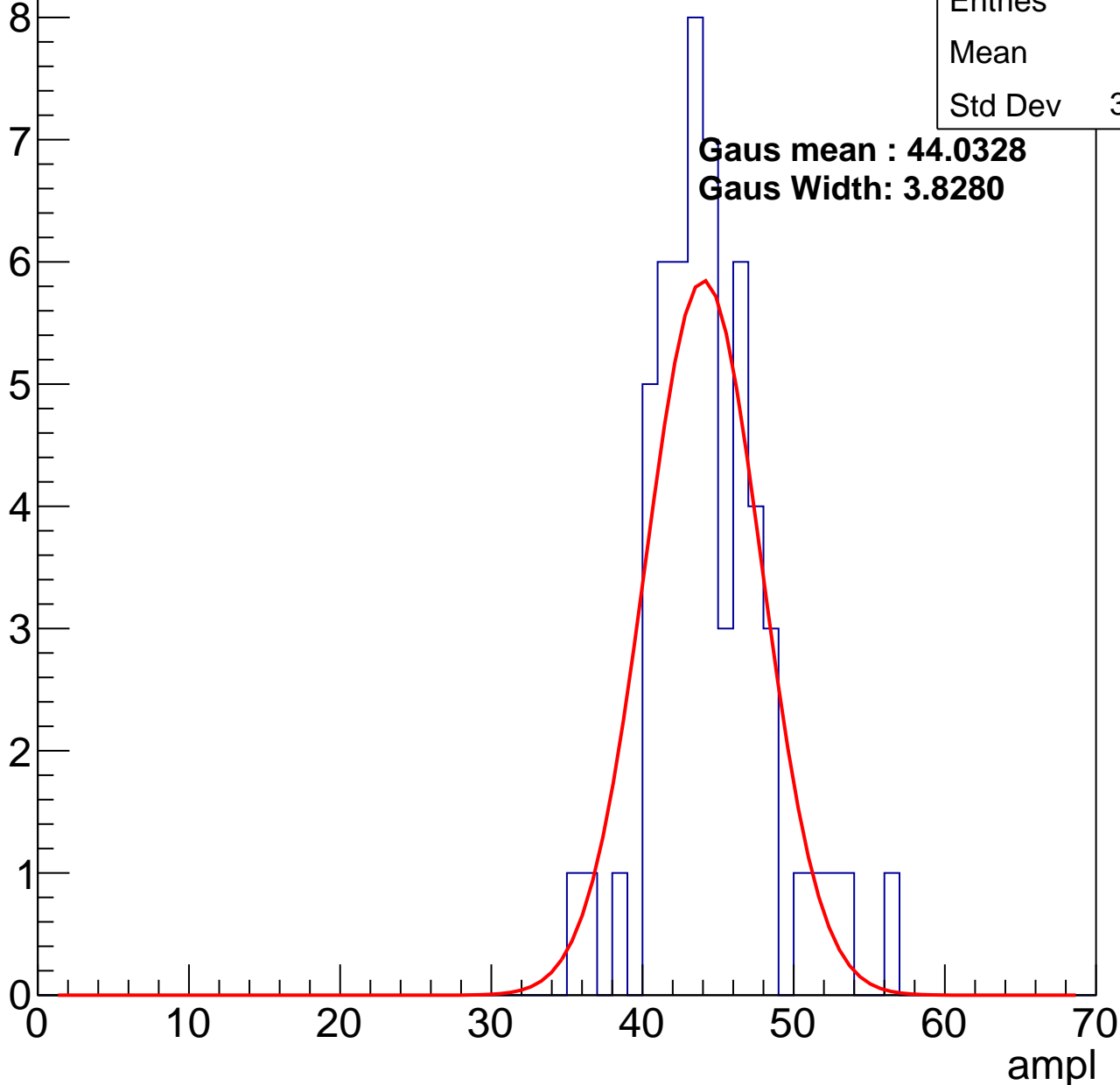
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	44
Std Dev	3.854

Gaus mean : 44.0328

Gaus Width: 3.8280

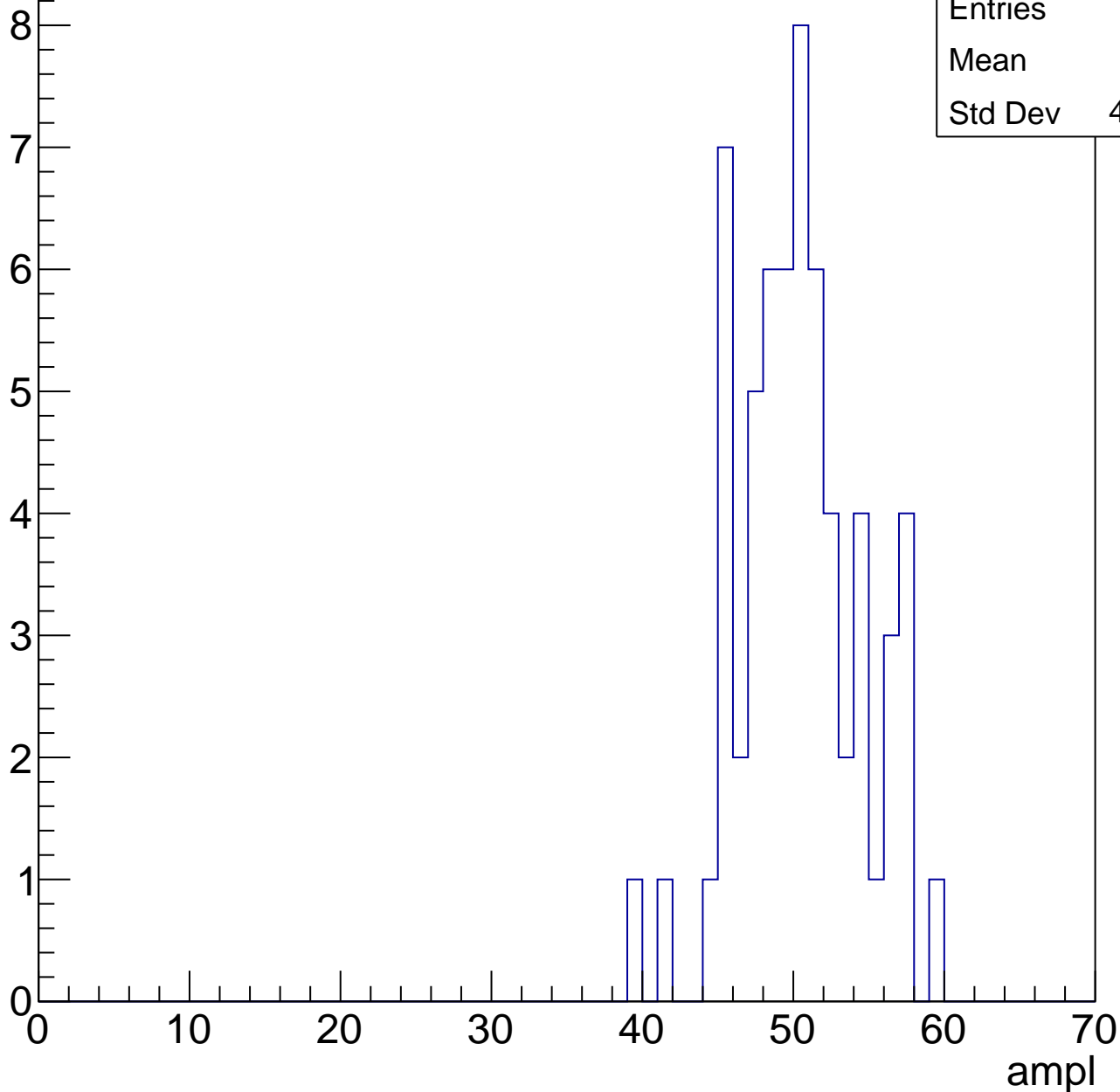


B0L102S, U5-ch111, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	49.9
Std Dev	4.083

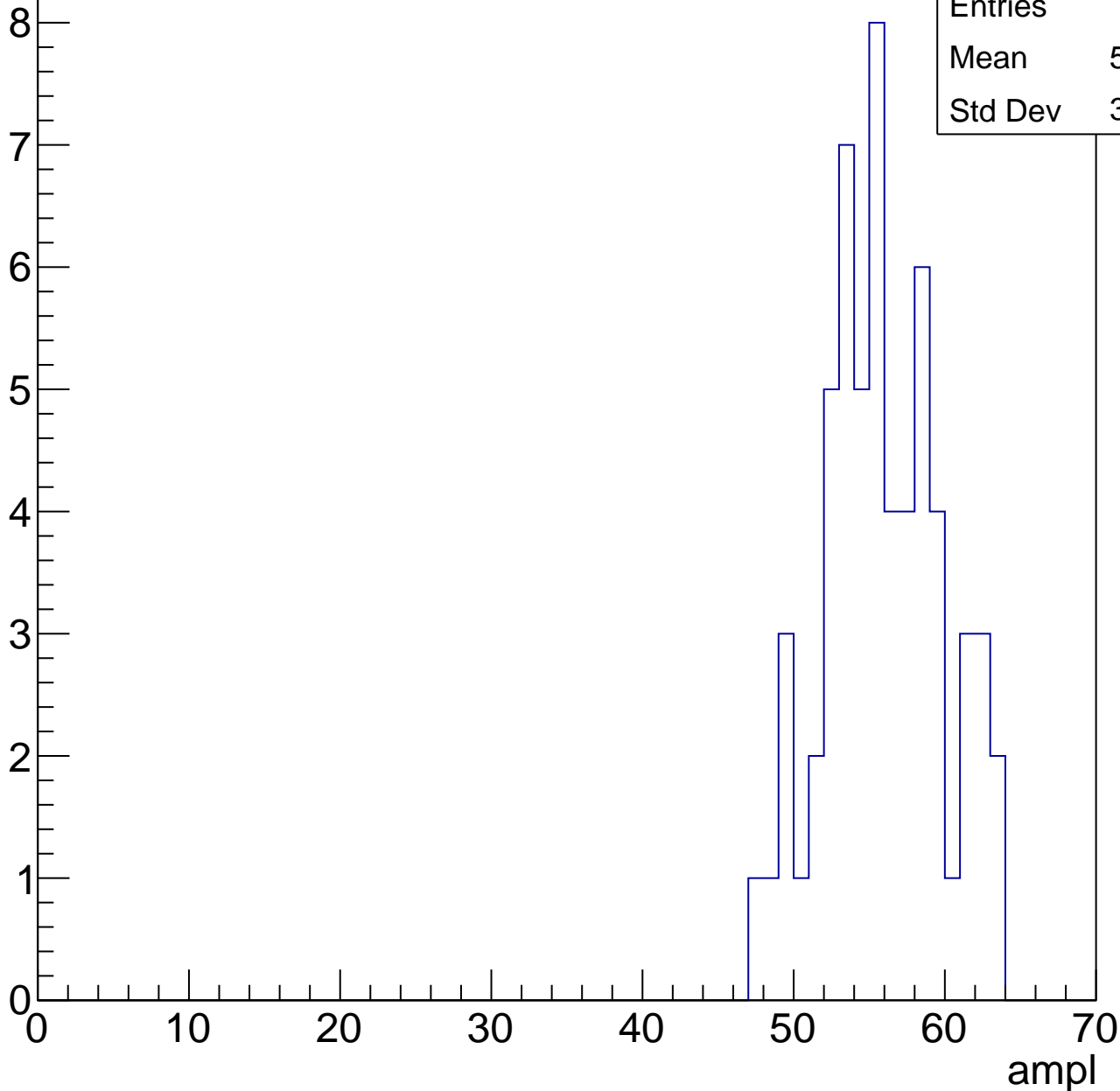


B0L102S, U5-ch111, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	55.43
Std Dev	3.853



B0L102S, U5-ch111, adc5

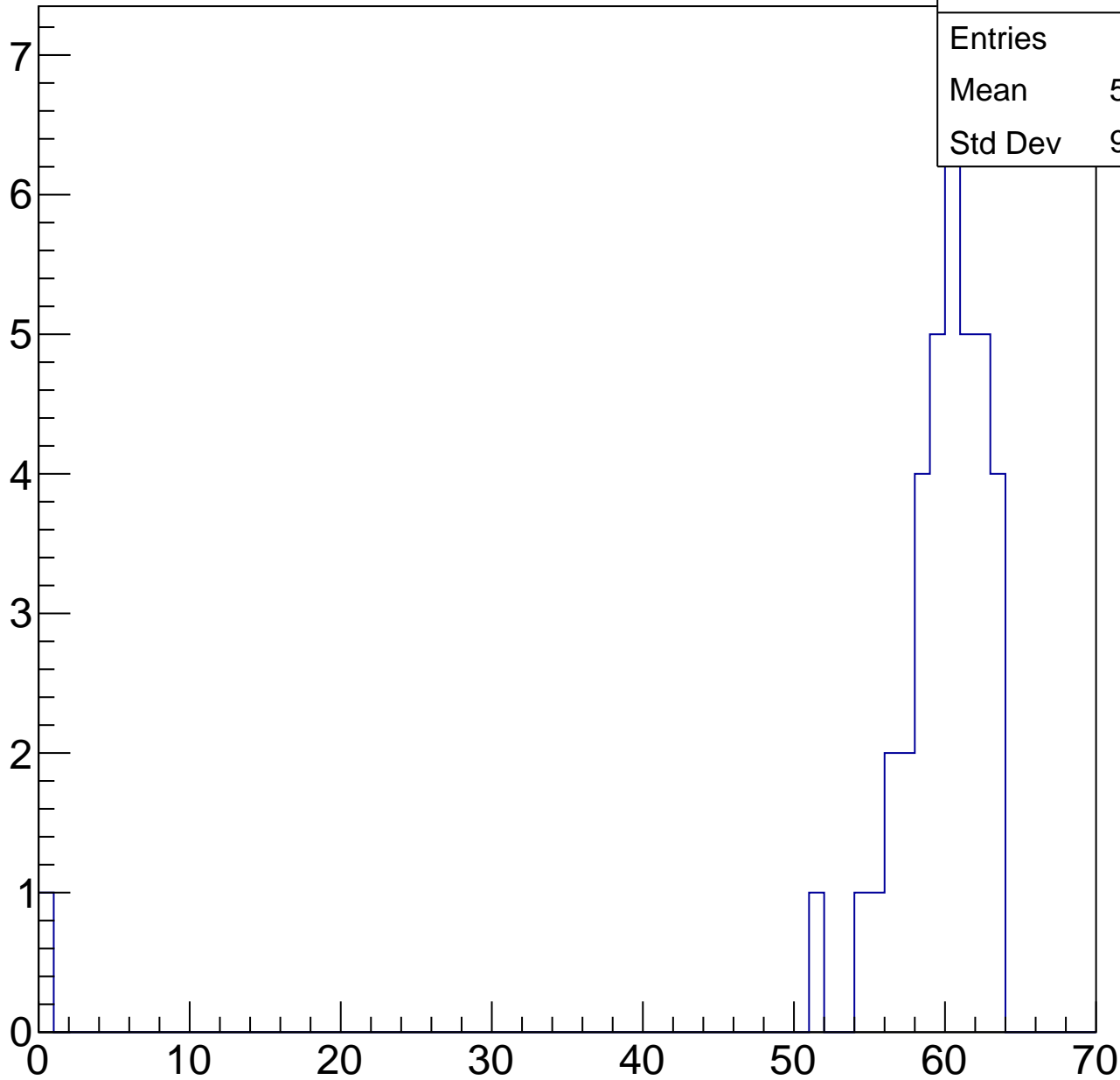
calib_packv5_042523_0143.root, FC#12, port B1

Entry

7
6
5
4
3
2
1
0

Entries	38
Mean	57.89
Std Dev	9.875

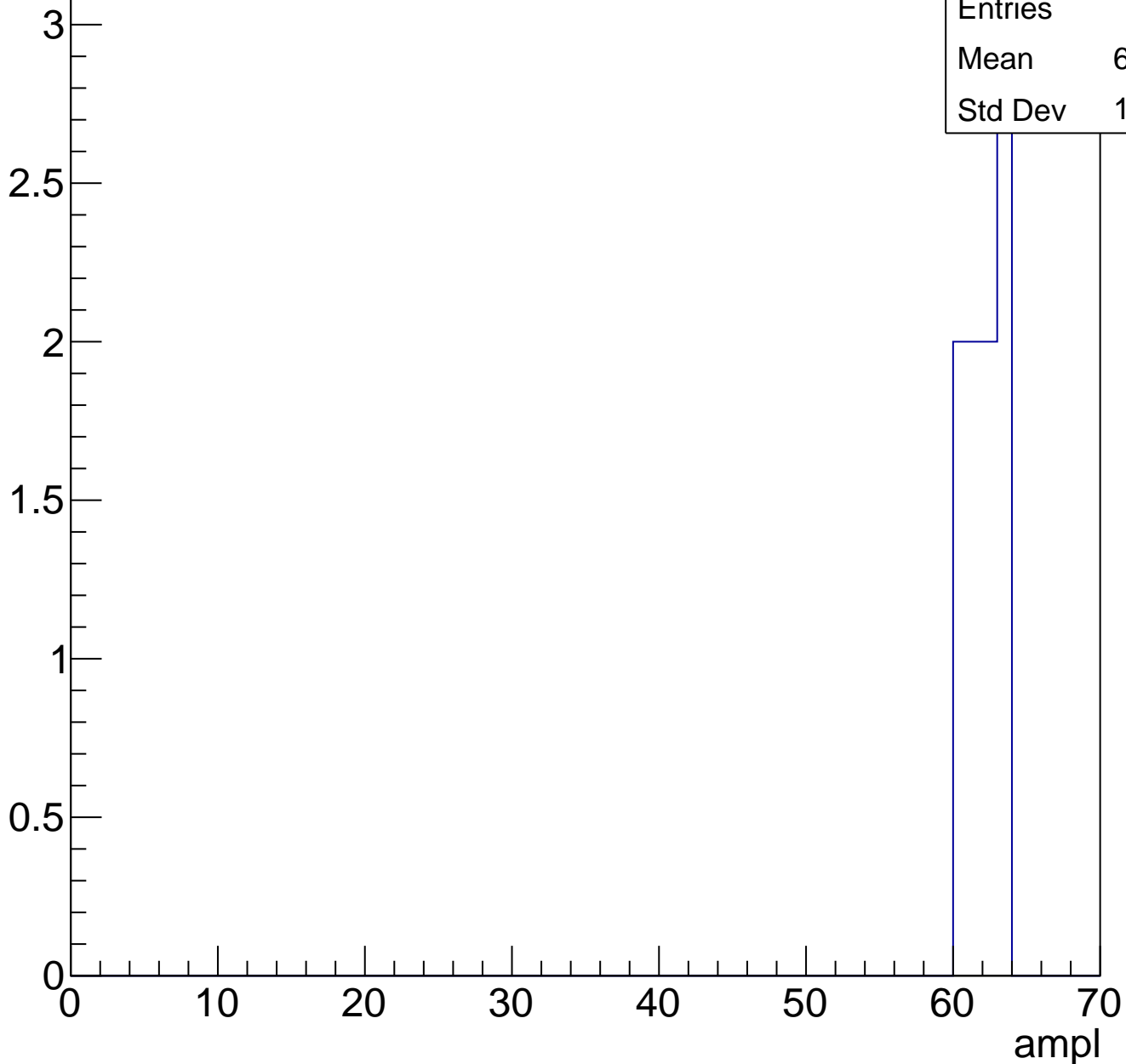
ampl



B0L102S, U5-ch111, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch111, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L102S, U5-ch112, adc0

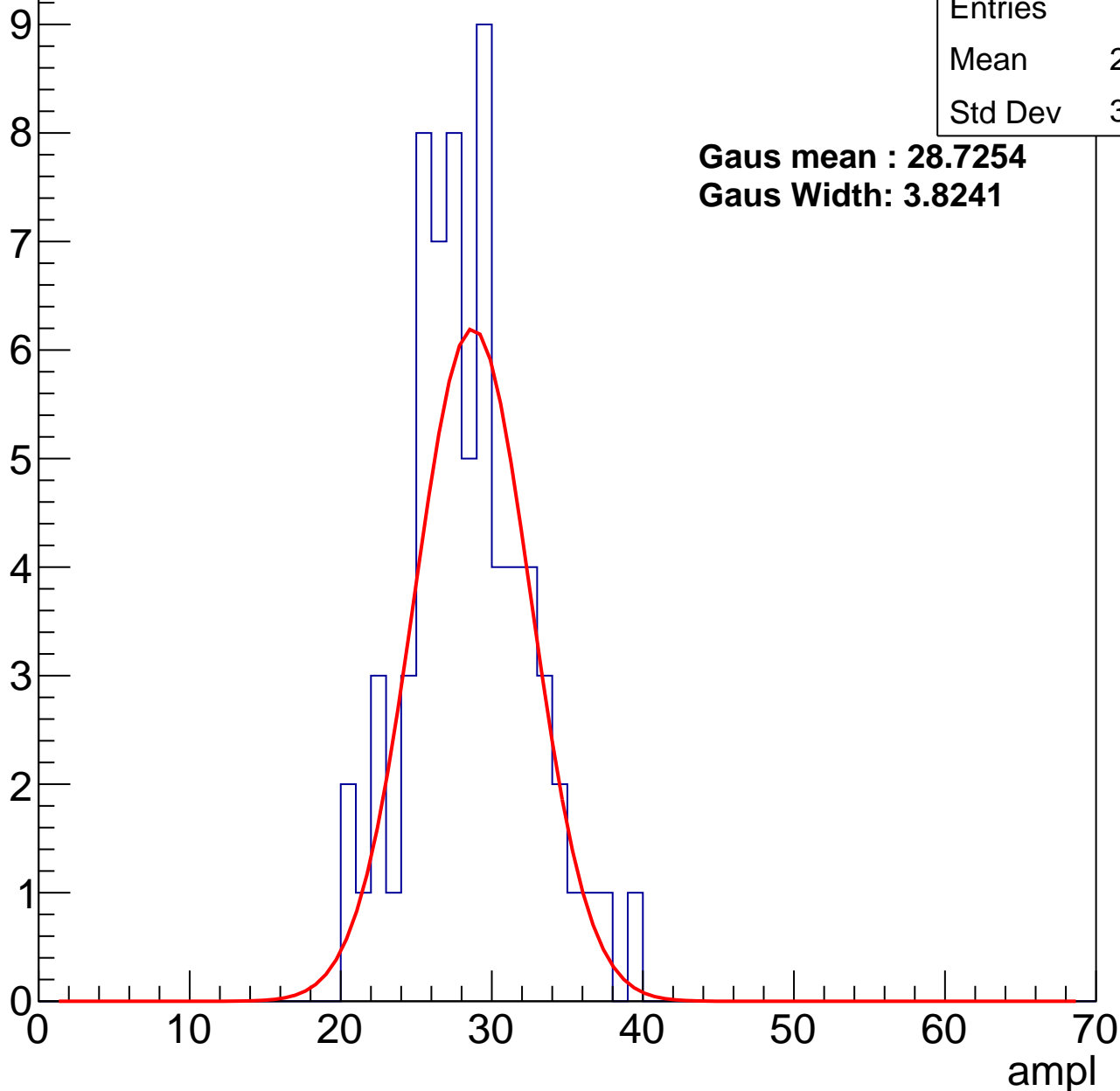
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	28.04
Std Dev	3.935

Gaus mean : 28.7254

Gaus Width: 3.8241



B0L102S, U5-ch112, adc1

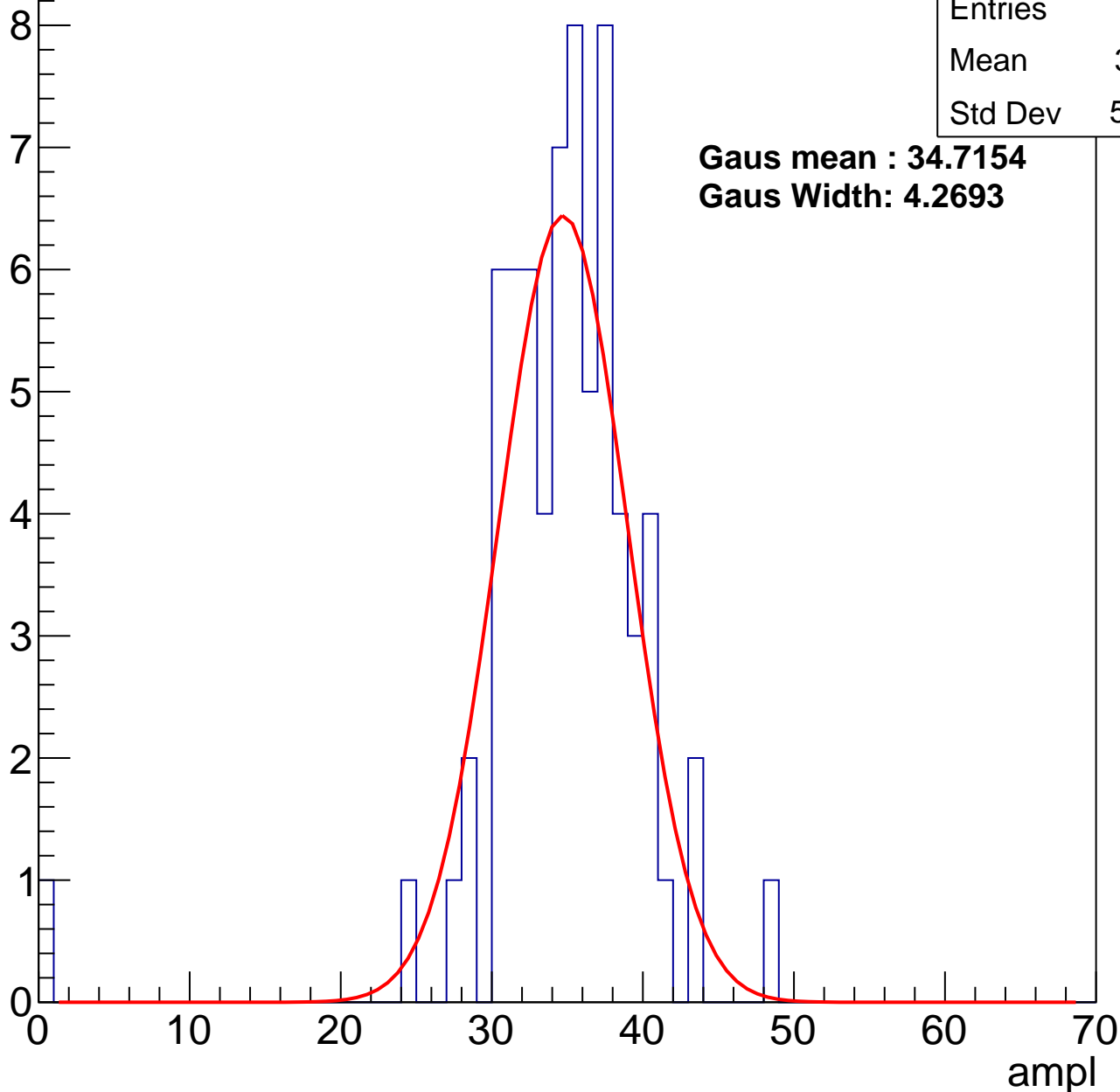
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	34.21
Std Dev	5.784

Gaus mean : 34.7154

Gaus Width: 4.2693



B0L102S, U5-ch112, adc2

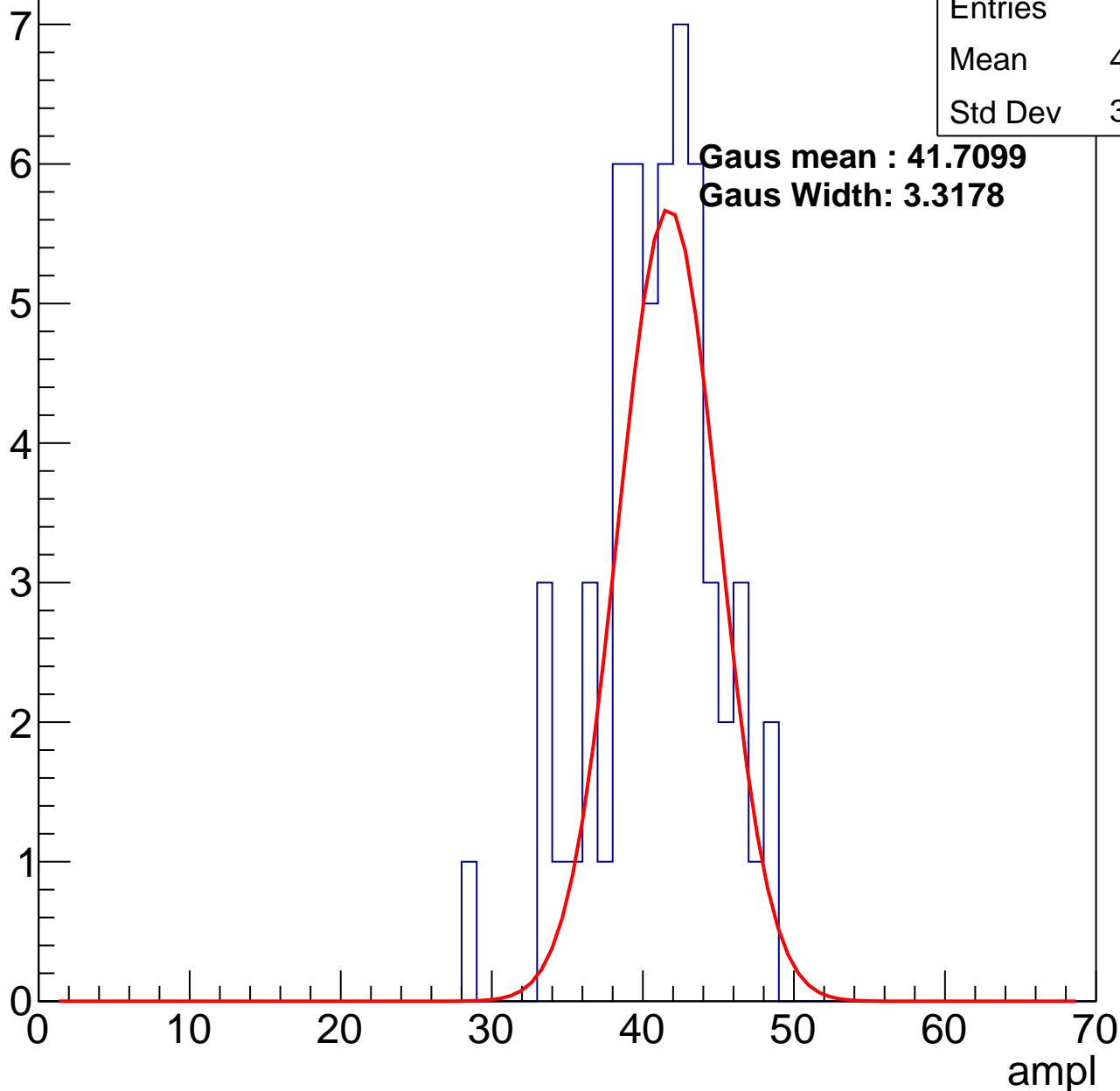
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	40.42
Std Dev	3.973

Gaus mean : 41.7099

Gaus Width: 3.3178

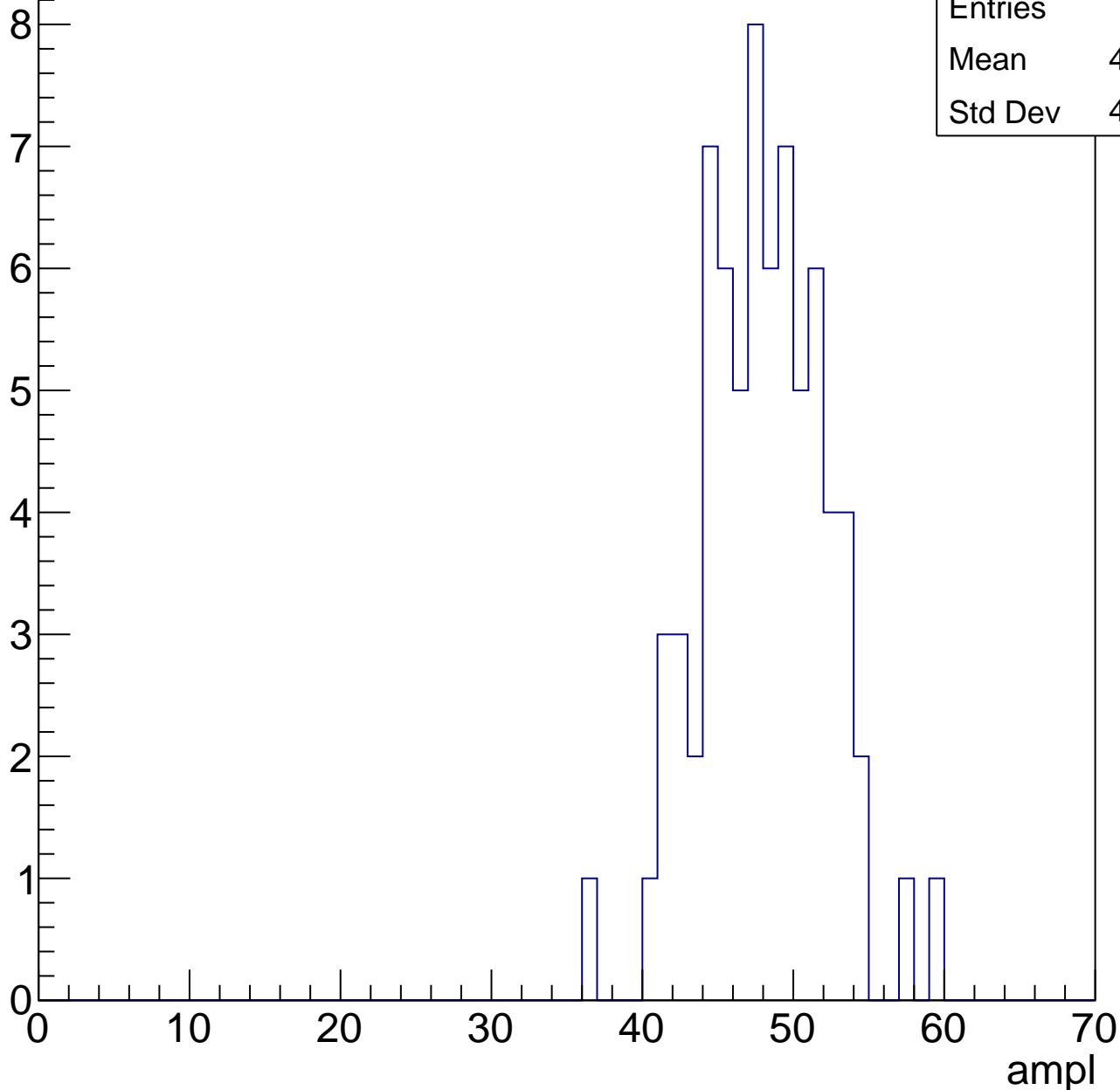


B0L102S, U5-ch112, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	47.58
Std Dev	4.105

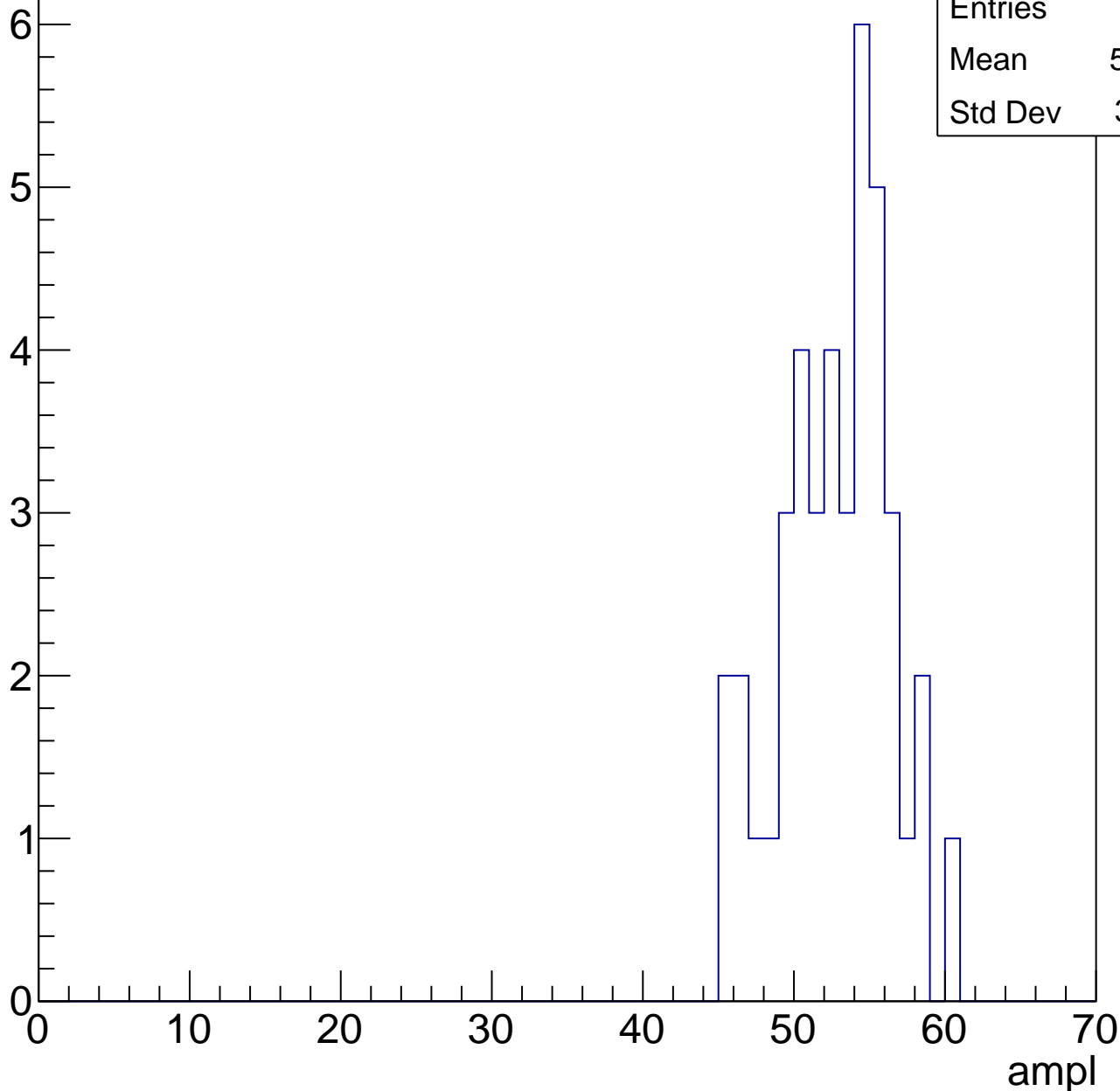


B0L102S, U5-ch112, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	52.29
Std Dev	3.611

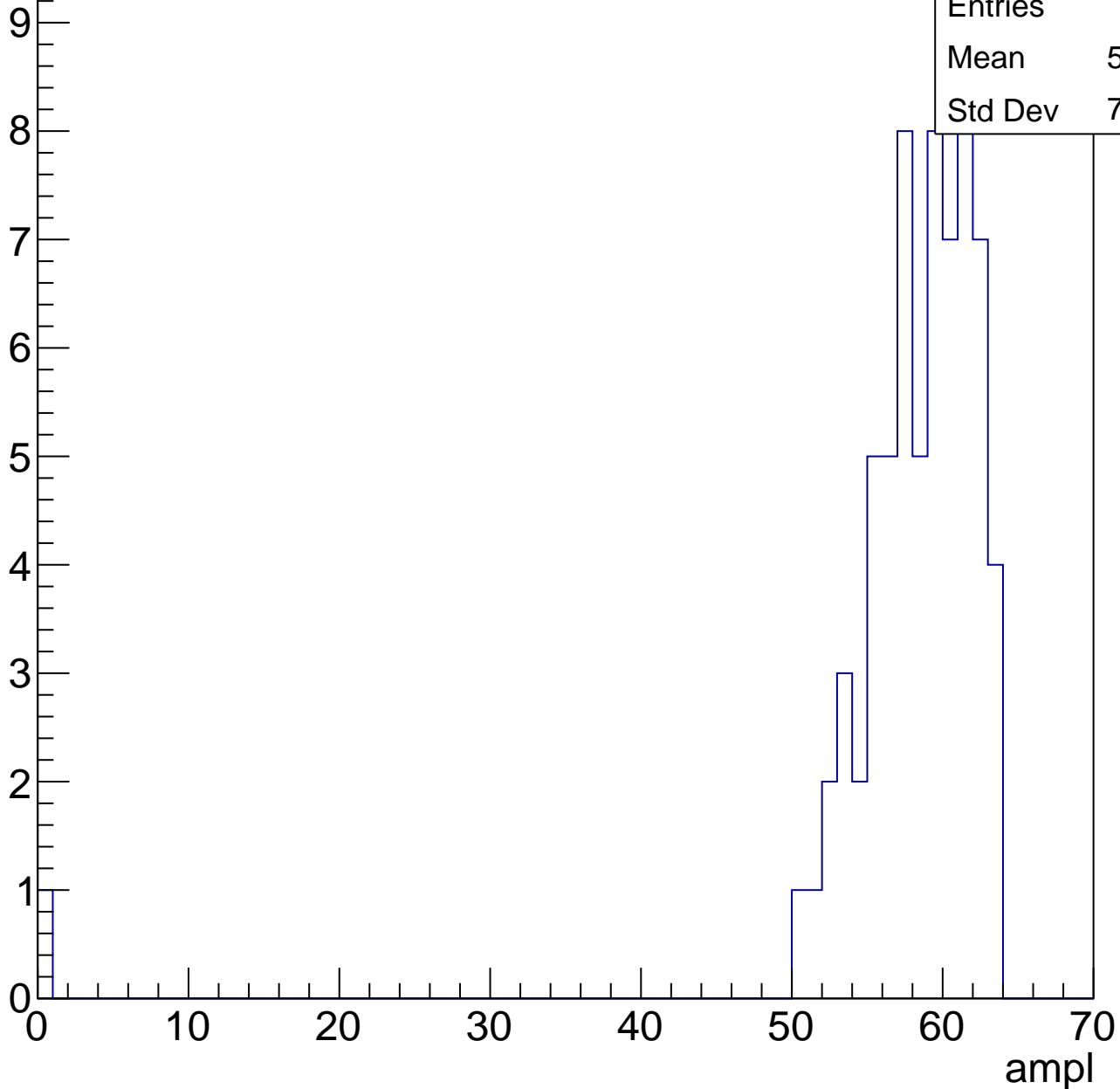


B0L102S, U5-ch112, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	57.35
Std Dev	7.698

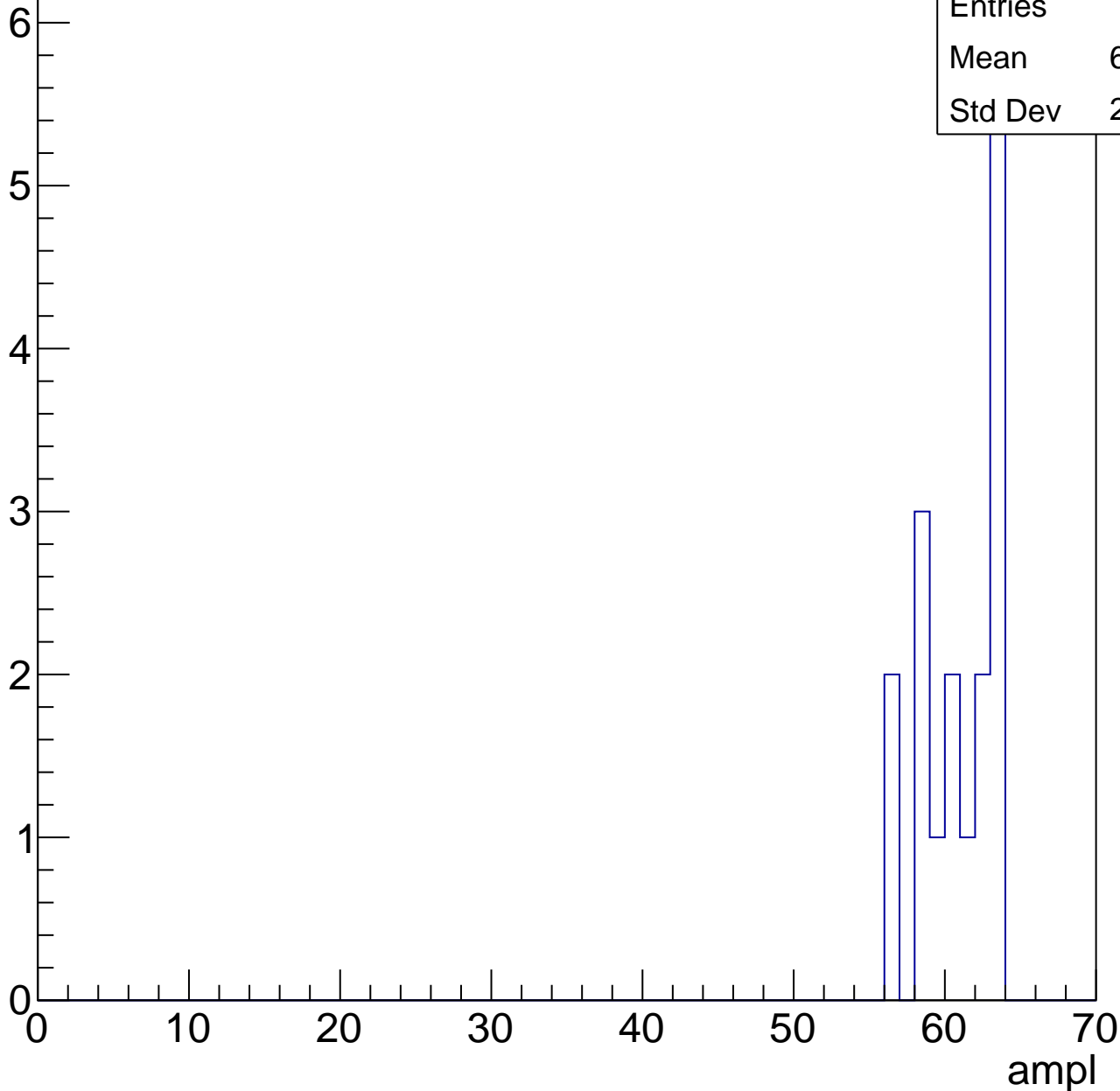


B0L102S, U5-ch112, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	17
Mean	60.47
Std Dev	2.476



B0L102S, U5-ch112, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L102S, U5-ch113, adc0

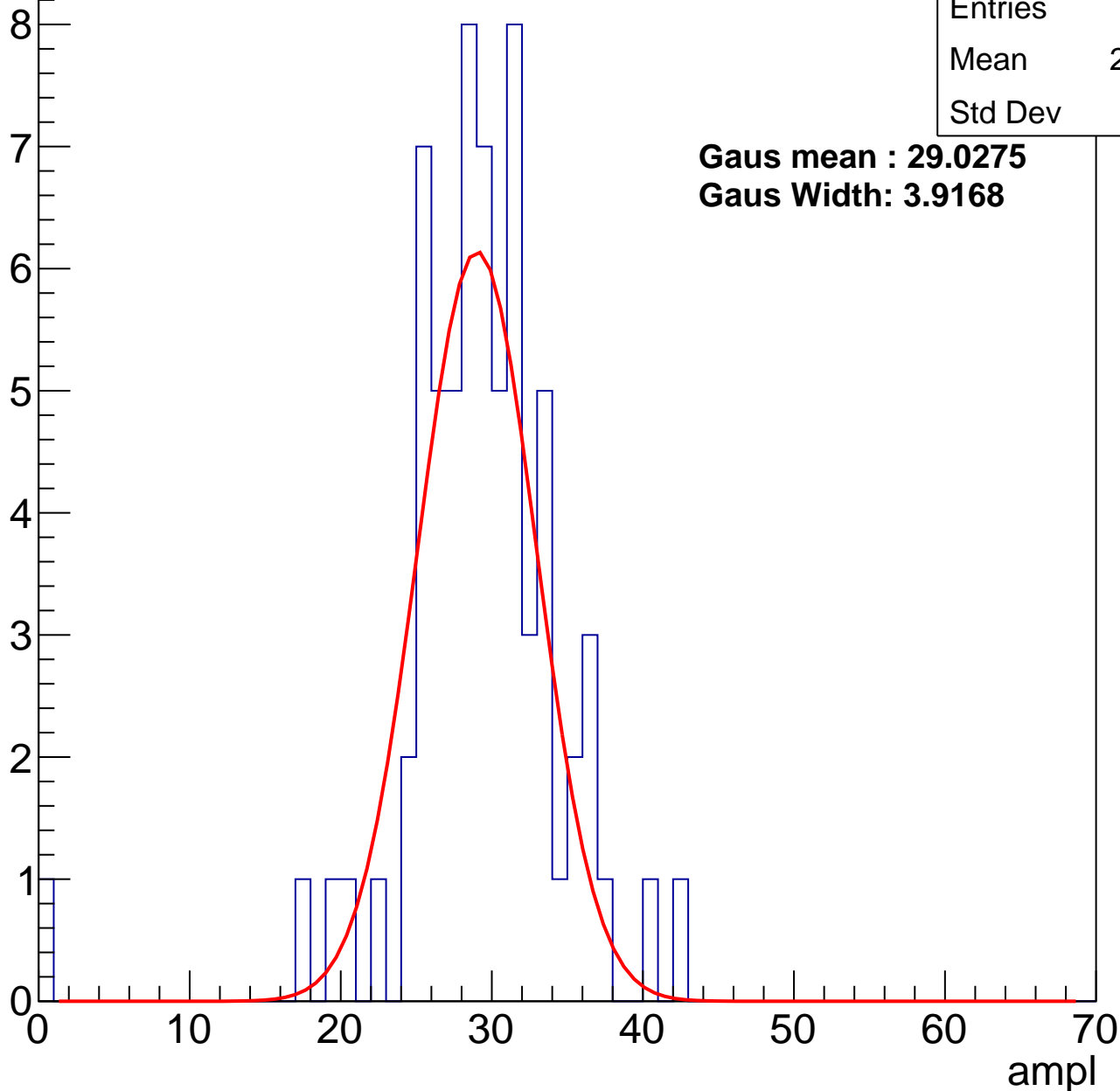
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	69
Mean	28.74
Std Dev	5.63

Gaus mean : 29.0275

Gaus Width: 3.9168



B0L102S, U5-ch113, adc1

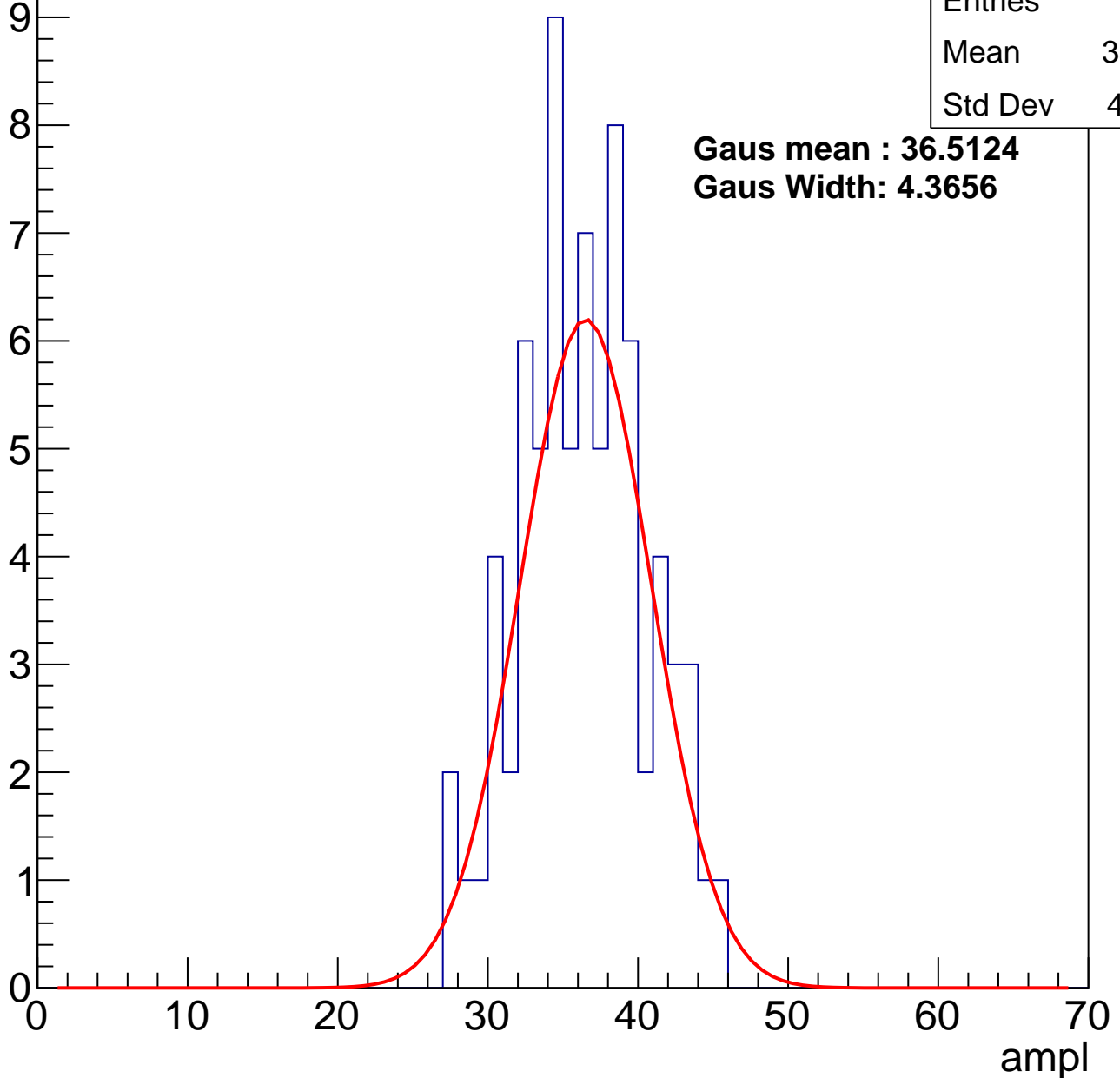
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.92
Std Dev	4.121

Gaus mean : 36.5124

Gaus Width: 4.3656



B0L102S, U5-ch113, adc2

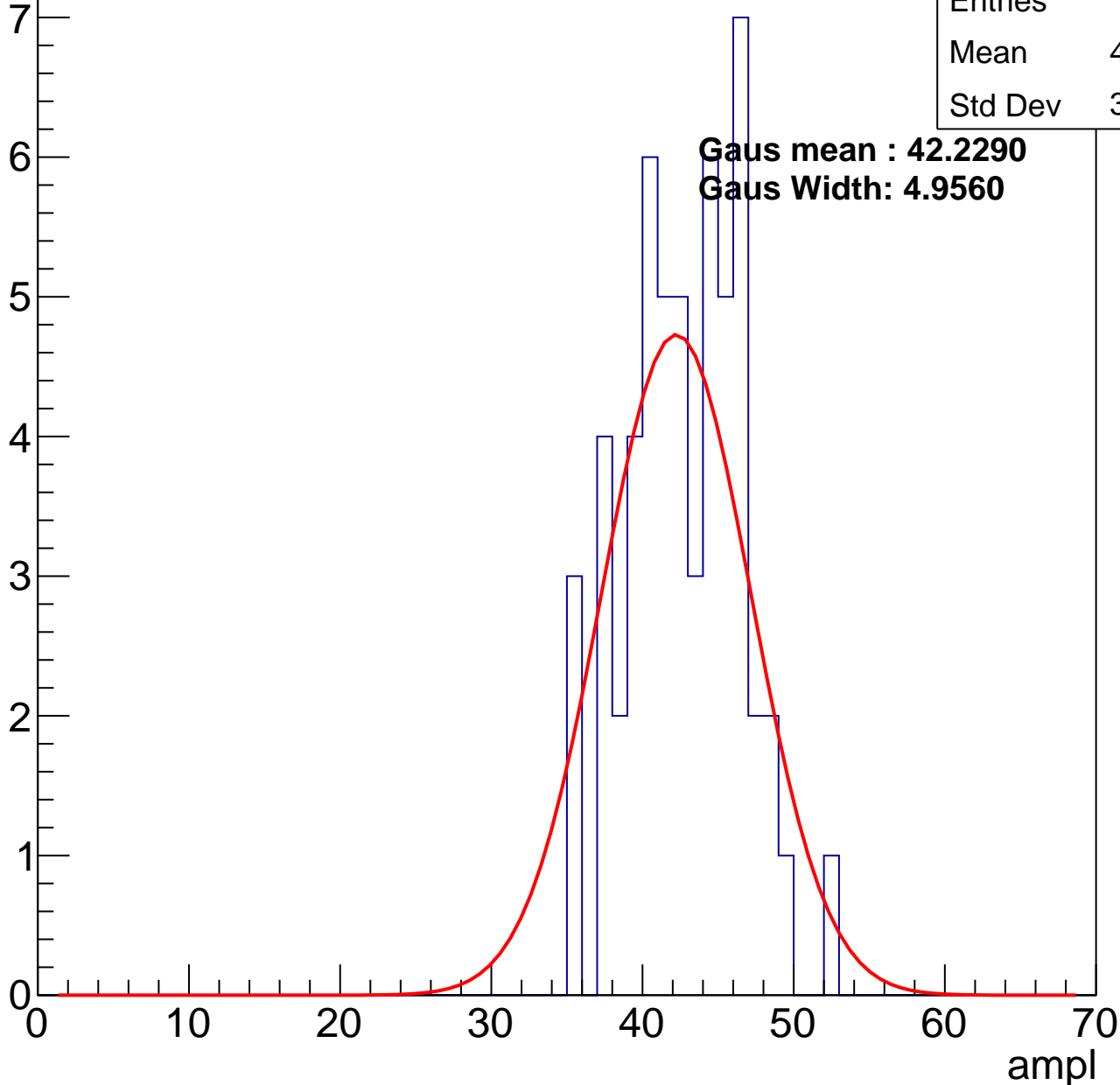
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	42.34
Std Dev	3.762

Gaus mean : 42.2290

Gaus Width: 4.9560

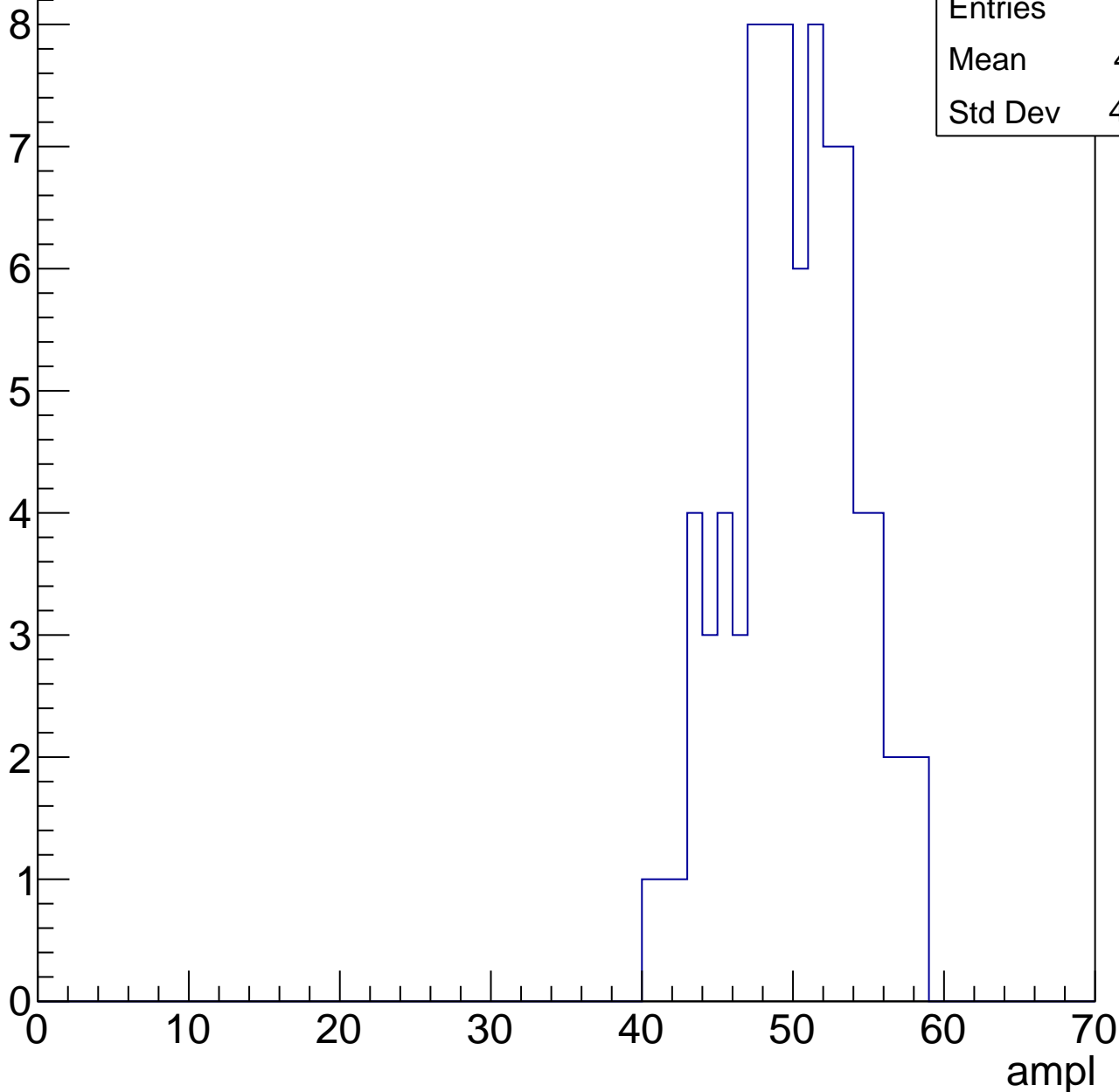


B0L102S, U5-ch113, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	49.61
Std Dev	4.044

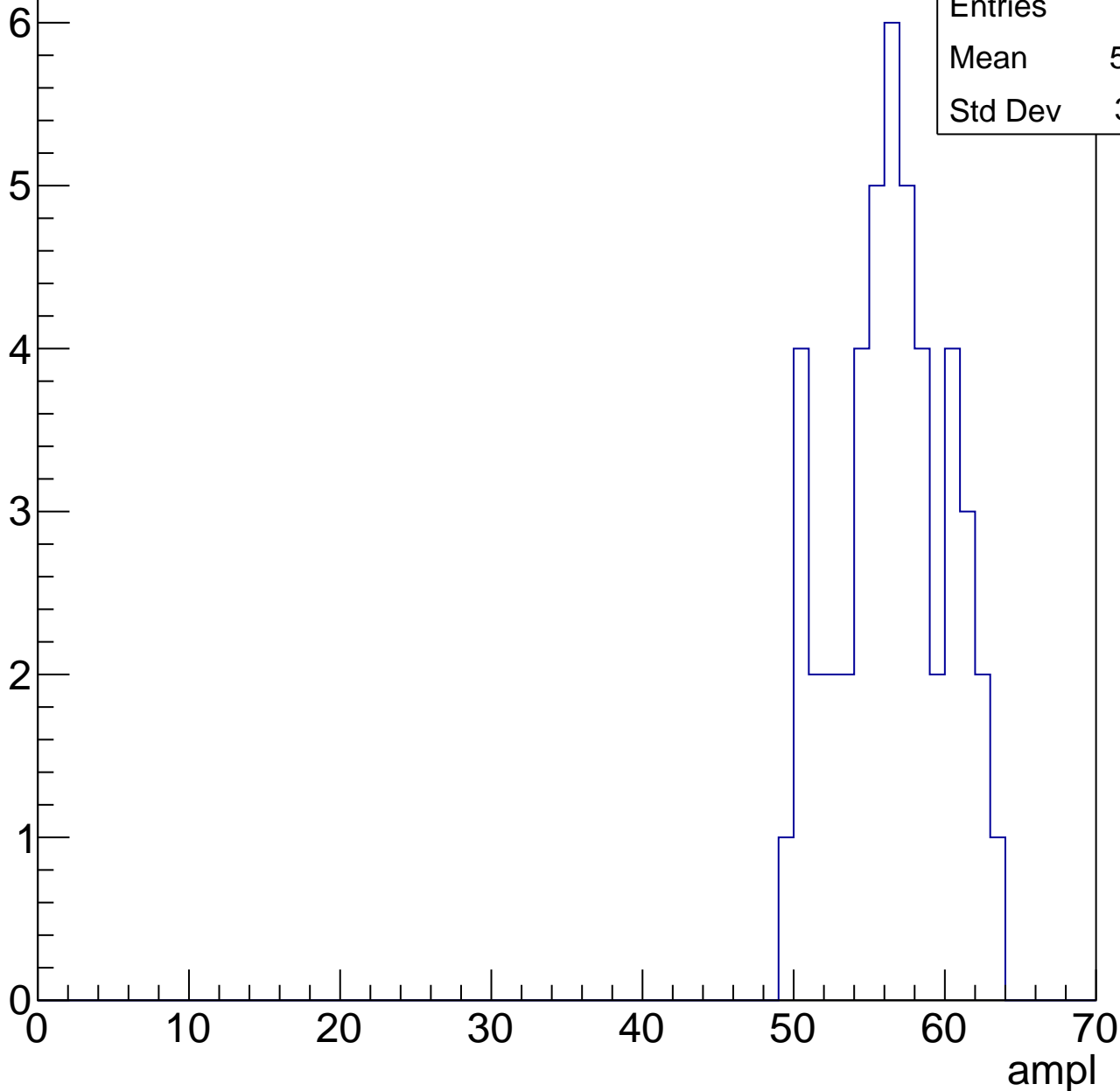


B0L102S, U5-ch113, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

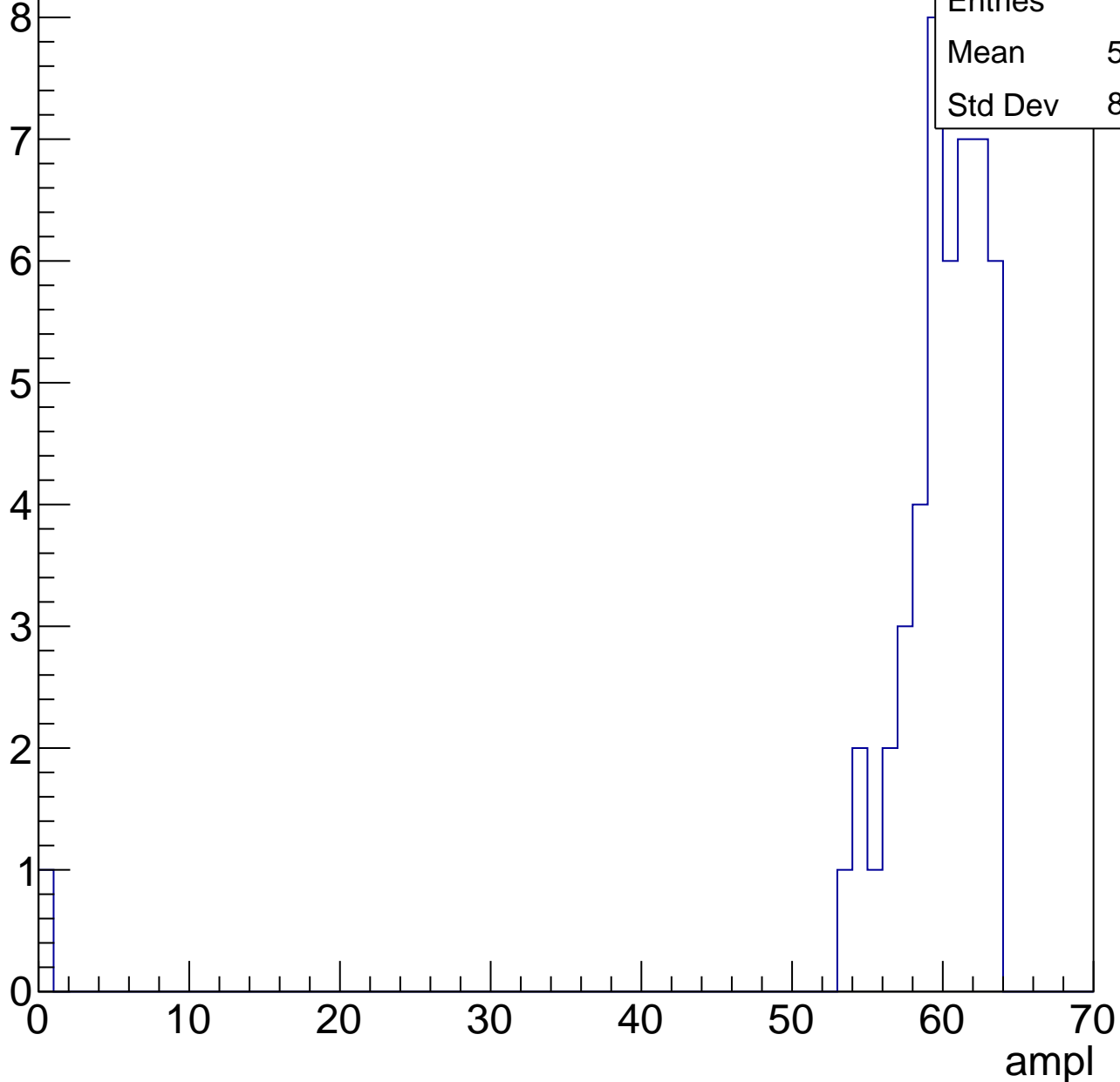
Entries	47
Mean	56.02
Std Dev	3.611



B0L102S, U5-ch113, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch113, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch113, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	1
Mean	0
Std Dev	0

B0L102S, U5-ch114, adc0

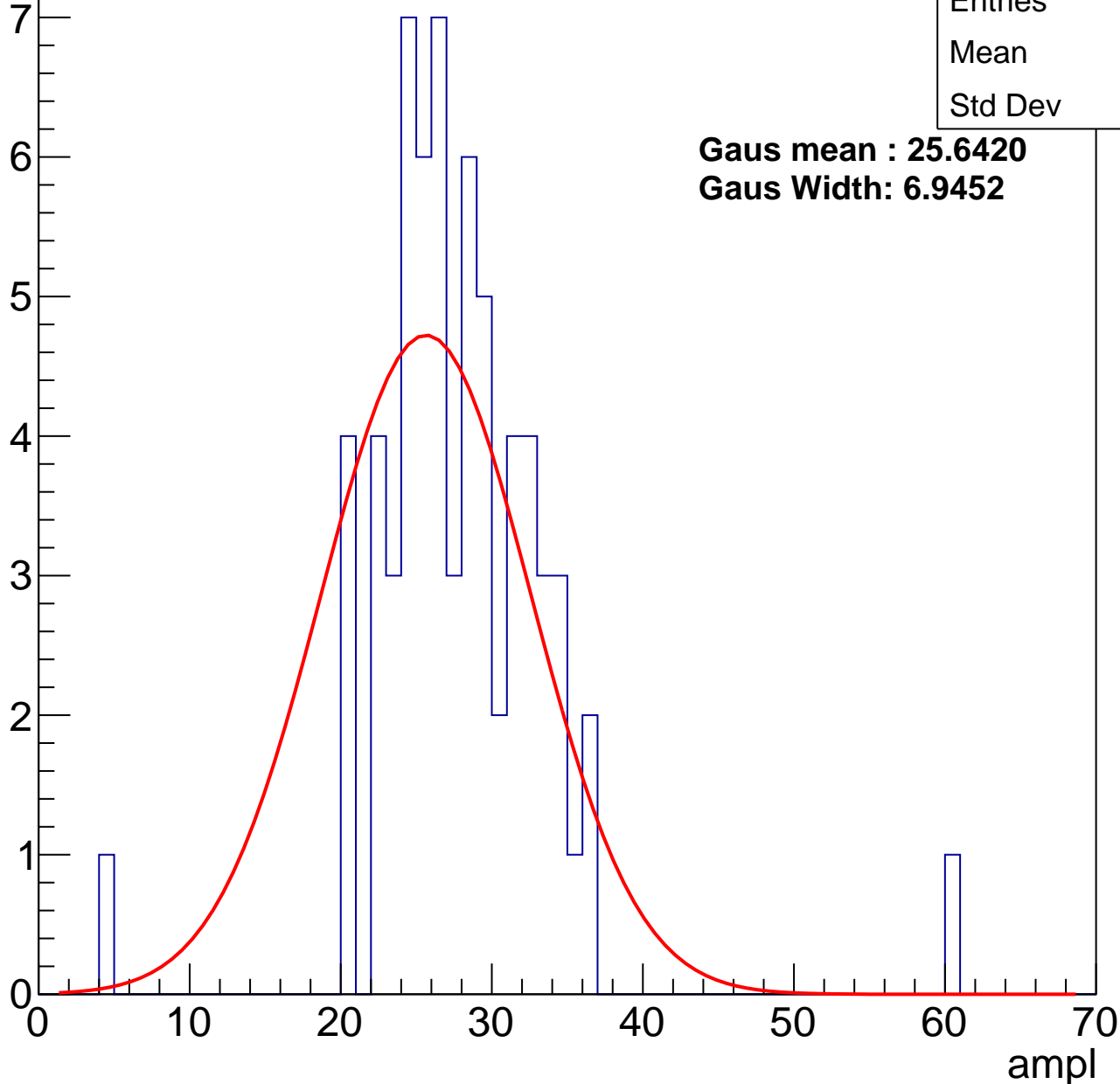
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	27.5
Std Dev	6.43

Gaus mean : 25.6420

Gaus Width: 6.9452



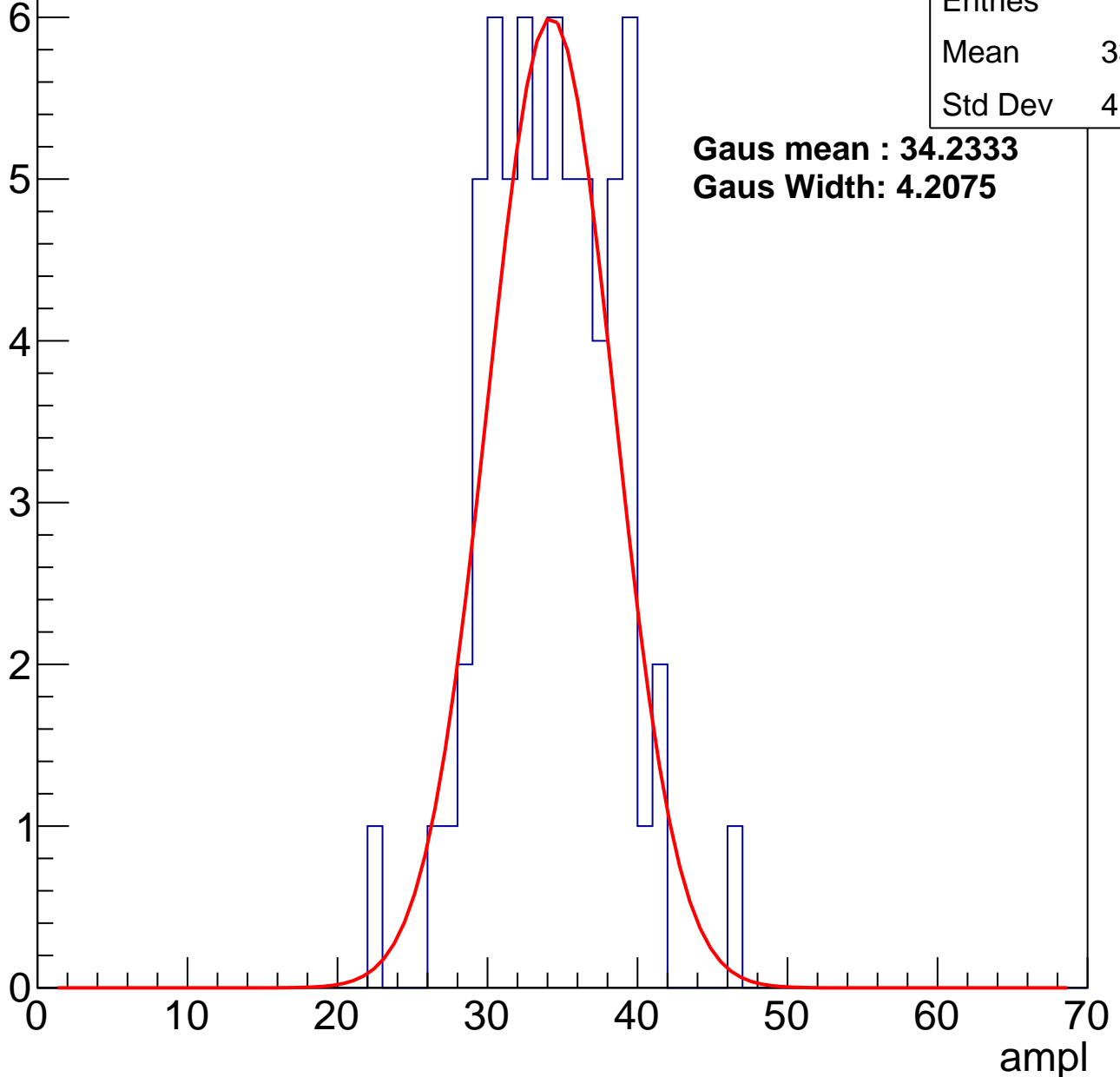
B0L102S, U5-ch114, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	33.84
Std Dev	4.217

Gaus mean : 34.2333
Gaus Width: 4.2075



B0L102S, U5-ch114, adc2

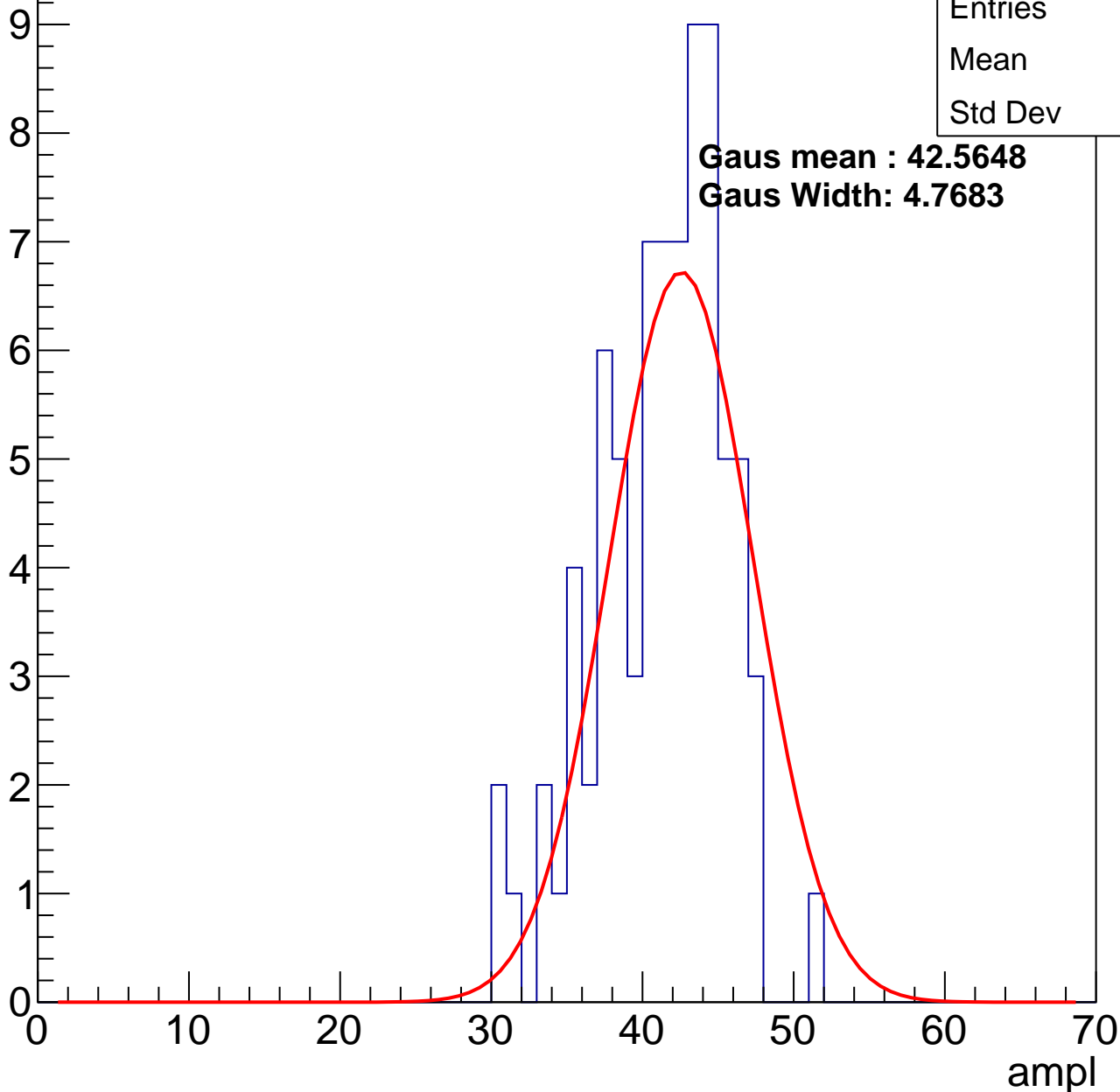
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	40.8
Std Dev	4.22

Gaus mean : 42.5648

Gaus Width: 4.7683

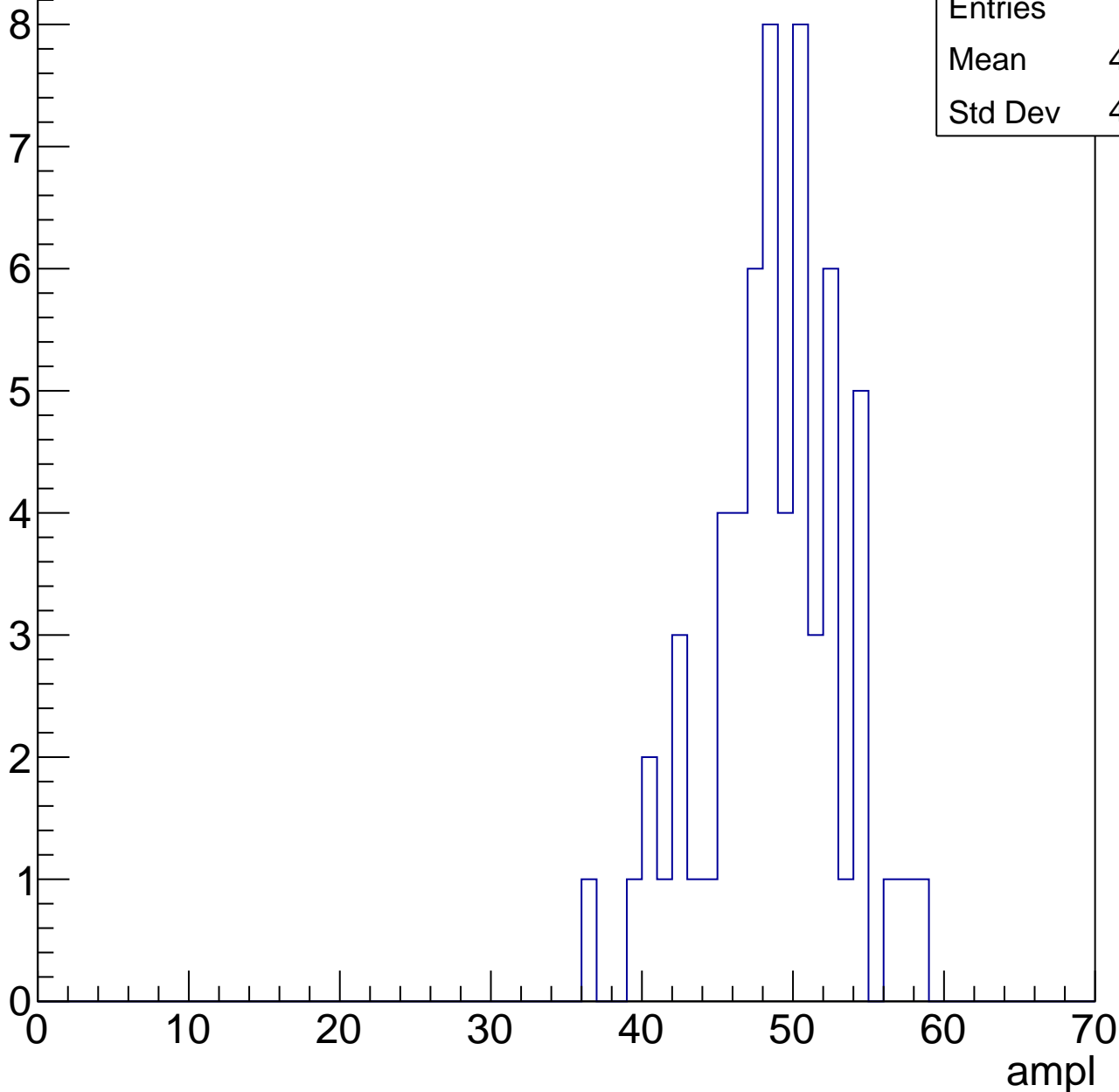


B0L102S, U5-ch114, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	48.29
Std Dev	4.423

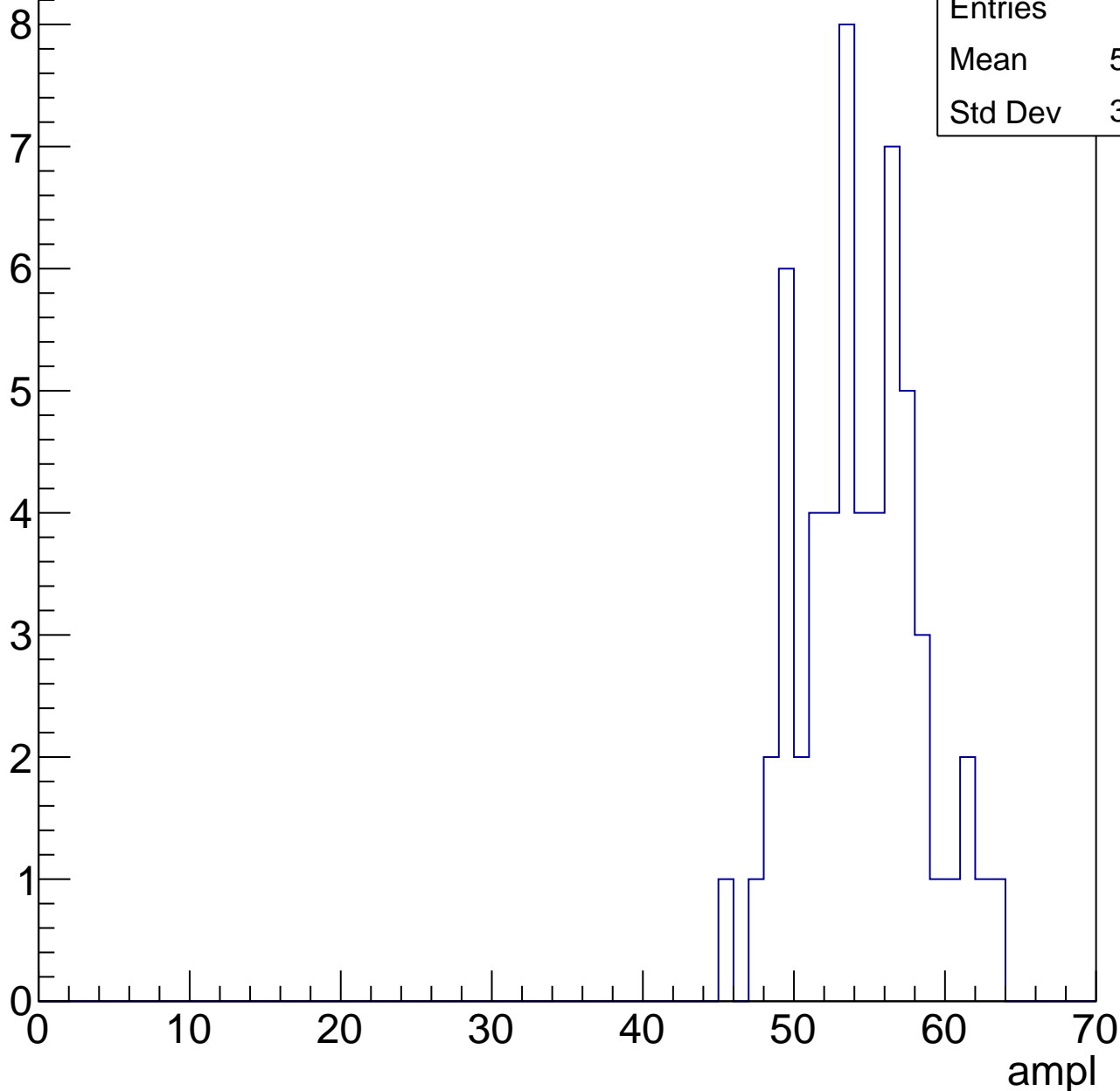


B0L102S, U5-ch114, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	53.88
Std Dev	3.912

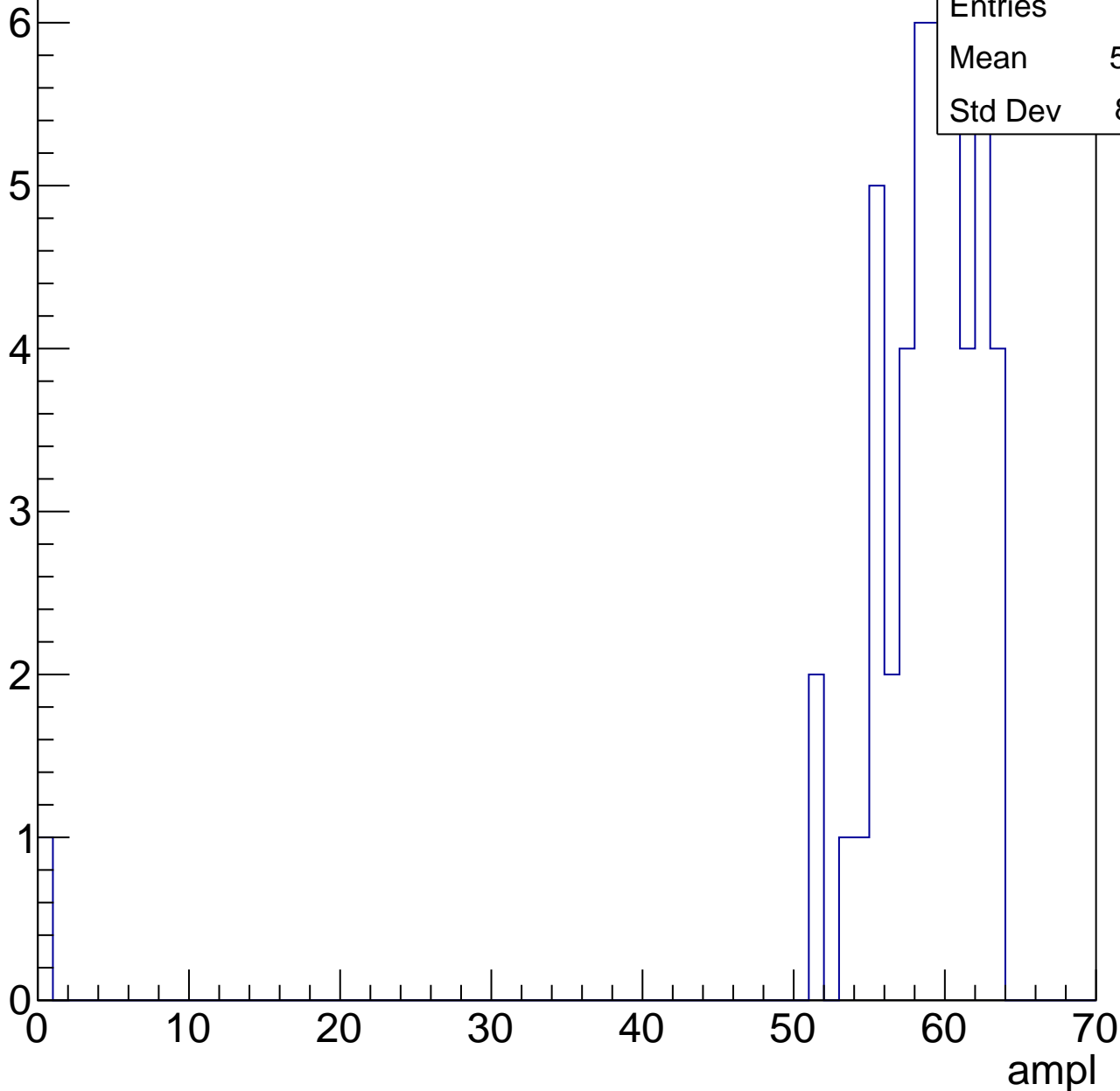


B0L102S, U5-ch114, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

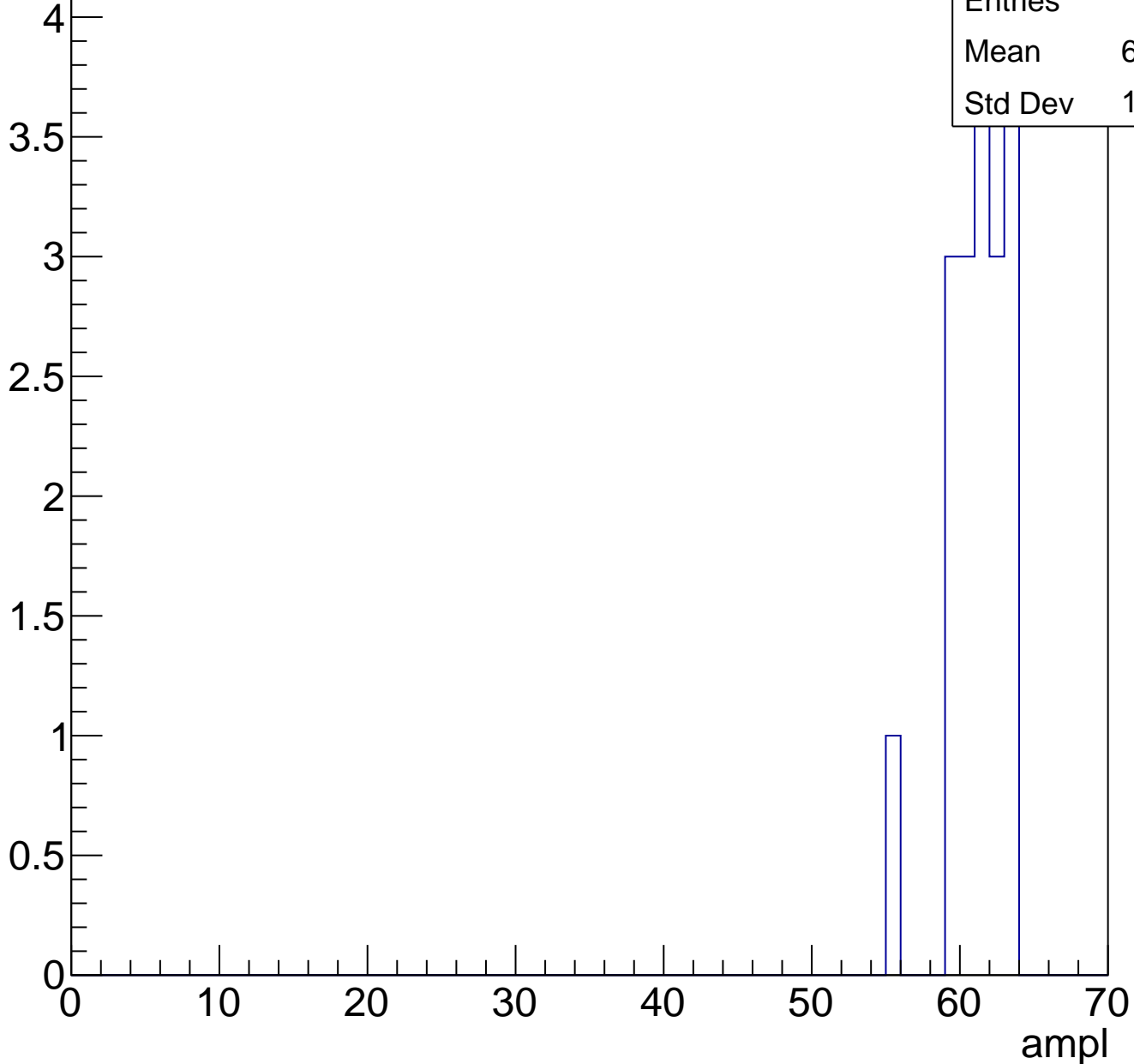
Entries	48
Mean	57.38
Std Dev	8.901



B0L102S, U5-ch114, adc6

calib_packv5_042523_0143.root, FC#12, port B1

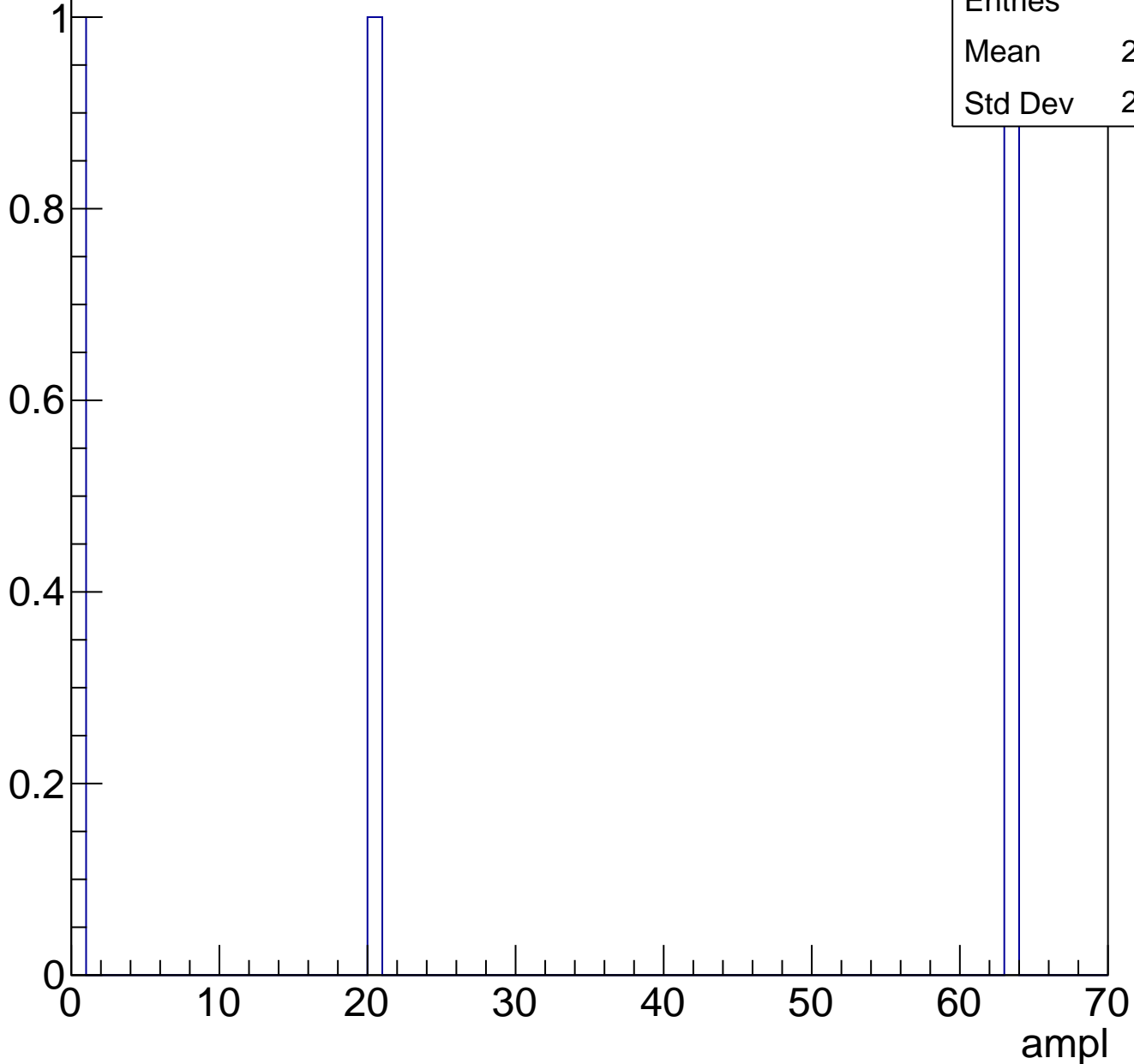
Entry



B0L102S, U5-ch114, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B0L102S, U5-ch115, adc0

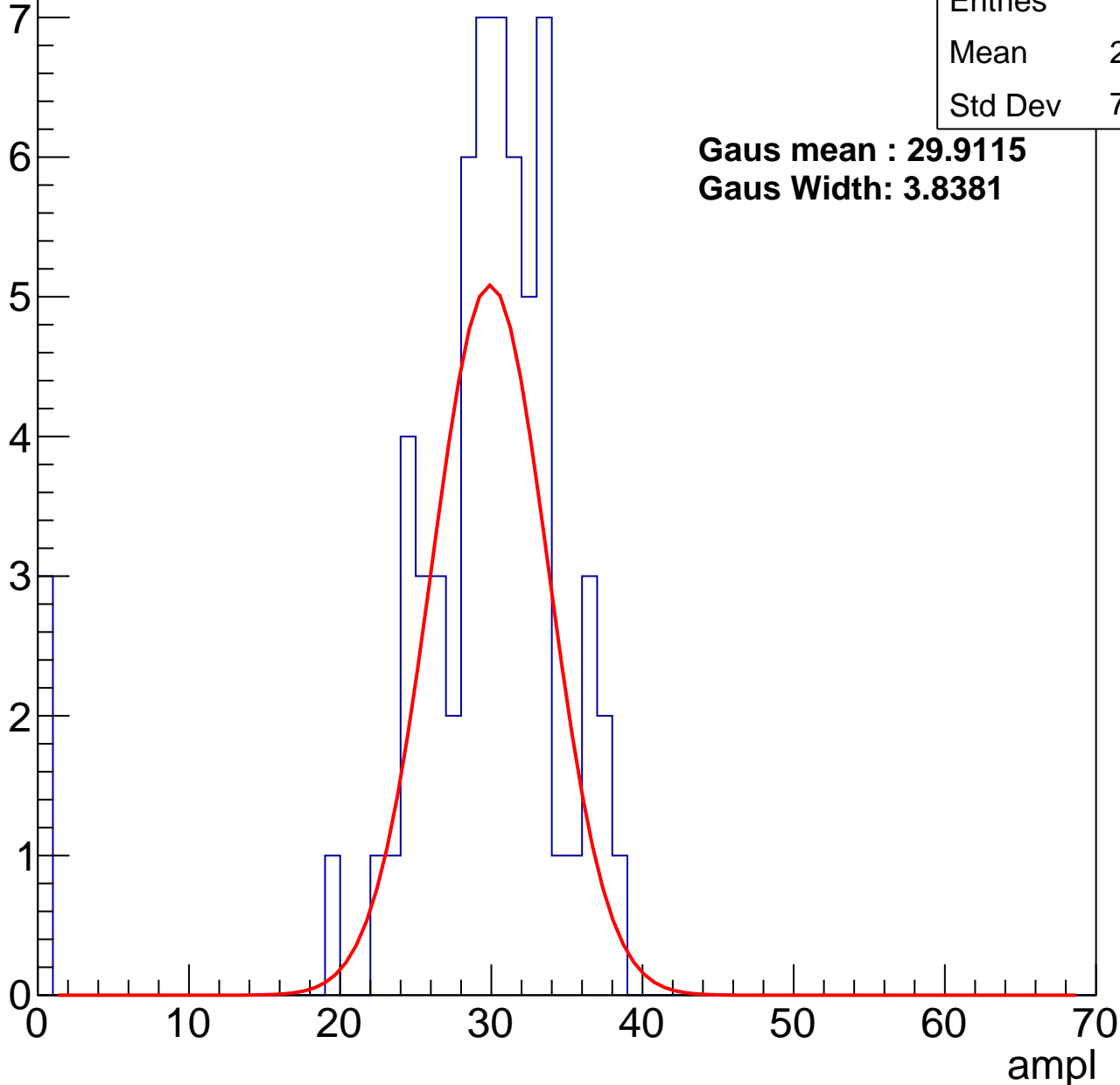
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	28.34
Std Dev	7.368

Gaus mean : 29.9115

Gaus Width: 3.8381



B0L102S, U5-ch115, adc1

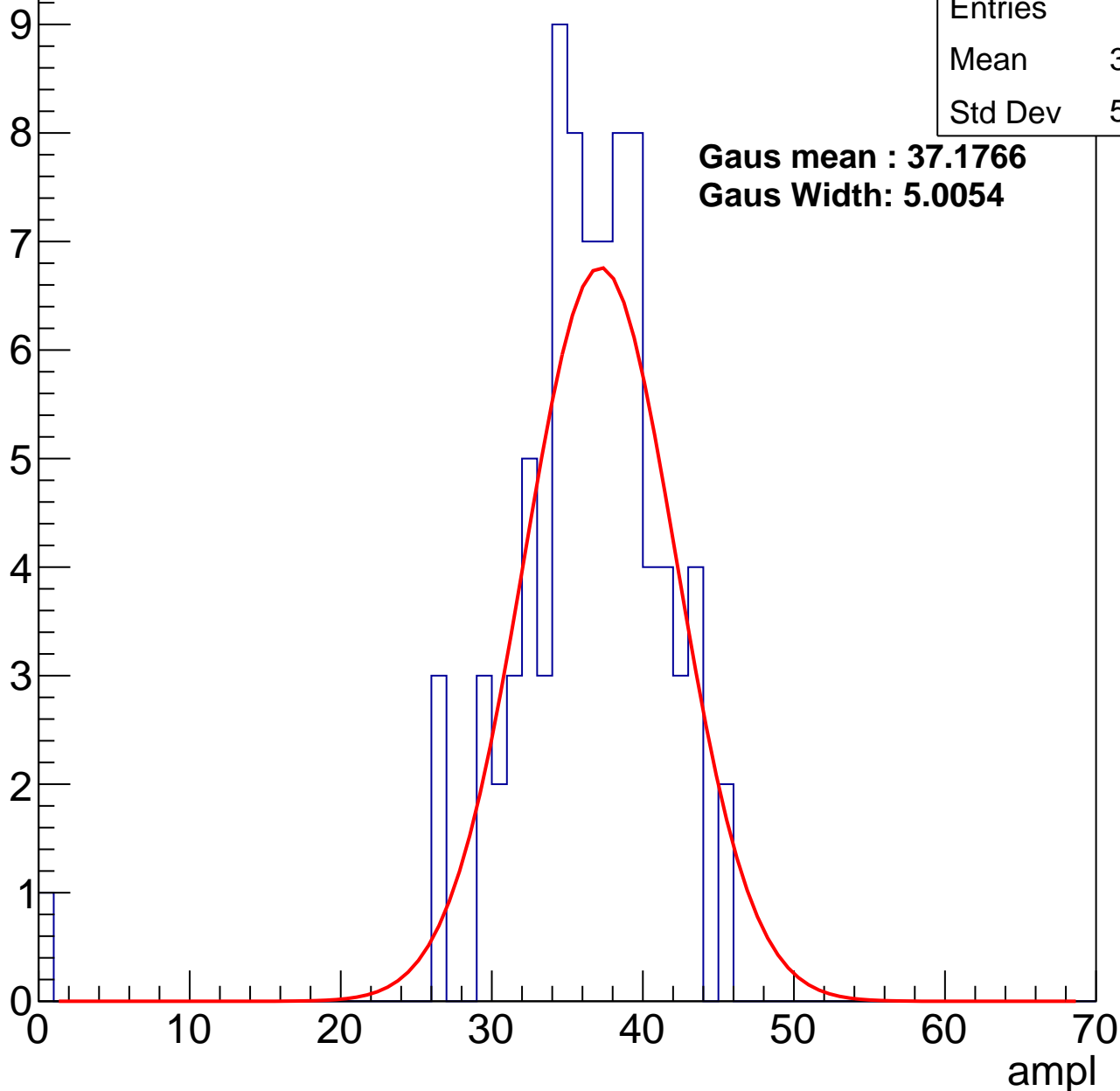
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	84
Mean	35.74
Std Dev	5.749

Gaus mean : 37.1766

Gaus Width: 5.0054



B0L102S, U5-ch115, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	70
Mean	44.1
Std Dev	3.398

Gaus mean : 44.6449

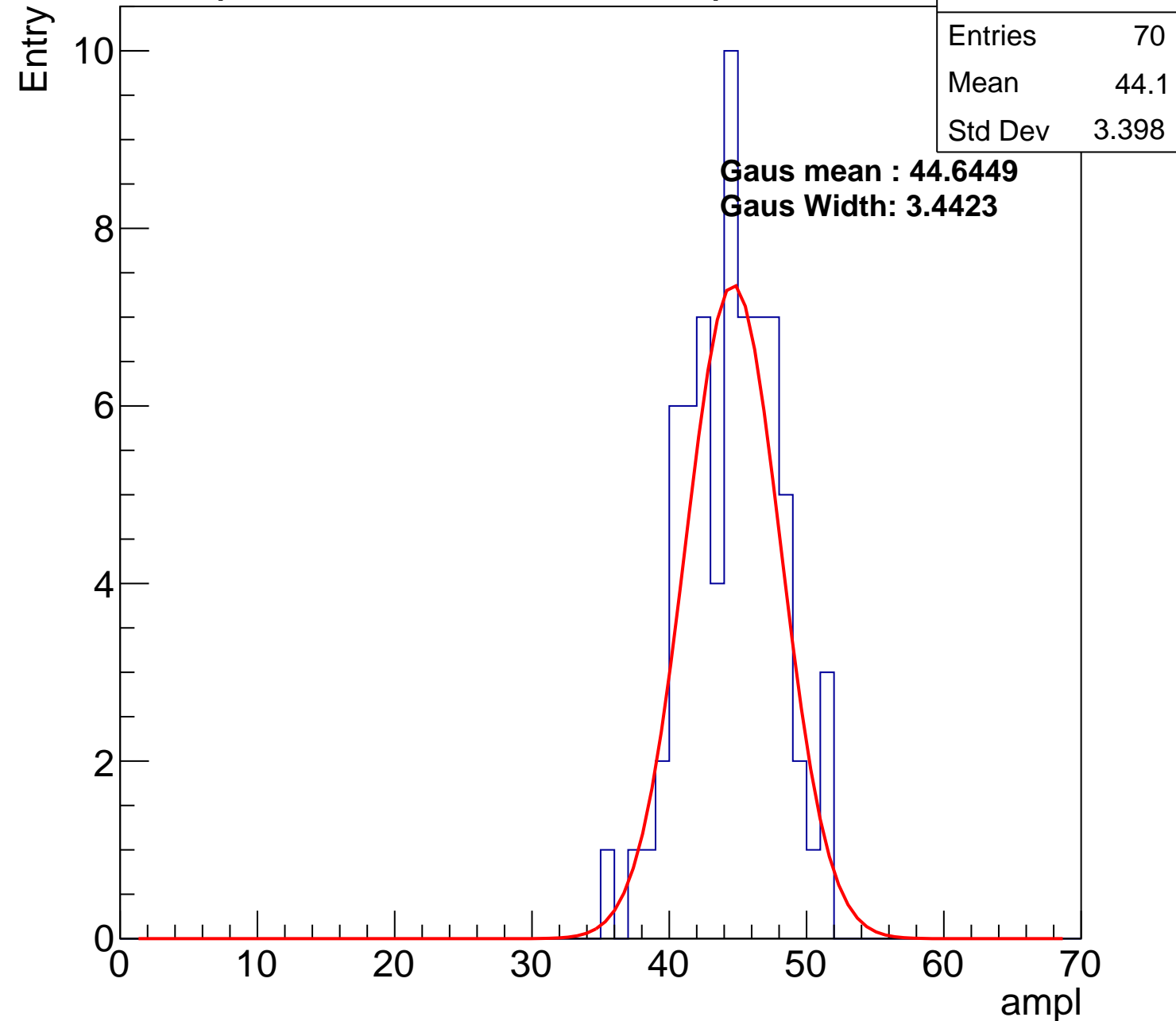
Gaus Width: 3.4423

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

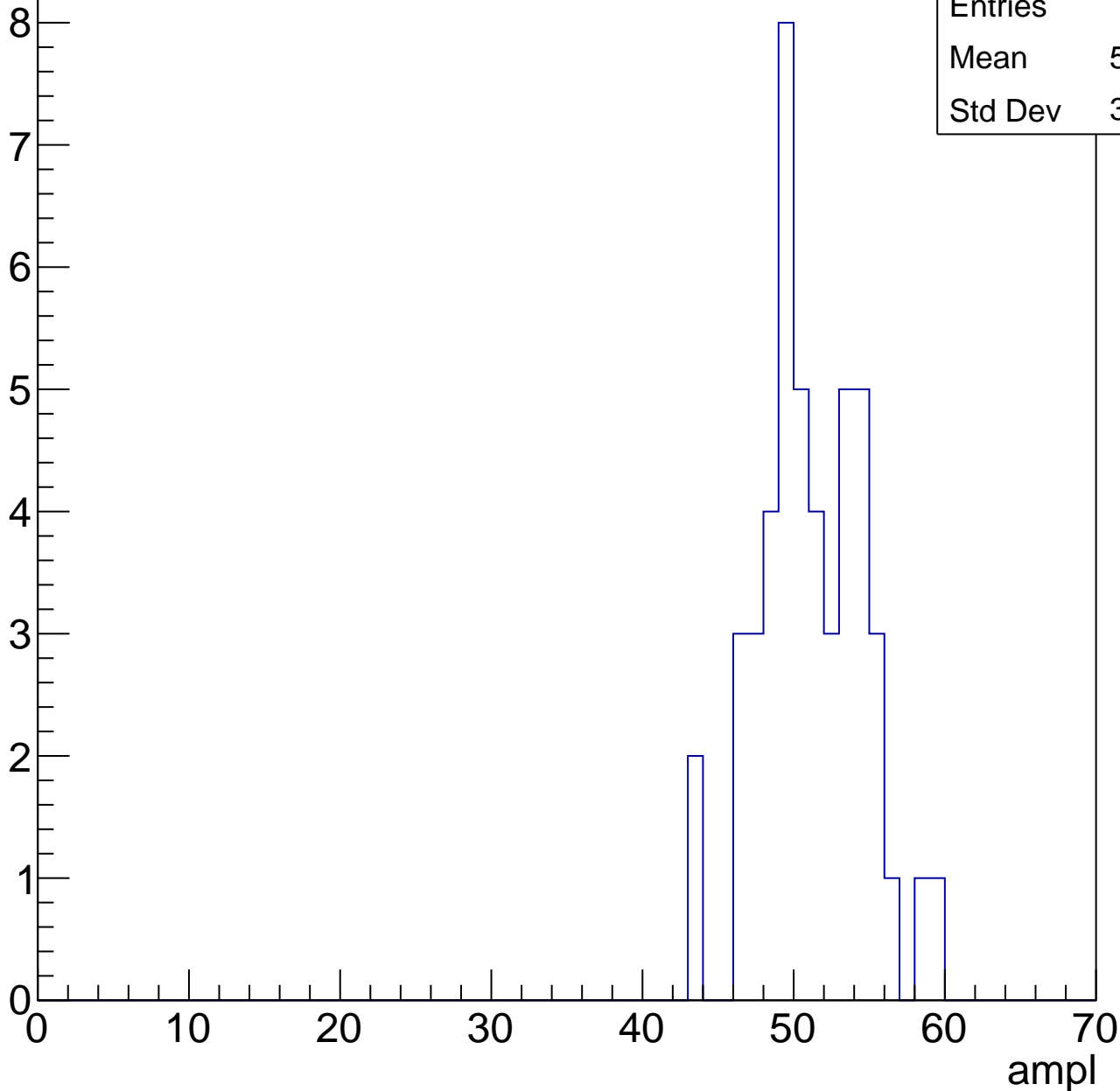


B0L102S, U5-ch115, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	48
Mean	50.67
Std Dev	3.454

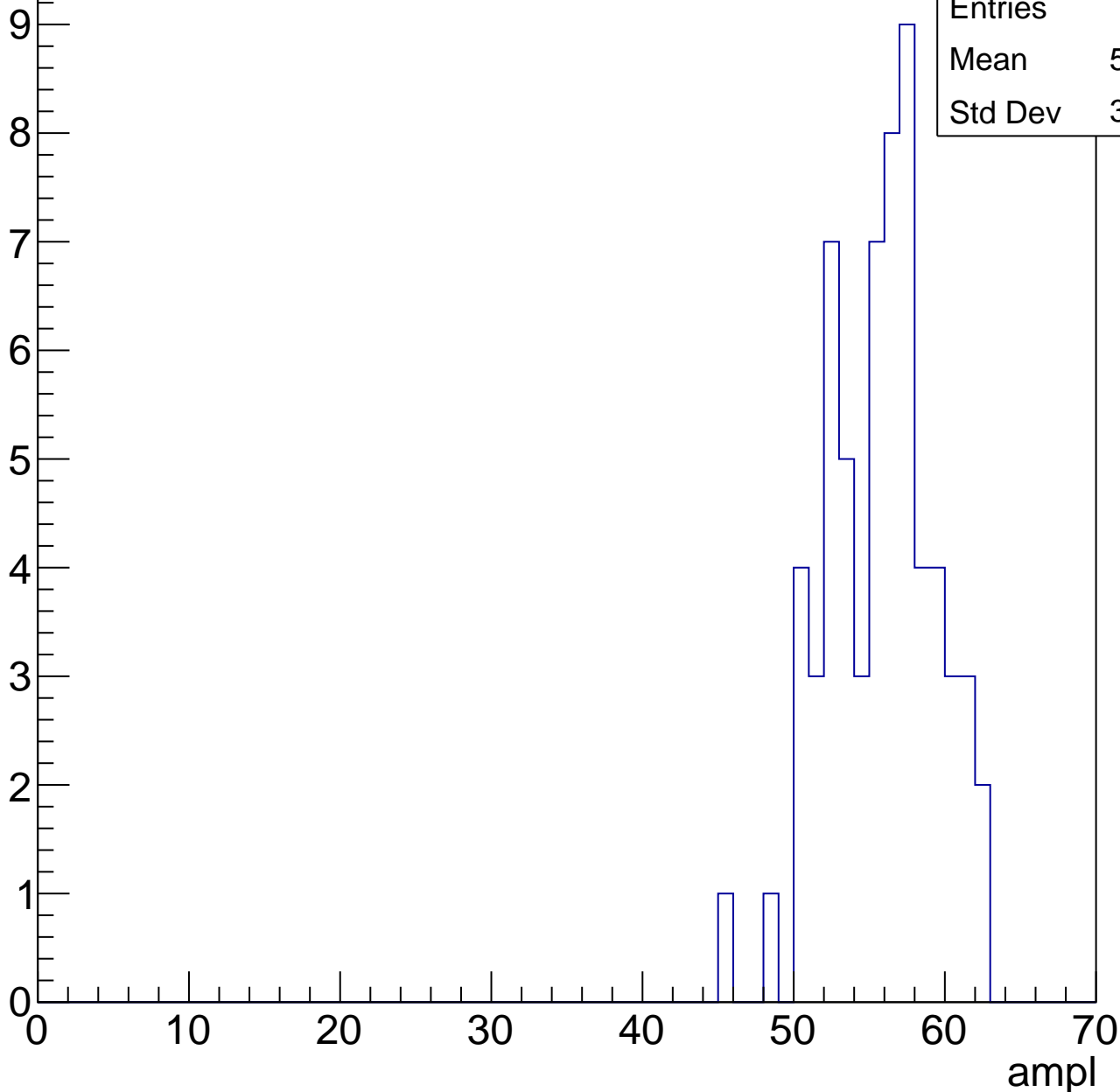


B0L102S, U5-ch115, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	55.28
Std Dev	3.542

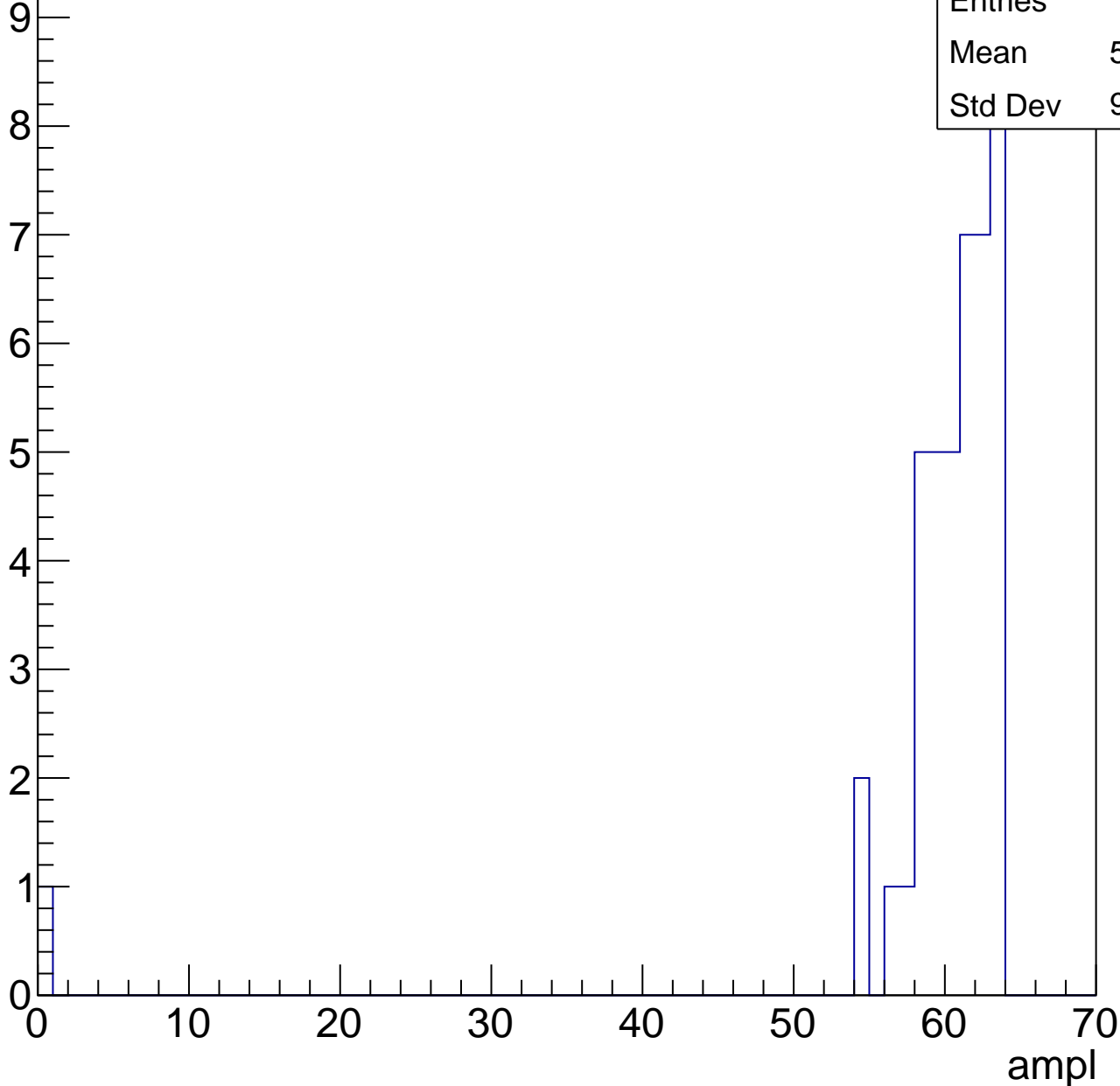


B0L102S, U5-ch115, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	58.93
Std Dev	9.387



B0L102S, U5-ch115, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.299

ampl

0 10 20 30 40 50 60 70

B0L102S, U5-ch115, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch116, adc0

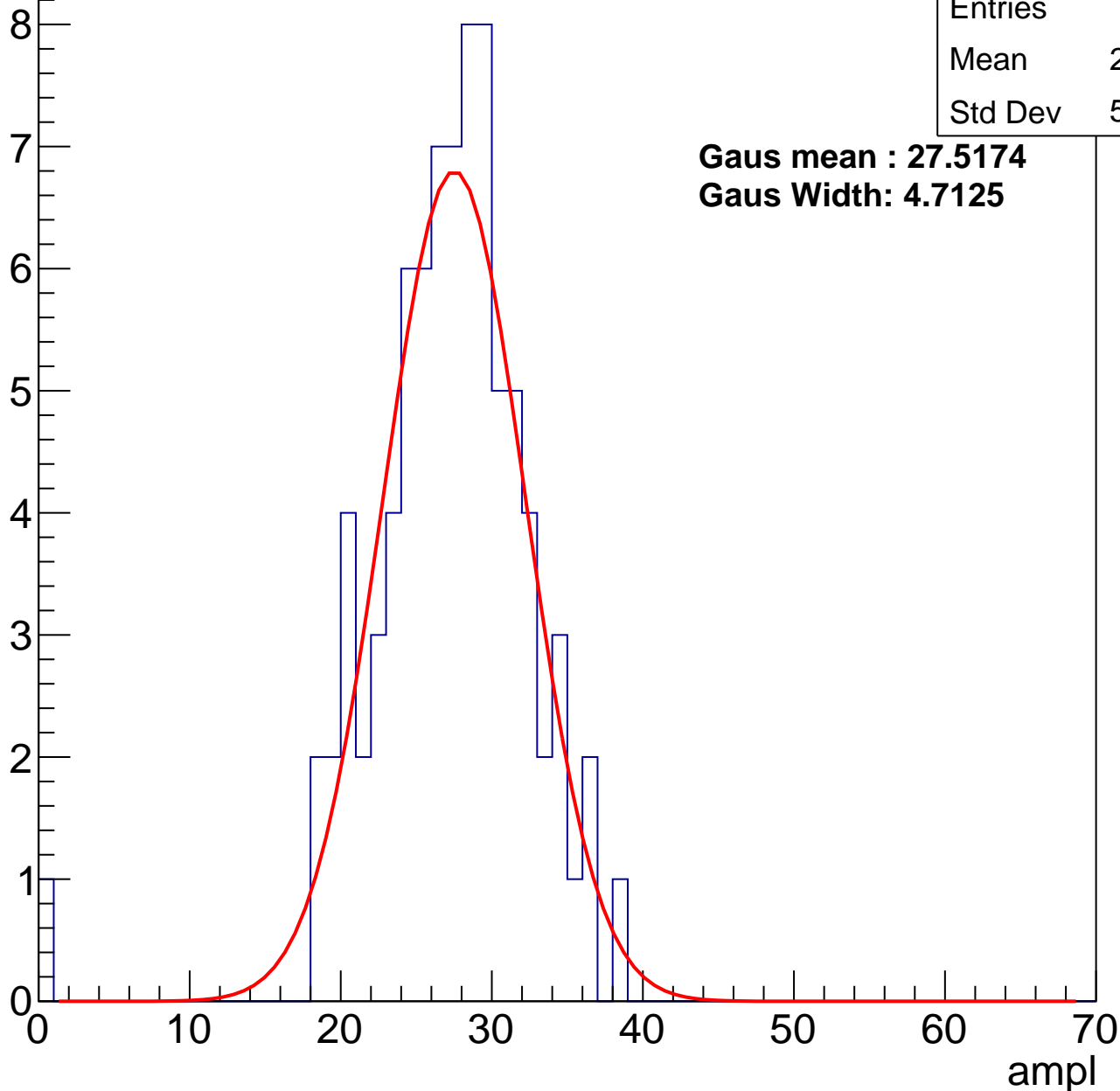
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	83
Mean	26.76
Std Dev	5.306

Gaus mean : 27.5174

Gaus Width: 4.7125



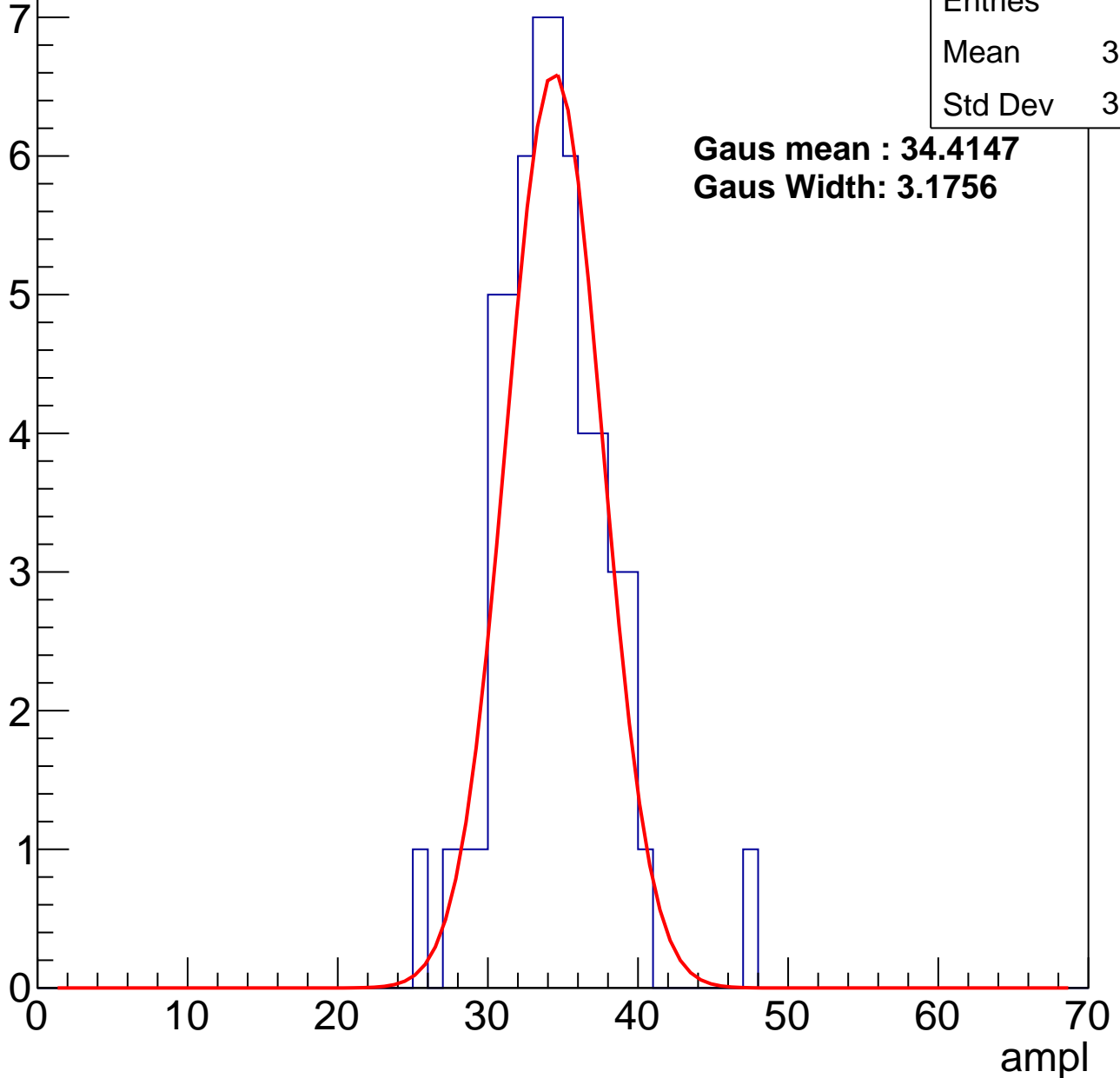
B0L102S, U5-ch116, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	33.84
Std Dev	3.614

Gaus mean : 34.4147
Gaus Width: 3.1756



B0L102S, U5-ch116, adc2

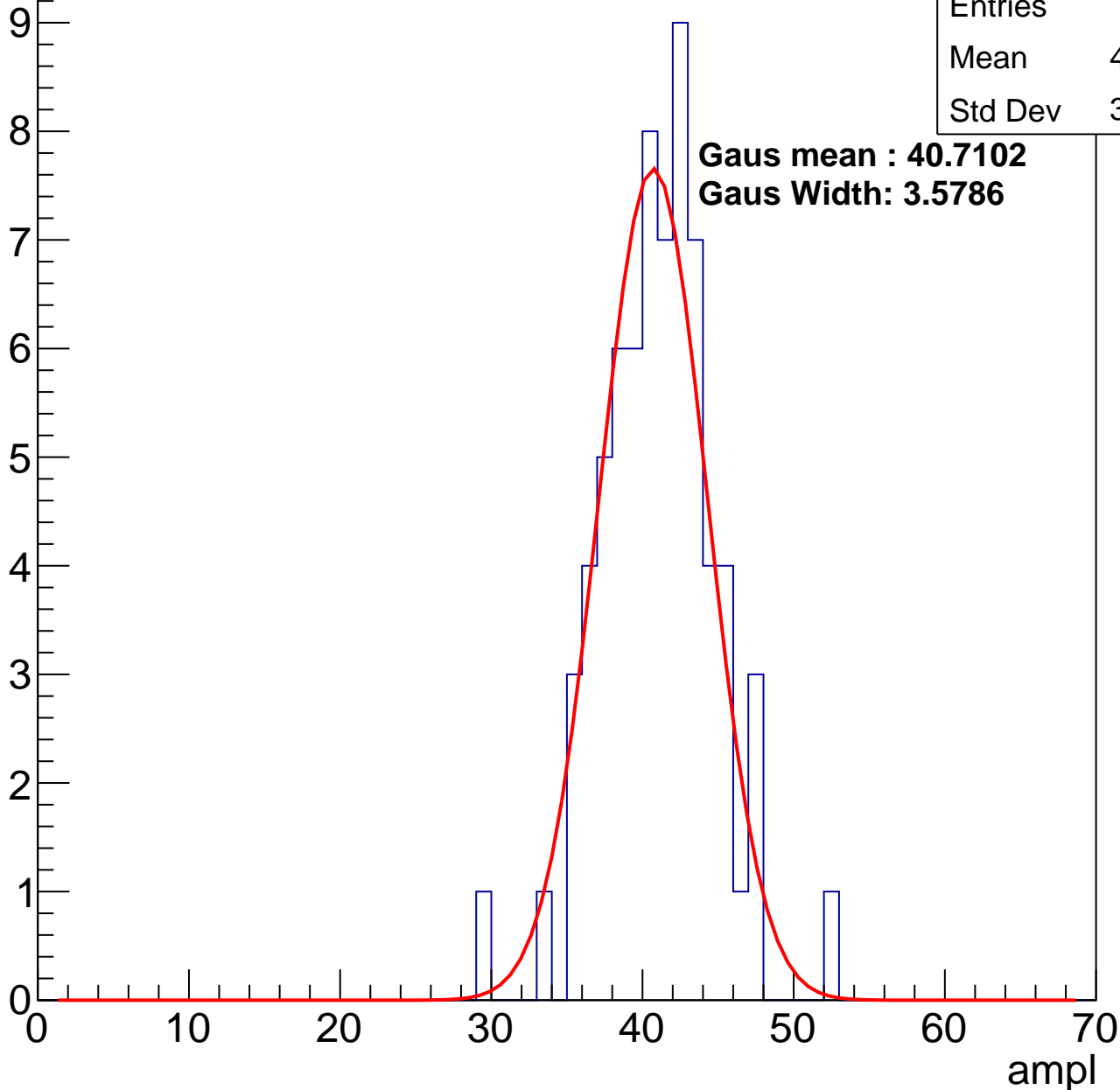
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	40.56
Std Dev	3.702

Gaus mean : 40.7102

Gaus Width: 3.5786

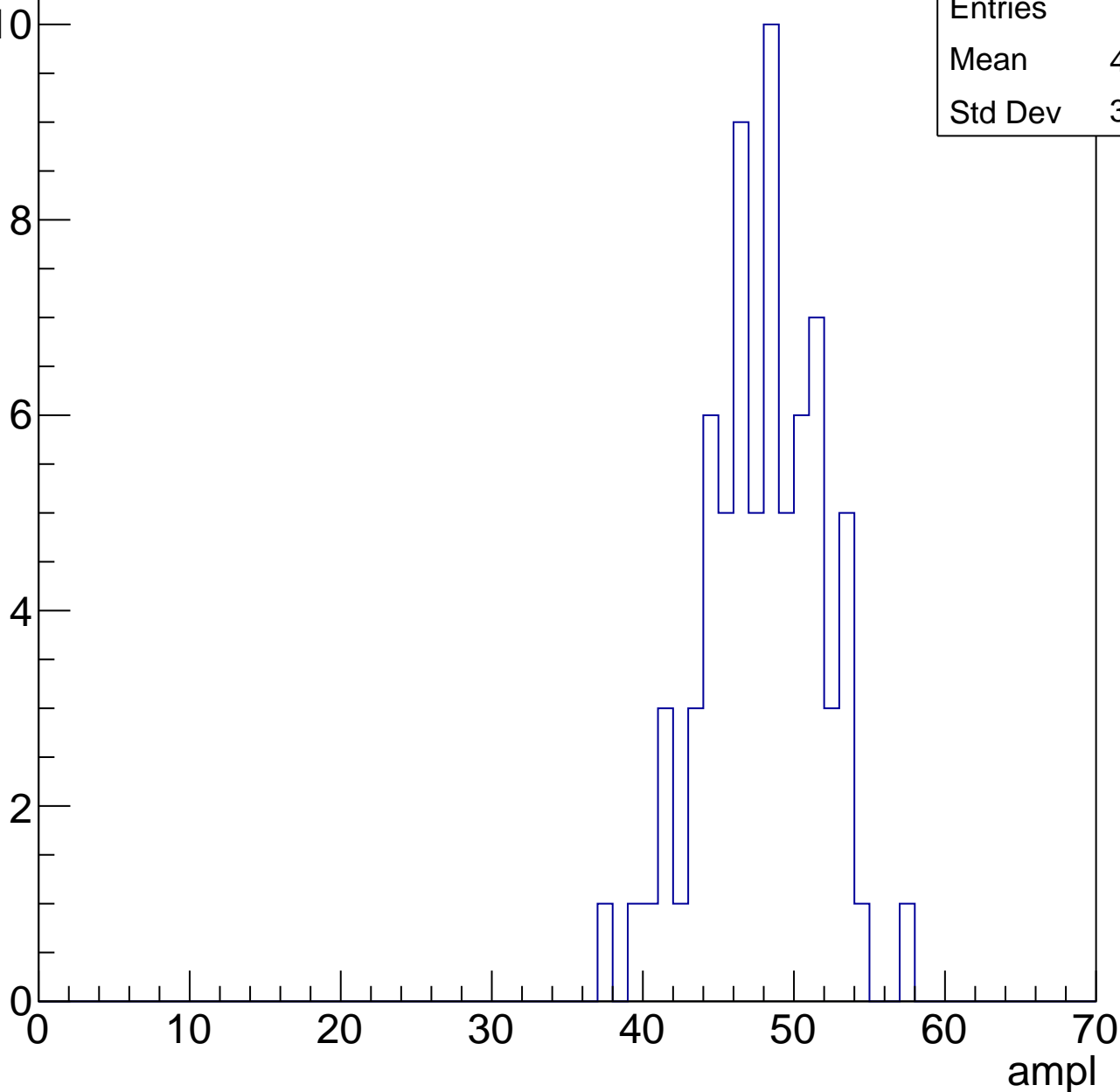


B0L102S, U5-ch116, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	47.42
Std Dev	3.817

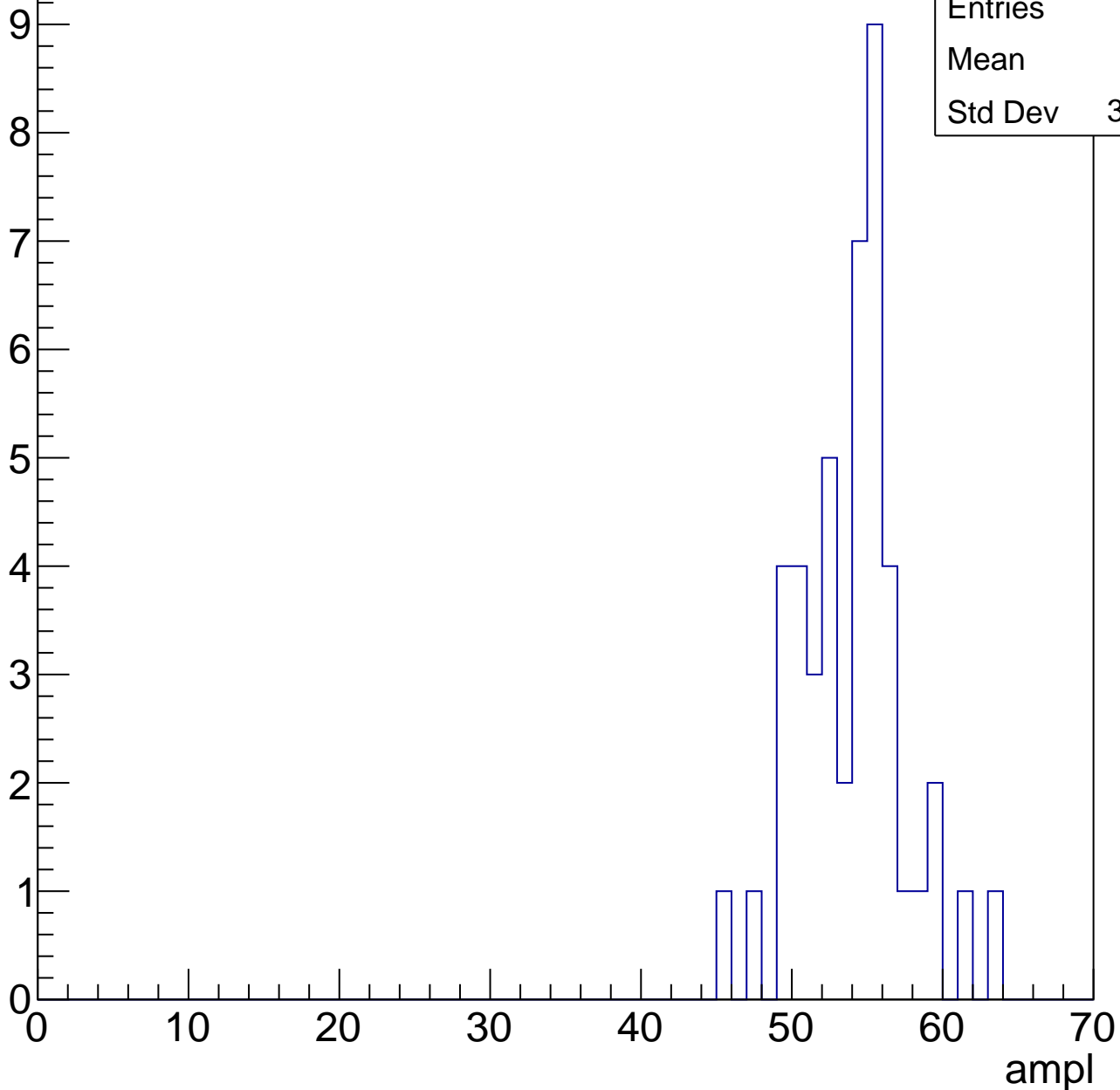


B0L102S, U5-ch116, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	53.5
Std Dev	3.494

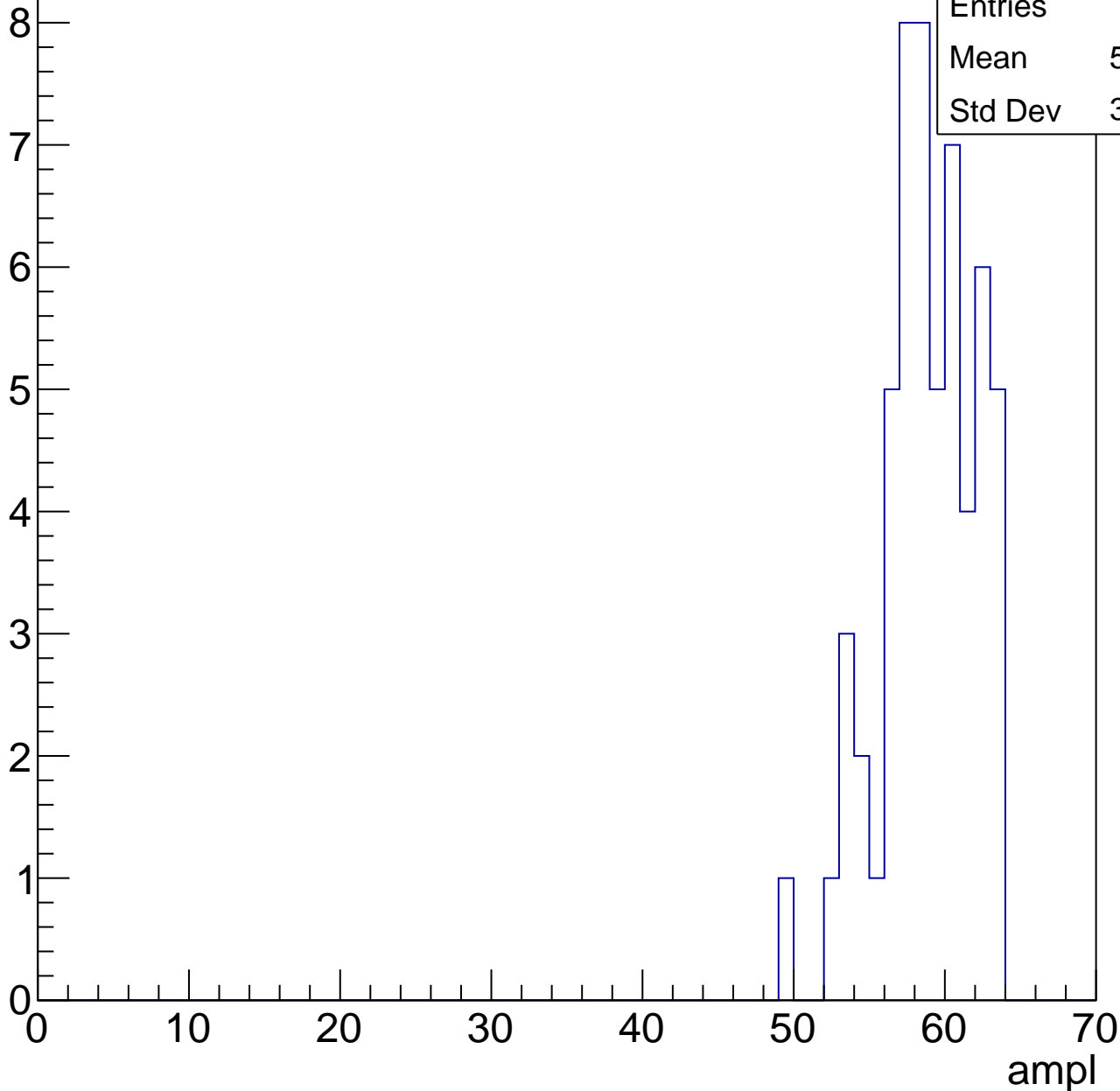


B0L102S, U5-ch116, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	56
Mean	58.38
Std Dev	3.114

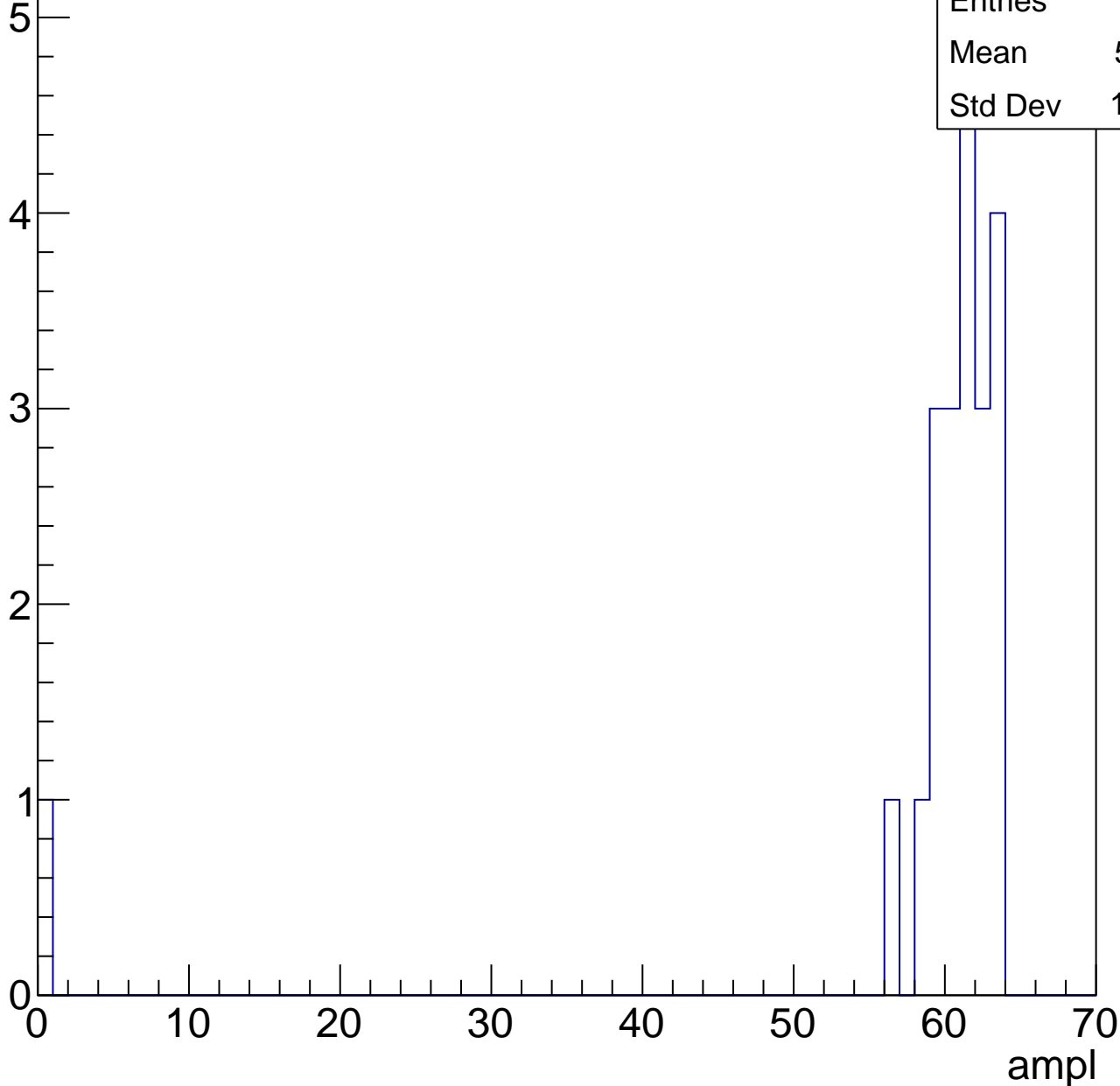


B0L102S, U5-ch116, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

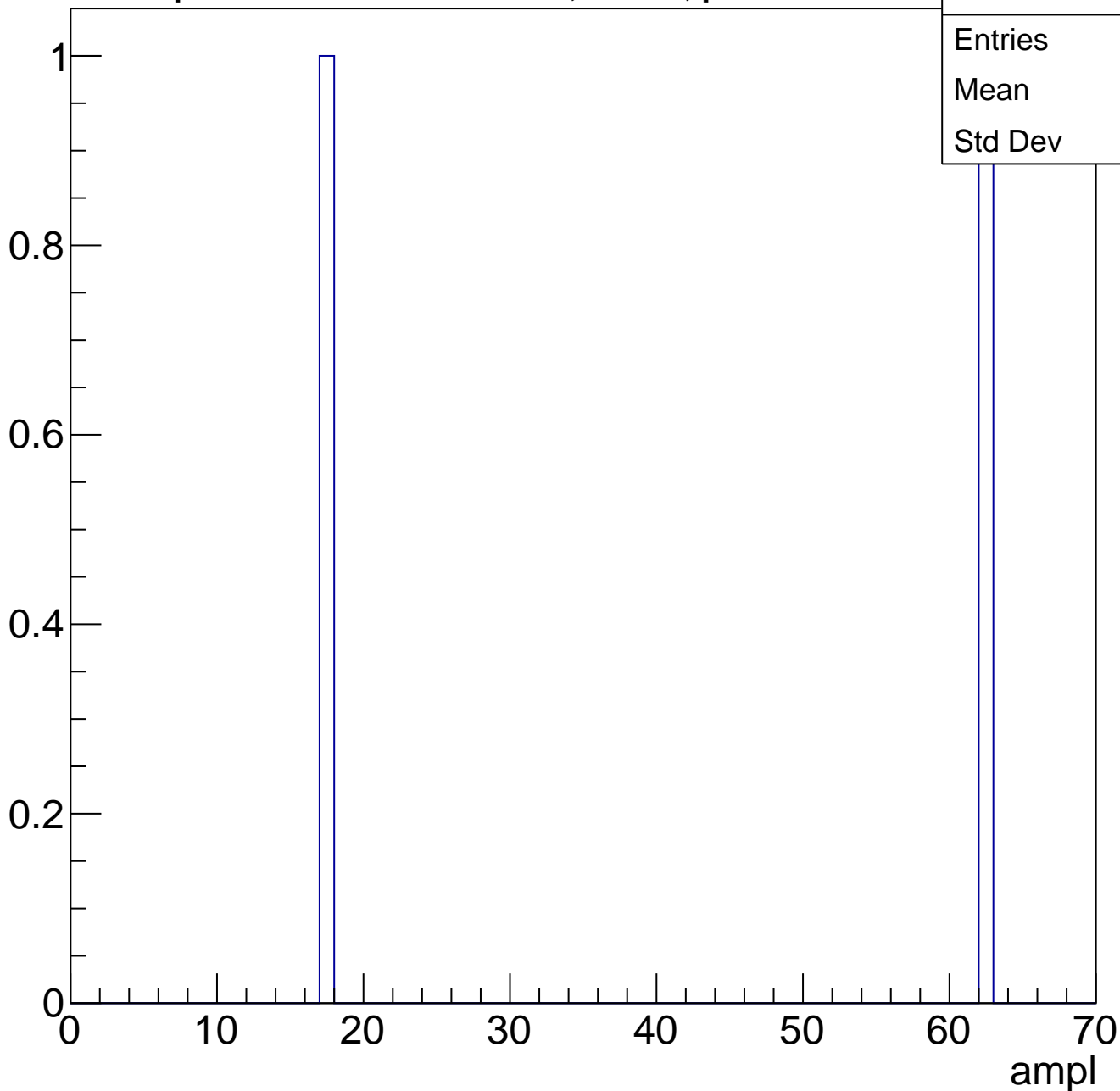
Entries	21
Mean	57.81
Std Dev	13.05



B0L102S, U5-ch116, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch117, adc0

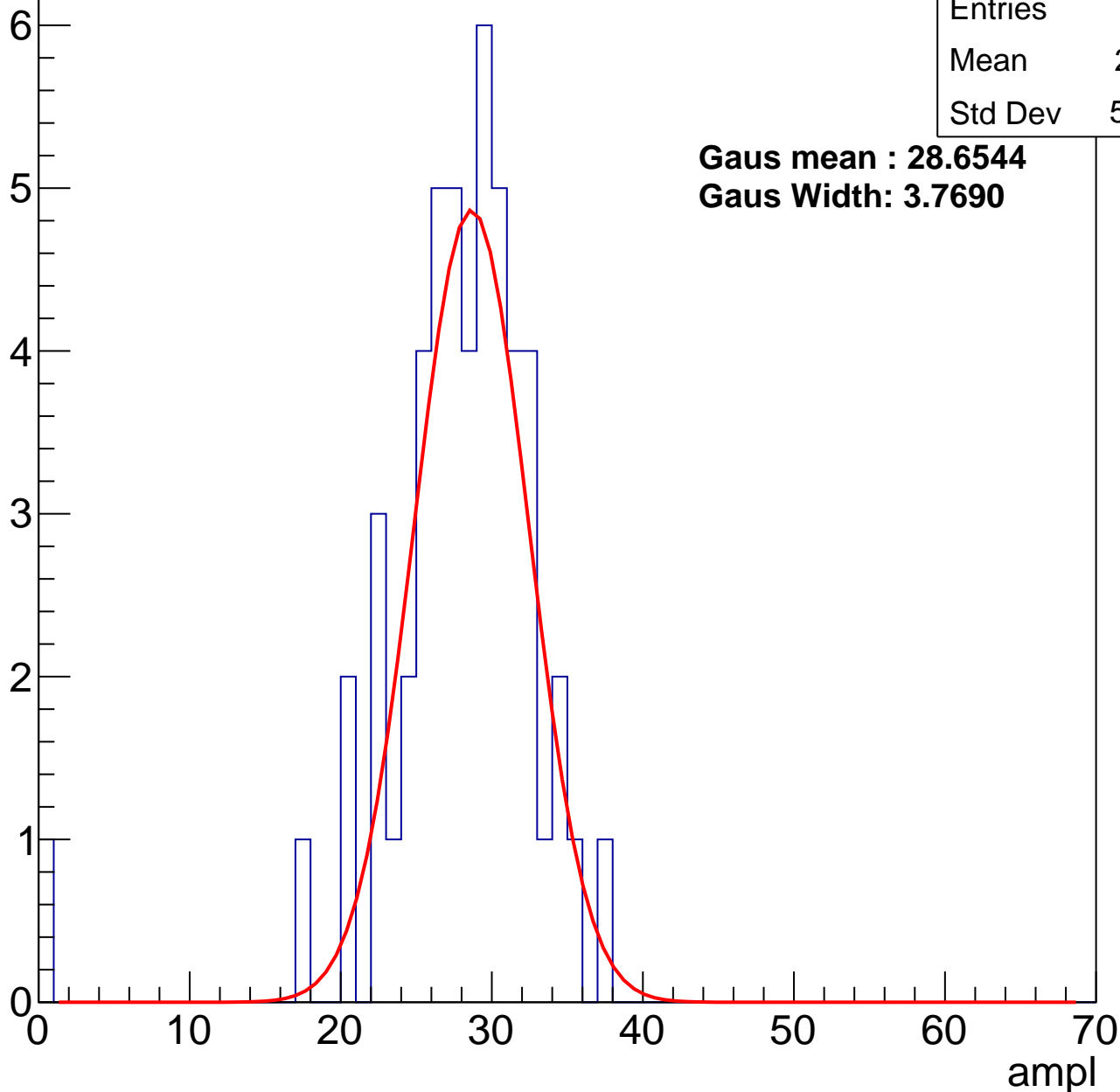
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	27.31
Std Dev	5.525

Gaus mean : 28.6544

Gaus Width: 3.7690



B0L102S, U5-ch117, adc1

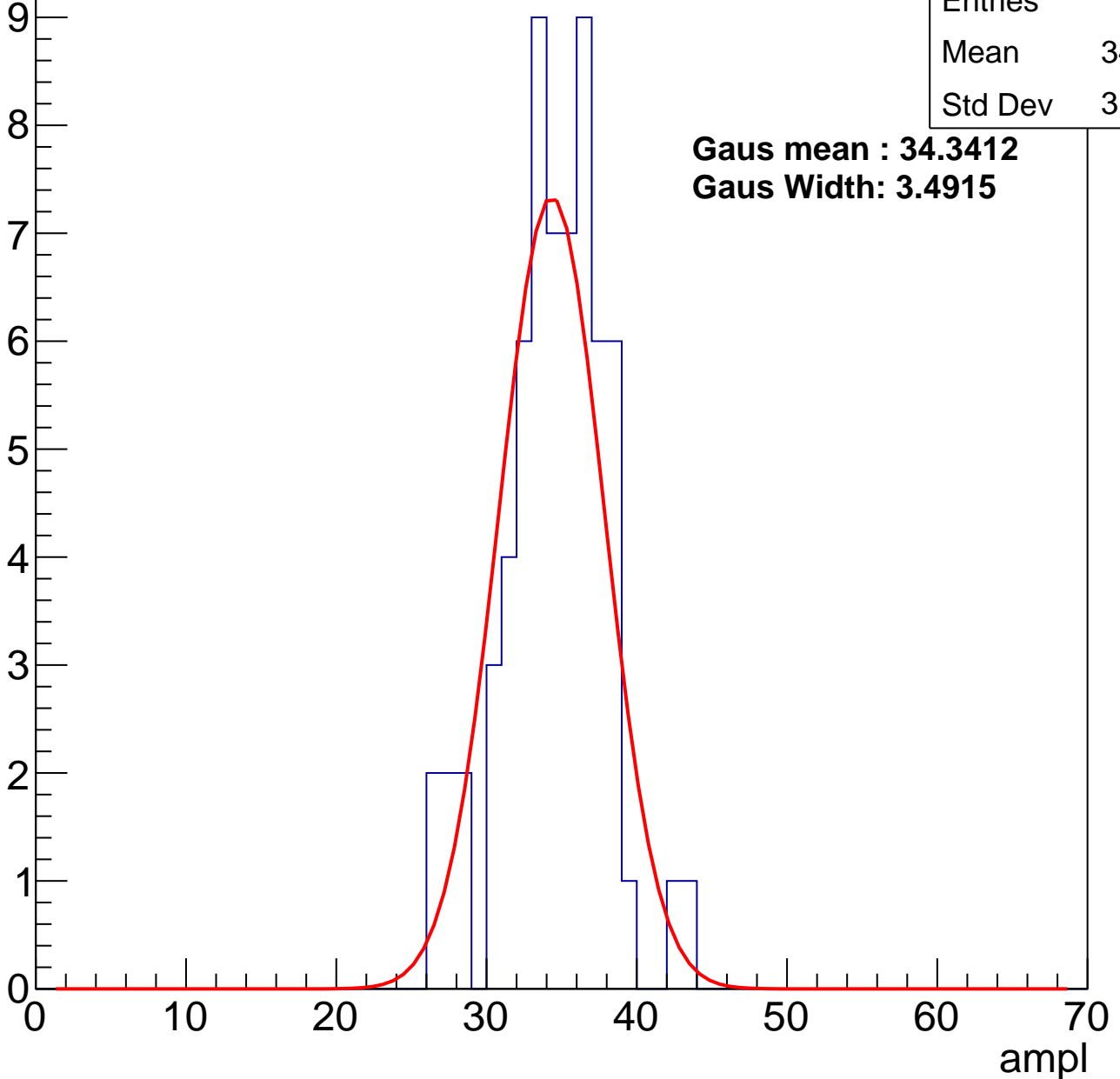
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	34.03
Std Dev	3.433

Gaus mean : 34.3412

Gaus Width: 3.4915



B0L102S, U5-ch117, adc2

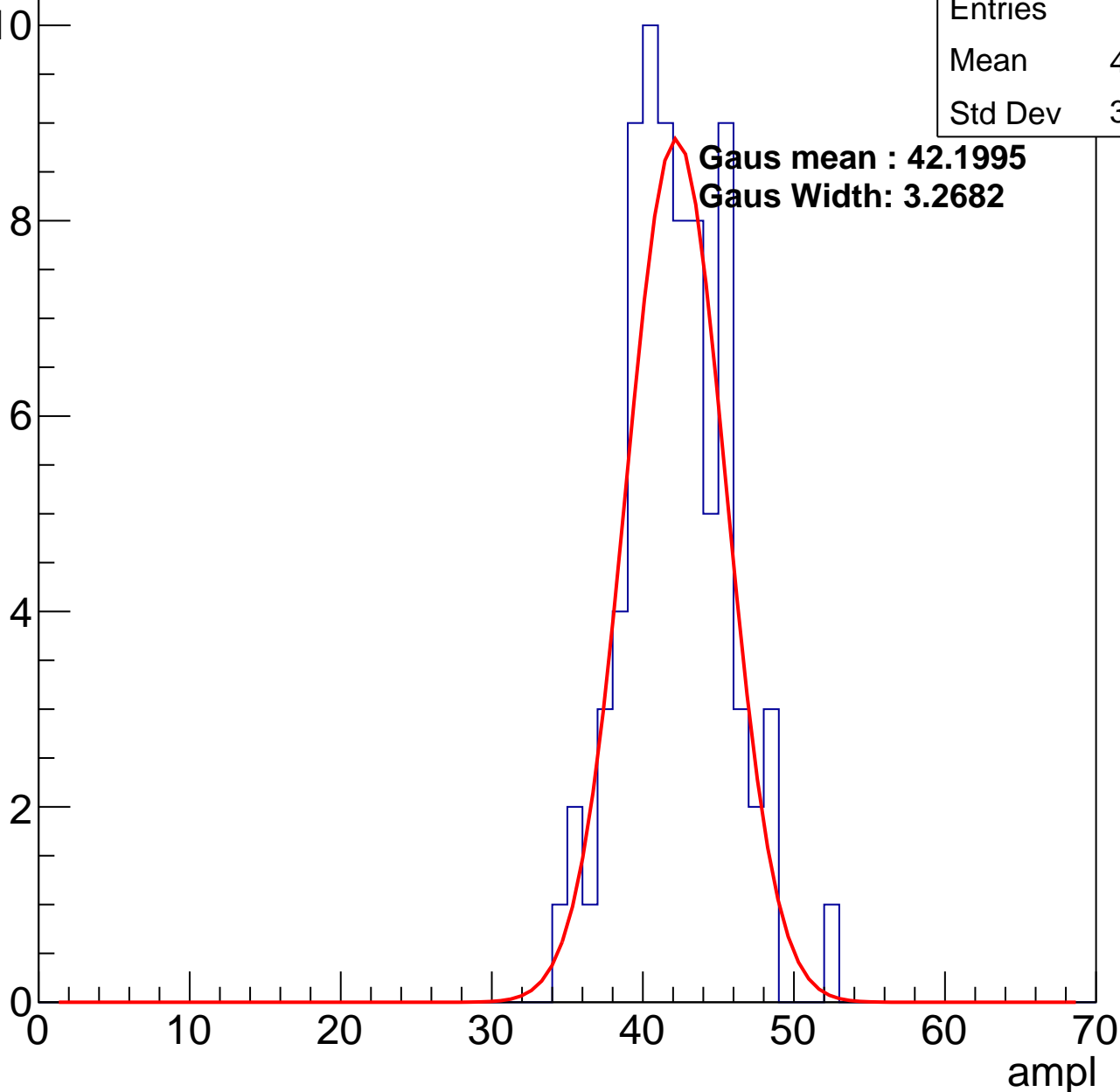
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	78
Mean	41.74
Std Dev	3.353

Gaus mean : 42.1995

Gaus Width: 3.2682

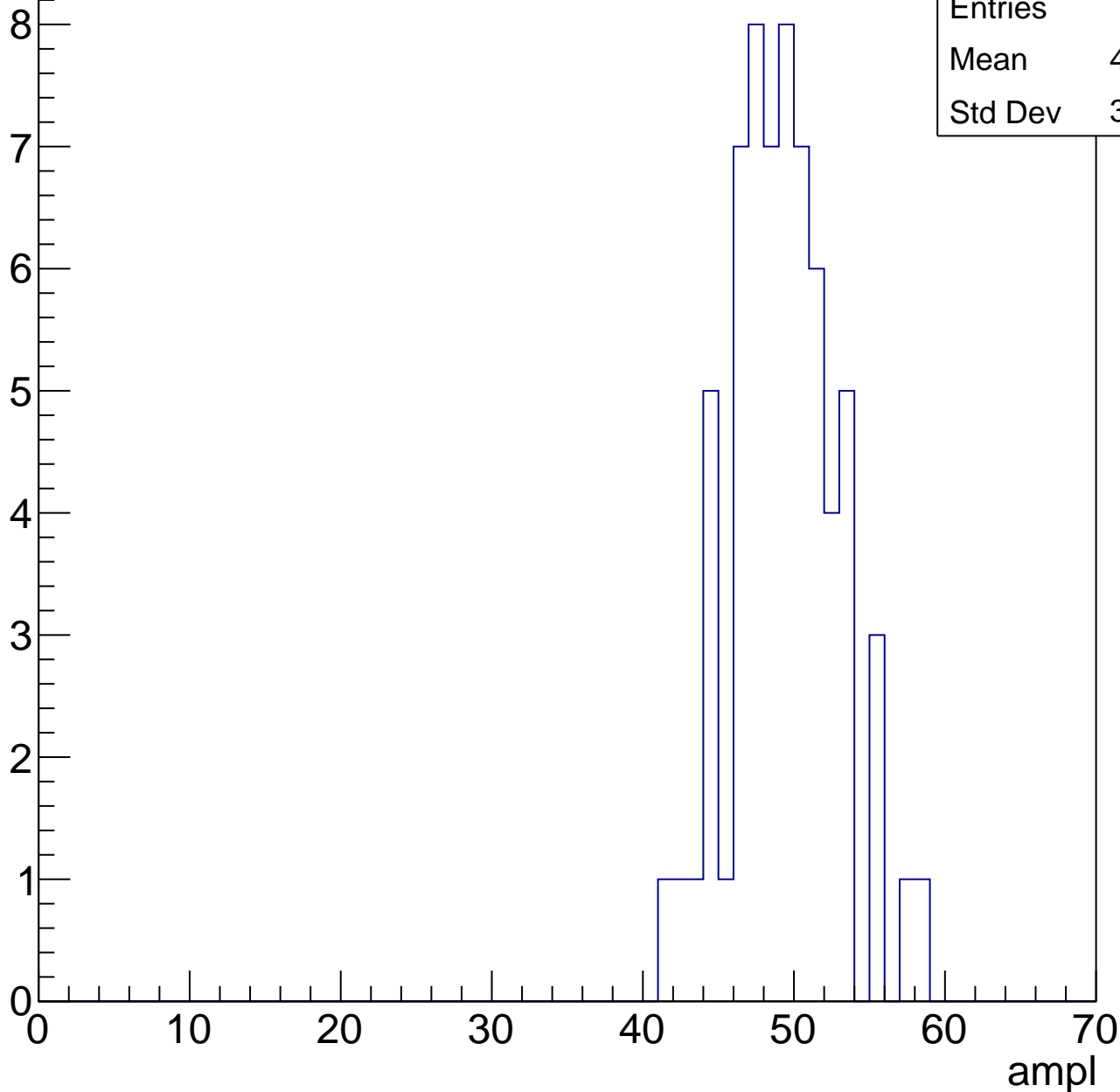


B0L102S, U5-ch117, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	48.88
Std Dev	3.462

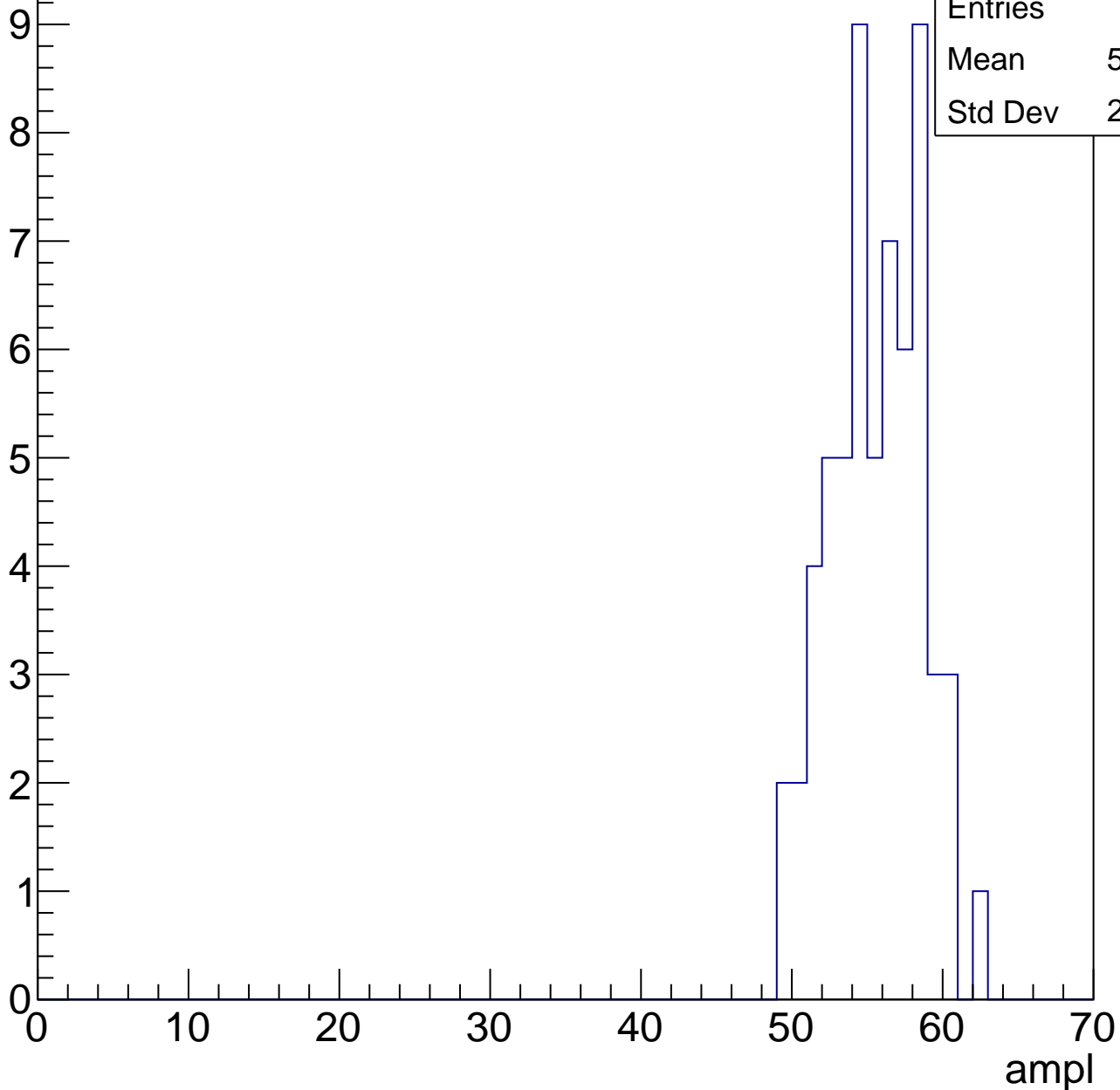


B0L102S, U5-ch117, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	61
Mean	55.13
Std Dev	2.983

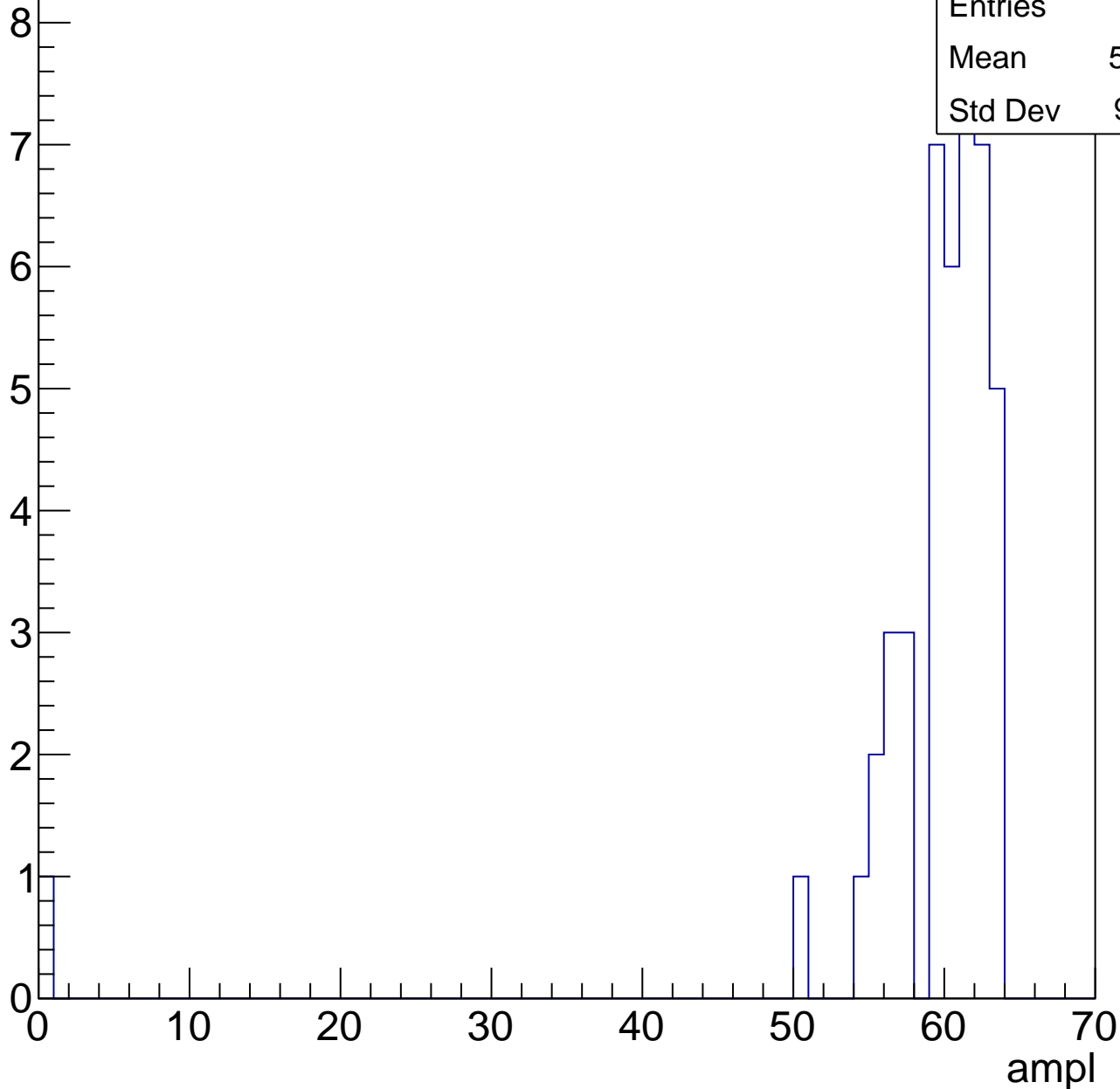


B0L102S, U5-ch117, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	44
Mean	58.25
Std Dev	9.311

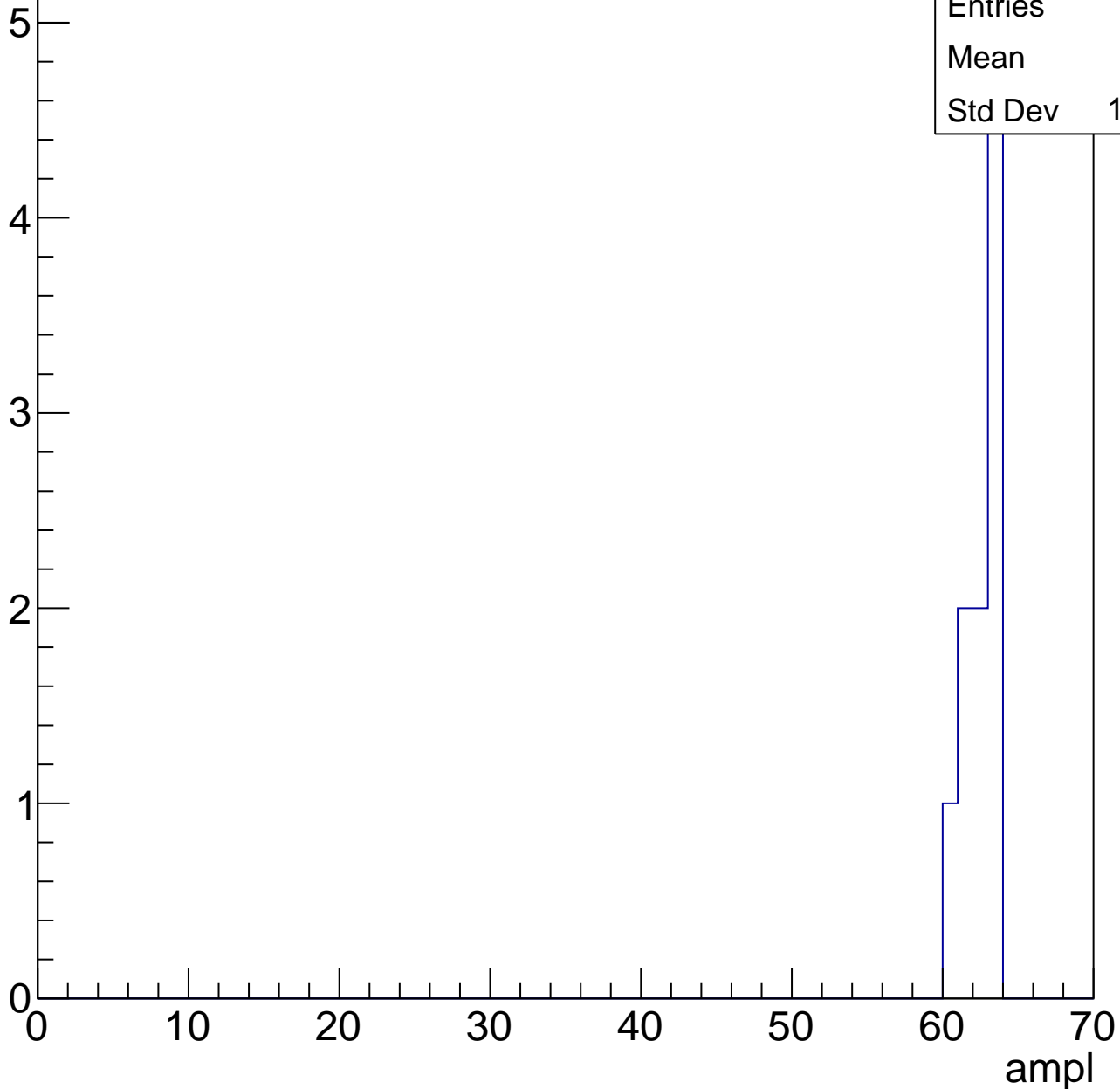


B0L102S, U5-ch117, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	10
Mean	62.1
Std Dev	1.044



B0L102S, U5-ch117, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch118, adc0

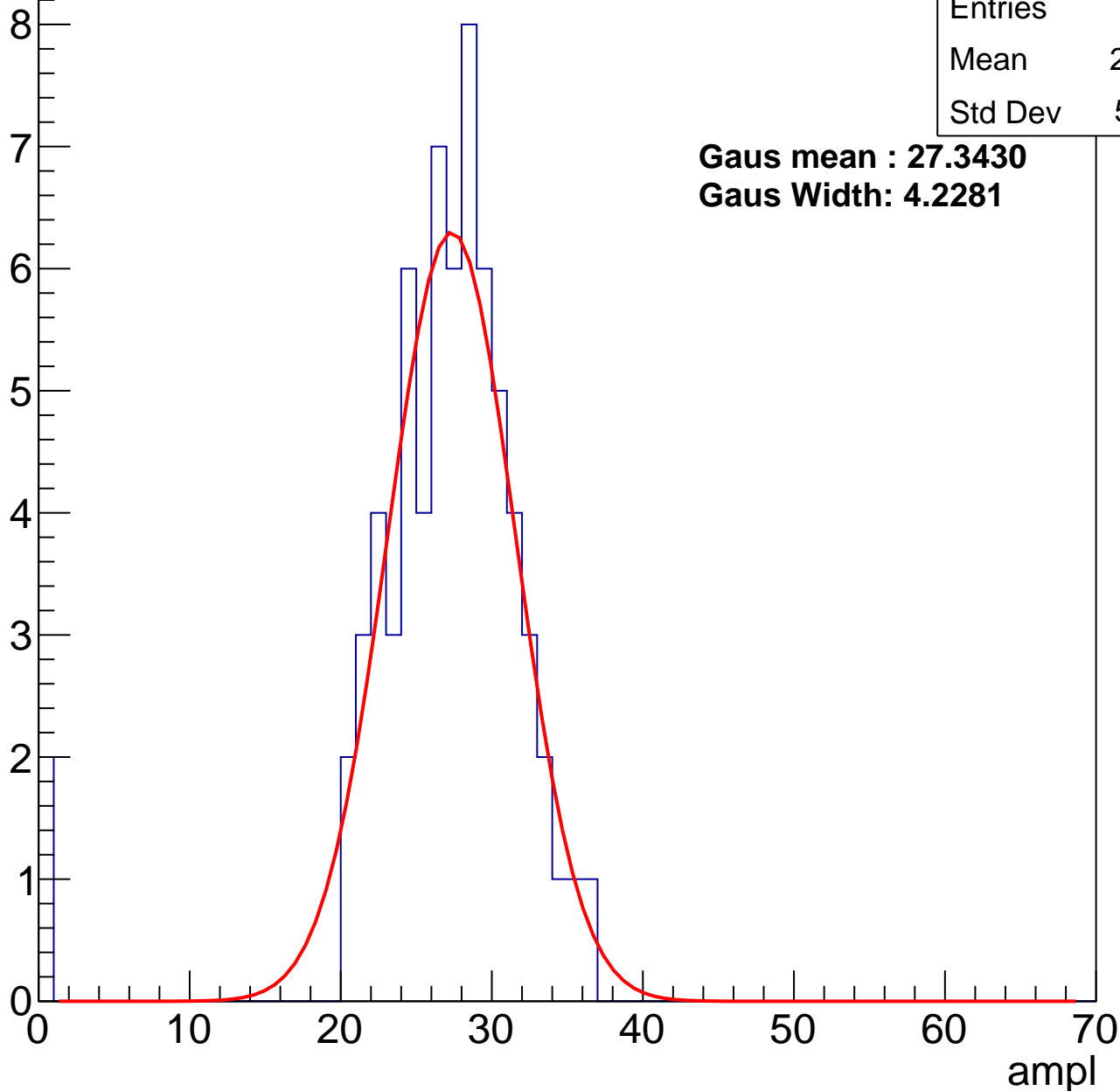
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	26.28
Std Dev	5.861

Gaus mean : 27.3430

Gaus Width: 4.2281



B0L102S, U5-ch118, adc1

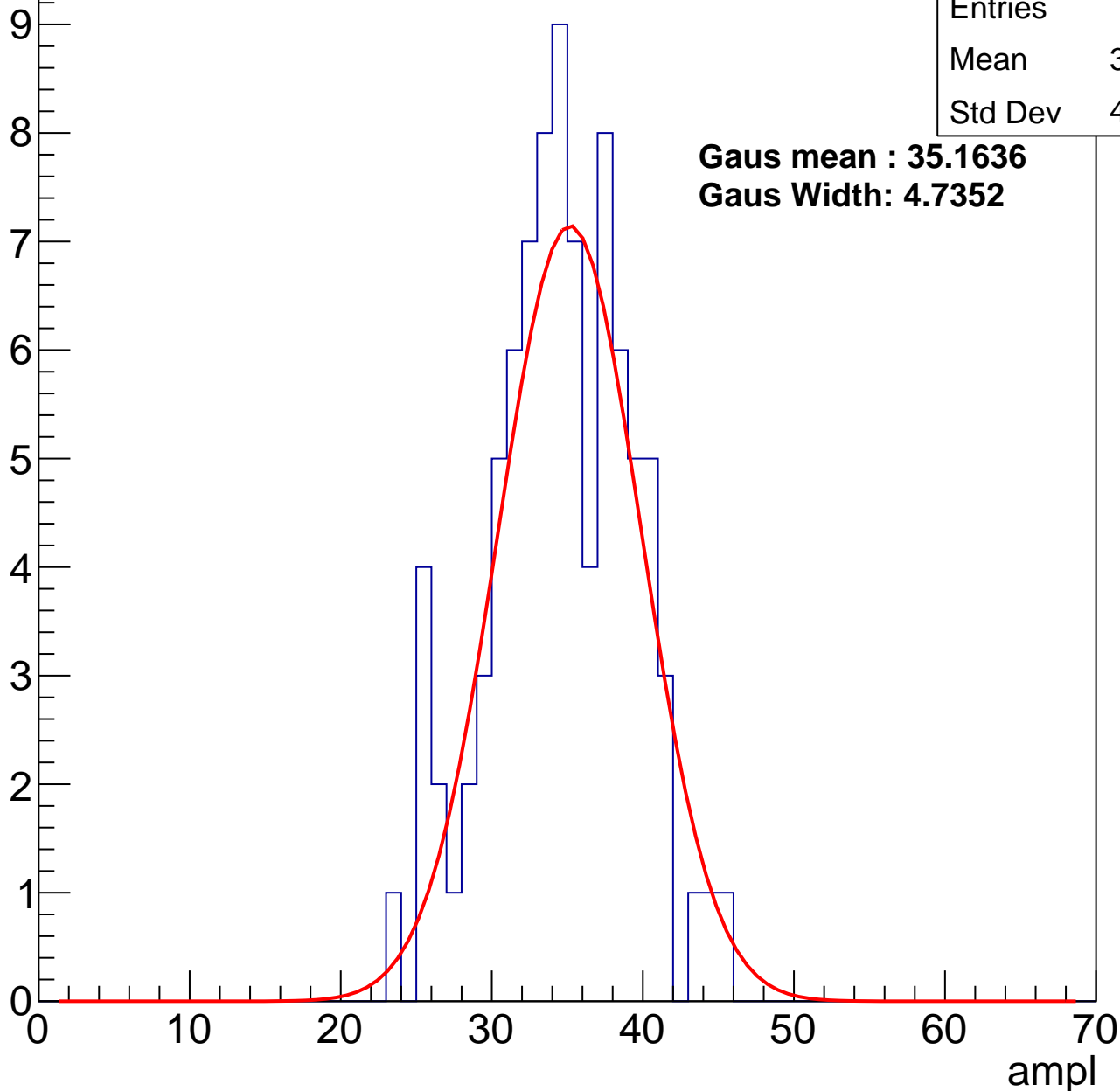
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	89
Mean	34.13
Std Dev	4.599

Gaus mean : 35.1636

Gaus Width: 4.7352



B0L102S, U5-ch118, adc2

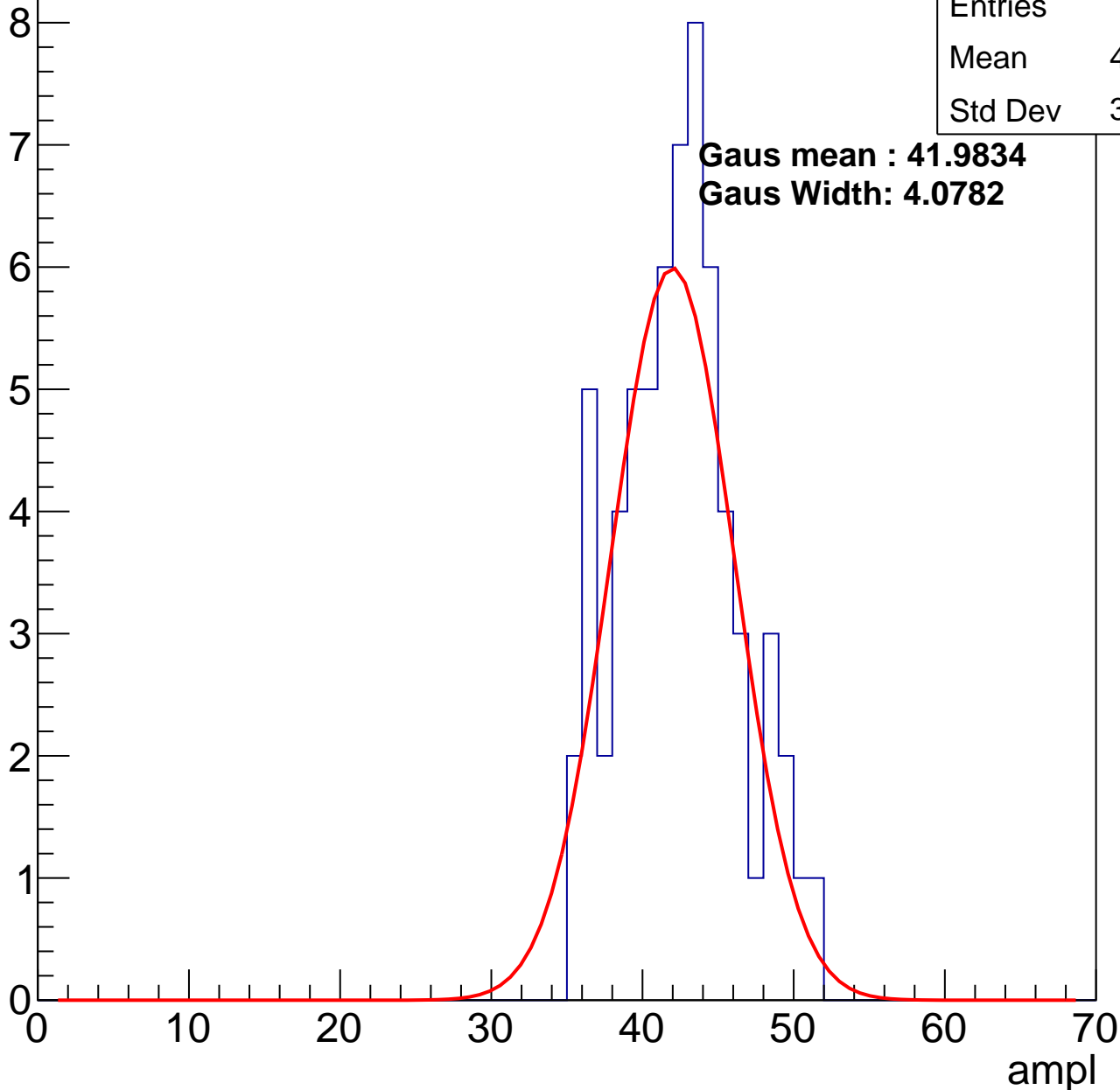
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	41.95
Std Dev	3.833

Gaus mean : 41.9834

Gaus Width: 4.0782

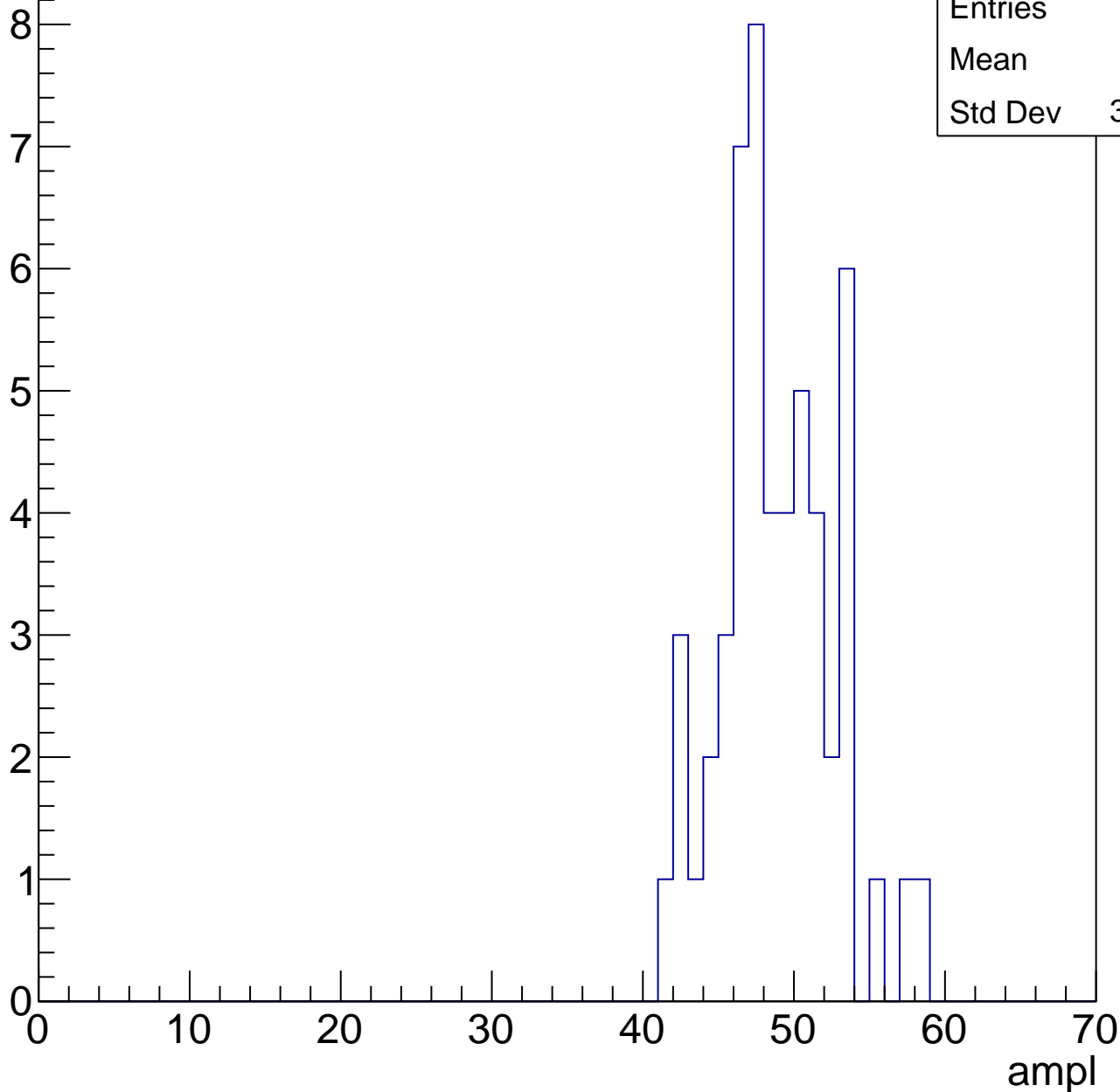


B0L102S, U5-ch118, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	53
Mean	48.4
Std Dev	3.738

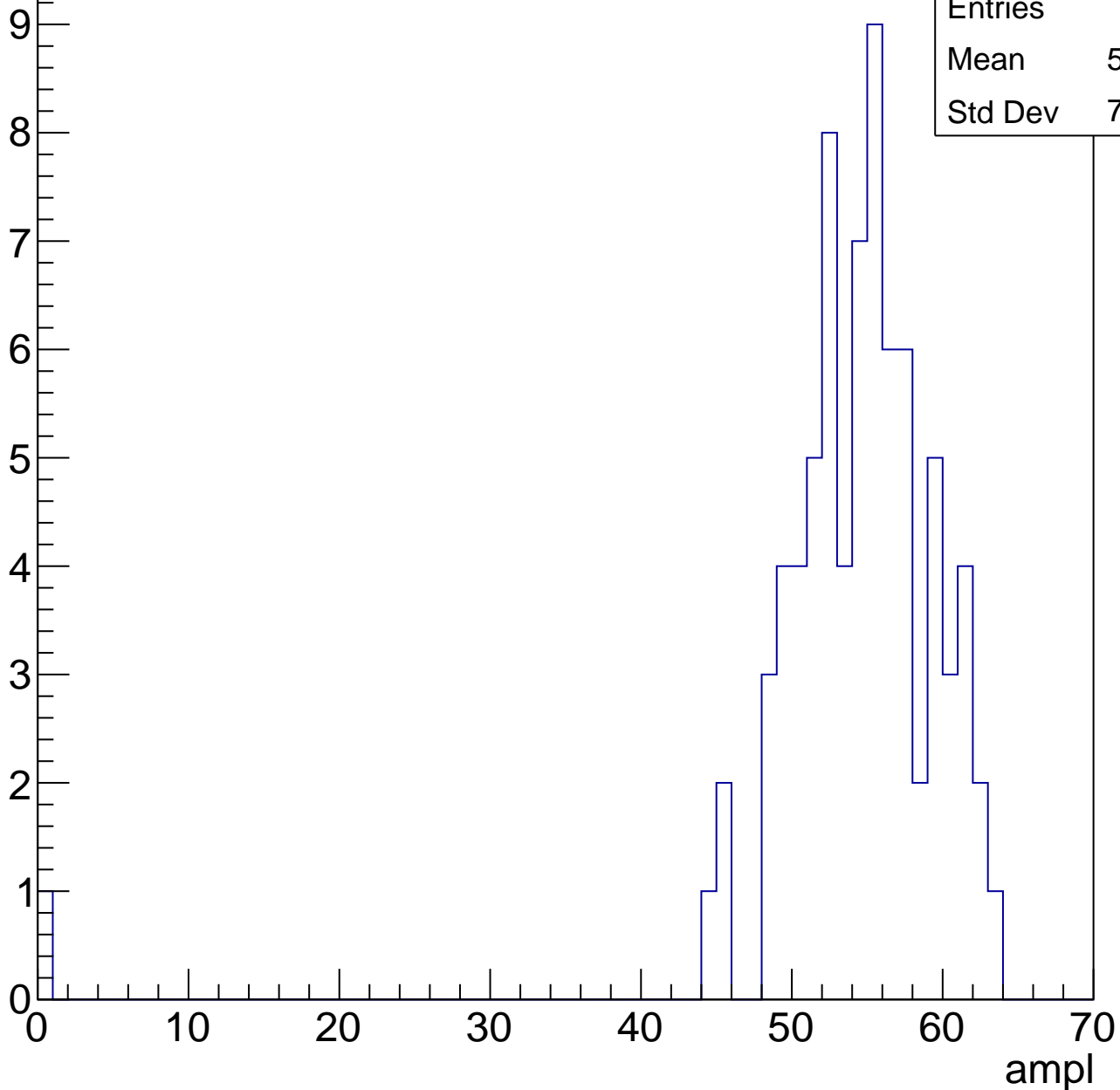


B0L102S, U5-ch118, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	77
Mean	53.64
Std Dev	7.454

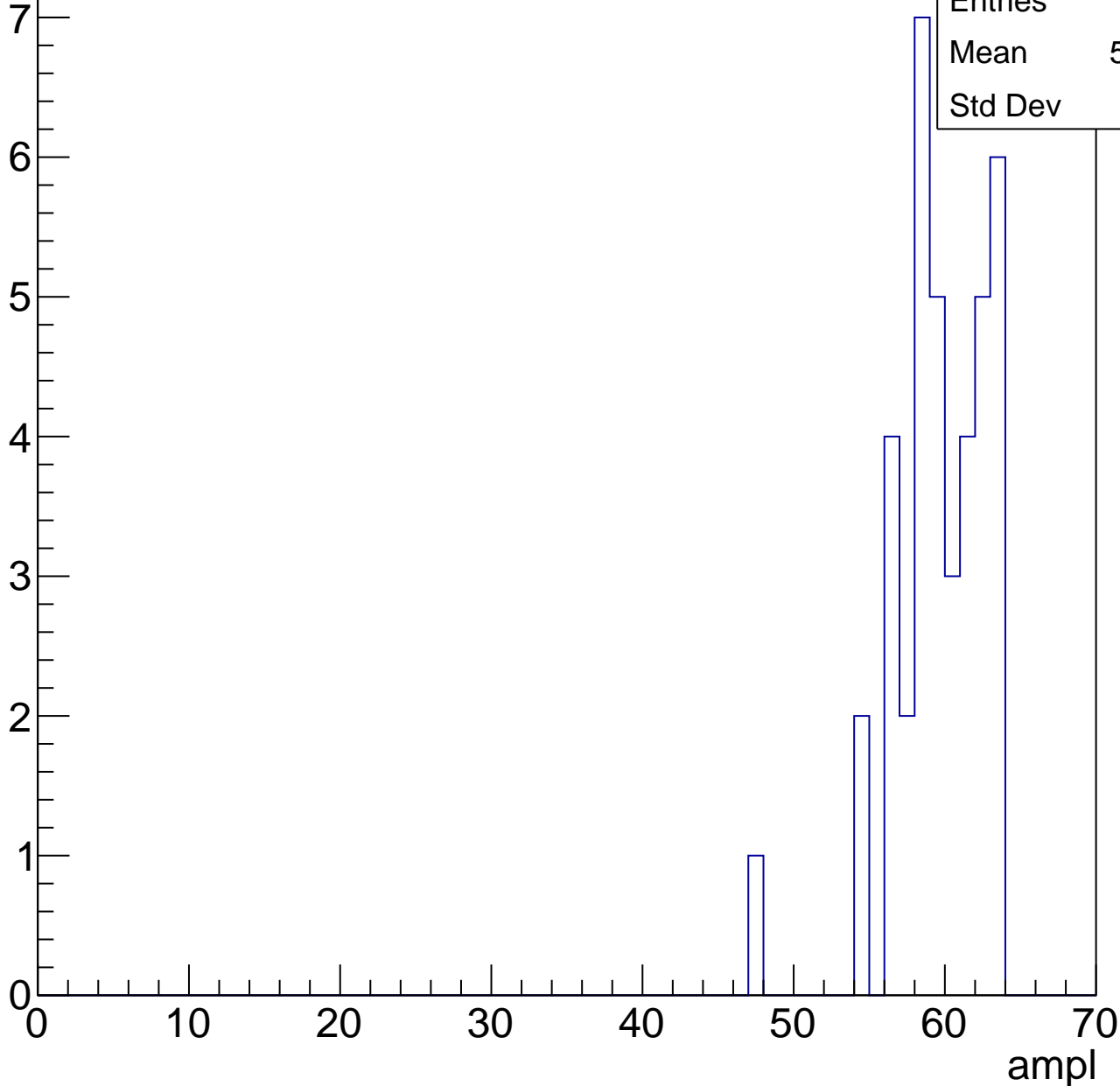


B0L102S, U5-ch118, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

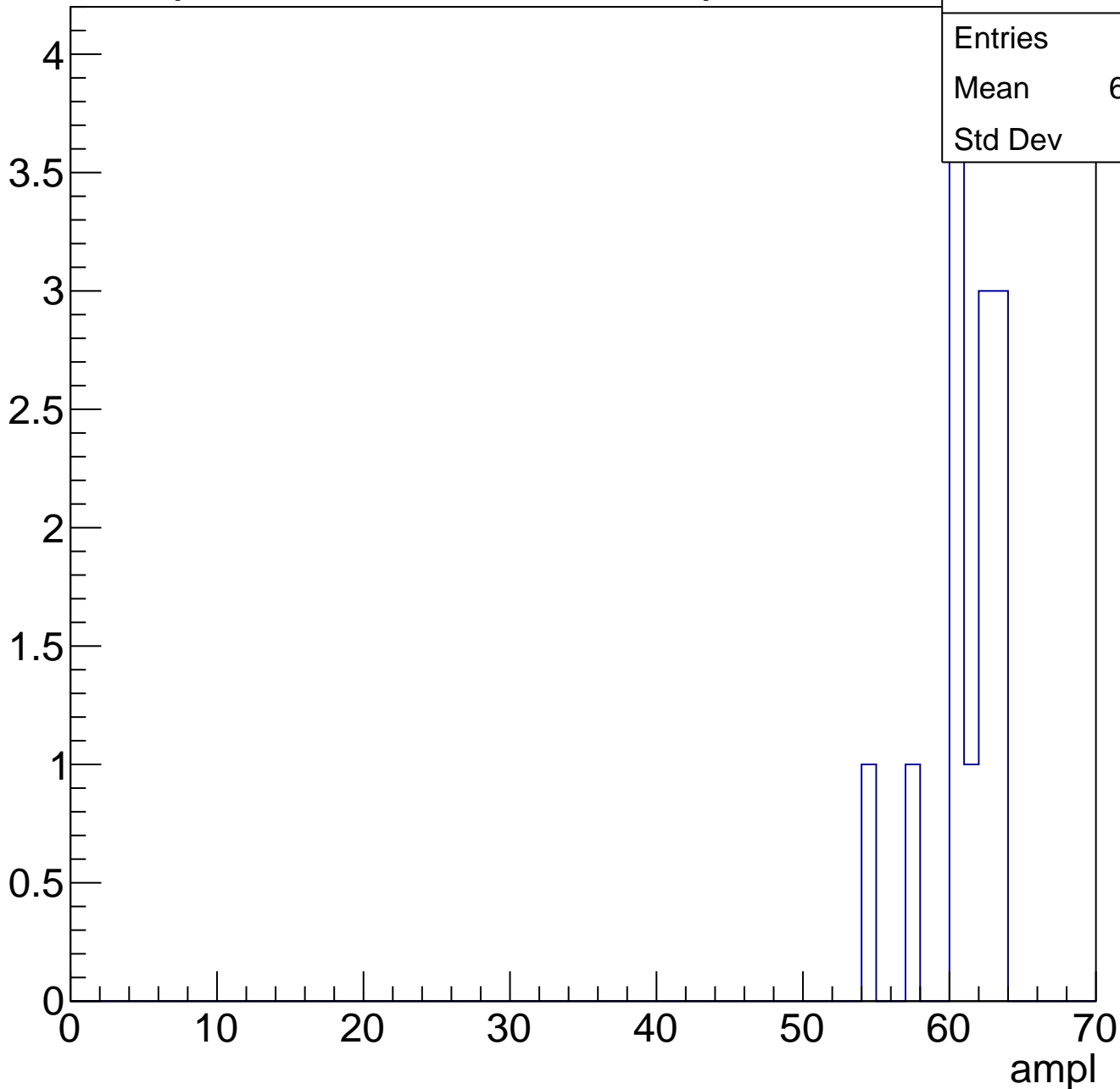
Entries	39
Mean	59.13
Std Dev	3.22



B0L102S, U5-ch118, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

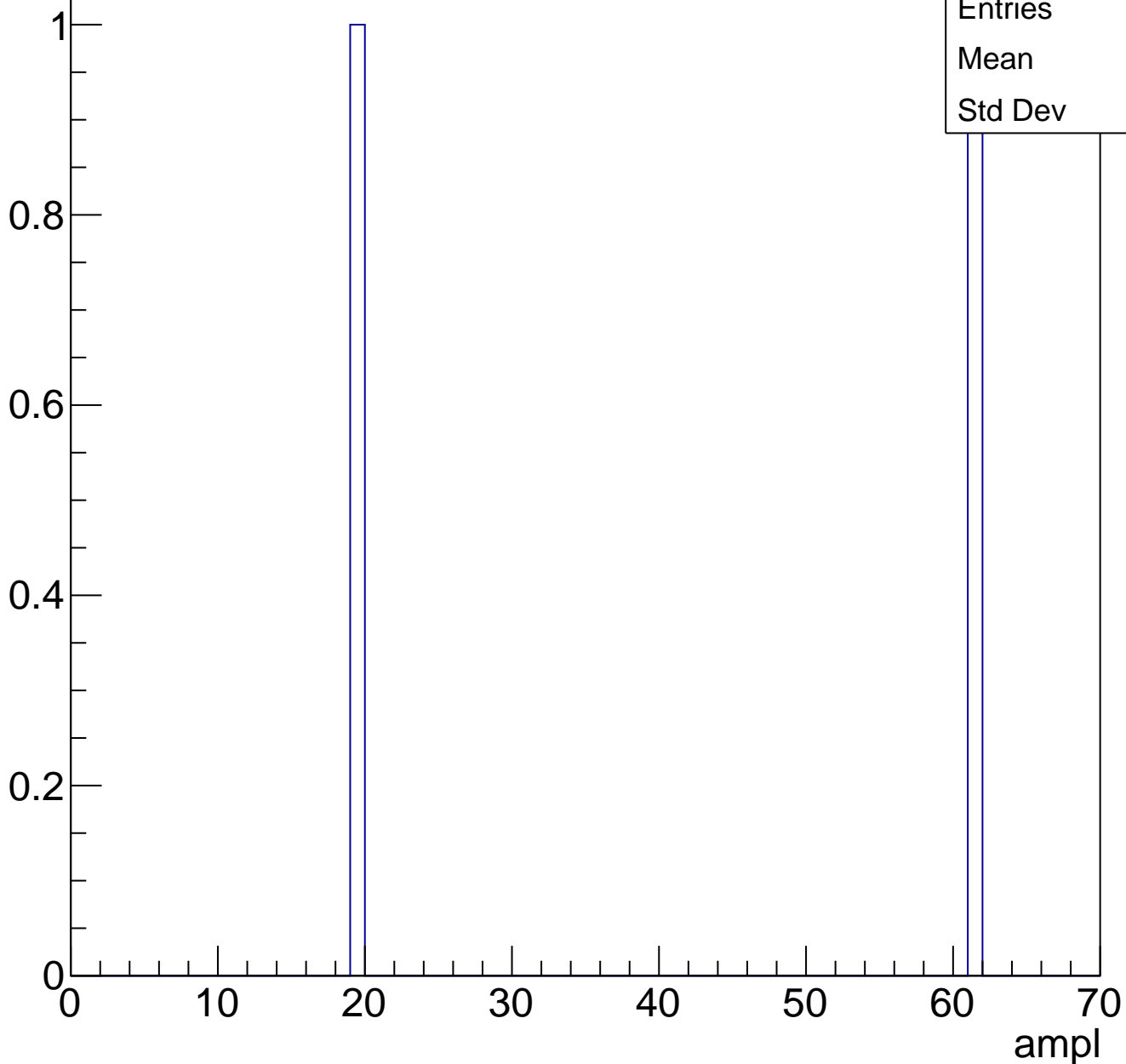


Entries	13
Mean	60.54
Std Dev	2.5

B0L102S, U5-ch118, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	2
Mean	40
Std Dev	21

B0L102S, U5-ch119, adc0

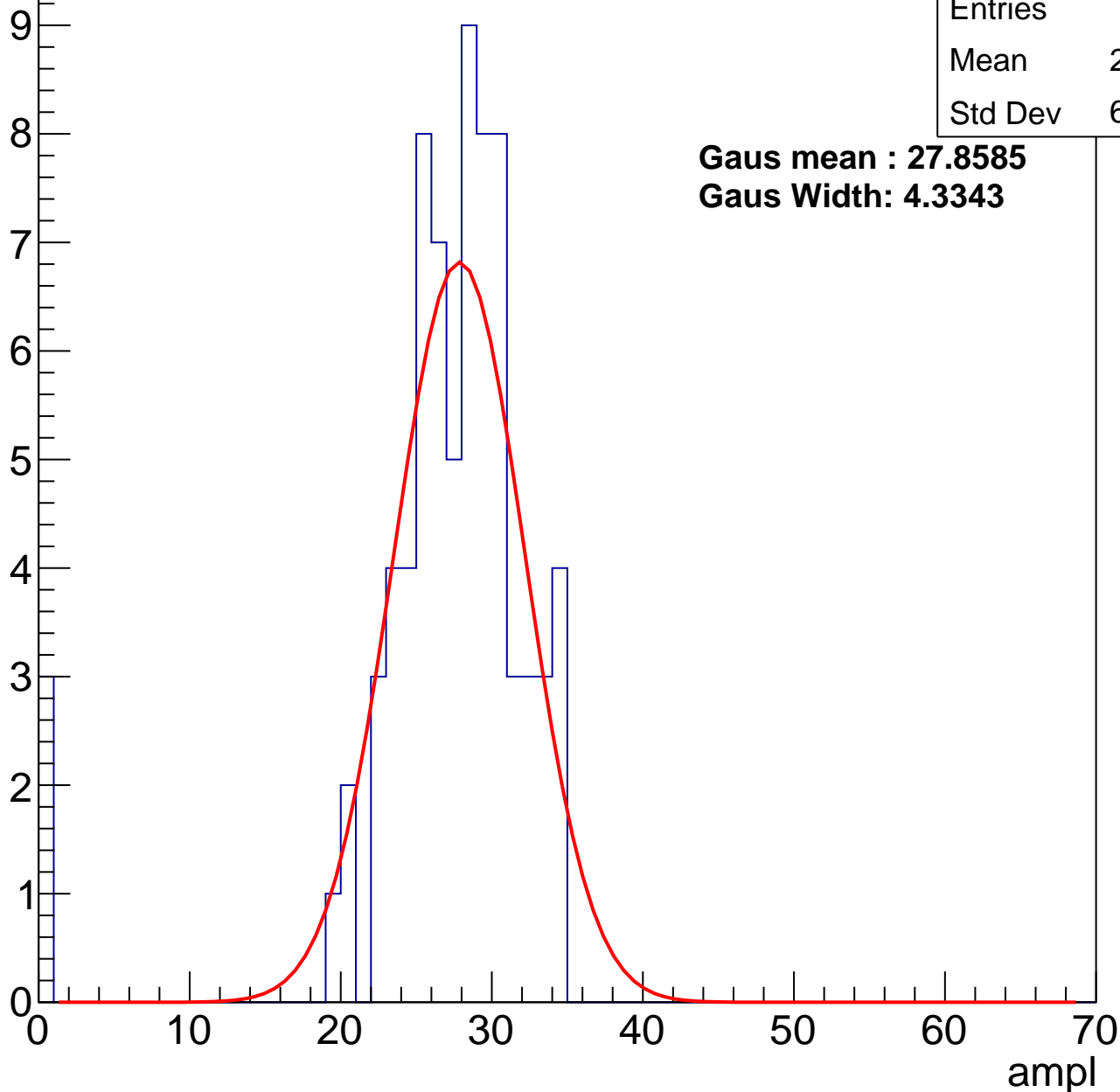
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	26.37
Std Dev	6.402

Gaus mean : 27.8585

Gaus Width: 4.3343



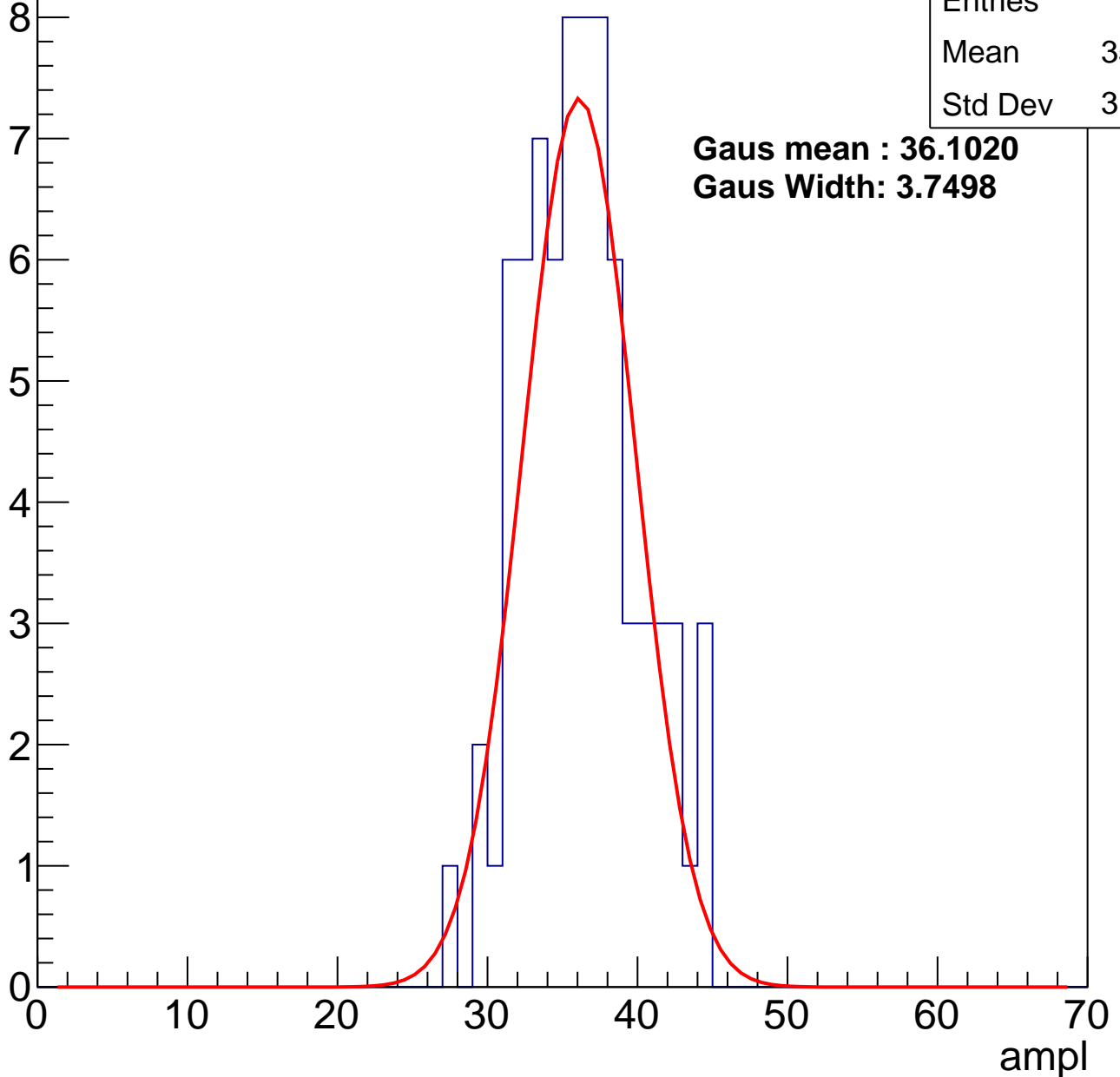
B0L102S, U5-ch119, adc1

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	75
Mean	35.75
Std Dev	3.799

Gaus mean : 36.1020
Gaus Width: 3.7498



B0L102S, U5-ch119, adc2

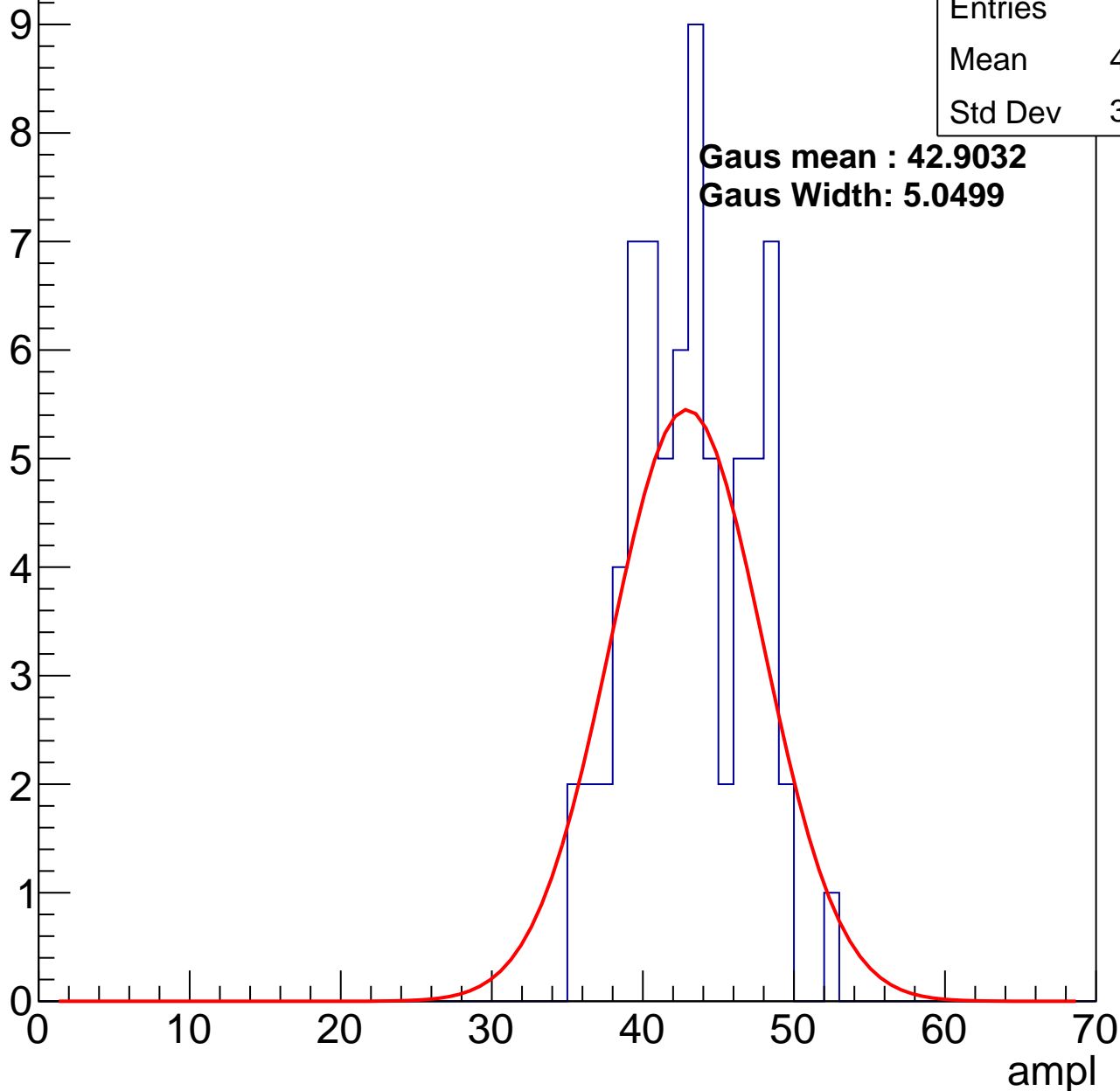
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	42.62
Std Dev	3.854

Gaus mean : 42.9032

Gaus Width: 5.0499



B0L102S, U5-ch119, adc3

calib_packv5_042523_0143.root, FC#12, port B1

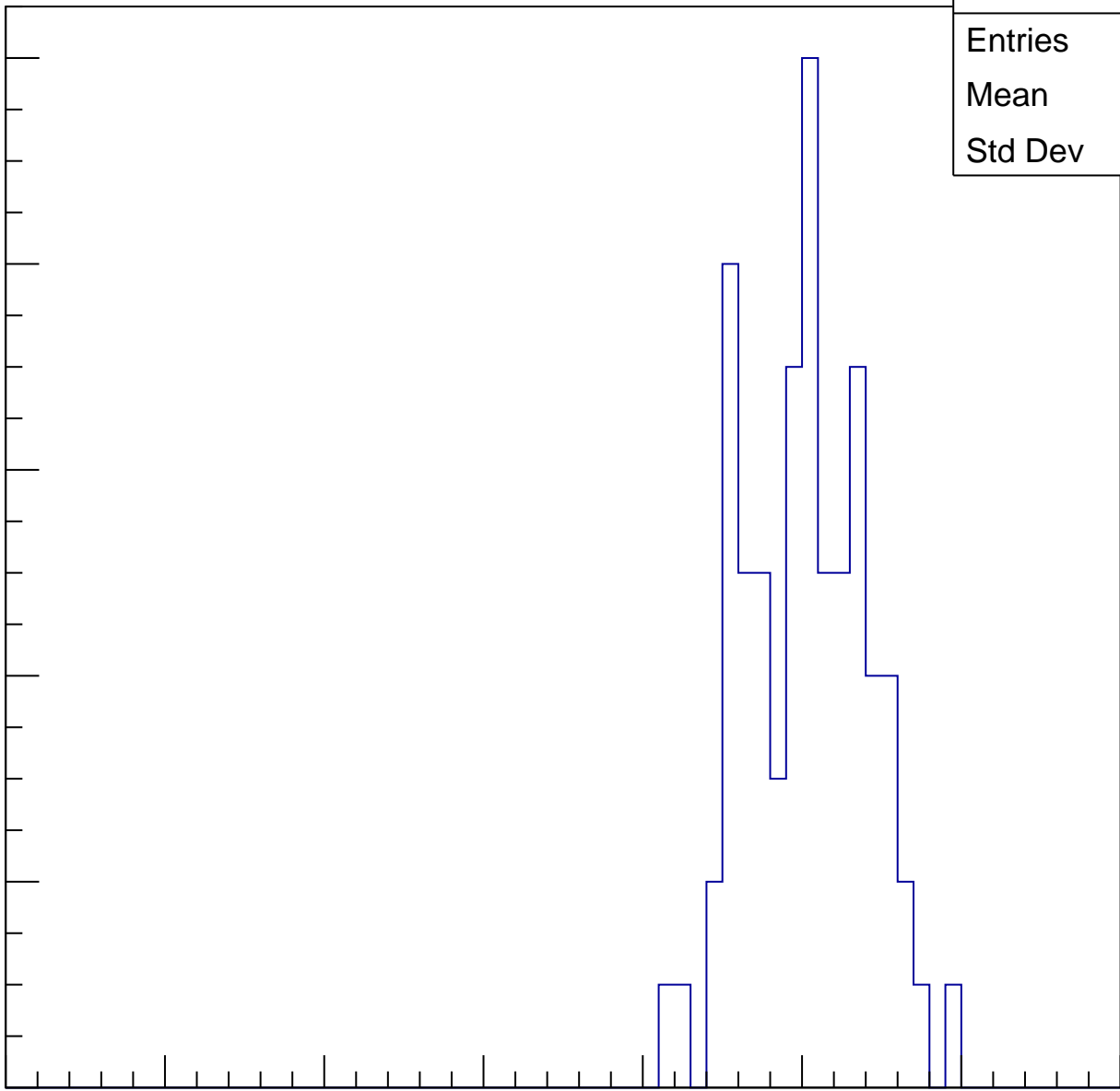
Entries	71
Mean	49.76
Std Dev	3.773

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

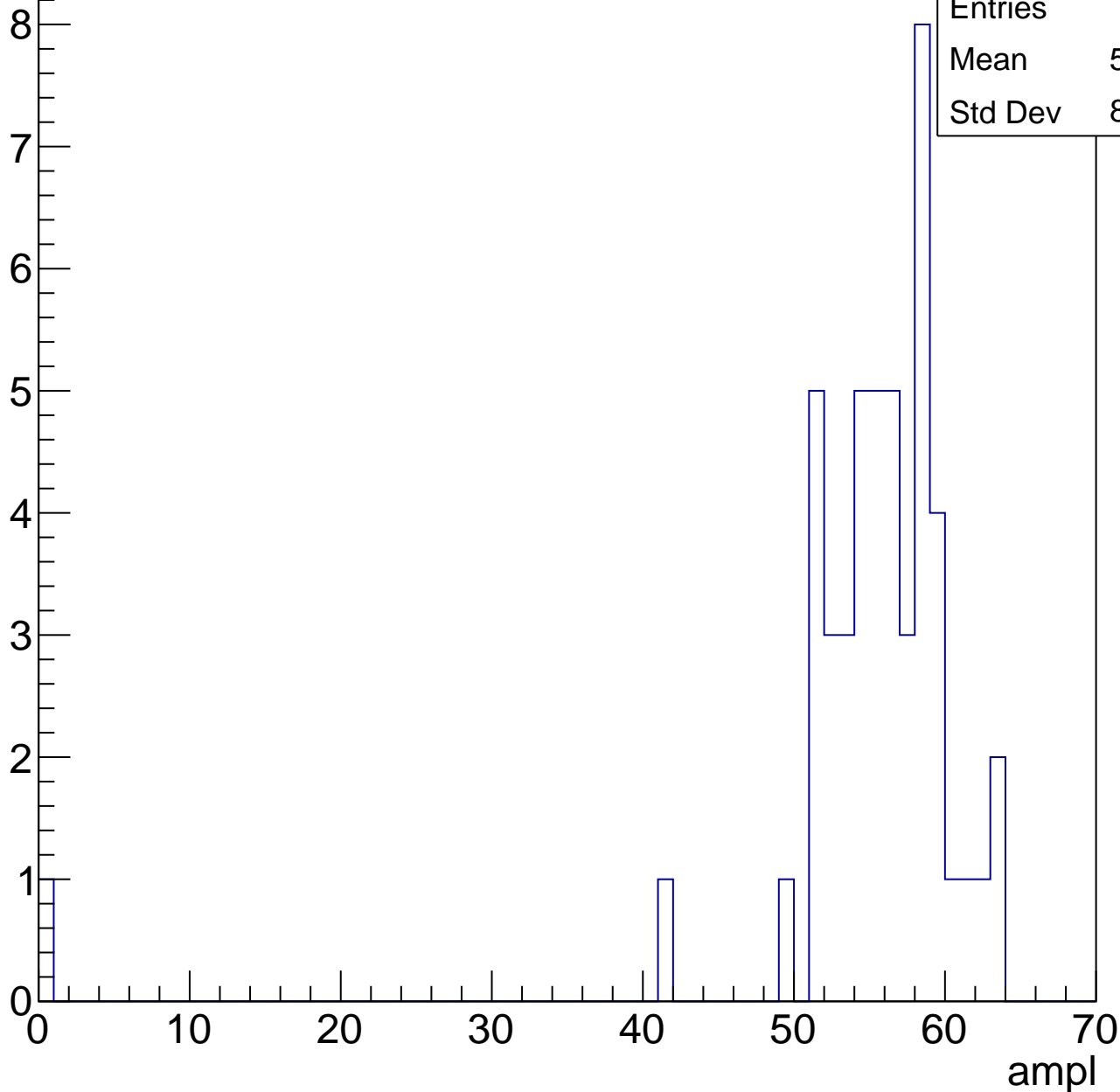


B0L102S, U5-ch119, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	49
Mean	54.39
Std Dev	8.752

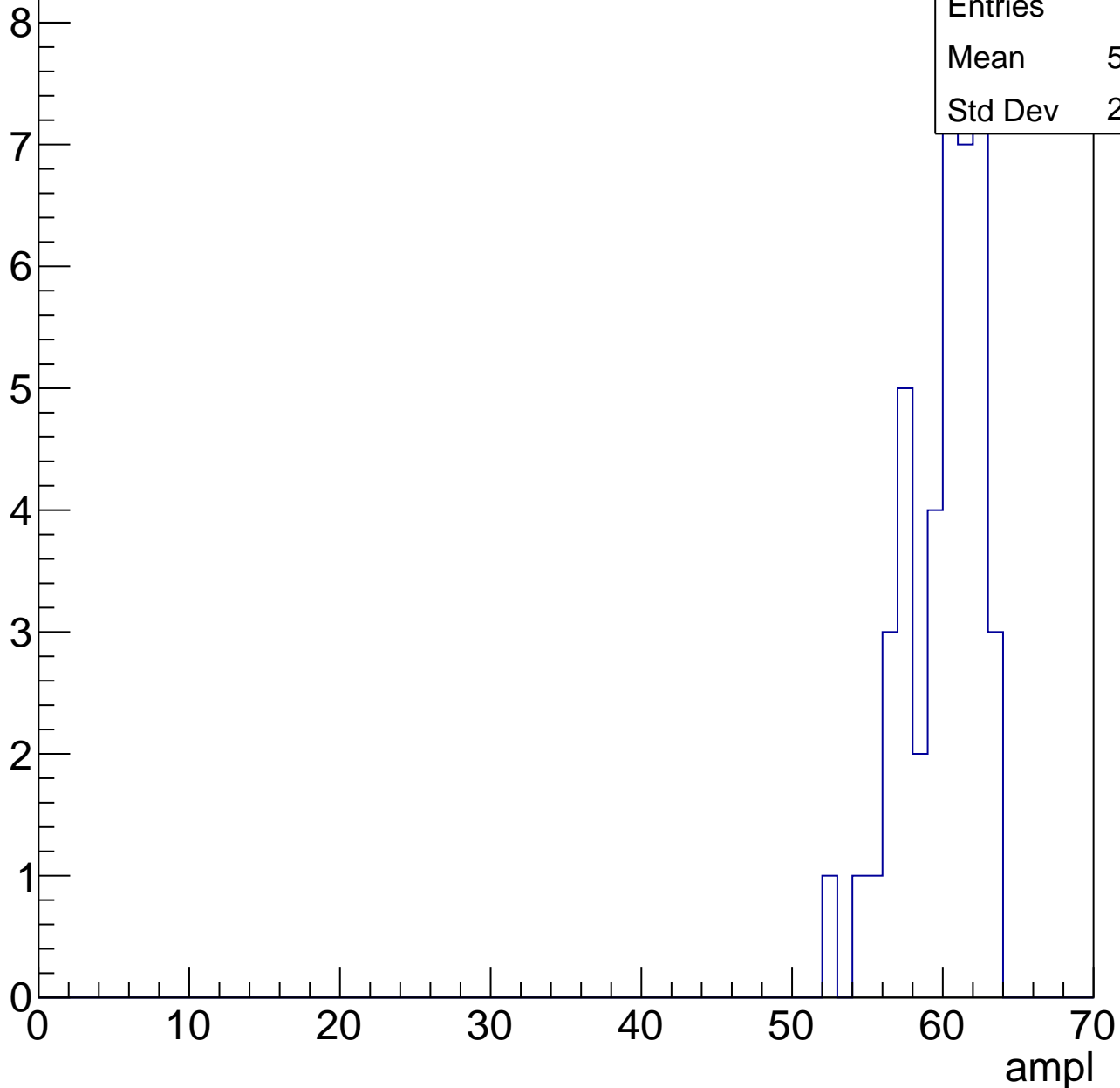


B0L102S, U5-ch119, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	43
Mean	59.49
Std Dev	2.573

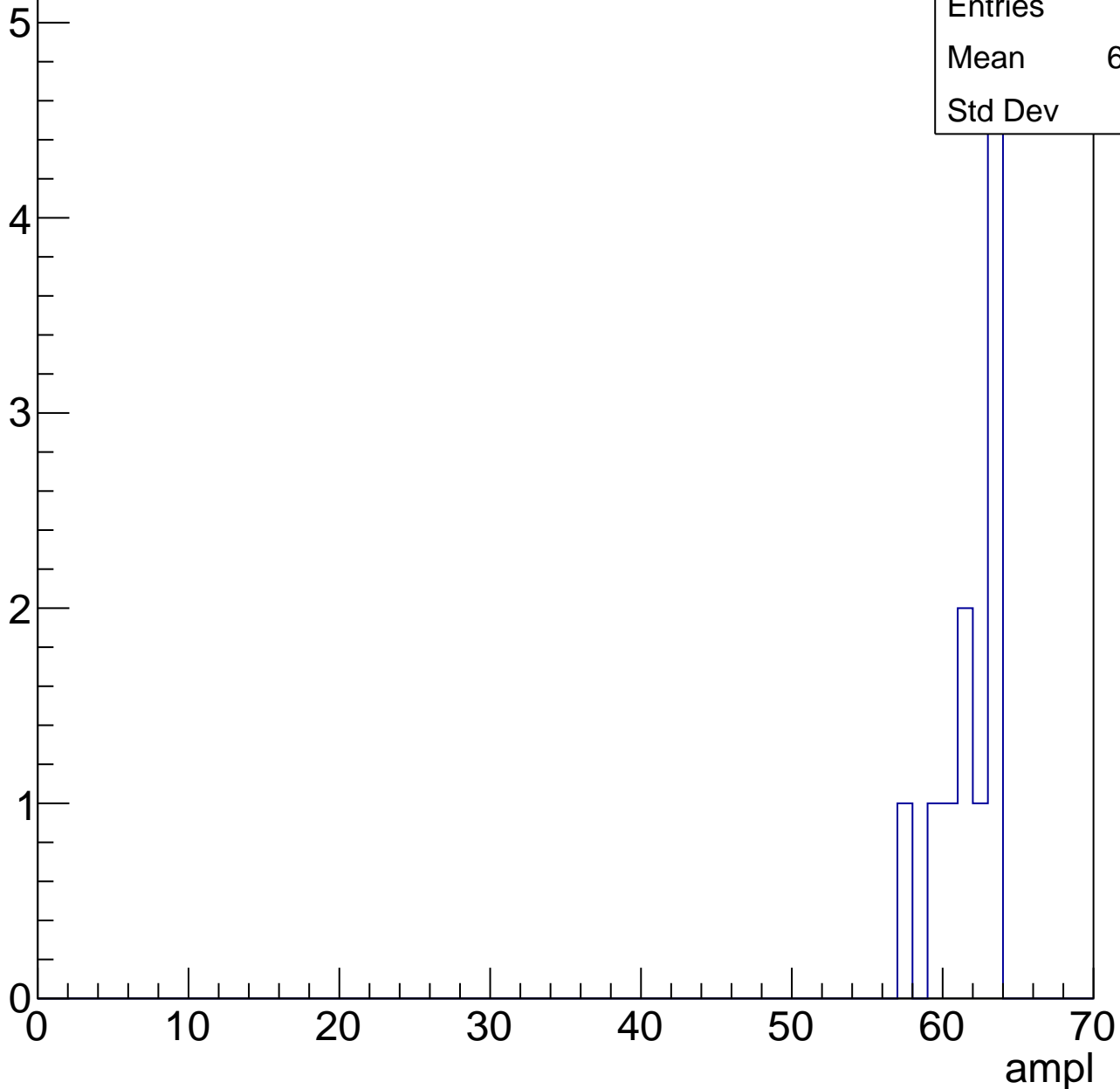


B0L102S, U5-ch119, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	11
Mean	61.36
Std Dev	1.92



B0L102S, U5-ch119, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch120, adc0

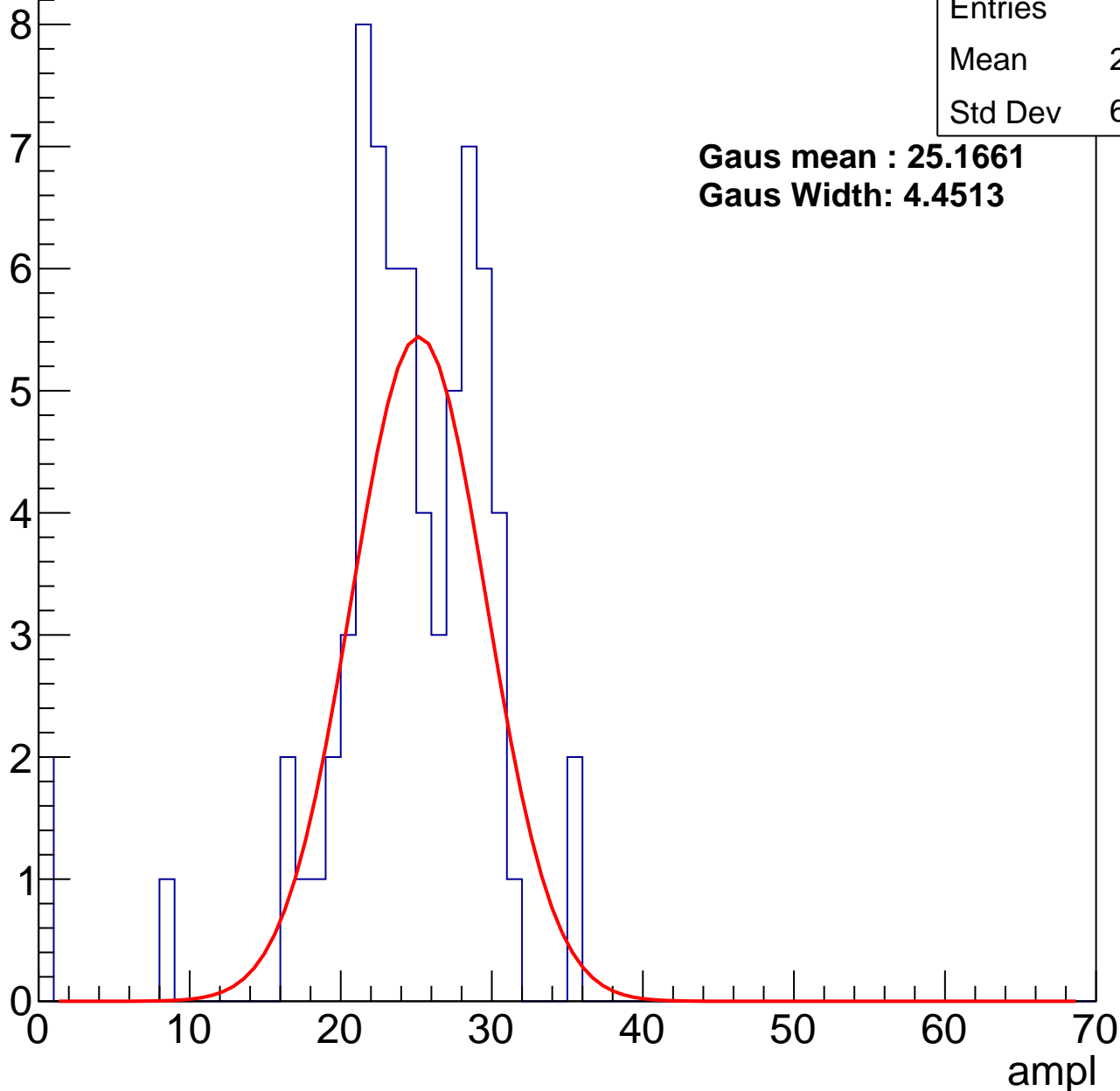
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	23.68
Std Dev	6.023

Gaus mean : 25.1661

Gaus Width: 4.4513



B0L102S, U5-ch120, adc1

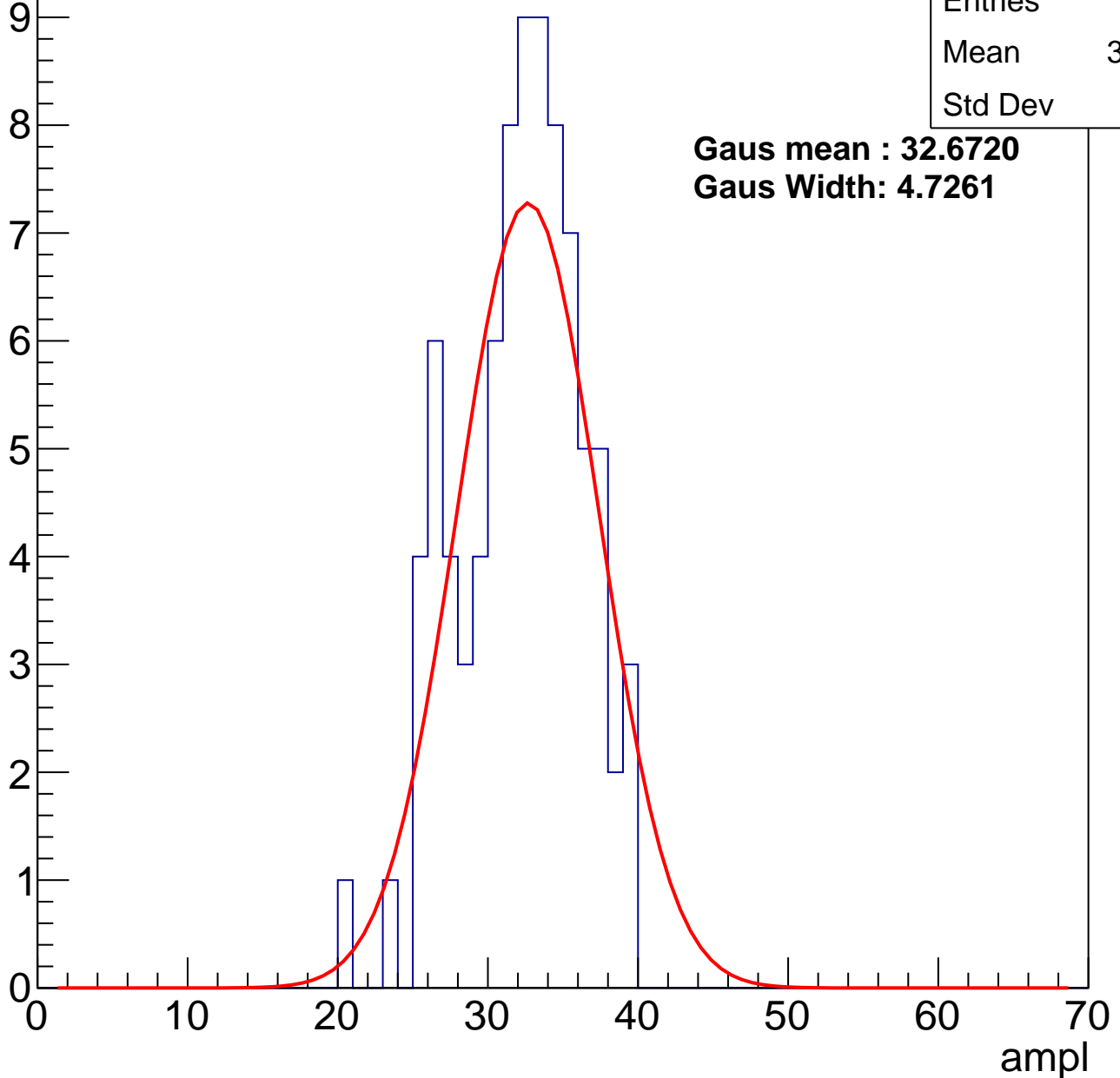
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	31.71
Std Dev	4.02

Gaus mean : 32.6720

Gaus Width: 4.7261



B0L102S, U5-ch120, adc2

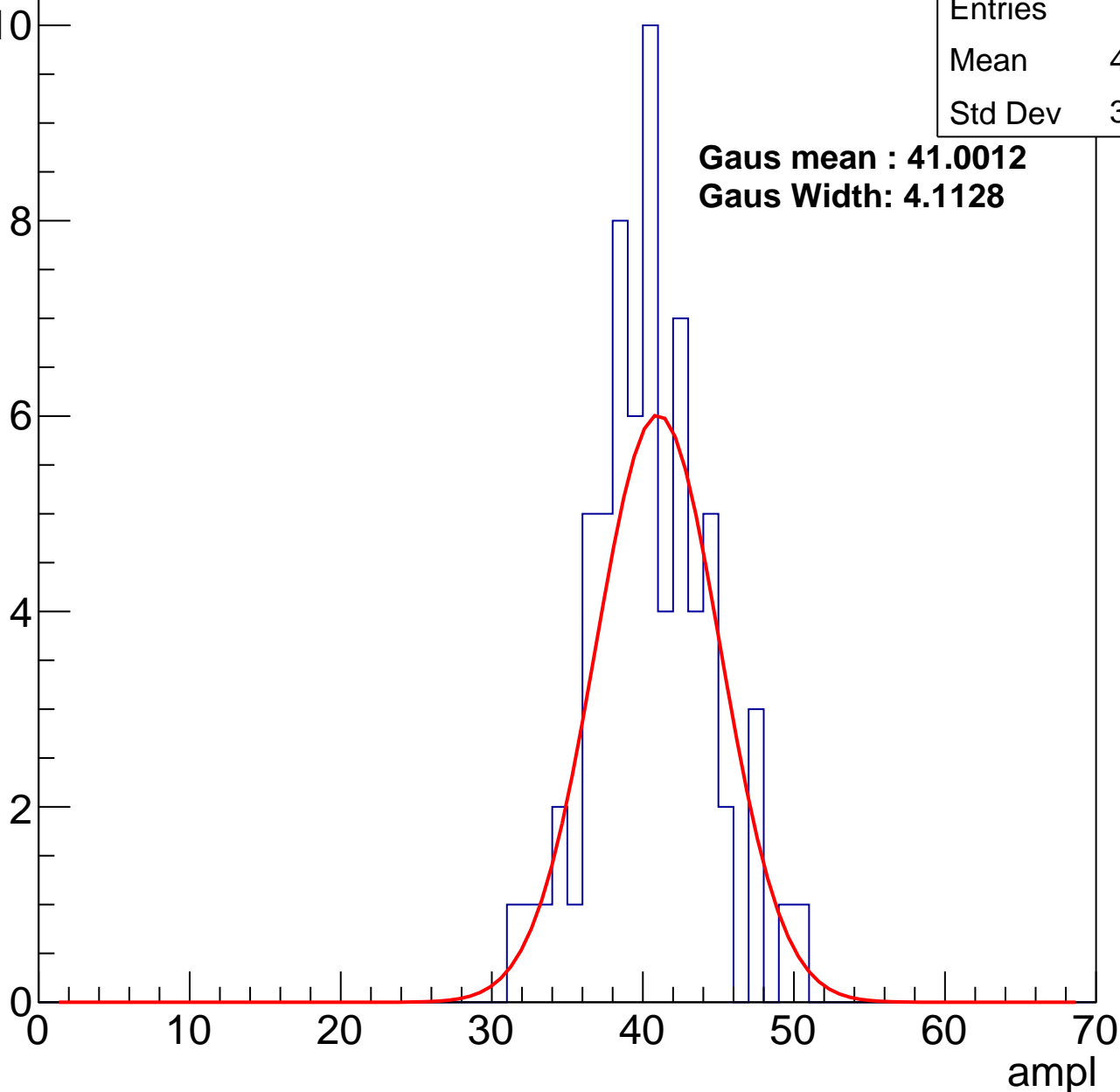
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	67
Mean	40.03
Std Dev	3.824

Gaus mean : 41.0012

Gaus Width: 4.1128

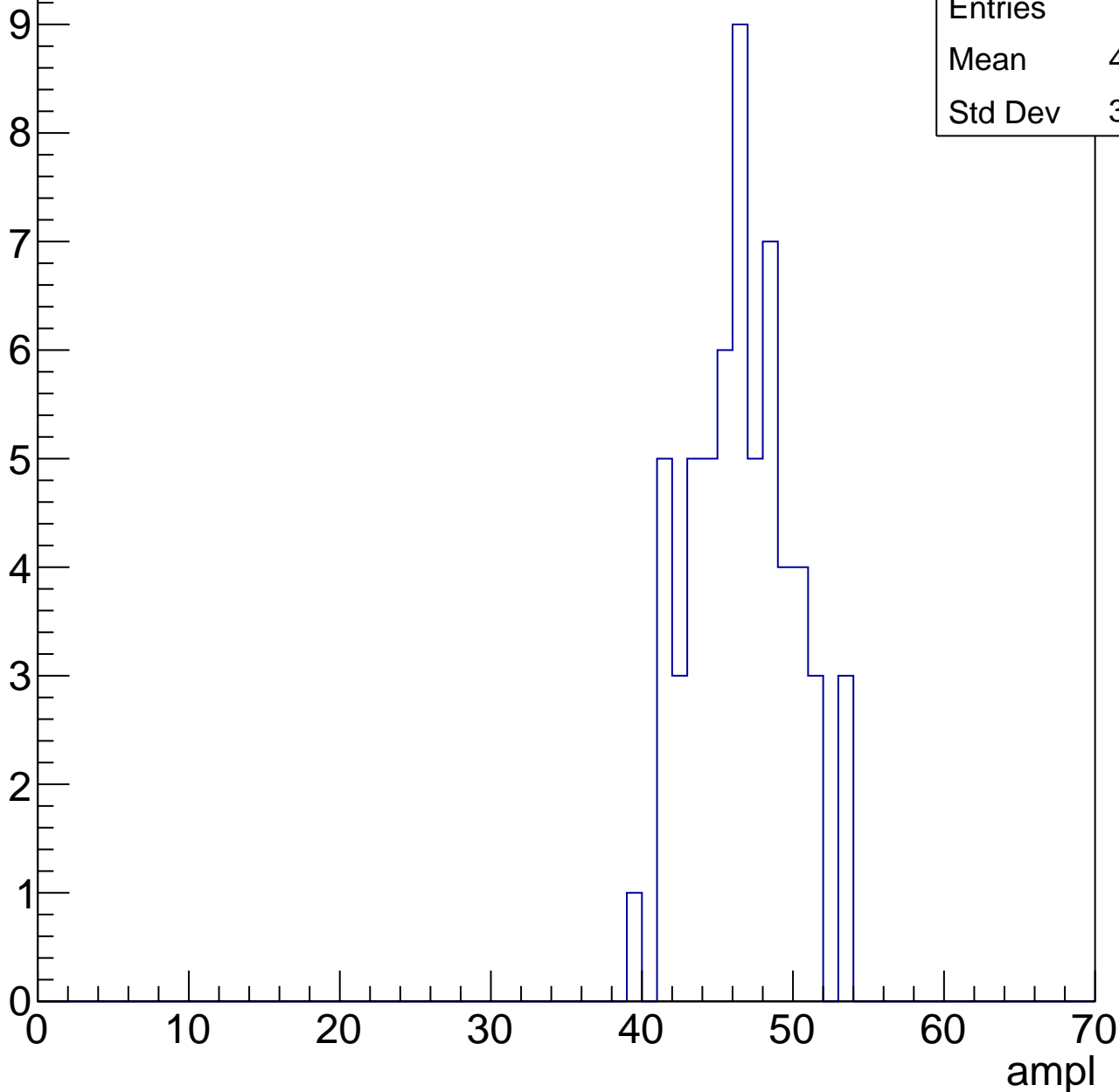


B0L102S, U5-ch120, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	46.13
Std Dev	3.284

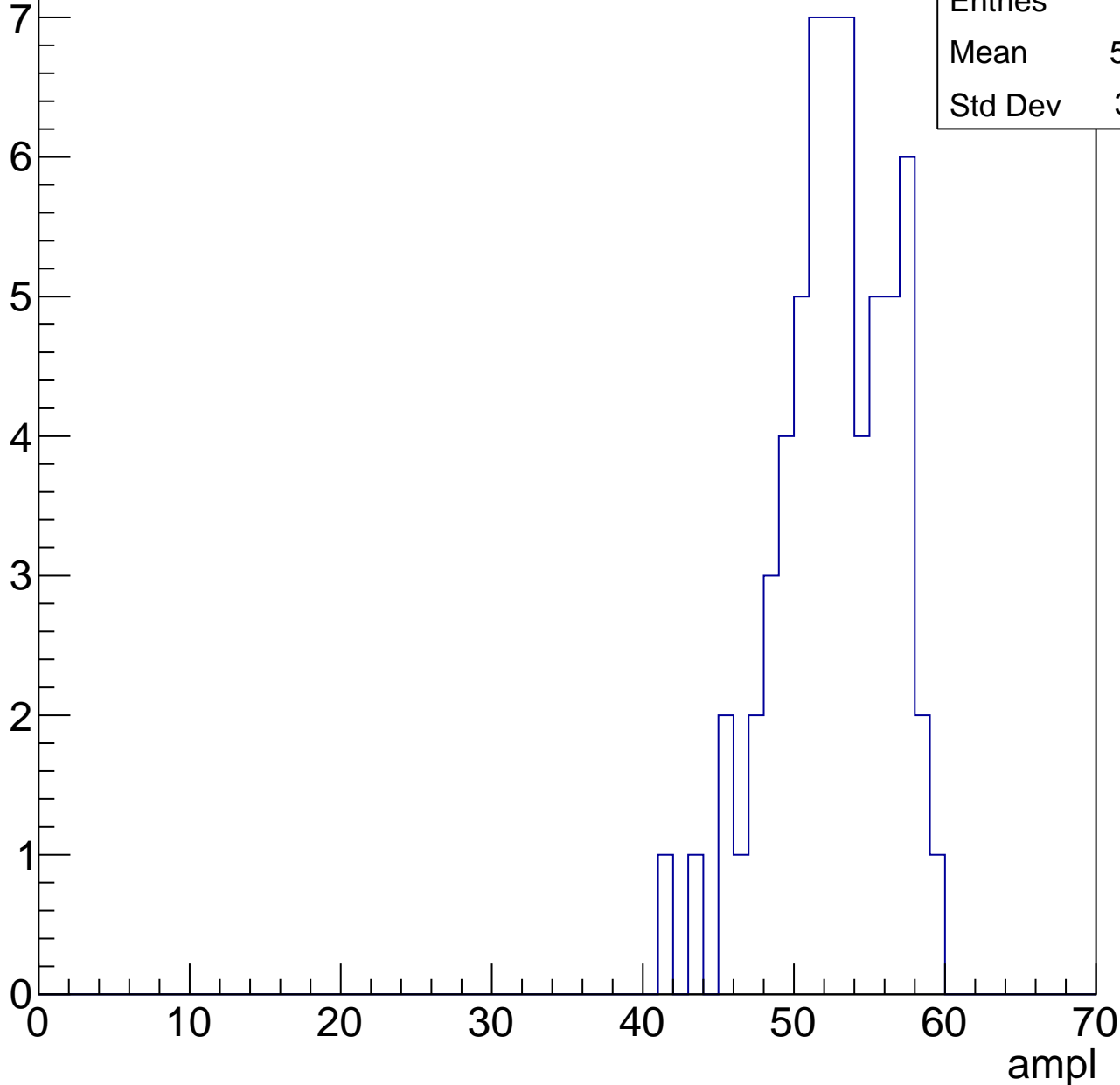


B0L102S, U5-ch120, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	52.13
Std Dev	3.811

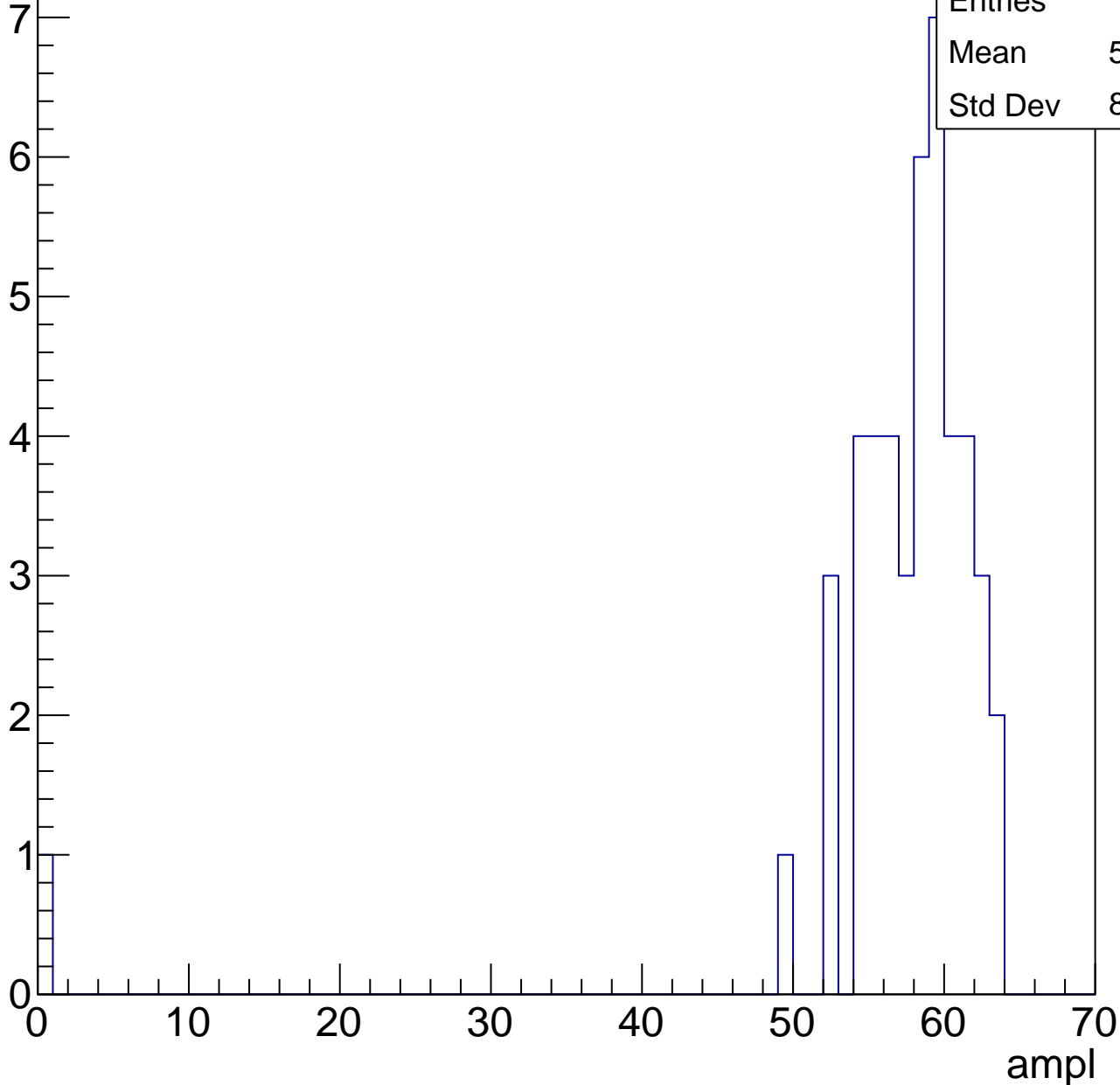


B0L102S, U5-ch120, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	46
Mean	56.37
Std Dev	8.977

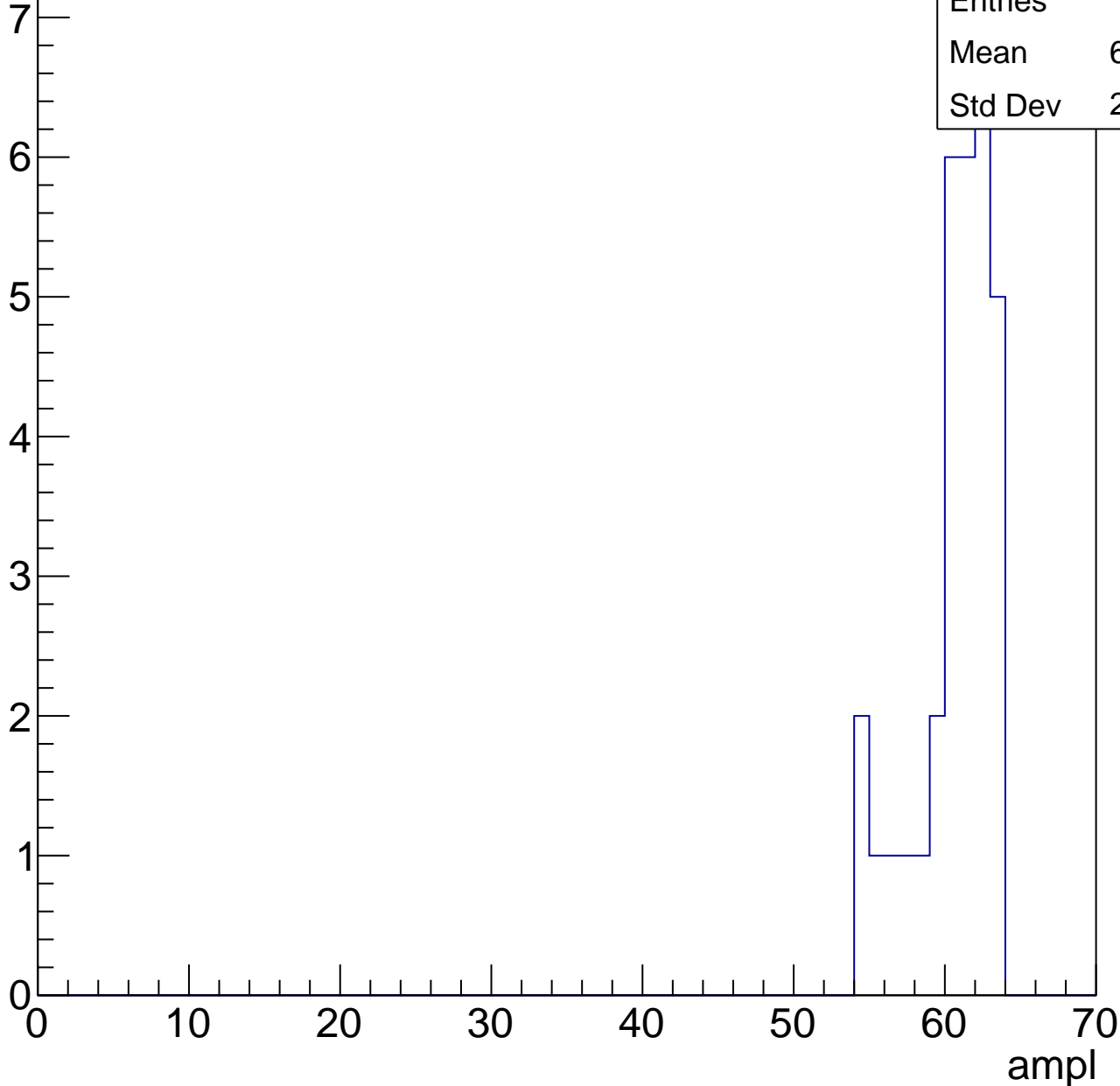


B0L102S, U5-ch120, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

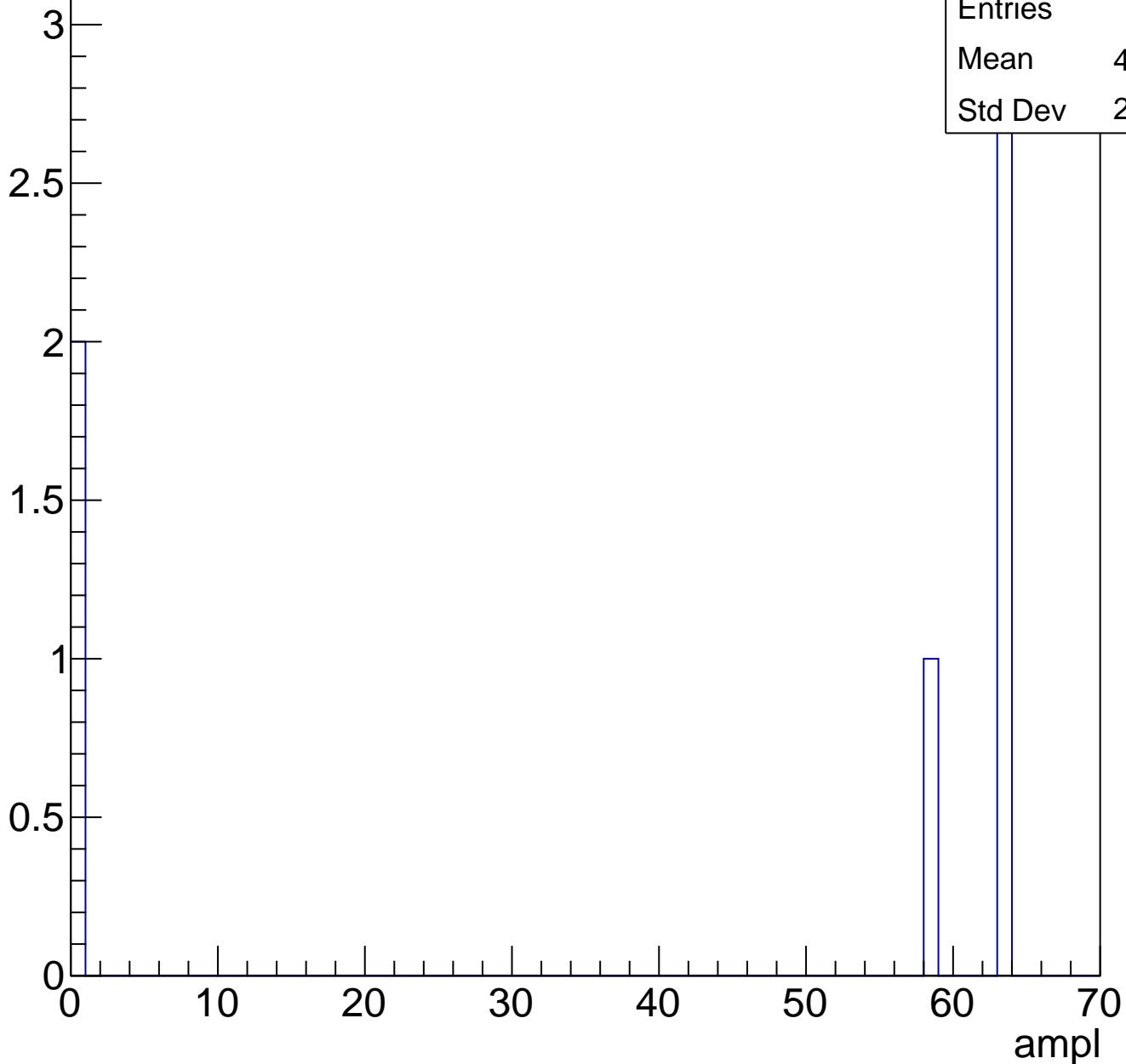
Entries	32
Mean	60.22
Std Dev	2.534



B0L102S, U5-ch120, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch121, adc0

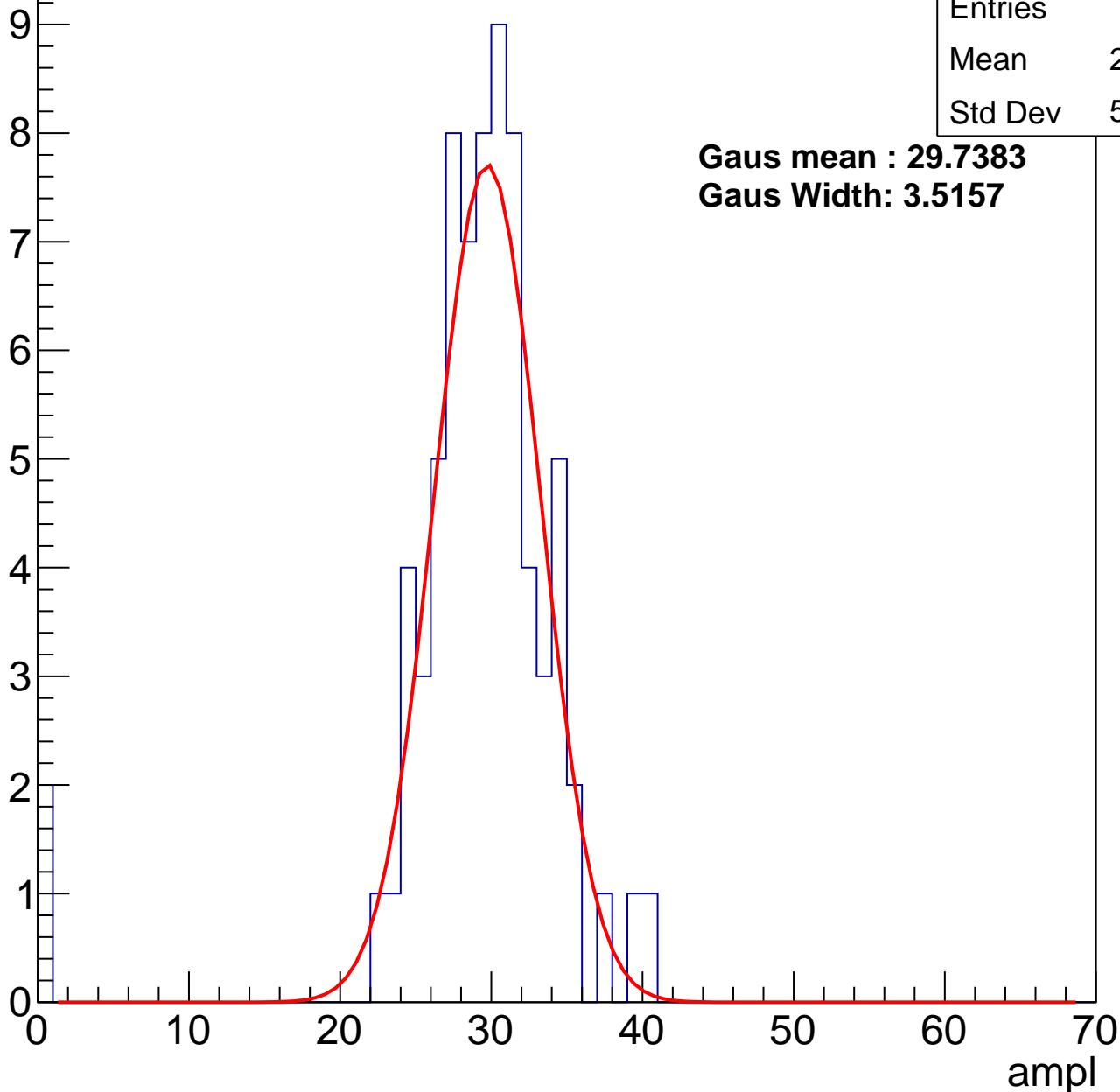
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	28.64
Std Dev	5.964

Gaus mean : 29.7383

Gaus Width: 3.5157



B0L102S, U5-ch121, adc1

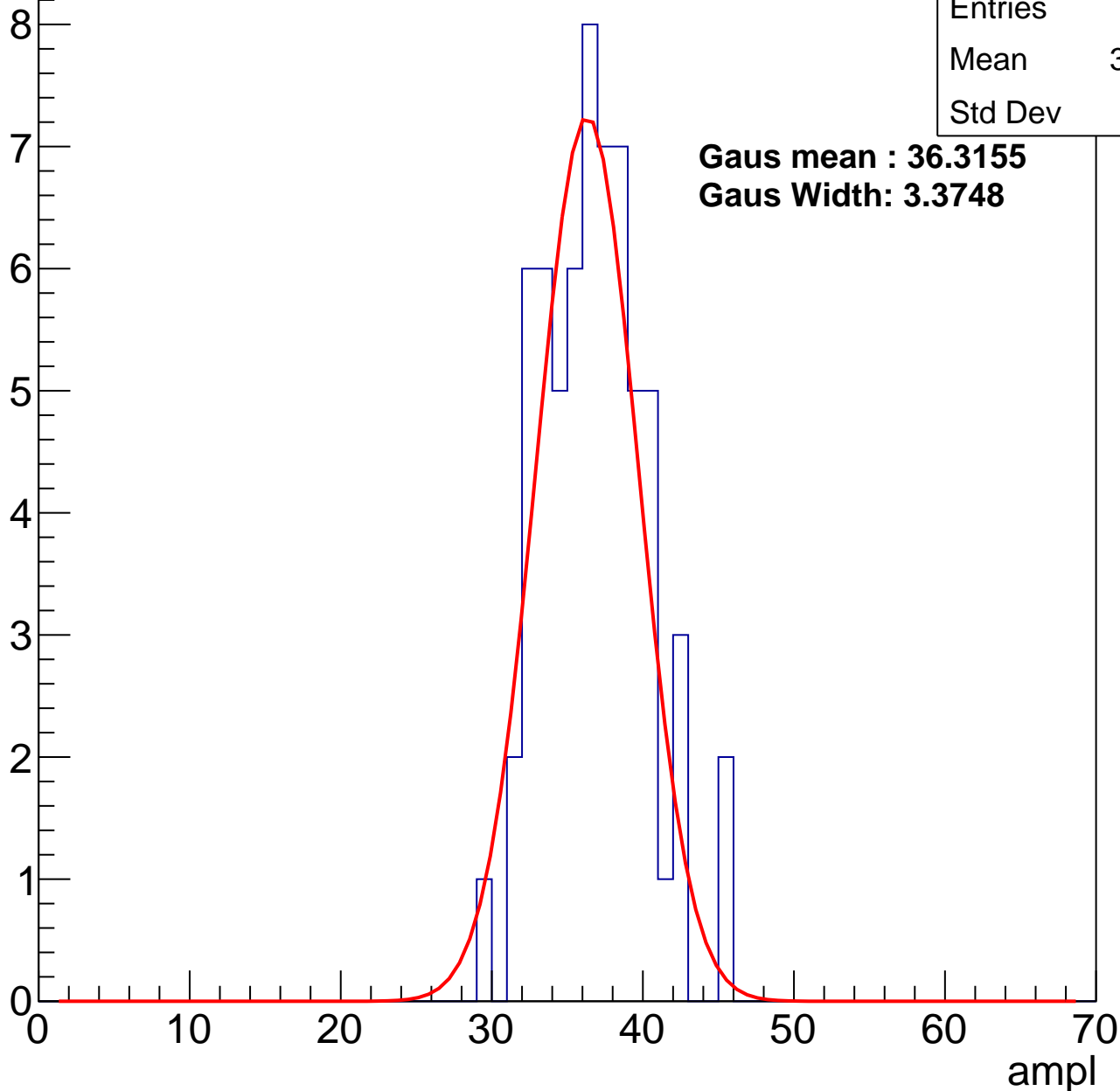
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	64
Mean	36.34
Std Dev	3.36

Gaus mean : 36.3155

Gaus Width: 3.3748



B0L102S, U5-ch121, adc2

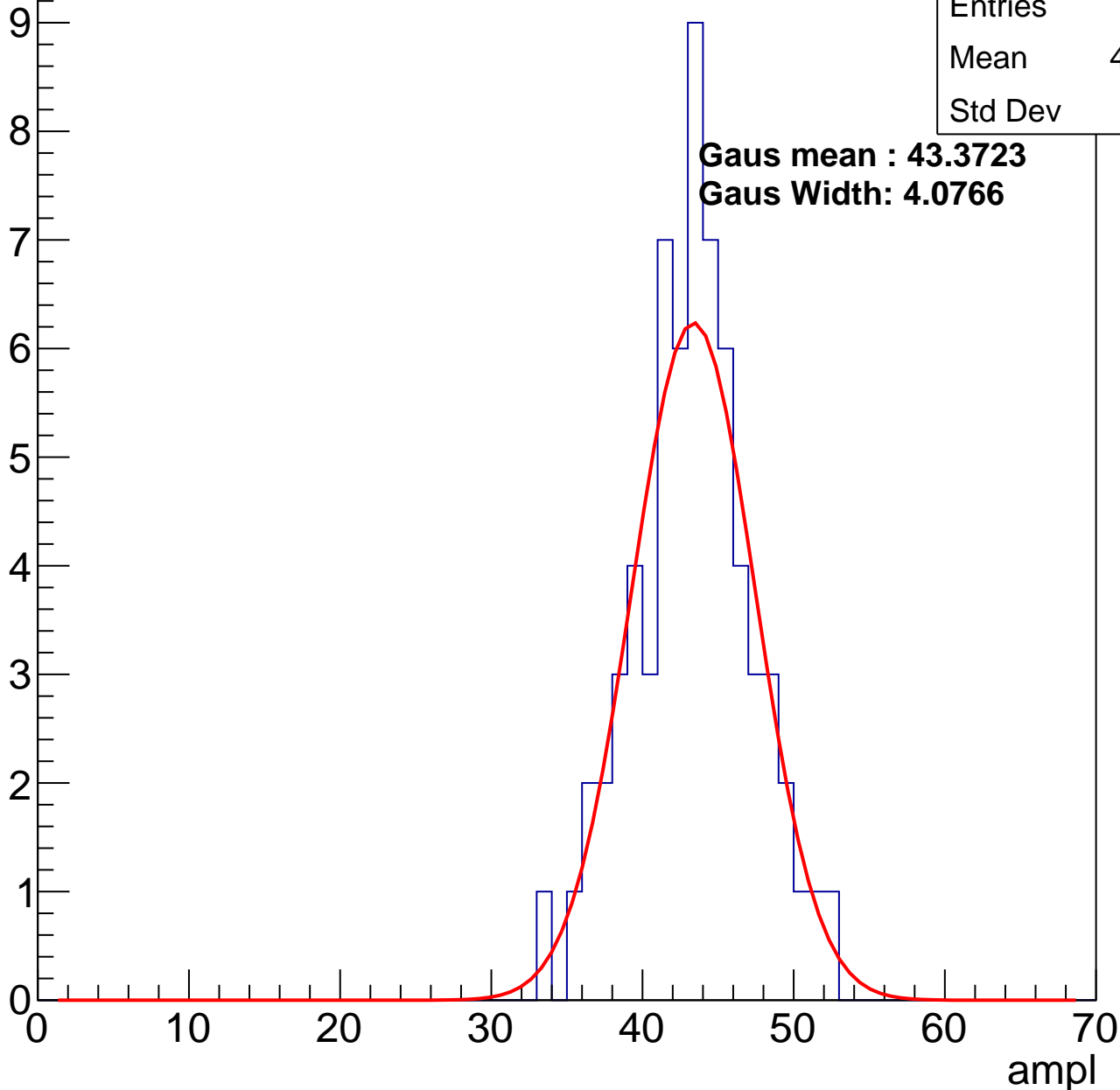
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	66
Mean	42.85
Std Dev	3.87

Gaus mean : 43.3723

Gaus Width: 4.0766

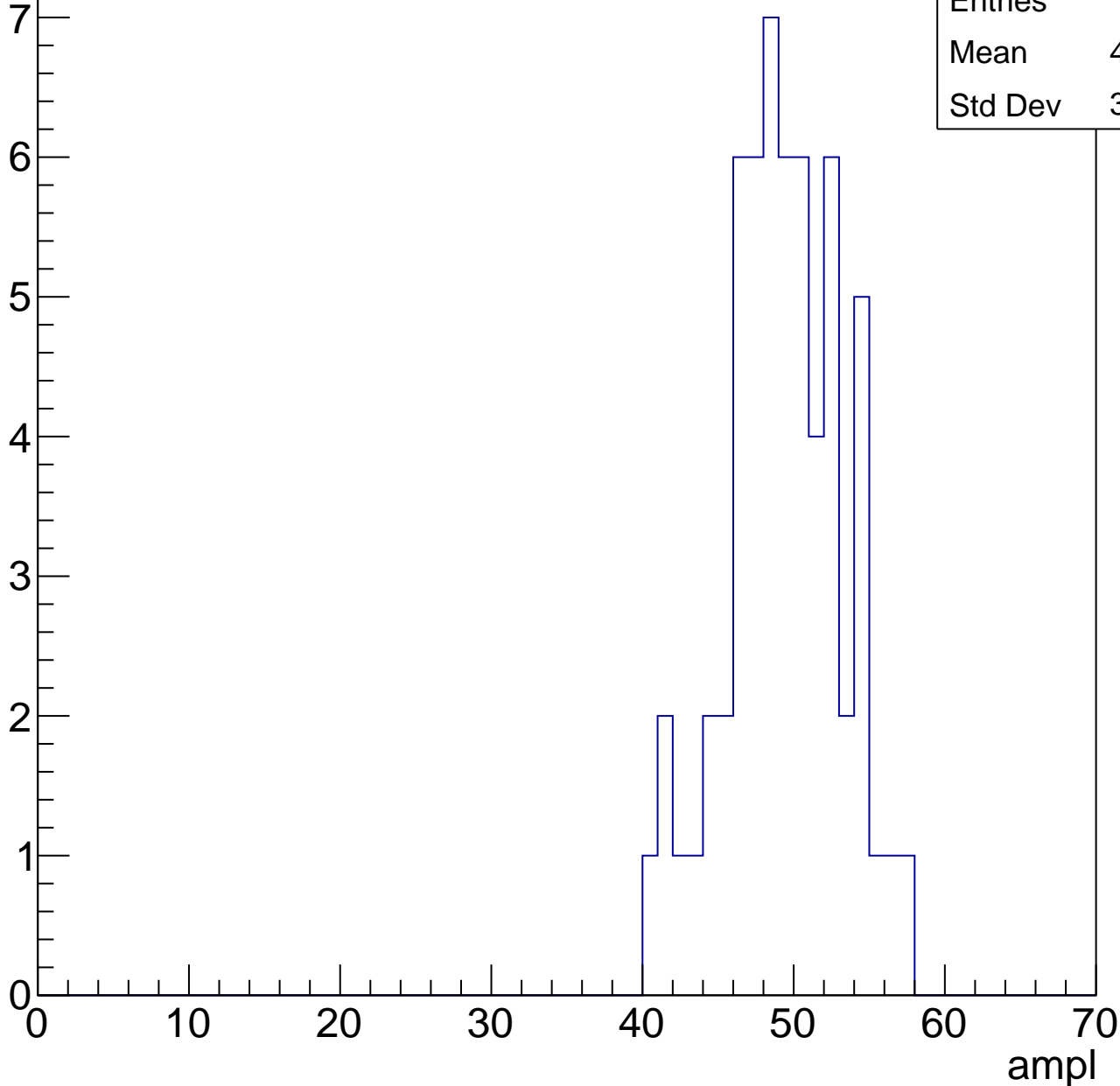


B0L102S, U5-ch121, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	48.88
Std Dev	3.747

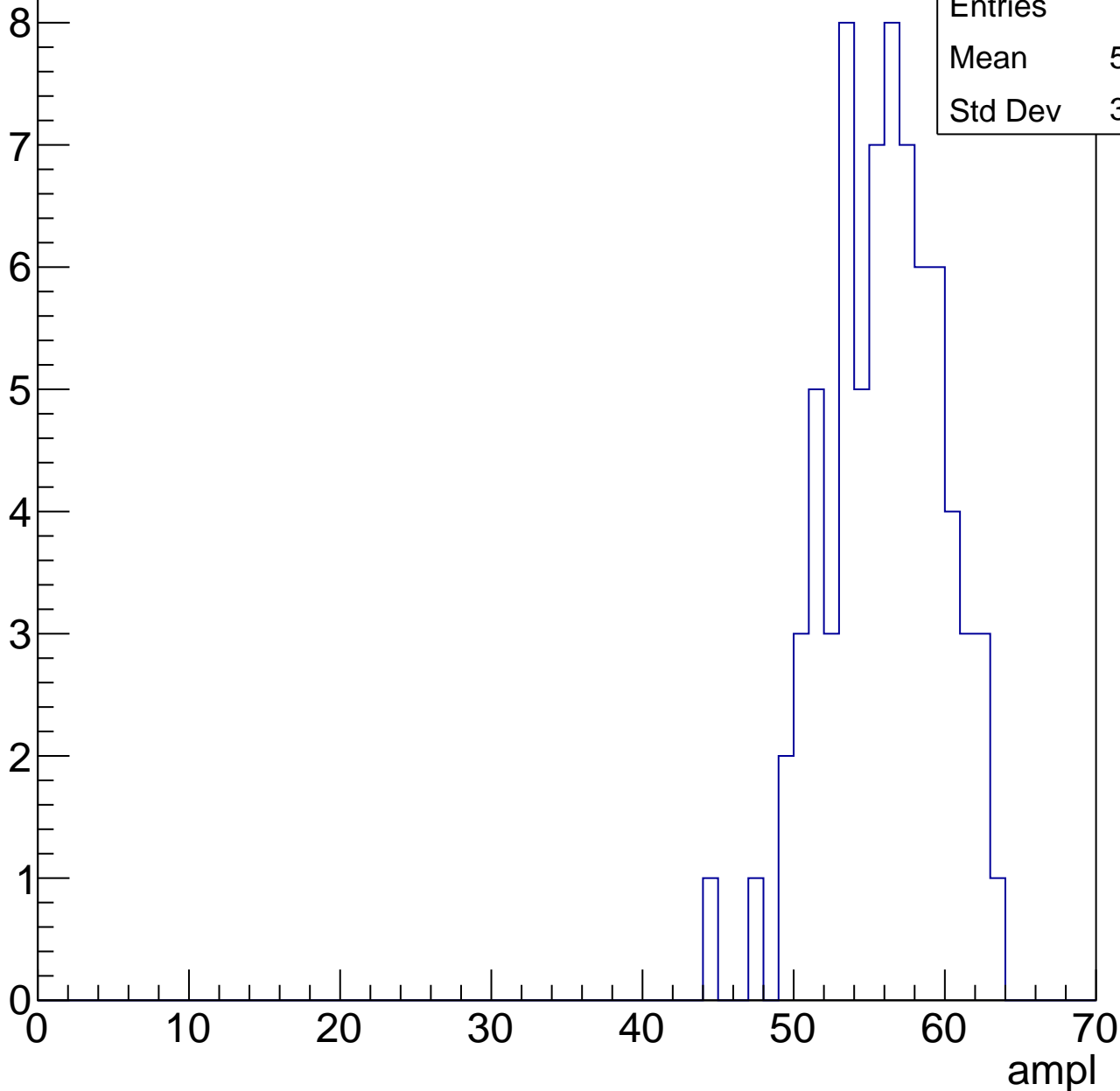


B0L102S, U5-ch121, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	55.48
Std Dev	3.818

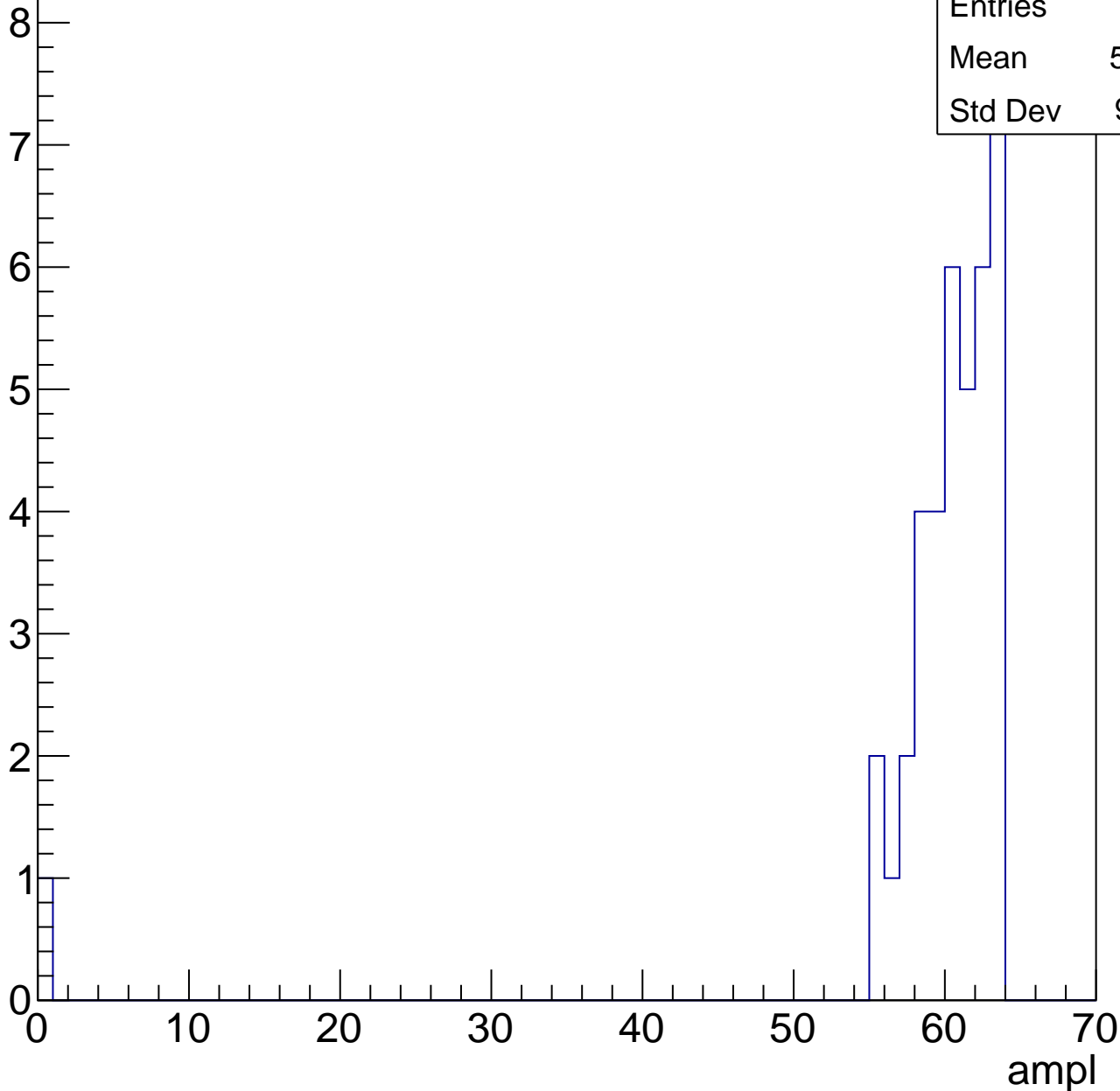


B0L102S, U5-ch121, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	39
Mean	58.69
Std Dev	9.791



B0L102S, U5-ch121, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch121, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B0L102S, U5-ch122, adc0

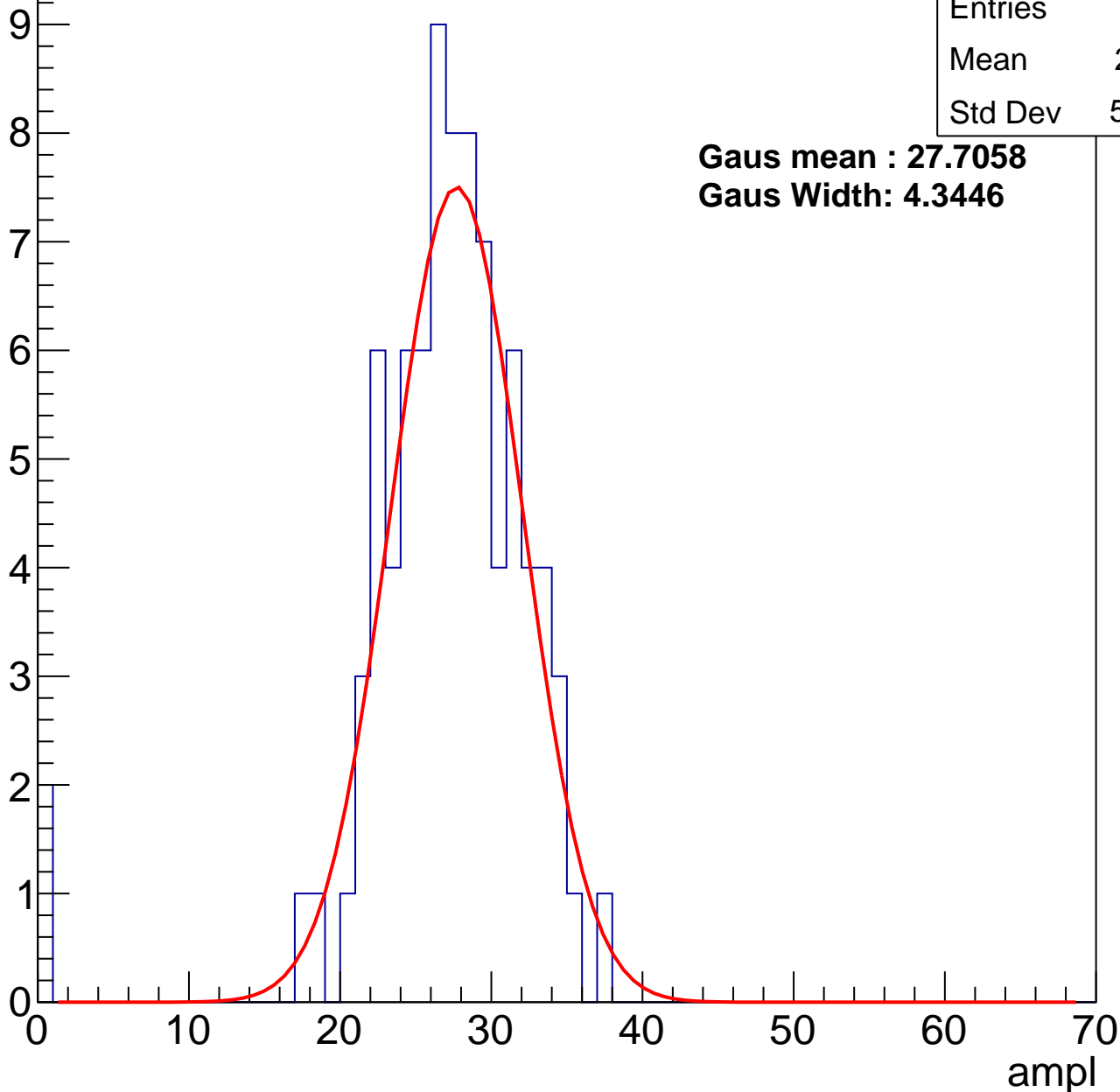
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	26.51
Std Dev	5.739

Gaus mean : 27.7058

Gaus Width: 4.3446



B0L102S, U5-ch122, adc1

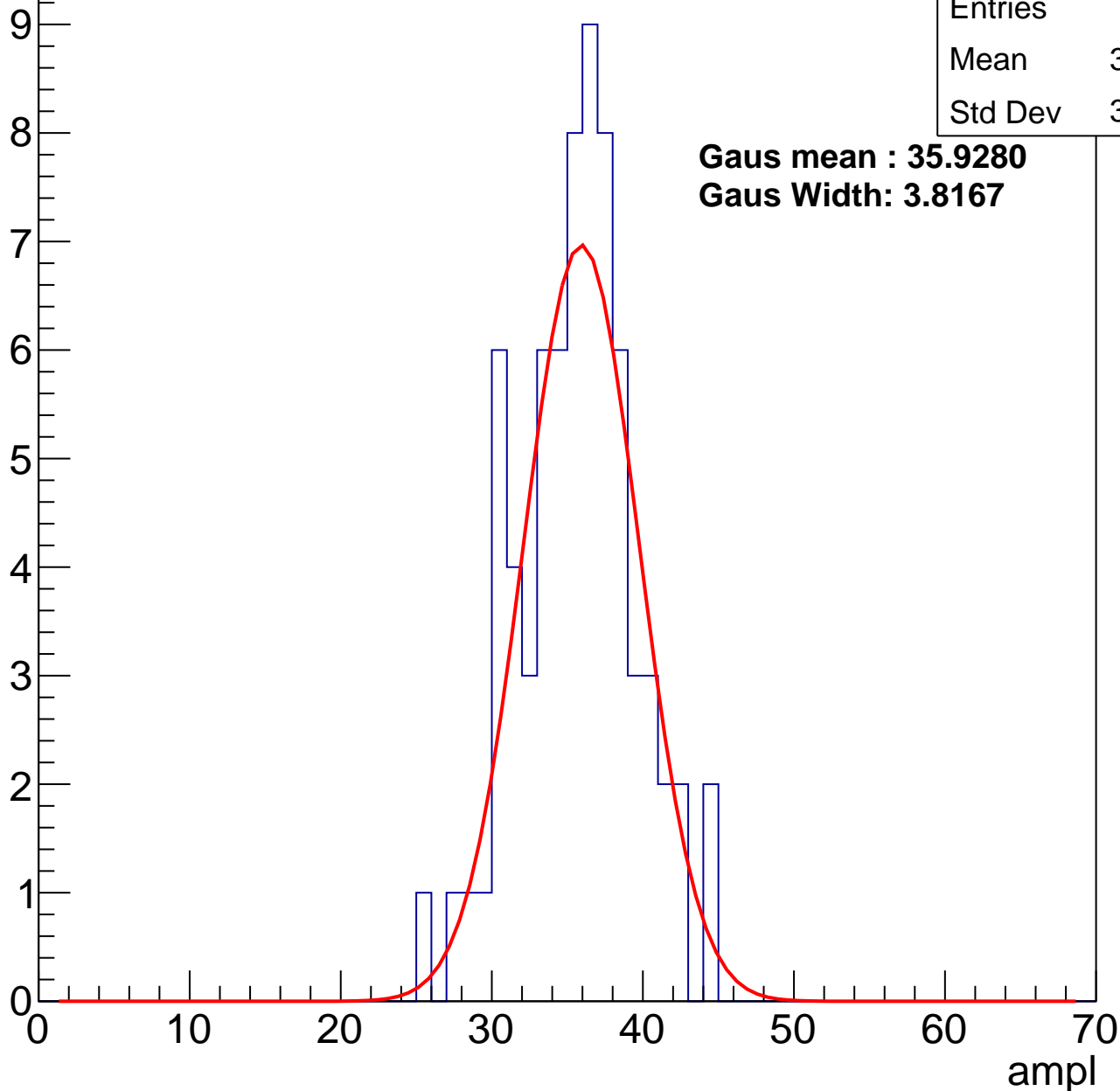
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	35.14
Std Dev	3.853

Gaus mean : 35.9280

Gaus Width: 3.8167



B0L102S, U5-ch122, adc2

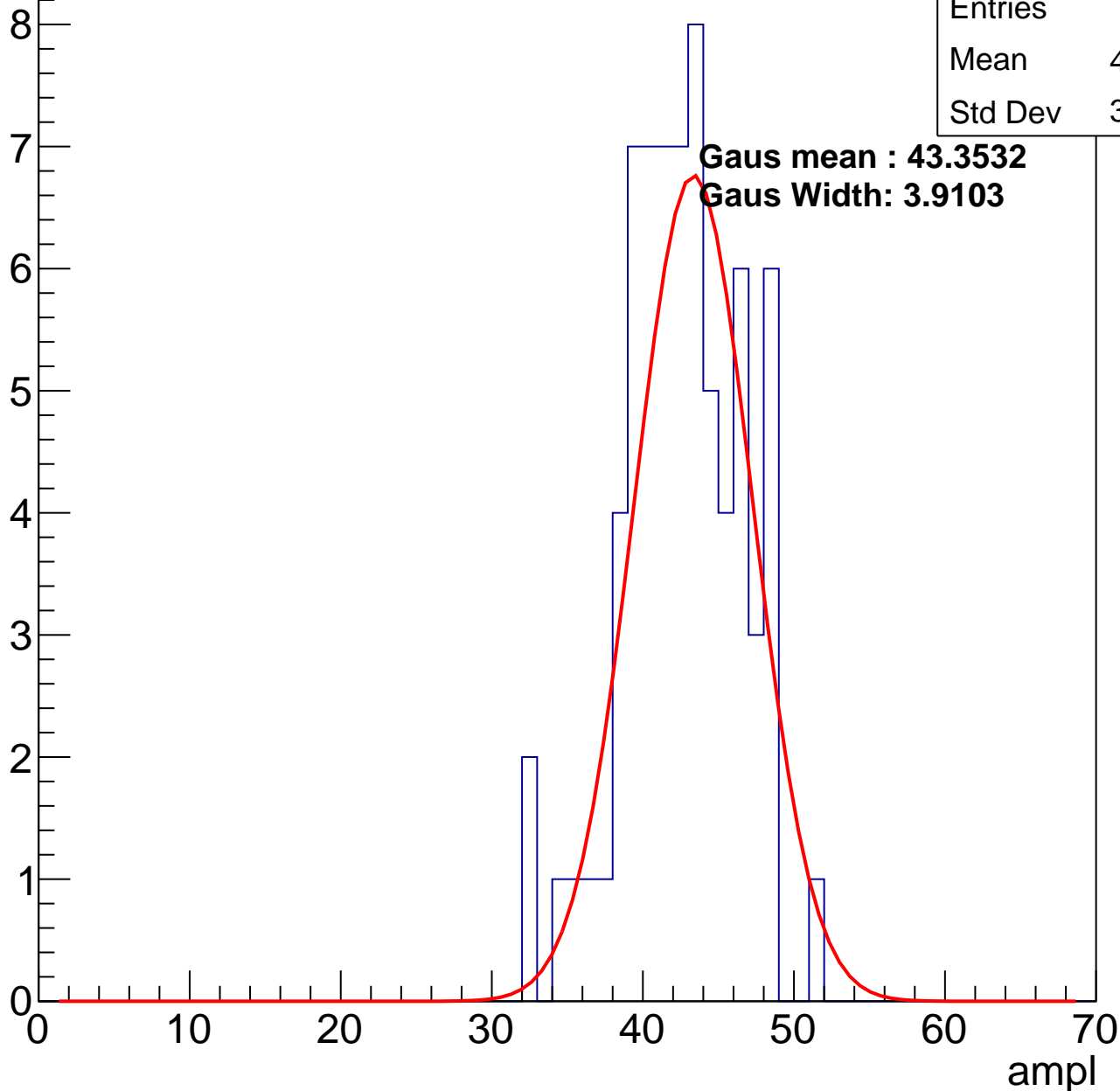
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	71
Mean	42.14
Std Dev	3.876

Gaus mean : 43.3532

Gaus Width: 3.9103

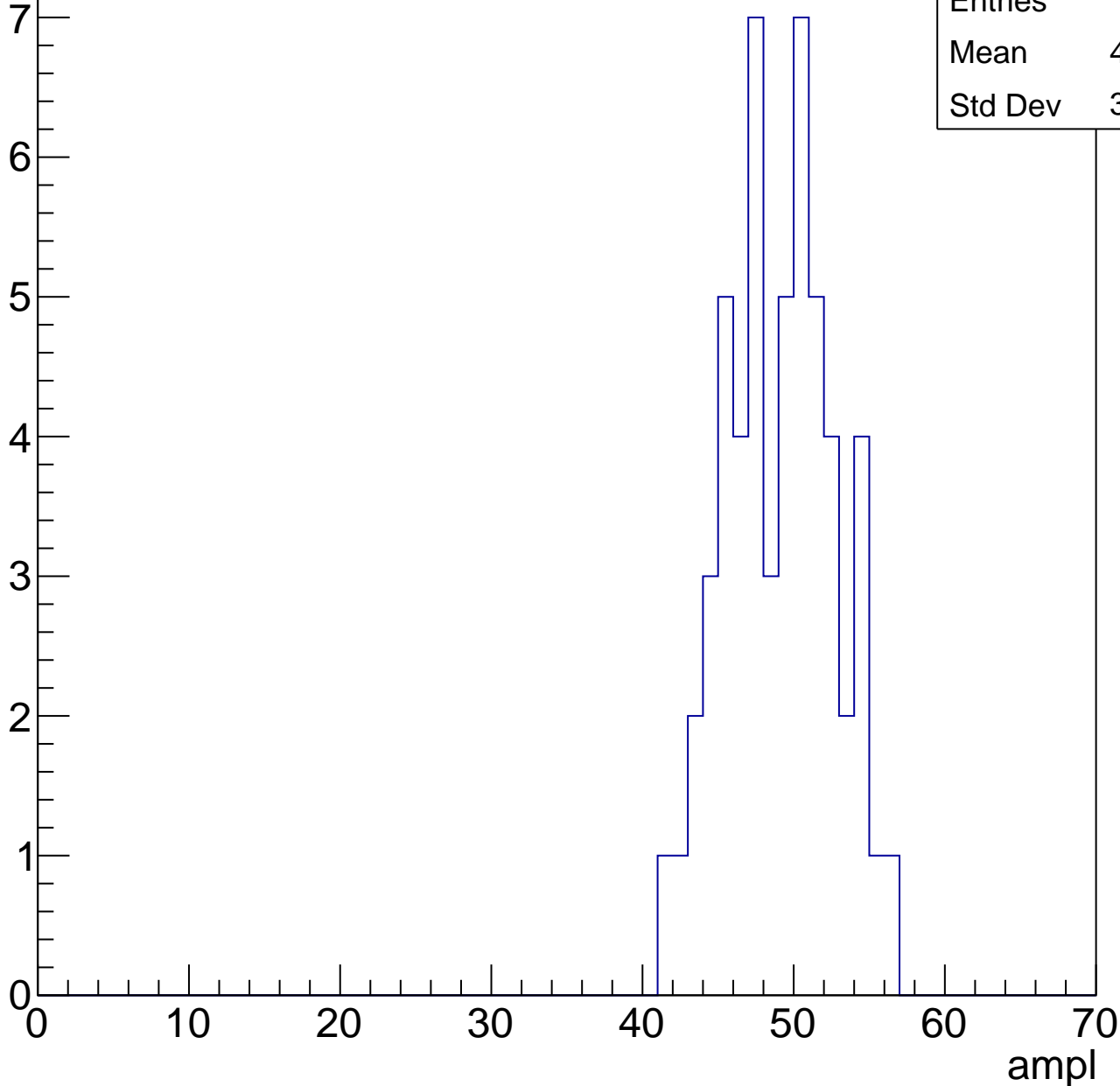


B0L102S, U5-ch122, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	55
Mean	48.62
Std Dev	3.514

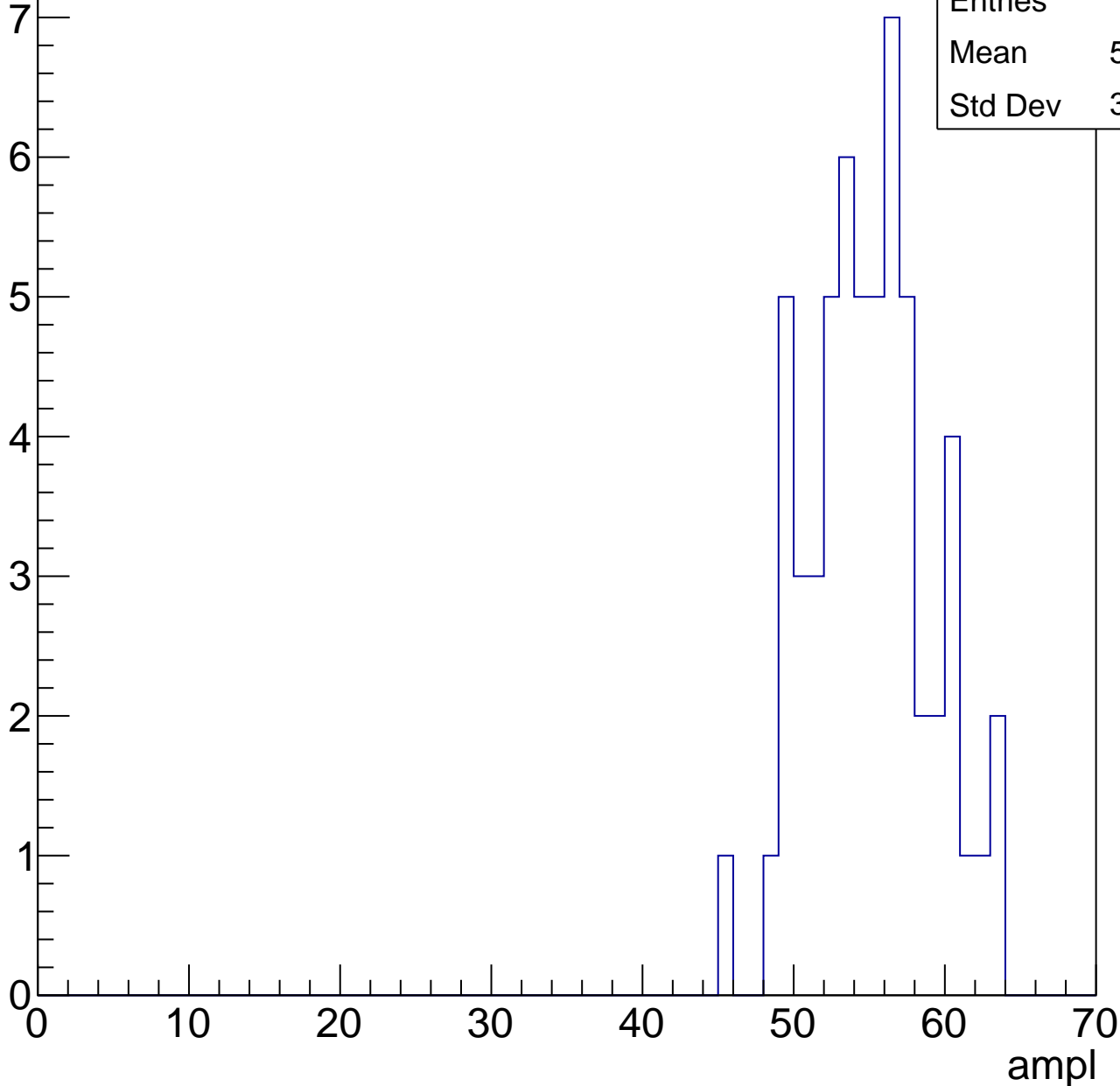


B0L102S, U5-ch122, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	54.55
Std Dev	3.949

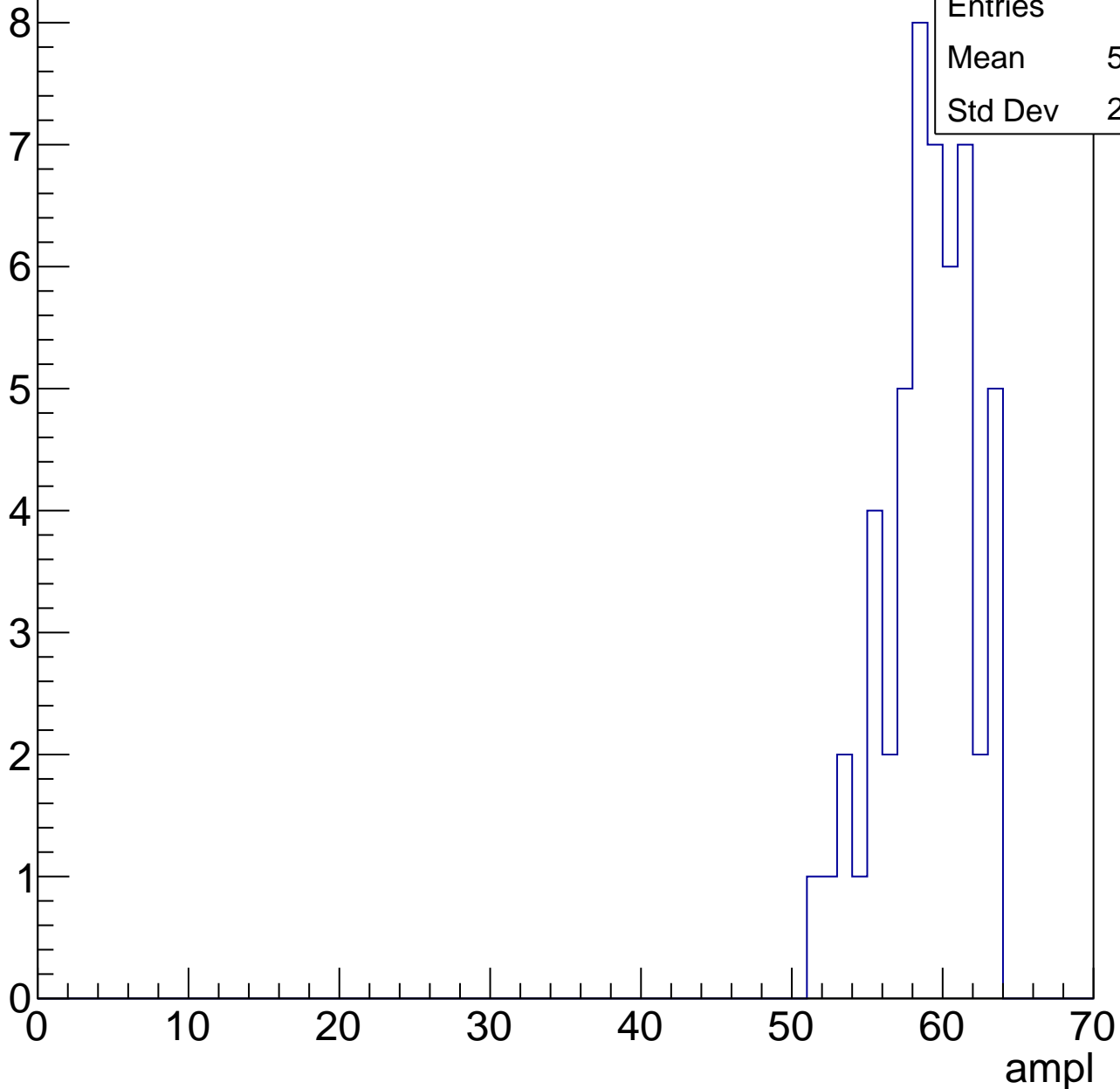


B0L102S, U5-ch122, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	51
Mean	58.49
Std Dev	2.933

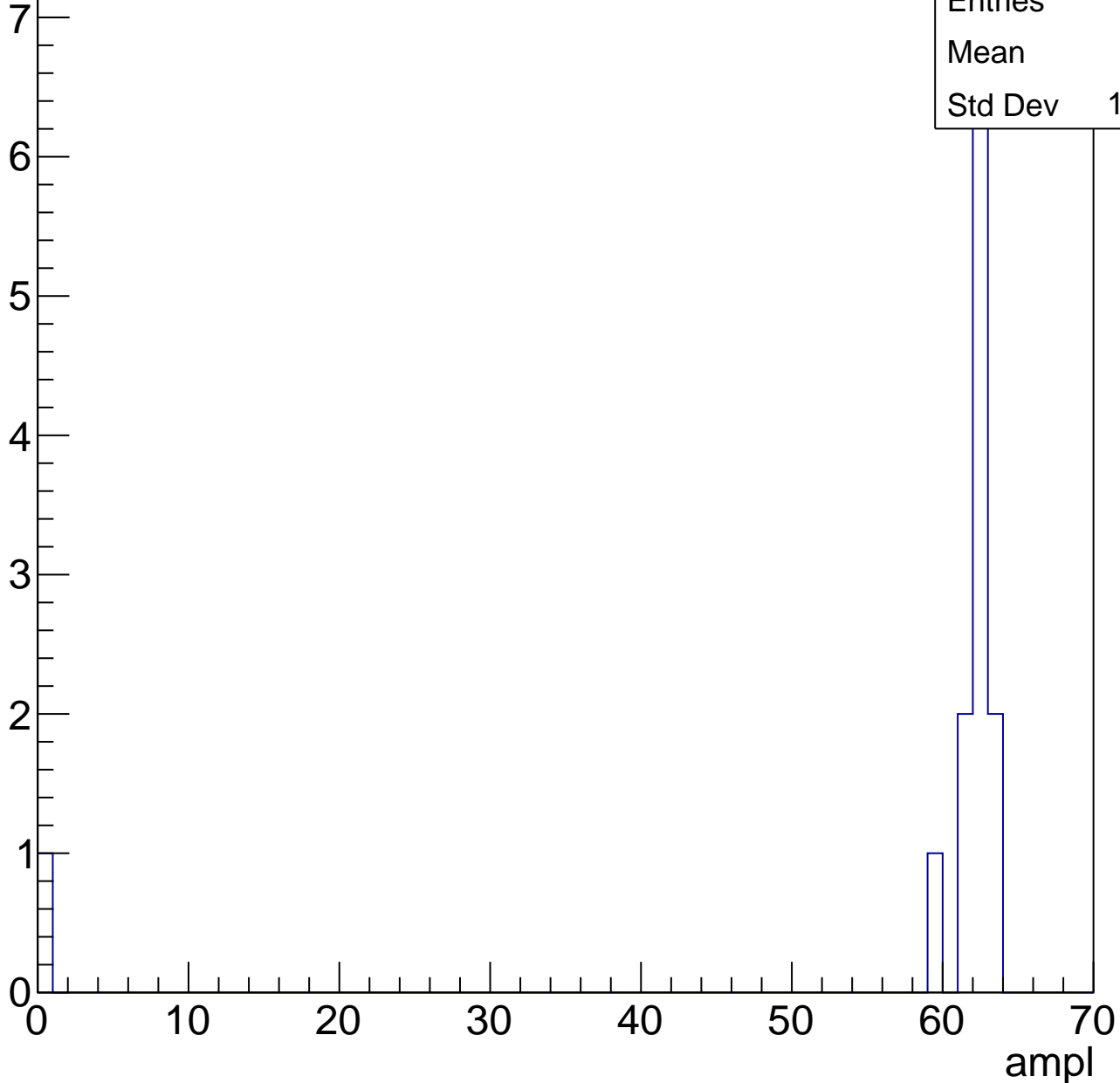


B0L102S, U5-ch122, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	13
Mean	57
Std Dev	16.48



B0L102S, U5-ch122, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch123, adc0

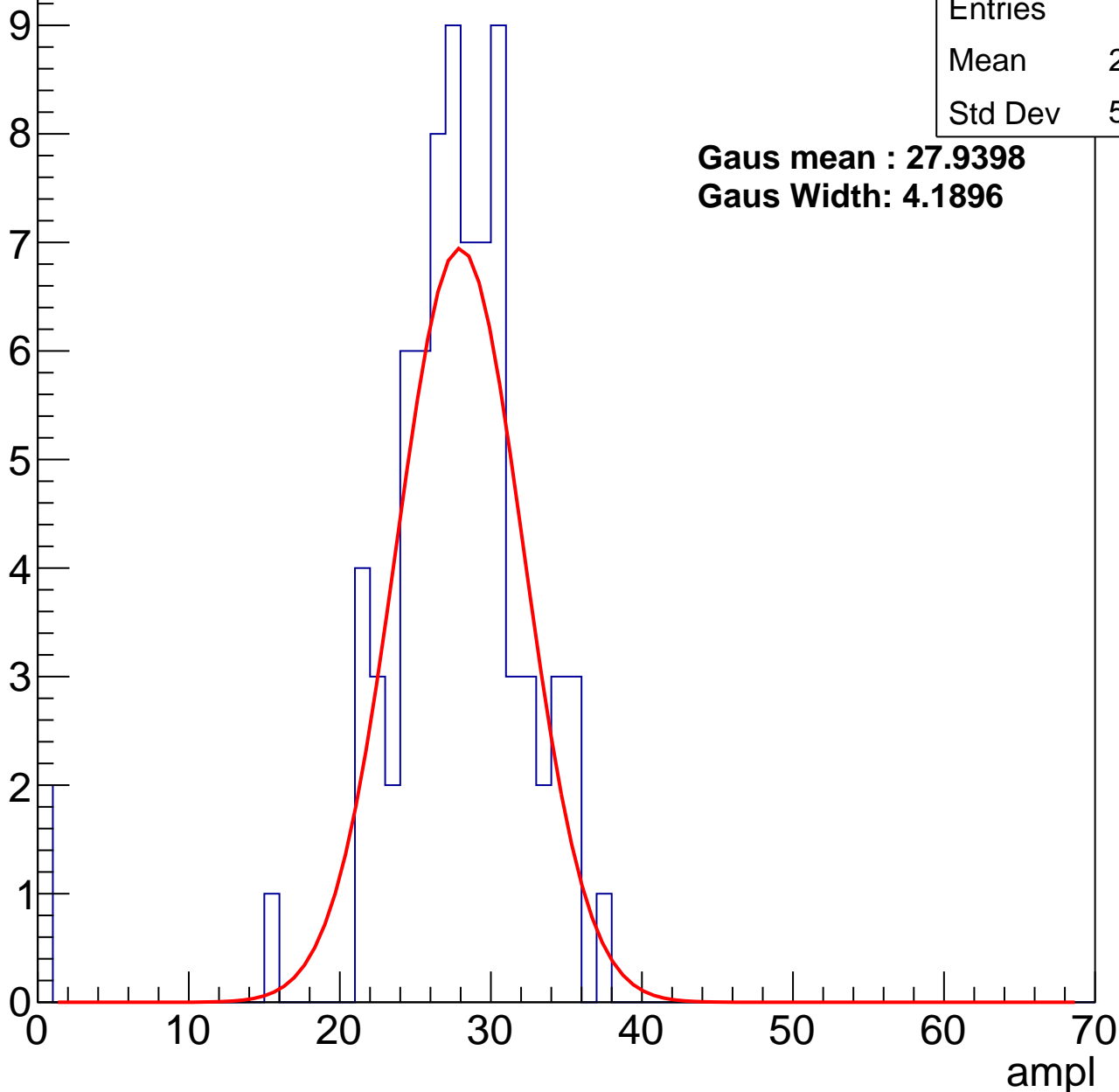
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	26.89
Std Dev	5.833

Gaus mean : 27.9398

Gaus Width: 4.1896



B0L102S, U5-ch123, adc1

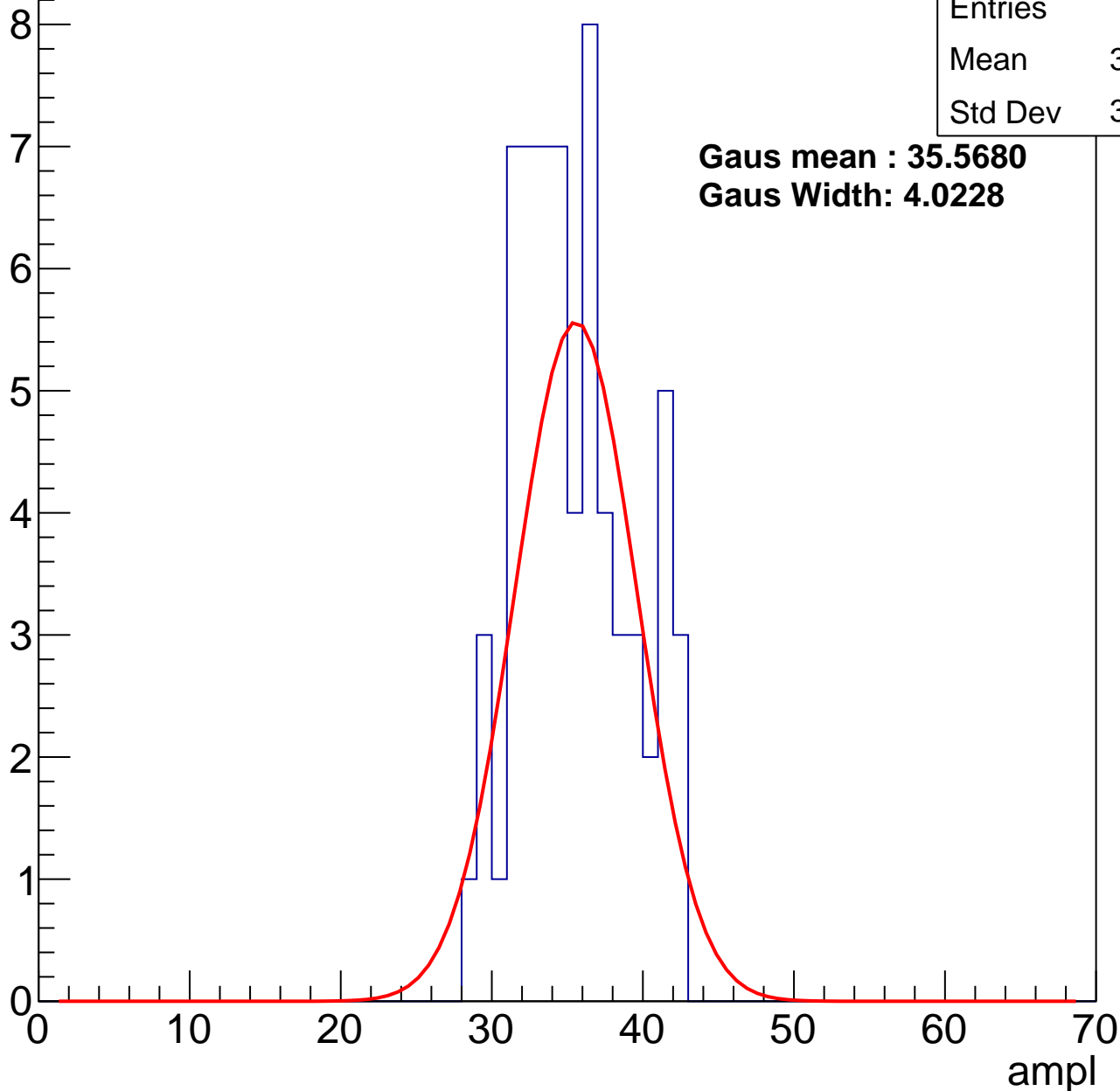
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	34.97
Std Dev	3.654

Gaus mean : 35.5680

Gaus Width: 4.0228



B0L102S, U5-ch123, adc2

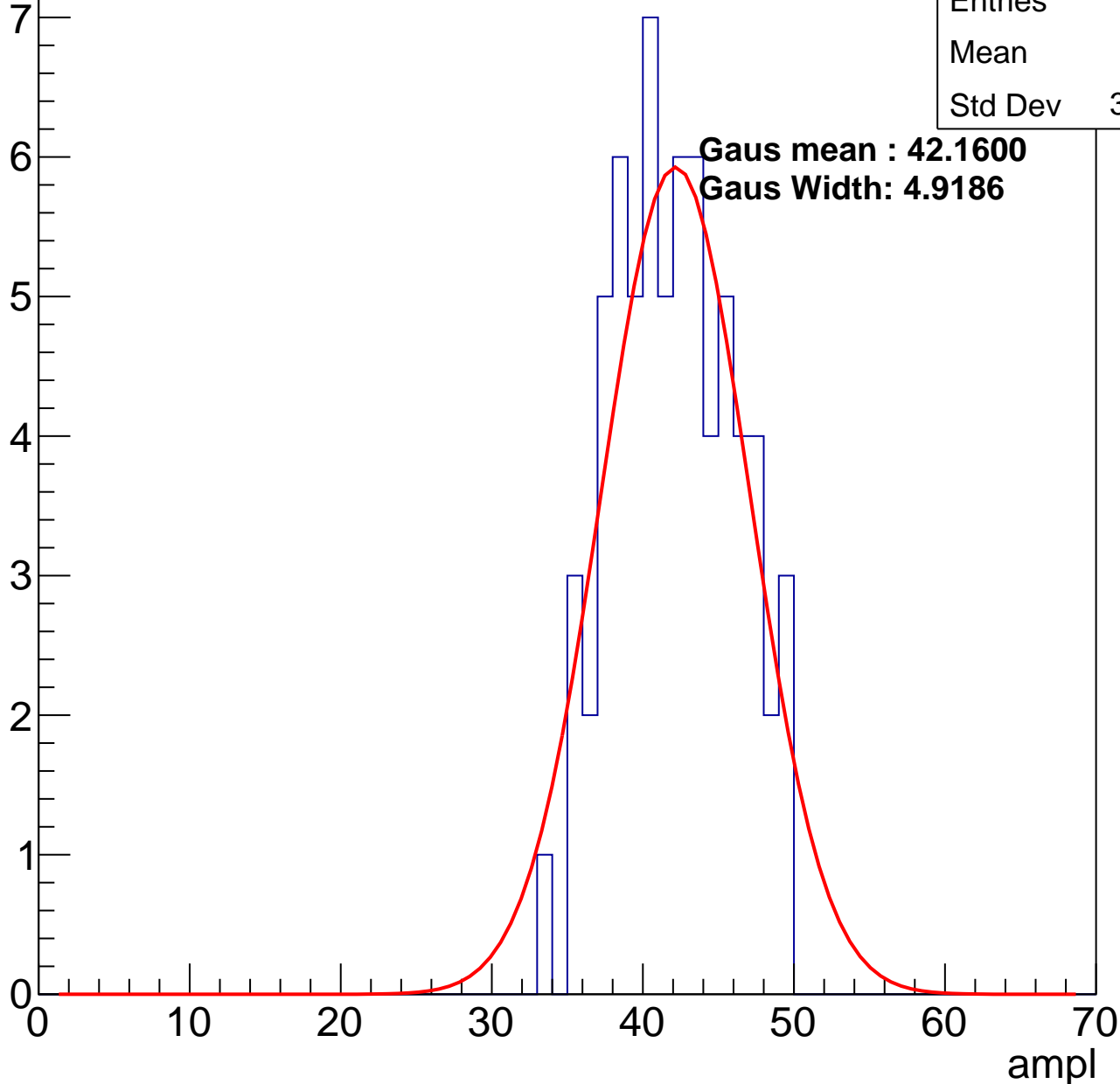
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	41.6
Std Dev	3.908

Gaus mean : 42.1600

Gaus Width: 4.9186

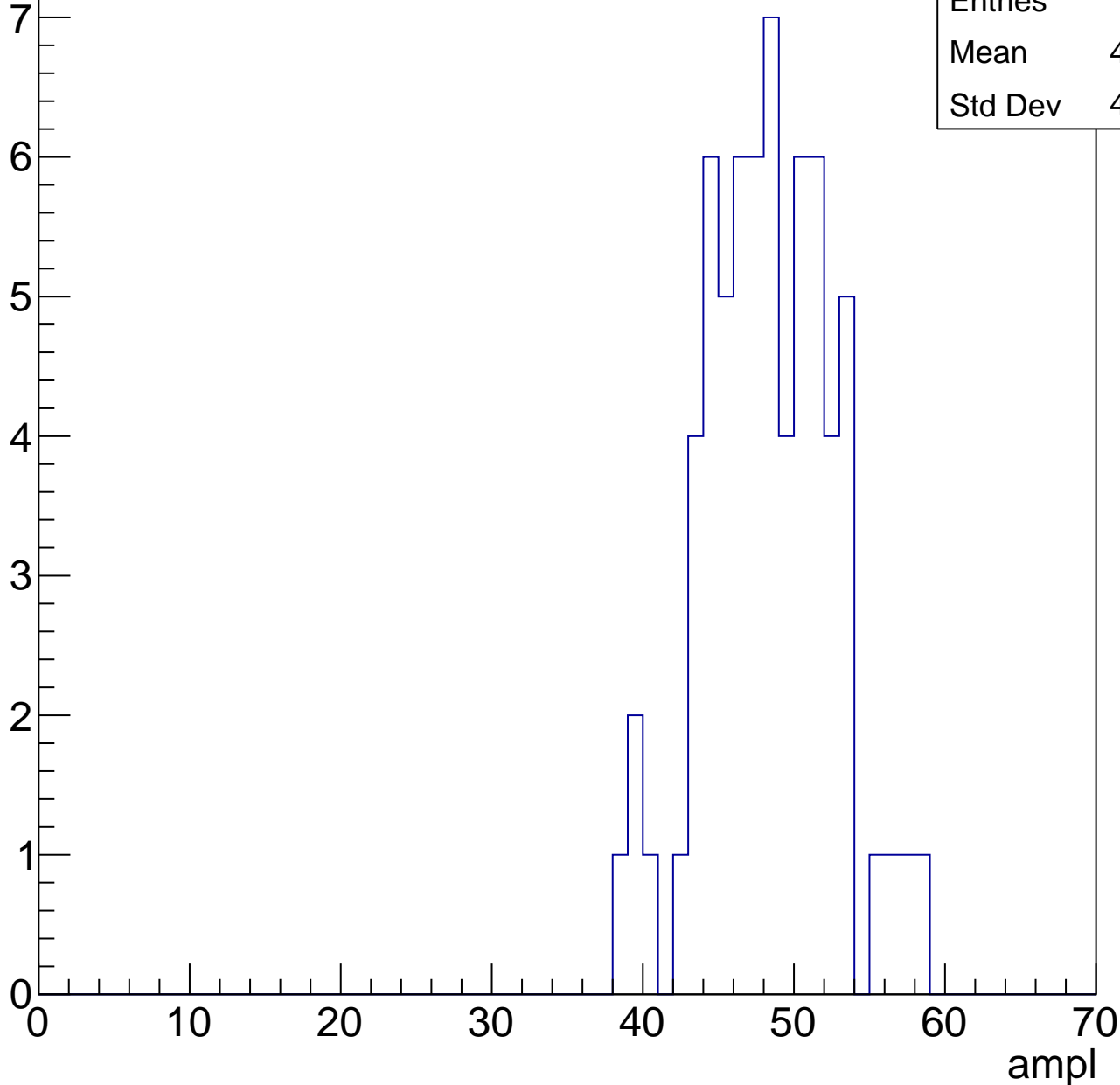


B0L102S, U5-ch123, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	68
Mean	47.85
Std Dev	4.195

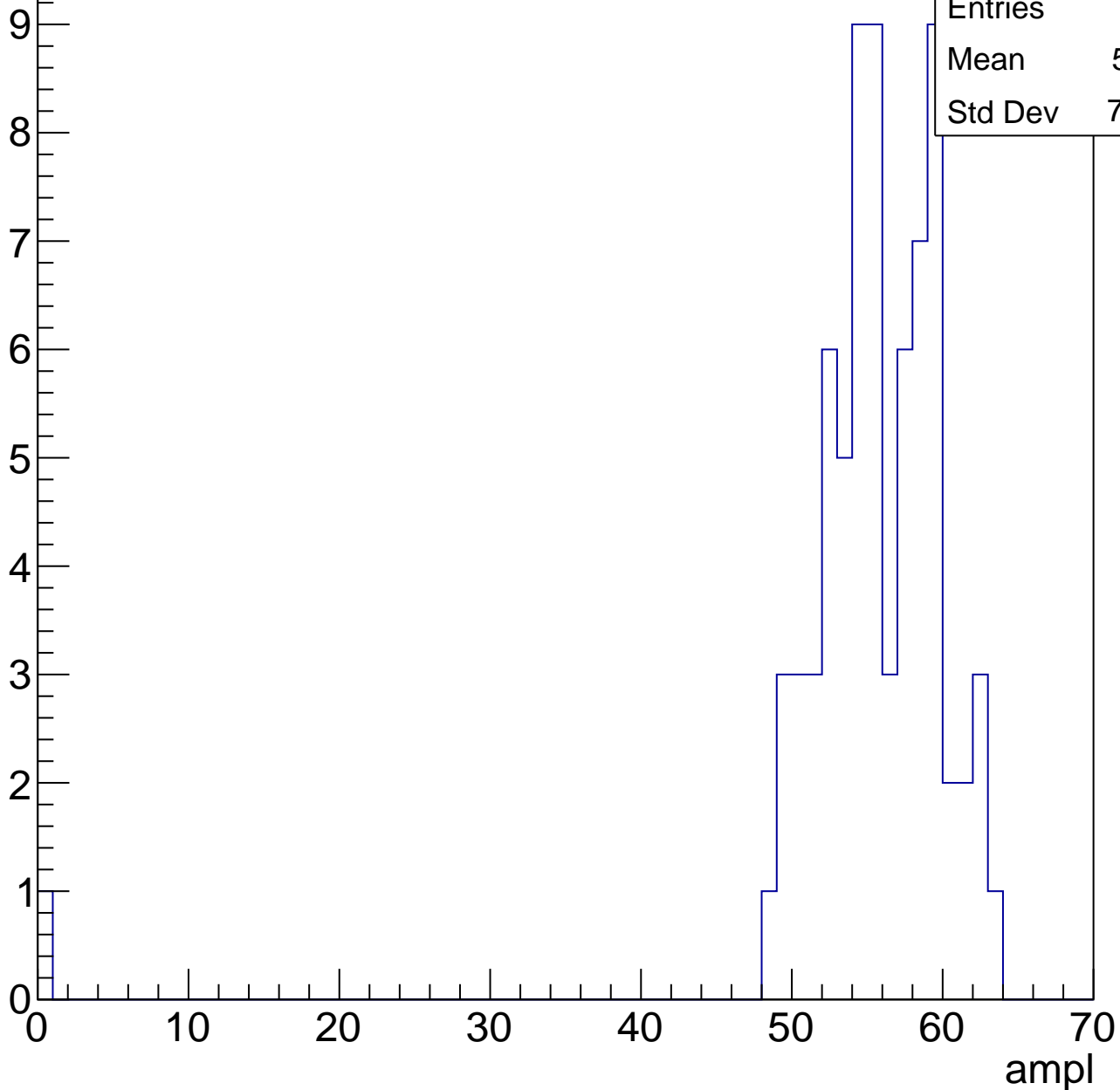


B0L102S, U5-ch123, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	54.71
Std Dev	7.355

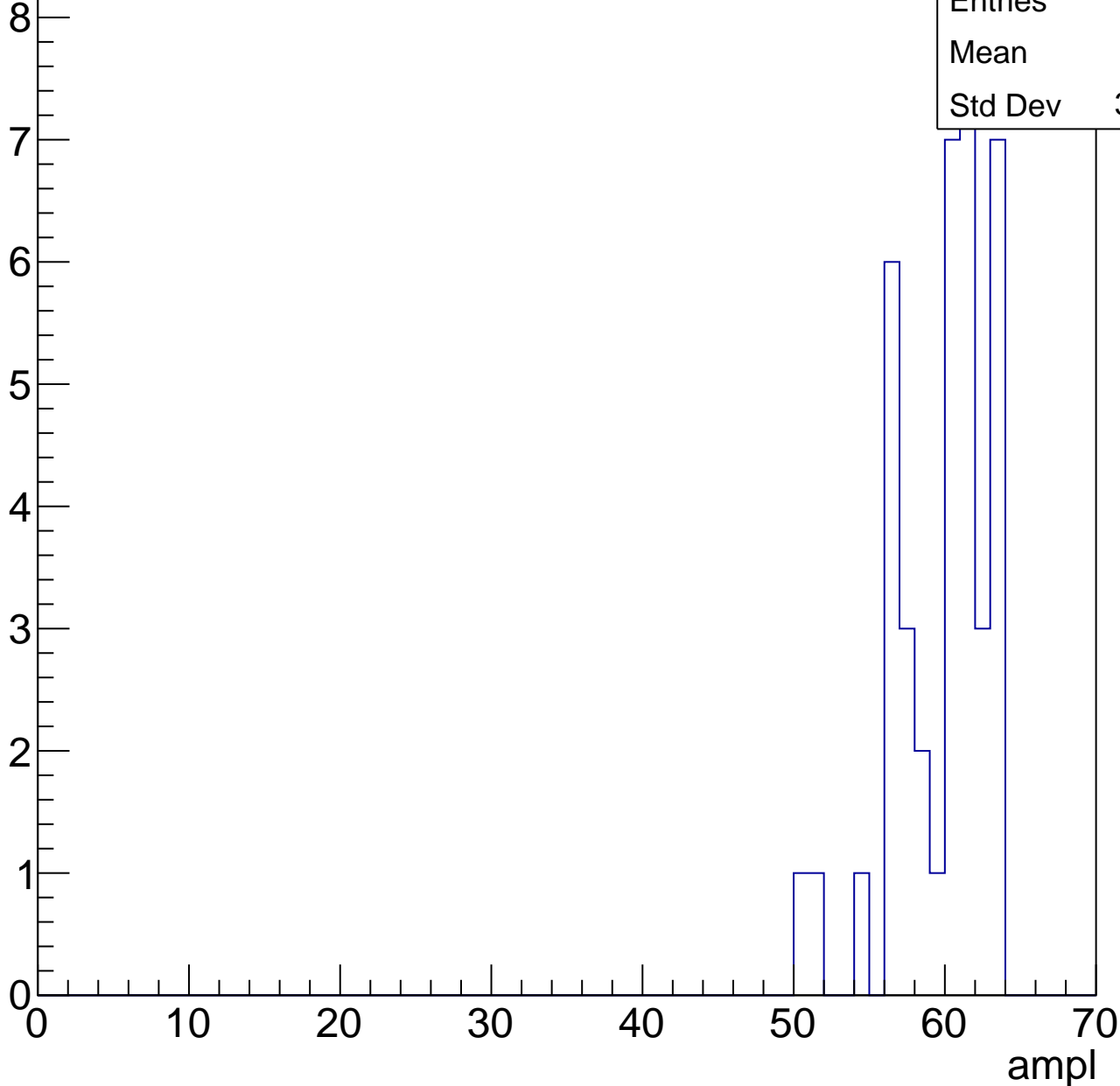


B0L102S, U5-ch123, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

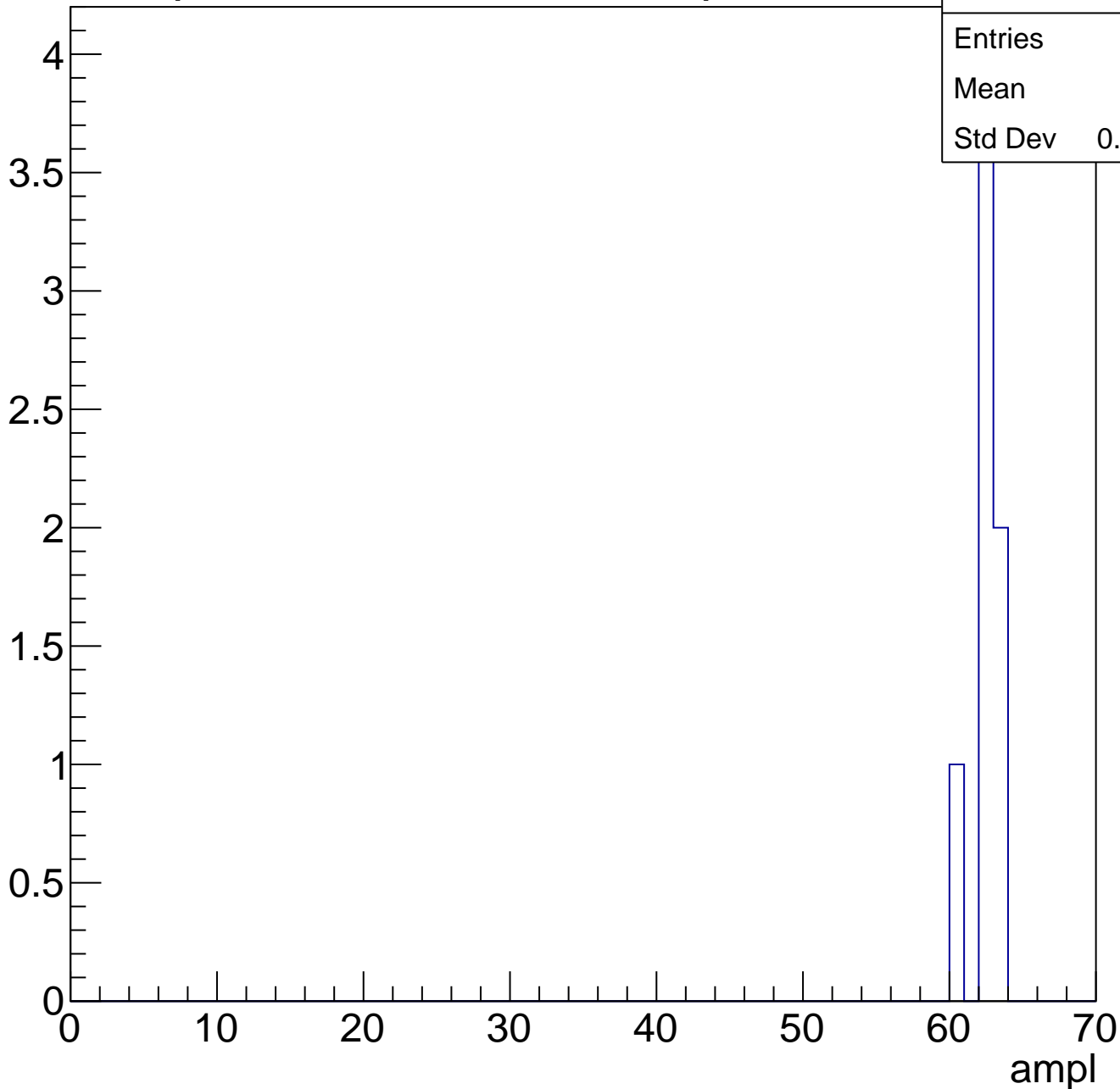
Entries	40
Mean	59.3
Std Dev	3.211



B0L102S, U5-ch123, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch123, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch124, adc0

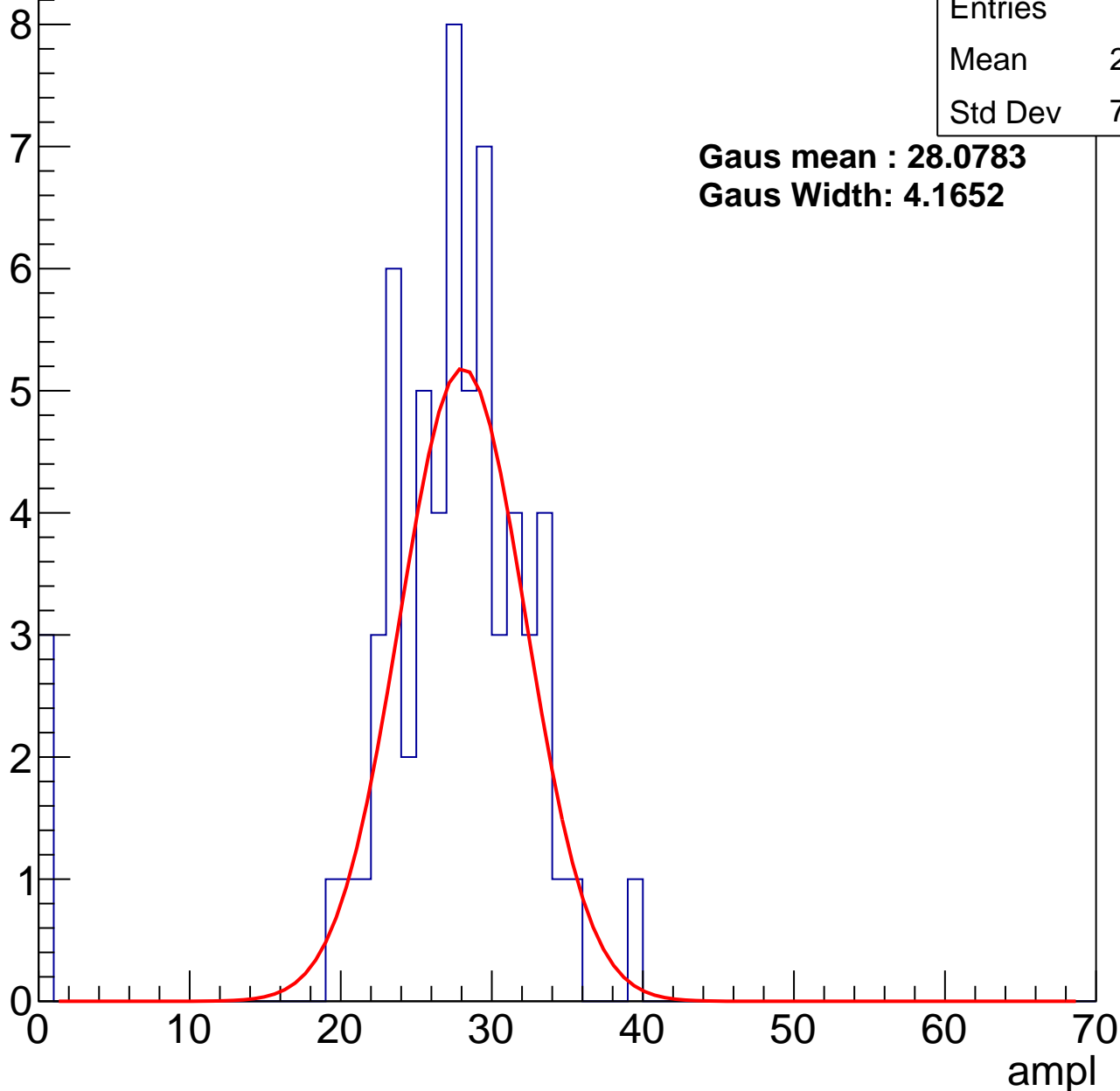
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	63
Mean	26.19
Std Dev	7.023

Gaus mean : 28.0783

Gaus Width: 4.1652



B0L102S, U5-ch124, adc1

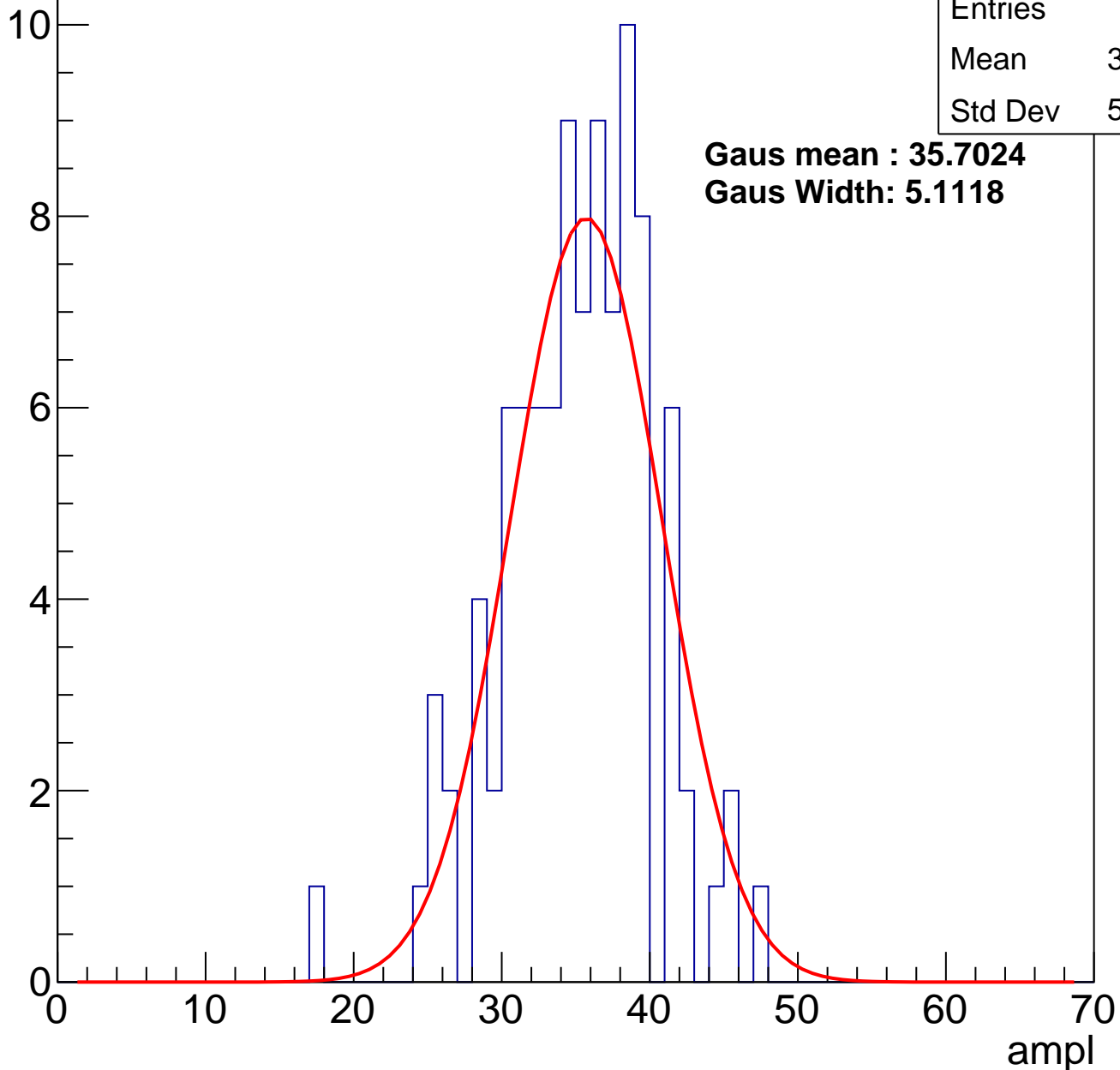
calib_packv5_042523_0143.root, FC#12, port B1

Entries	99
Mean	34.66
Std Dev	5.015

Gaus mean : 35.7024

Gaus Width: 5.1118

Entry



B0L102S, U5-ch124, adc2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	67
Mean	43.49
Std Dev	3.818

Gaus mean : 42.9099

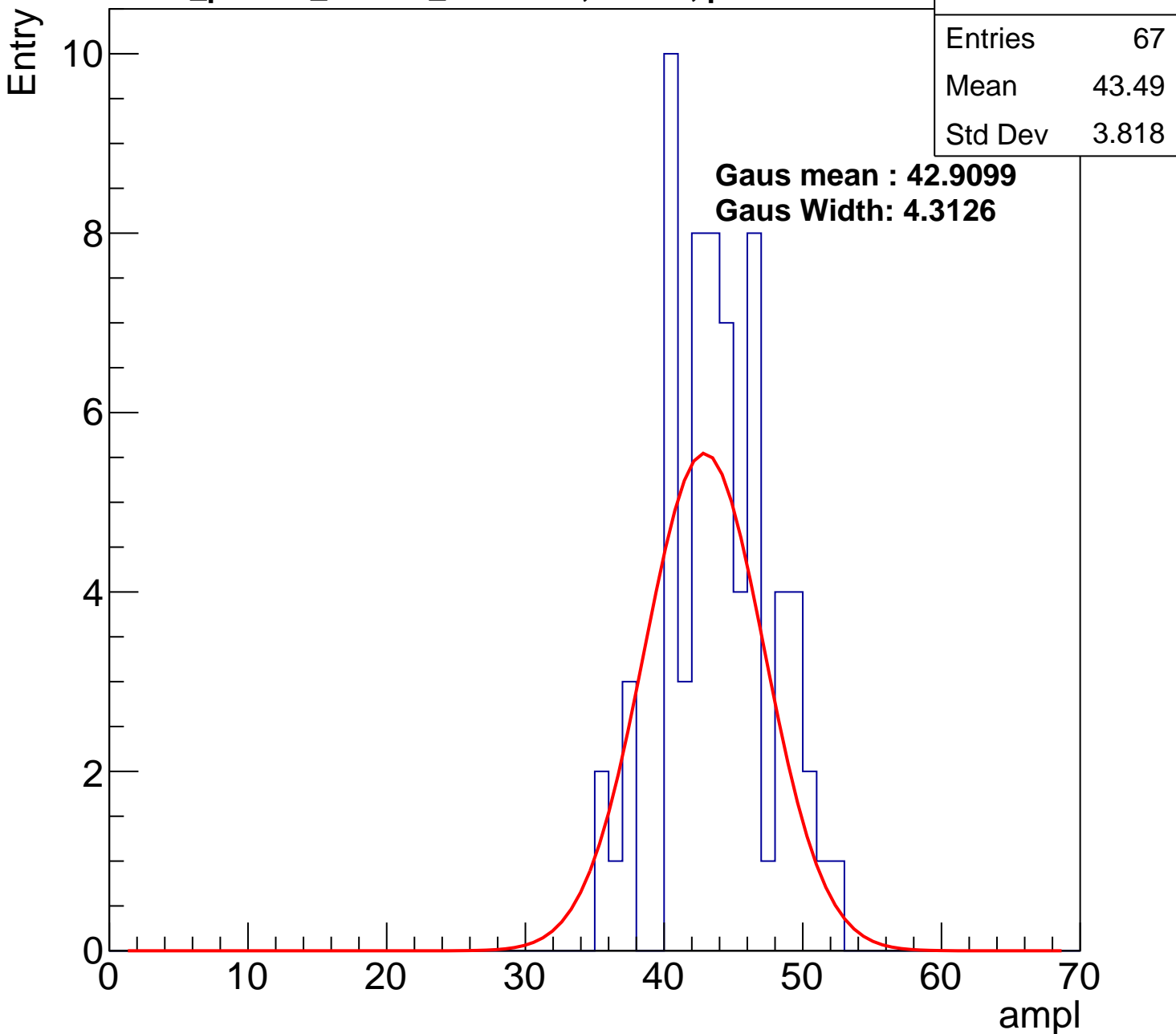
Gaus Width: 4.3126

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

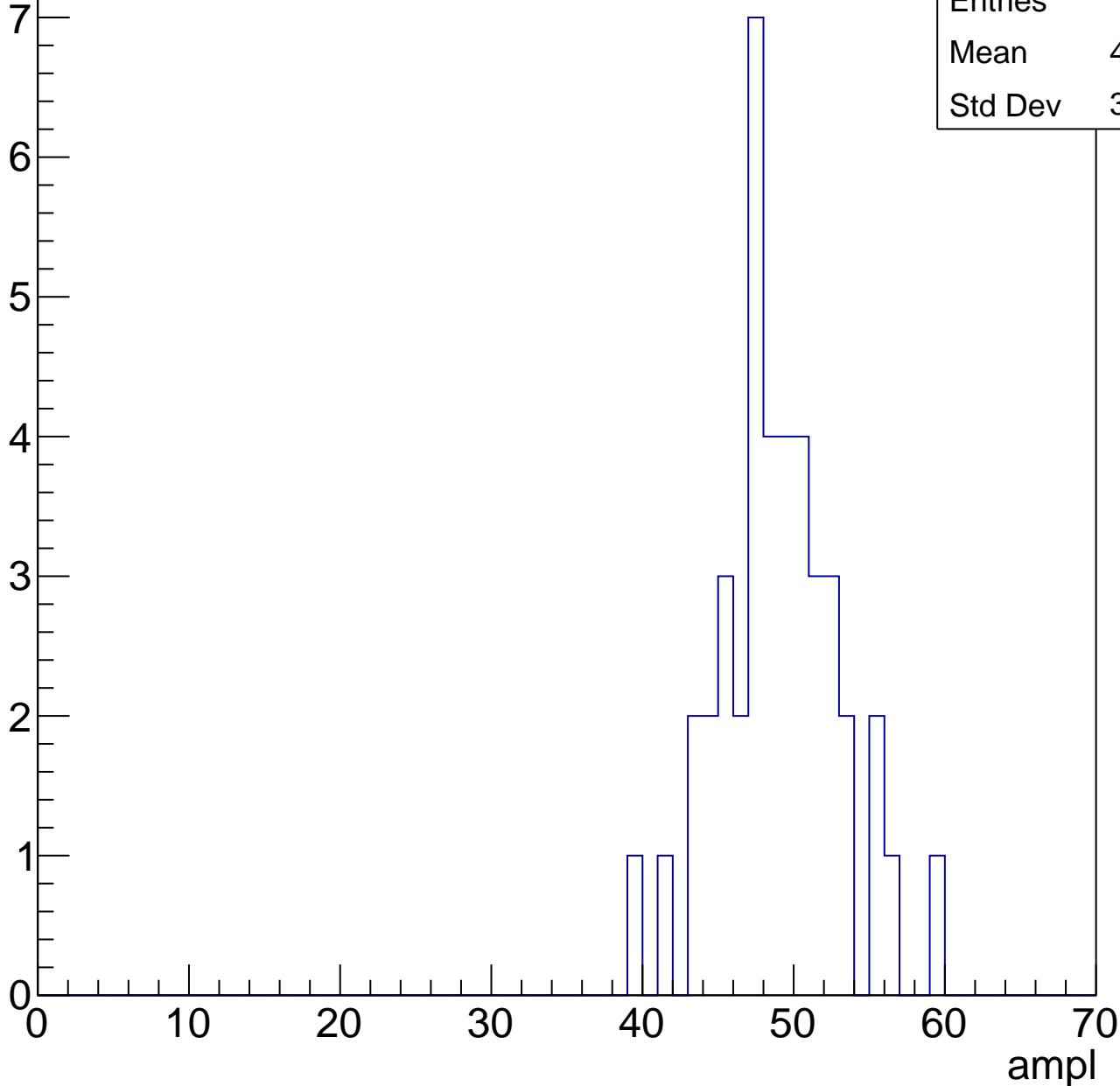


B0L102S, U5-ch124, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	42
Mean	48.52
Std Dev	3.995



B0L102S, U5-ch124, adc4

calib_packv5_042523_0143.root, FC#12, port B1

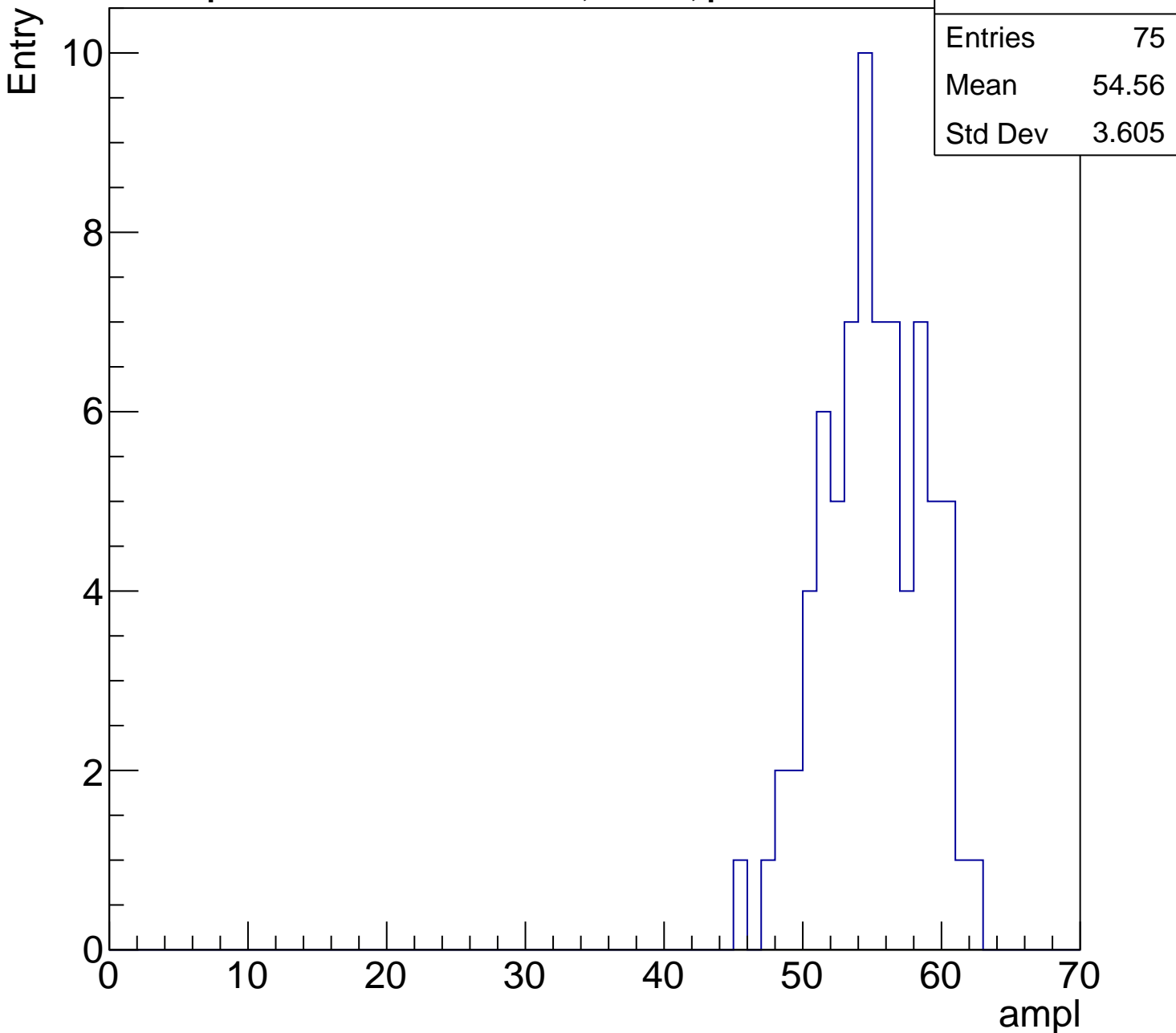
Entries	75
Mean	54.56
Std Dev	3.605

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

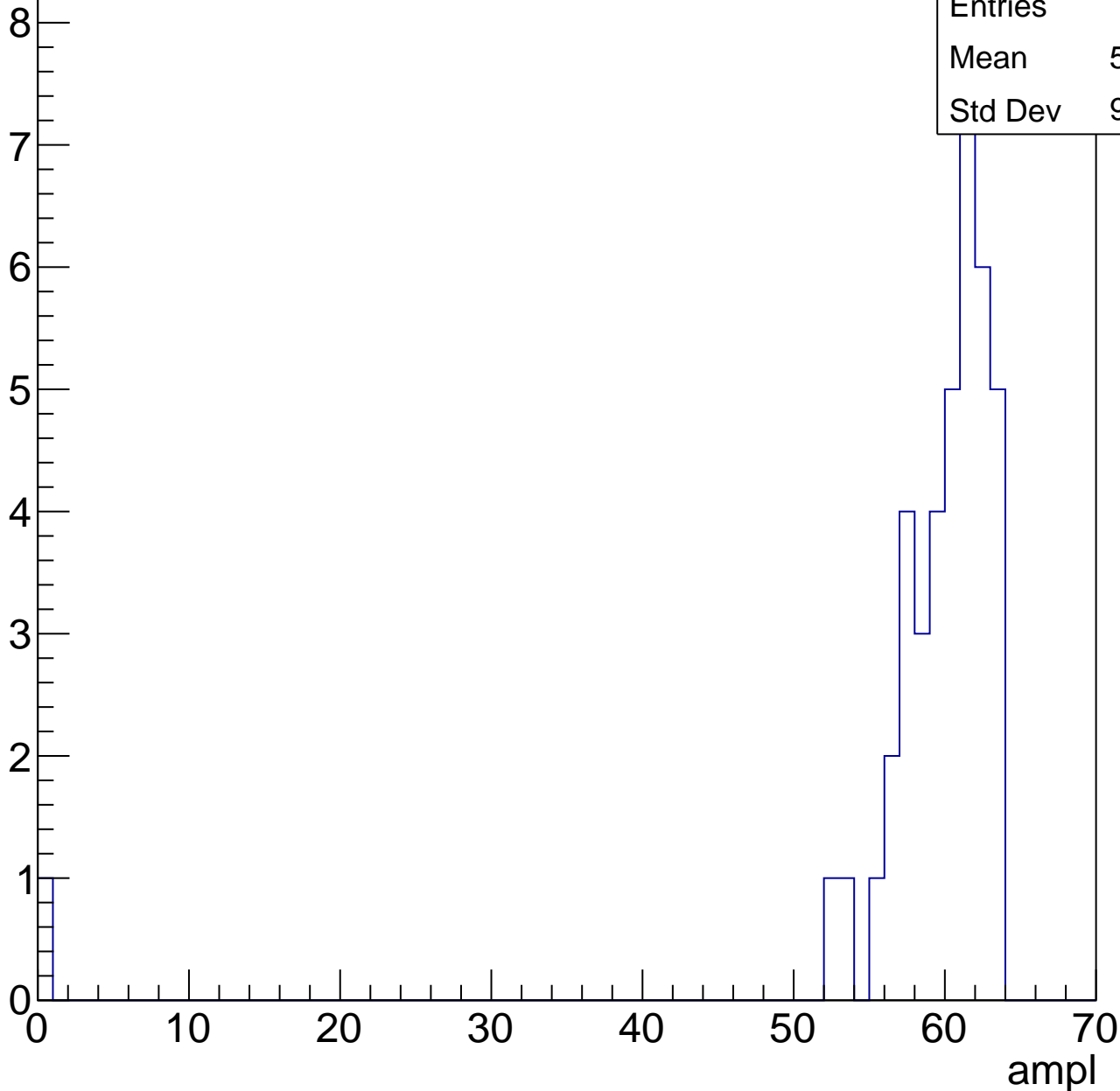


B0L102S, U5-ch124, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	41
Mean	58.17
Std Dev	9.579

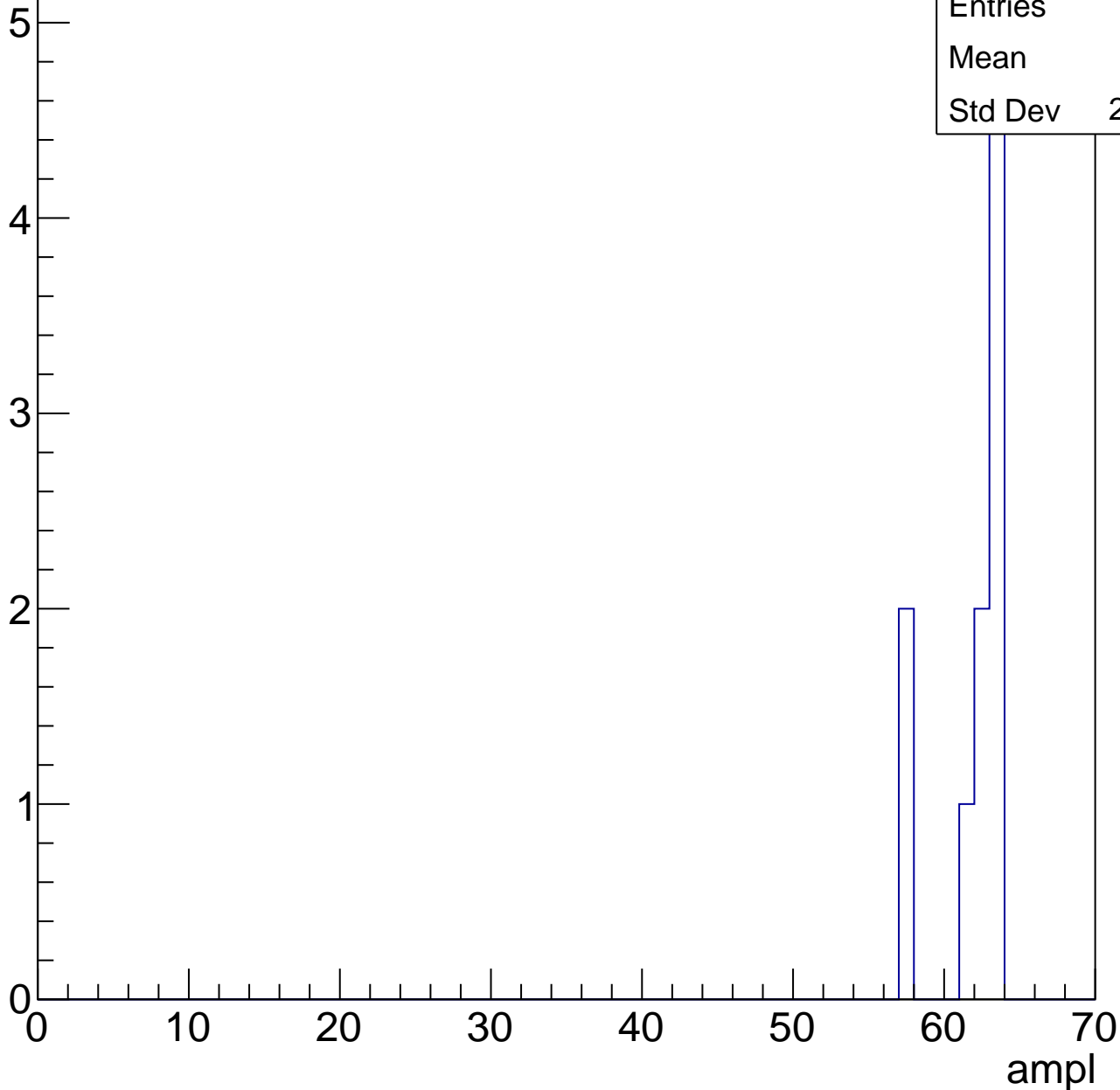


B0L102S, U5-ch124, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

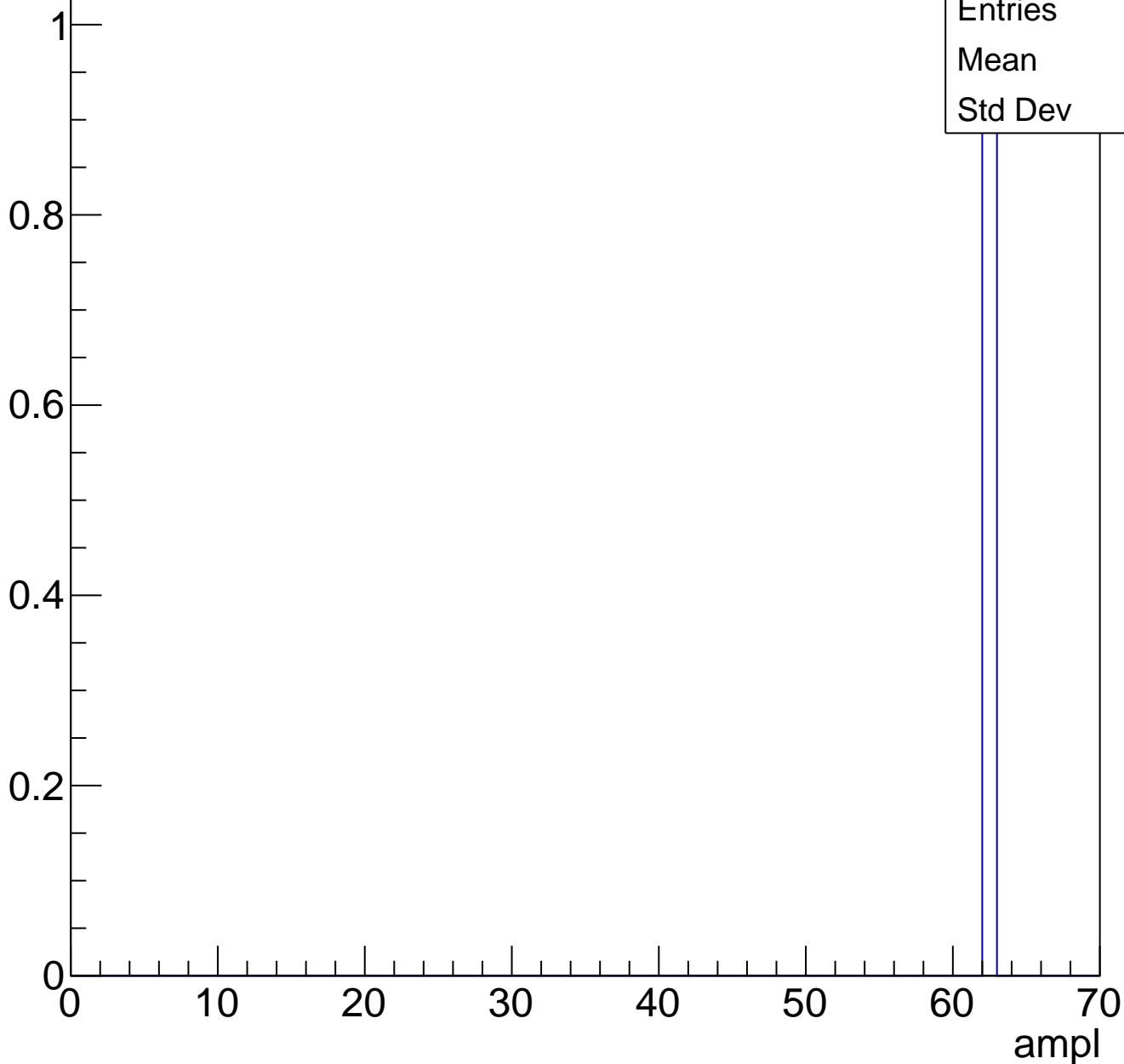
Entries	10
Mean	61.4
Std Dev	2.289



B0L102S, U5-ch124, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch125, adc0

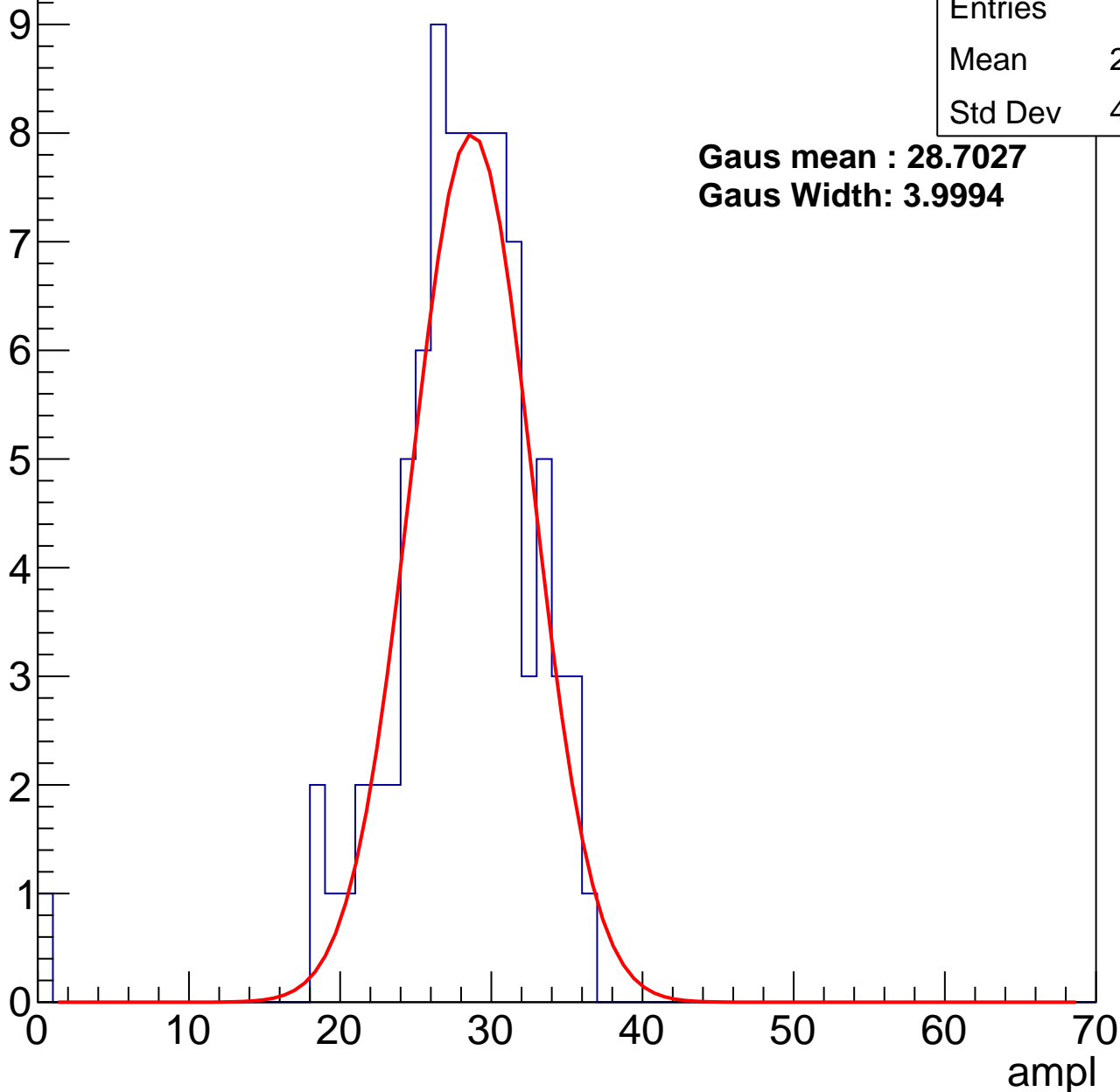
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	85
Mean	27.58
Std Dev	4.978

Gaus mean : 28.7027

Gaus Width: 3.9994



B0L102S, U5-ch125, adc1

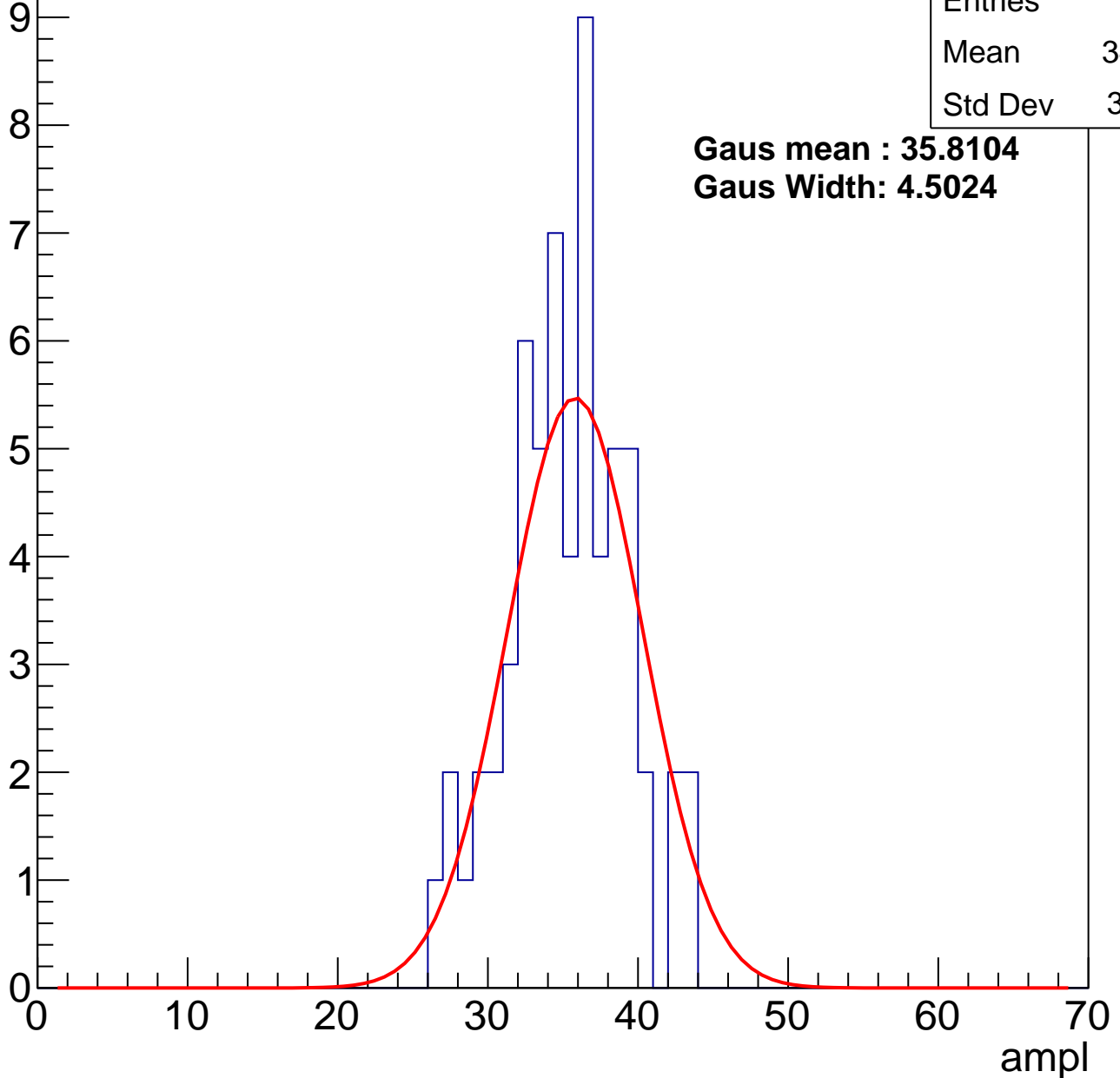
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	62
Mean	34.85
Std Dev	3.881

Gaus mean : 35.8104

Gaus Width: 4.5024



B0L102S, U5-ch125, adc2

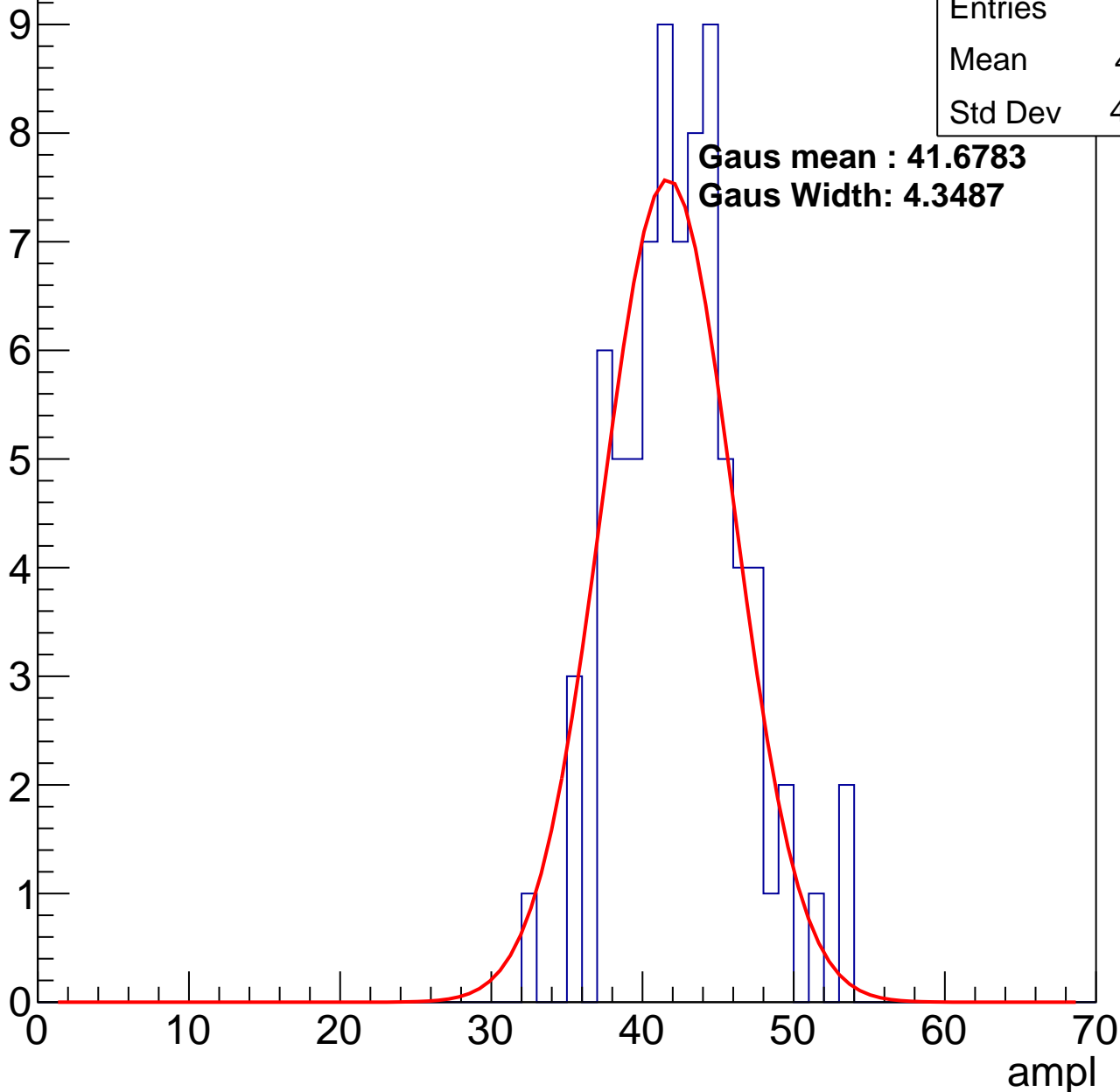
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	42.11
Std Dev	4.006

Gaus mean : 41.6783

Gaus Width: 4.3487

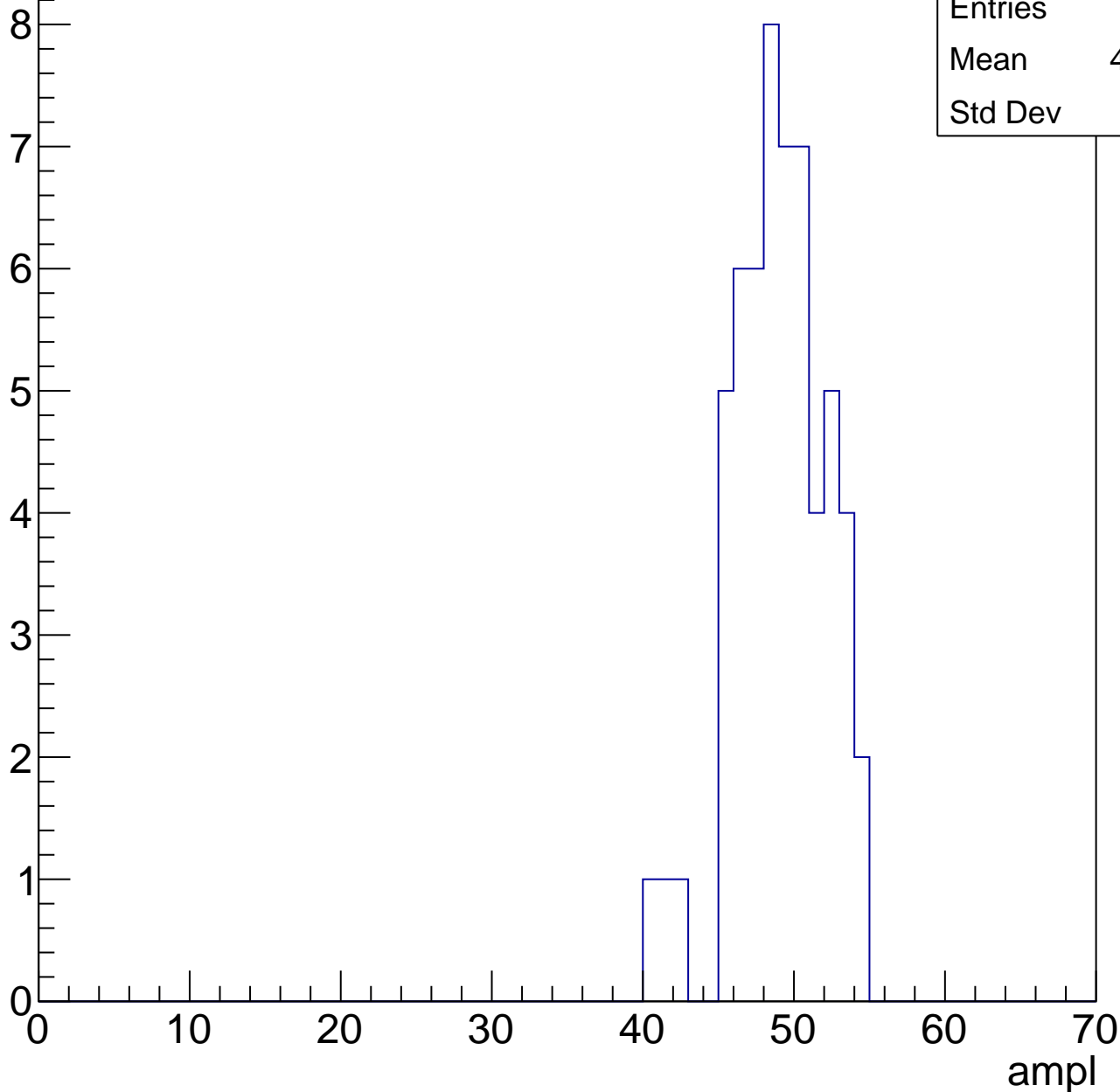


B0L102S, U5-ch125, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	57
Mean	48.54
Std Dev	3.05

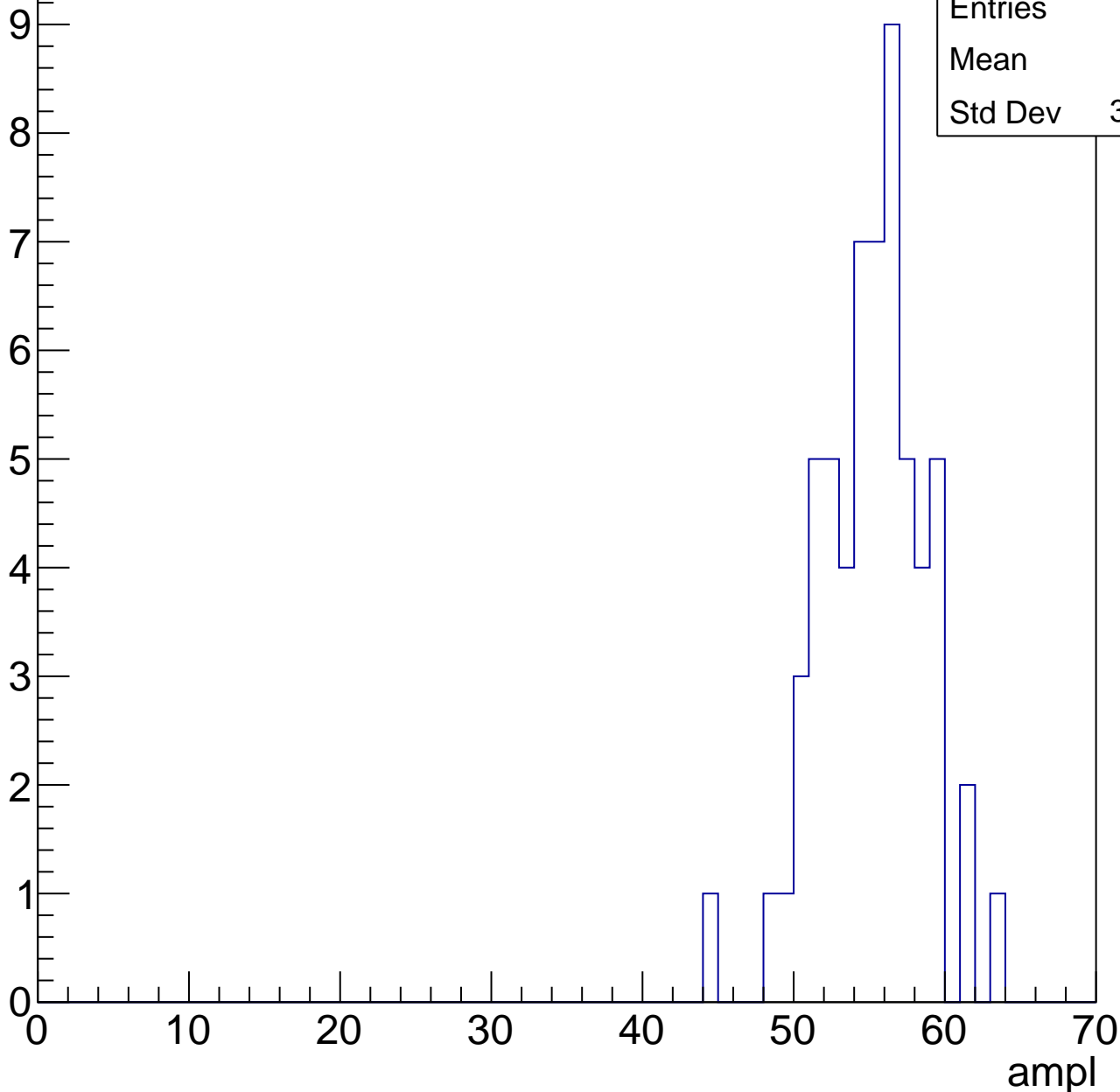


B0L102S, U5-ch125, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	60
Mean	54.7
Std Dev	3.427

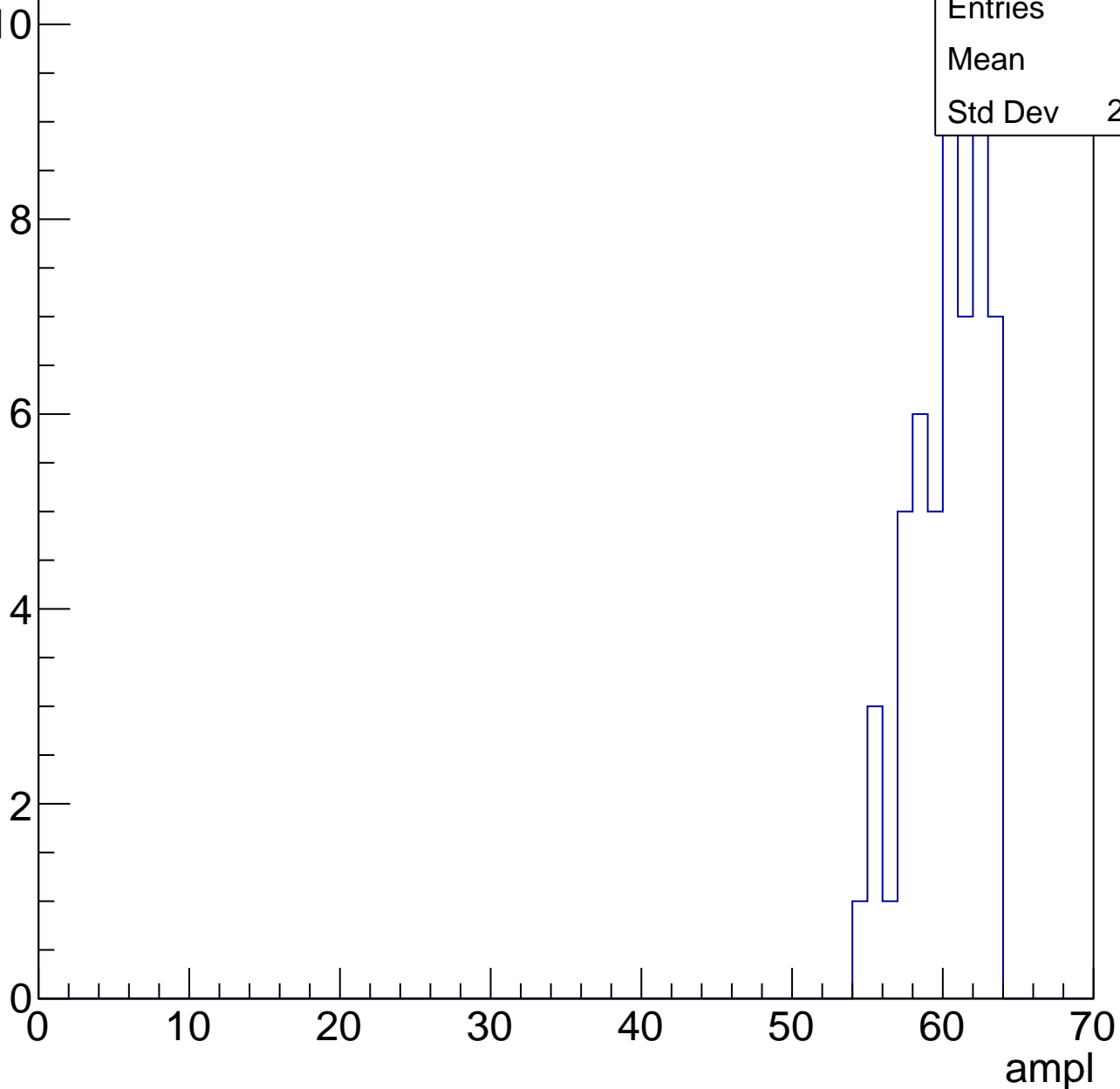


B0L102S, U5-ch125, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	54
Mean	59.8
Std Dev	2.376



B0L102S, U5-ch125, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B0L102S, U5-ch125, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



B0L102S, U5-ch126, adc0

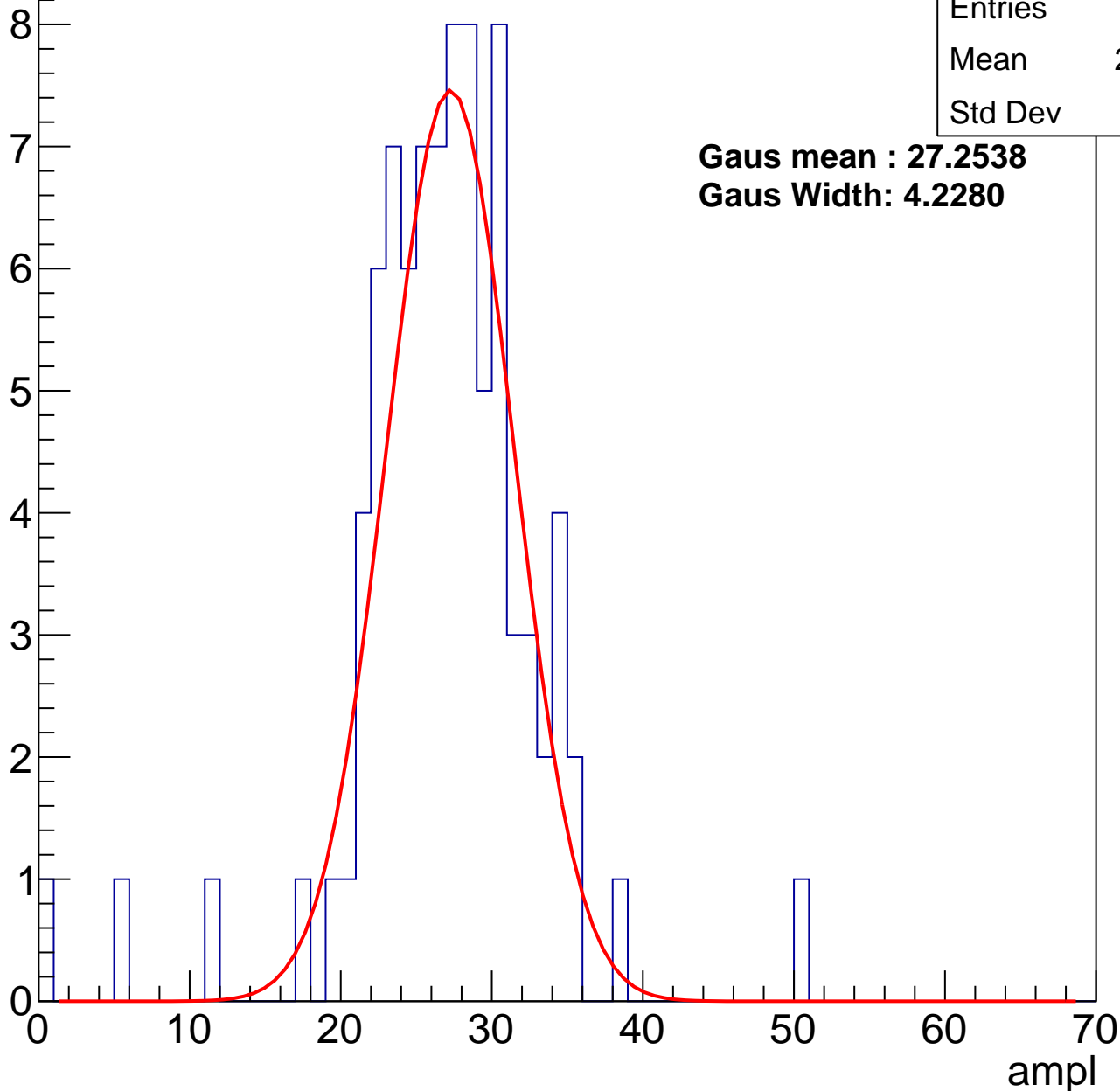
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	88
Mean	26.41
Std Dev	6.23

Gaus mean : 27.2538

Gaus Width: 4.2280



B0L102S, U5-ch126, adc1

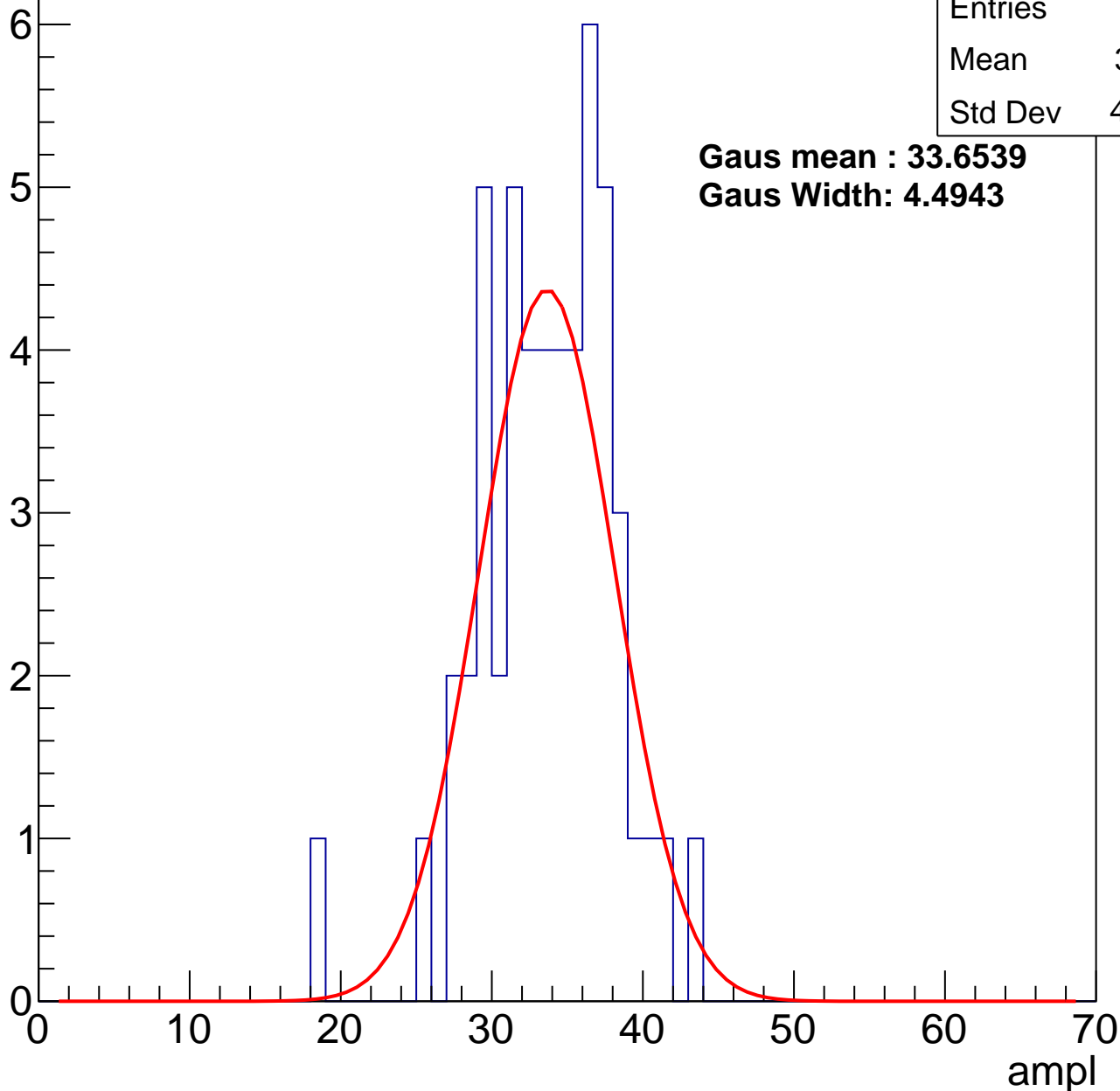
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	52
Mean	33.21
Std Dev	4.404

Gaus mean : 33.6539

Gaus Width: 4.4943



B0L102S, U5-ch126, adc2

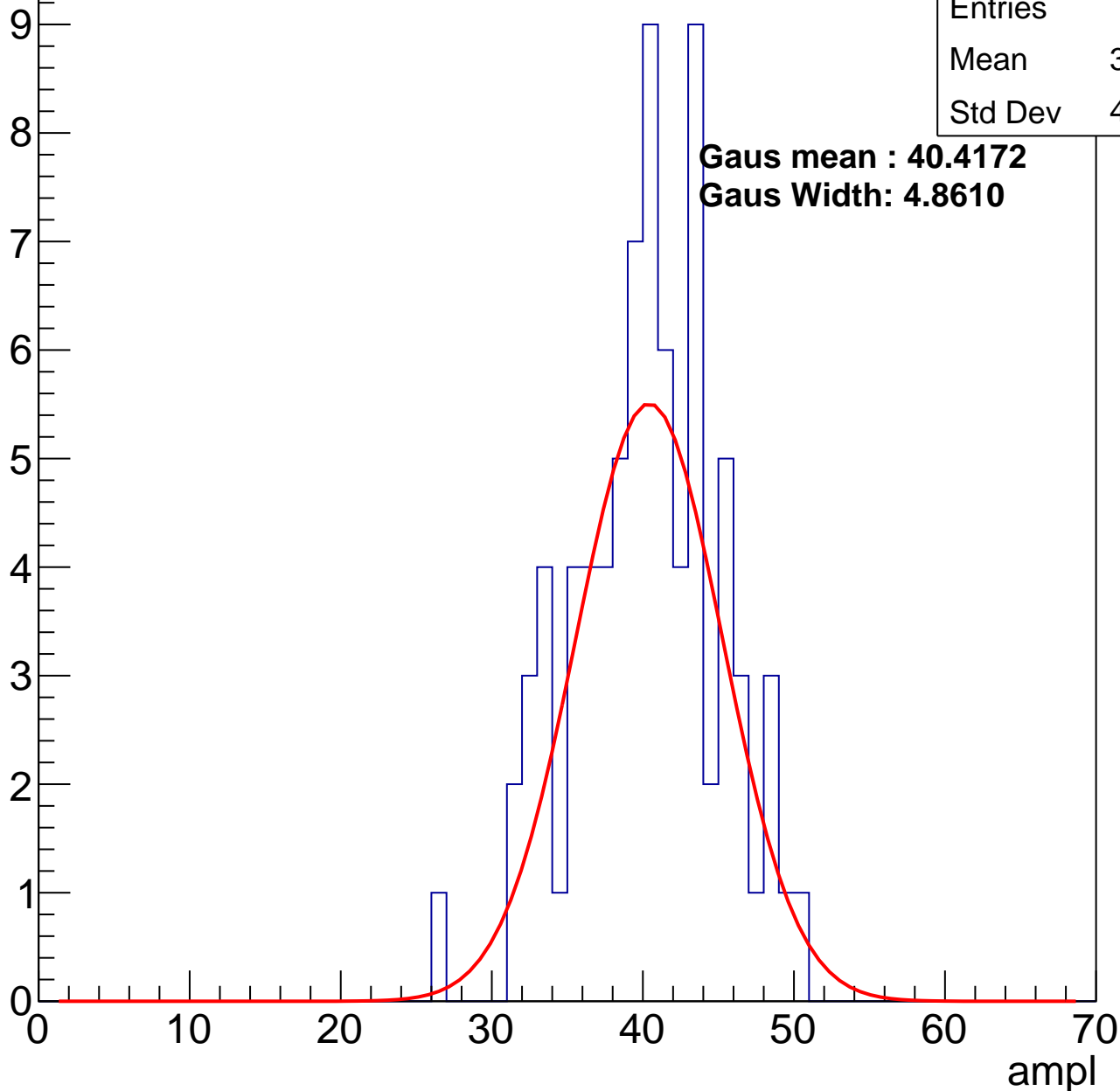
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	39.84
Std Dev	4.769

Gaus mean : 40.4172

Gaus Width: 4.8610

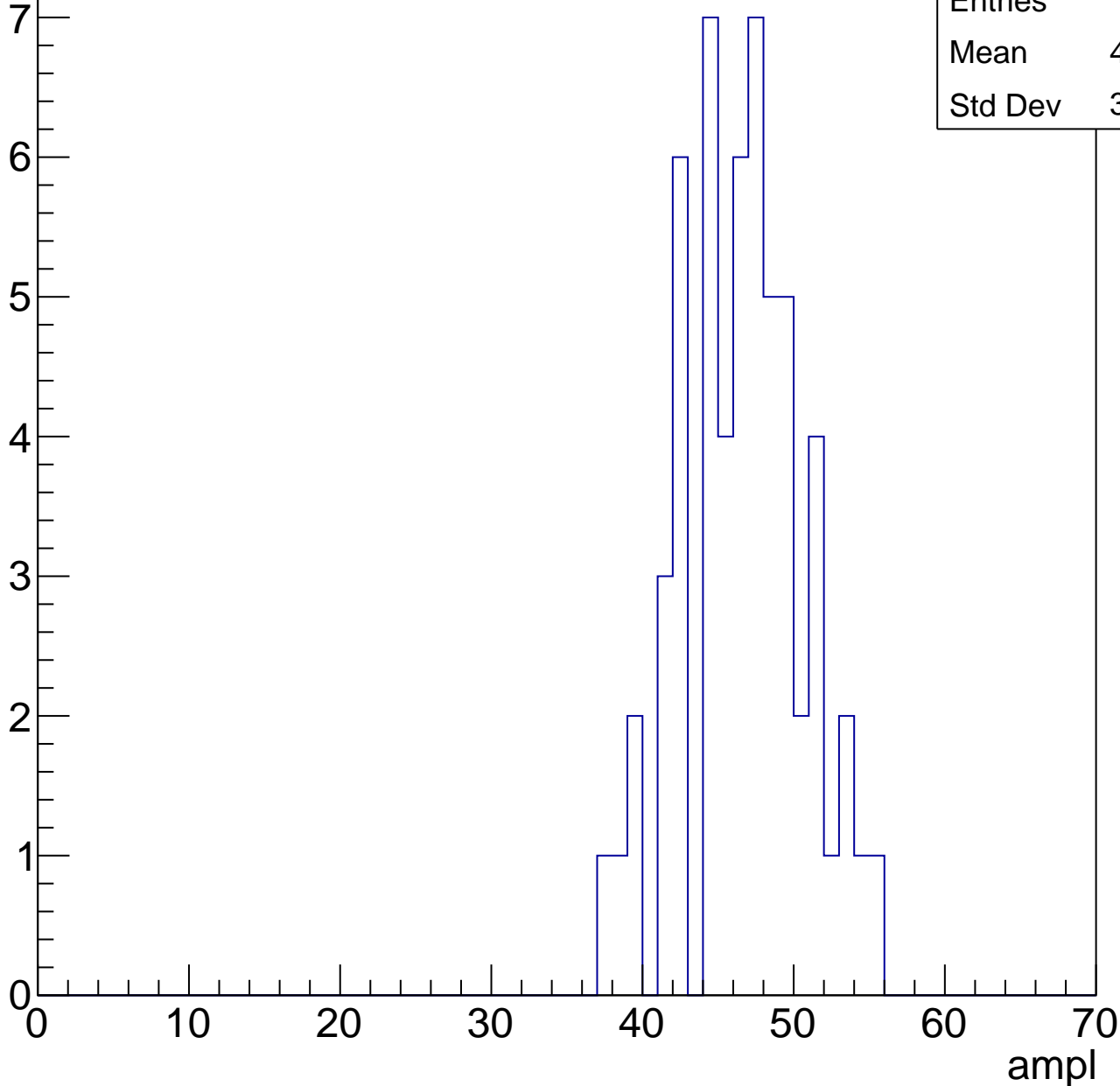


B0L102S, U5-ch126, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	46.16
Std Dev	3.999

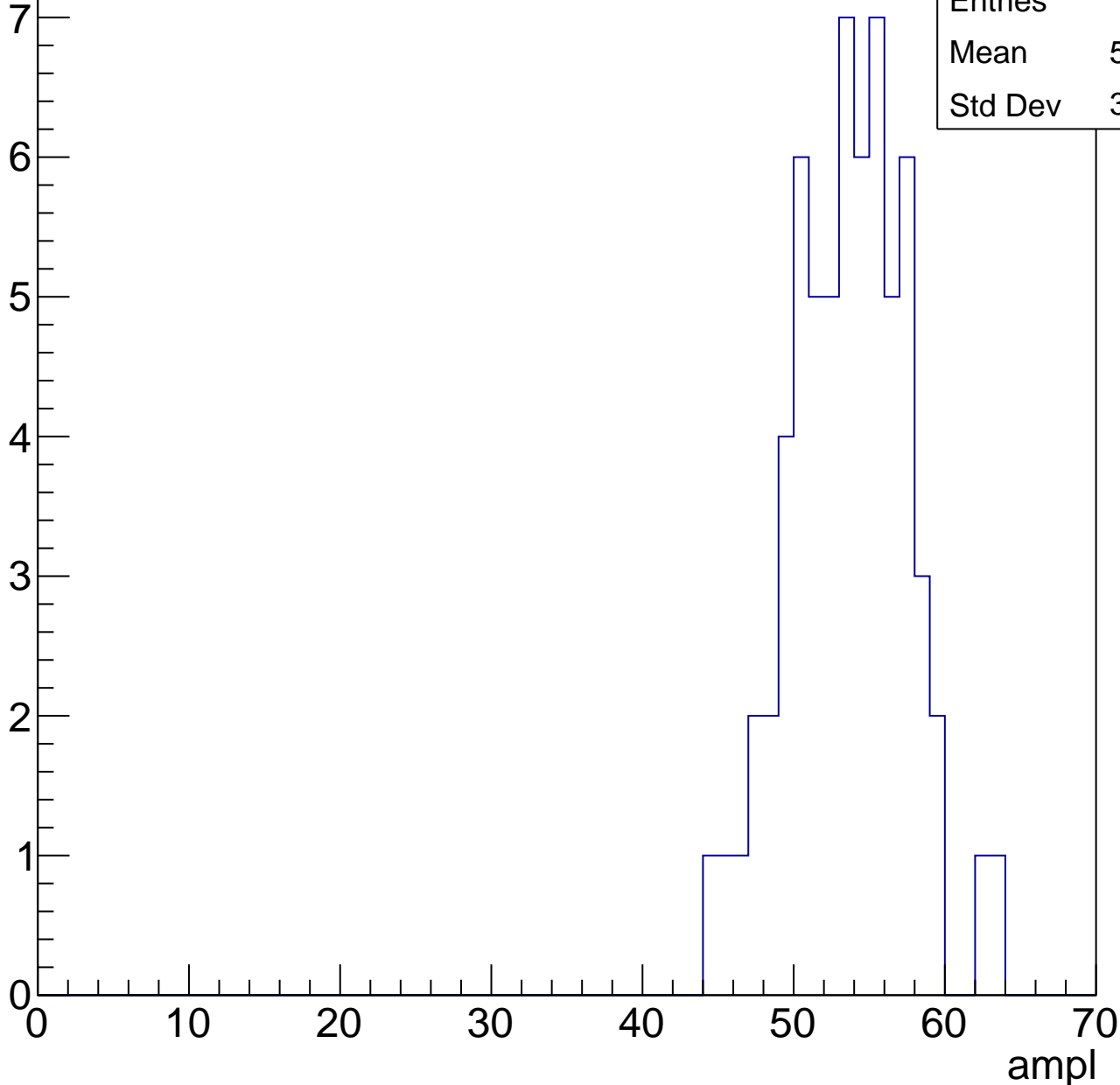


B0L102S, U5-ch126, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	65
Mean	53.15
Std Dev	3.844

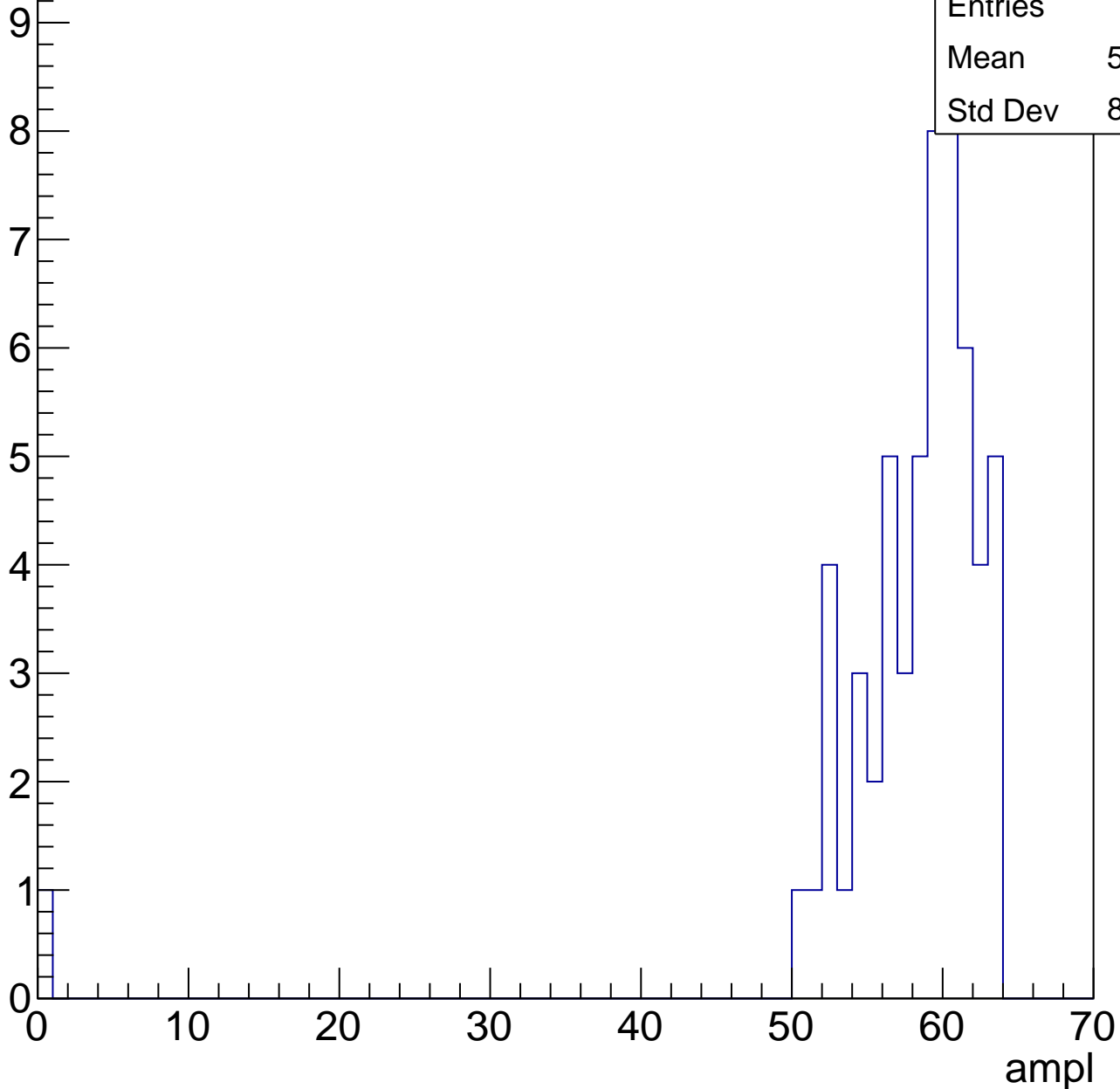


B0L102S, U5-ch126, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	58
Mean	57.17
Std Dev	8.286

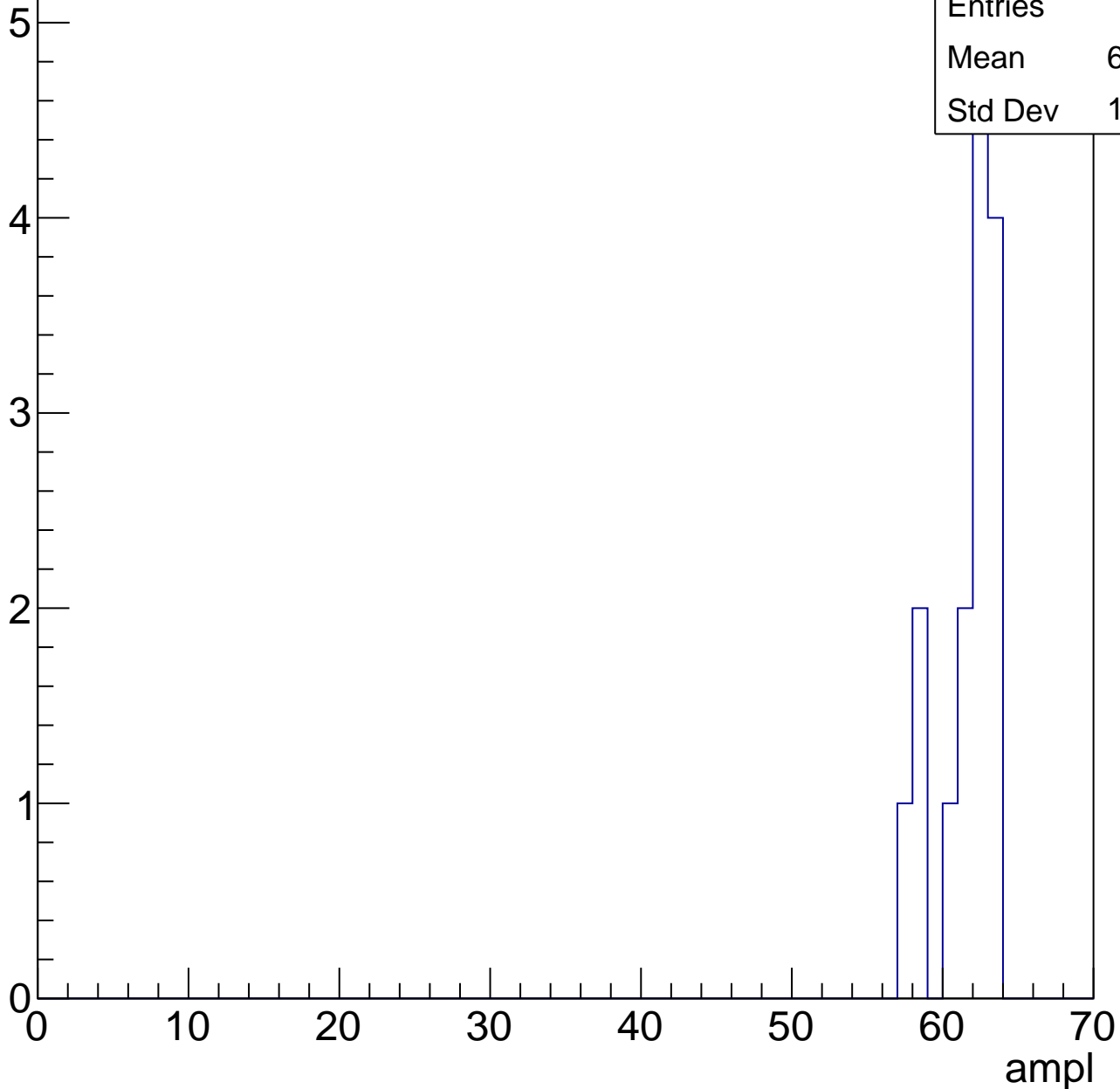


B0L102S, U5-ch126, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry

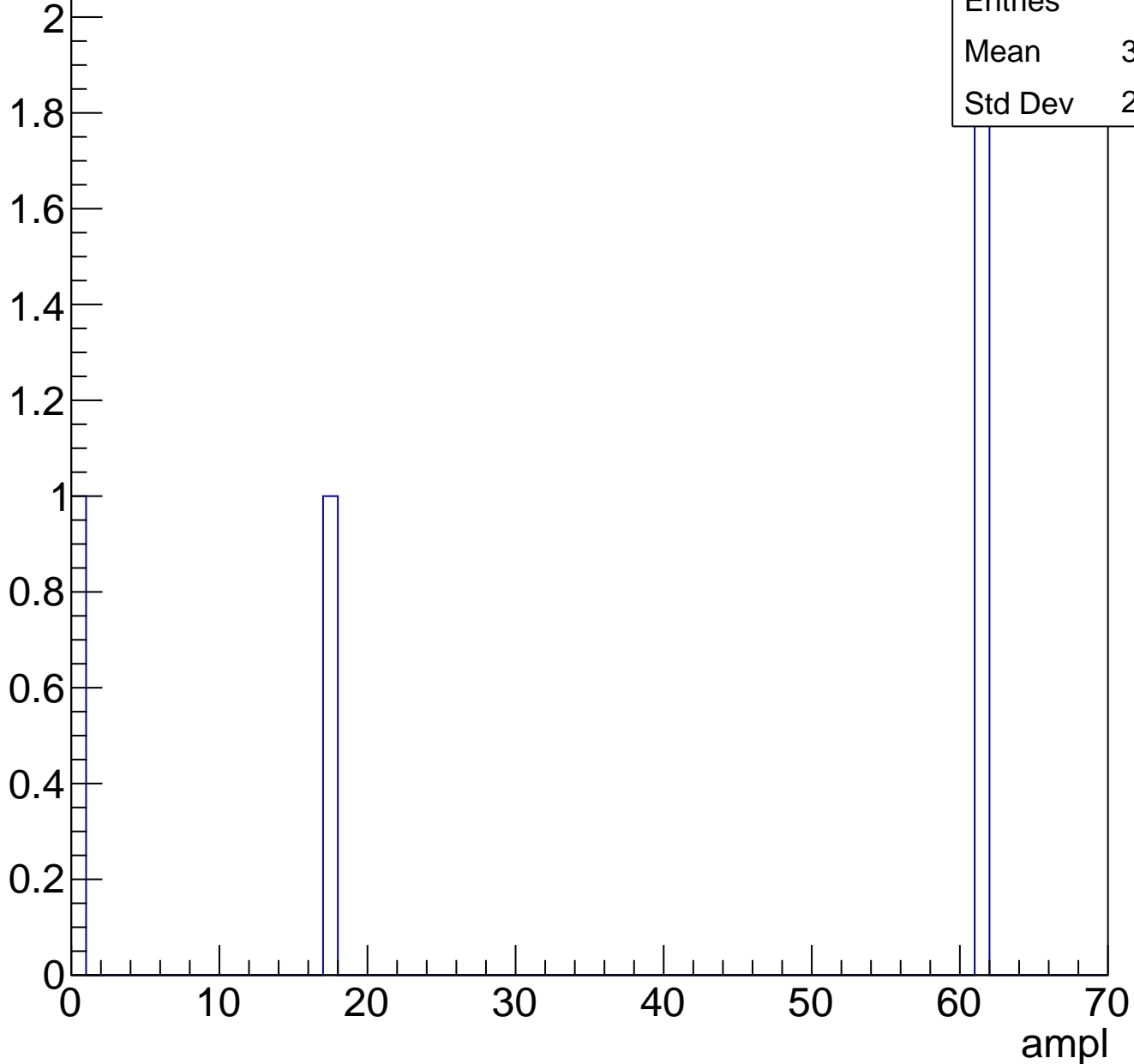
Entries	15
Mean	61.13
Std Dev	1.928



B0L102S, U5-ch126, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	4
Mean	34.75
Std Dev	26.93

B0L102S, U5-ch127, adc0

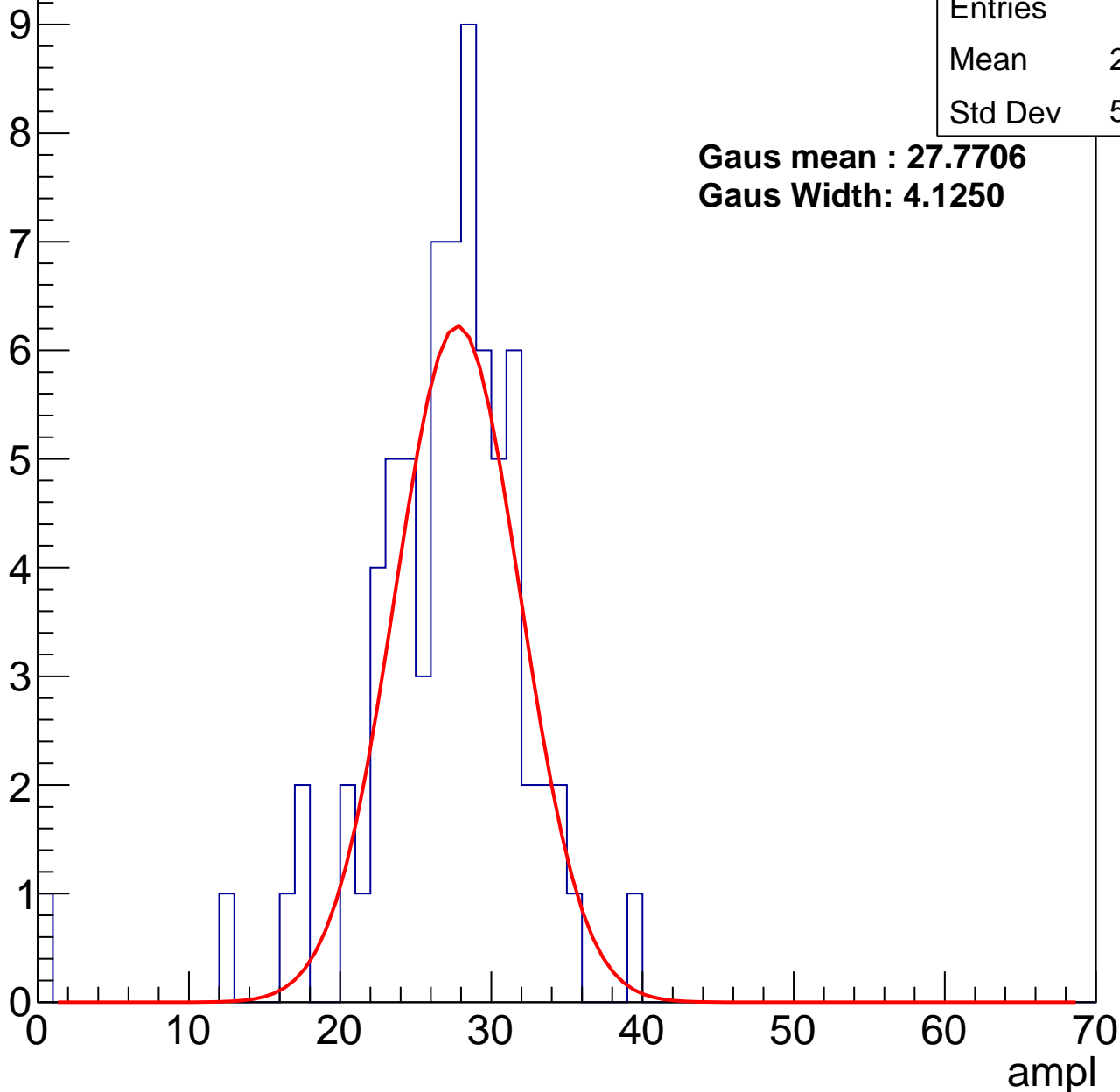
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	73
Mean	26.38
Std Dev	5.524

Gaus mean : 27.7706

Gaus Width: 4.1250



B0L102S, U5-ch127, adc1

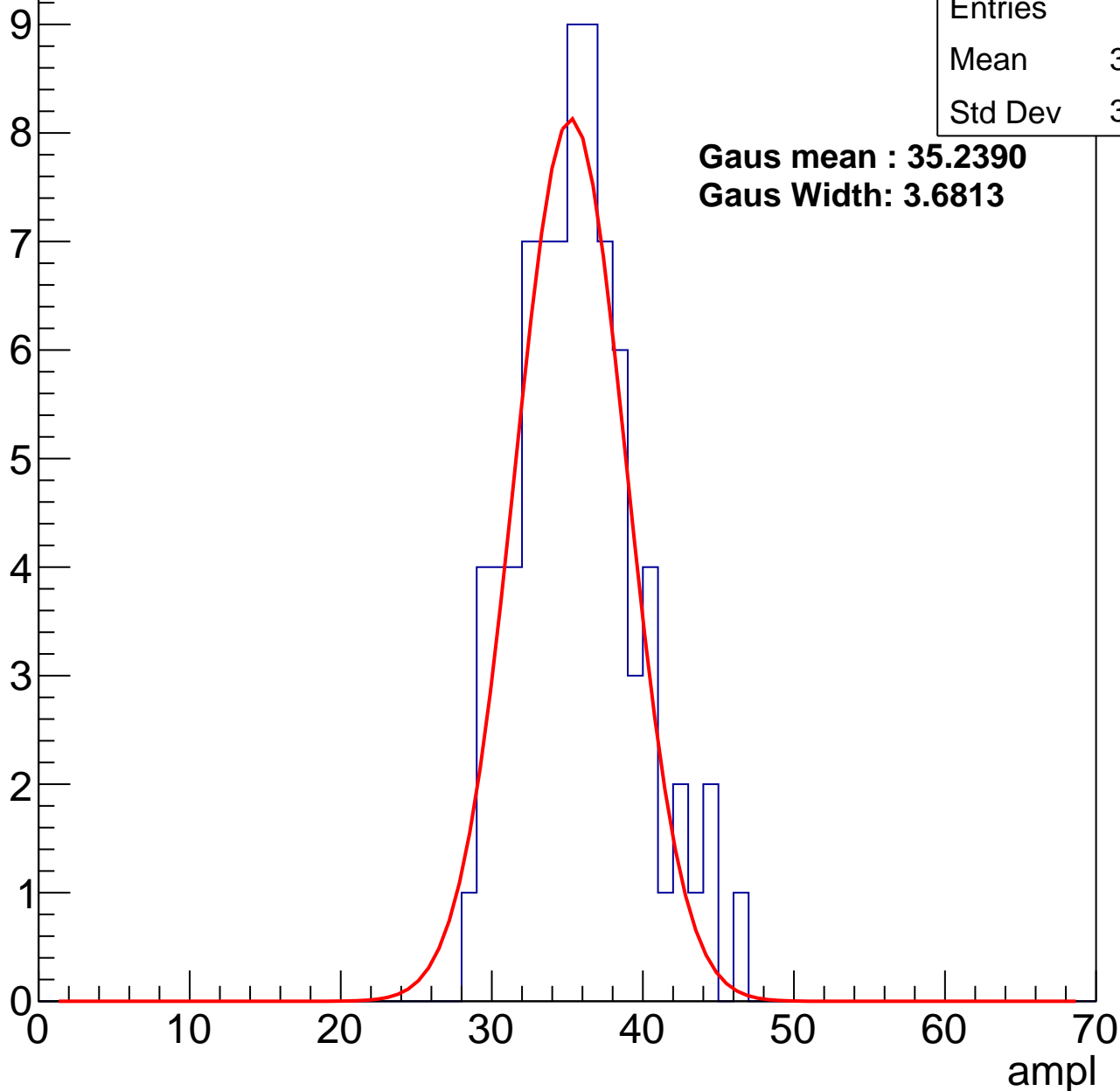
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	79
Mean	35.27
Std Dev	3.847

Gaus mean : 35.2390

Gaus Width: 3.6813



B0L102S, U5-ch127, adc2

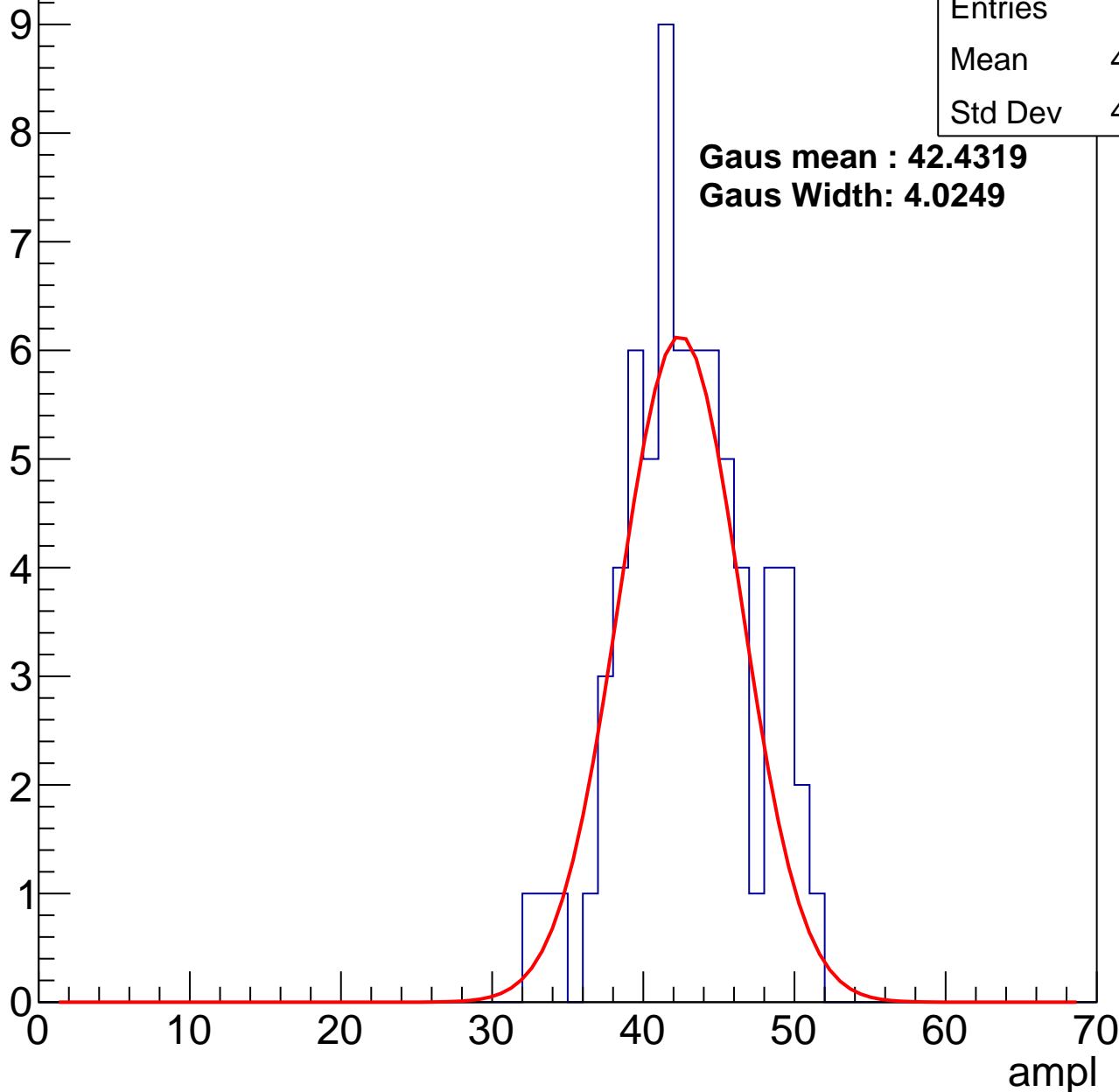
calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	70
Mean	42.43
Std Dev	4.146

Gaus mean : 42.4319

Gaus Width: 4.0249

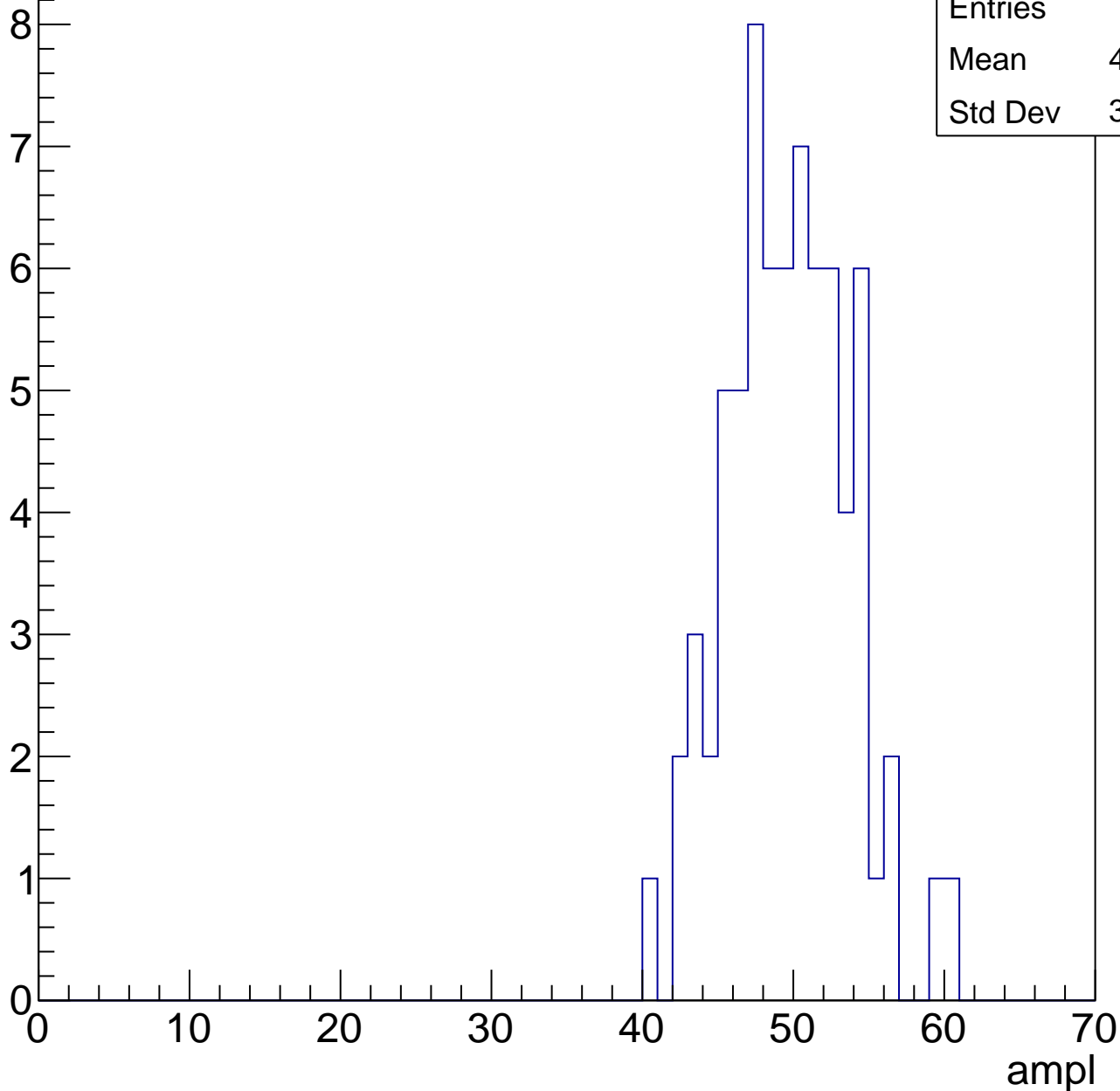


B0L102S, U5-ch127, adc3

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	72
Mean	49.22
Std Dev	3.997

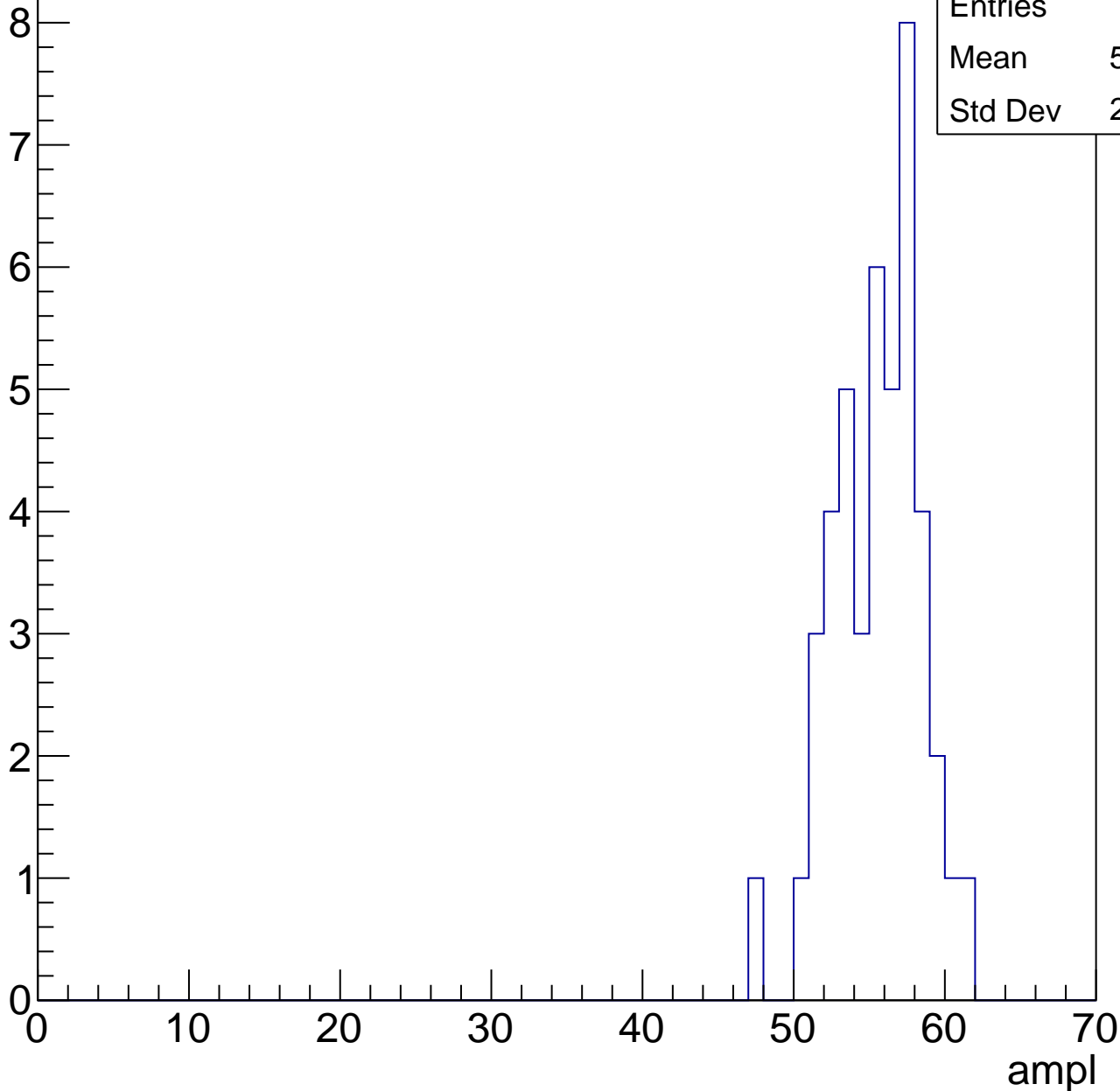


B0L102S, U5-ch127, adc4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

Entries	44
Mean	55.05
Std Dev	2.868

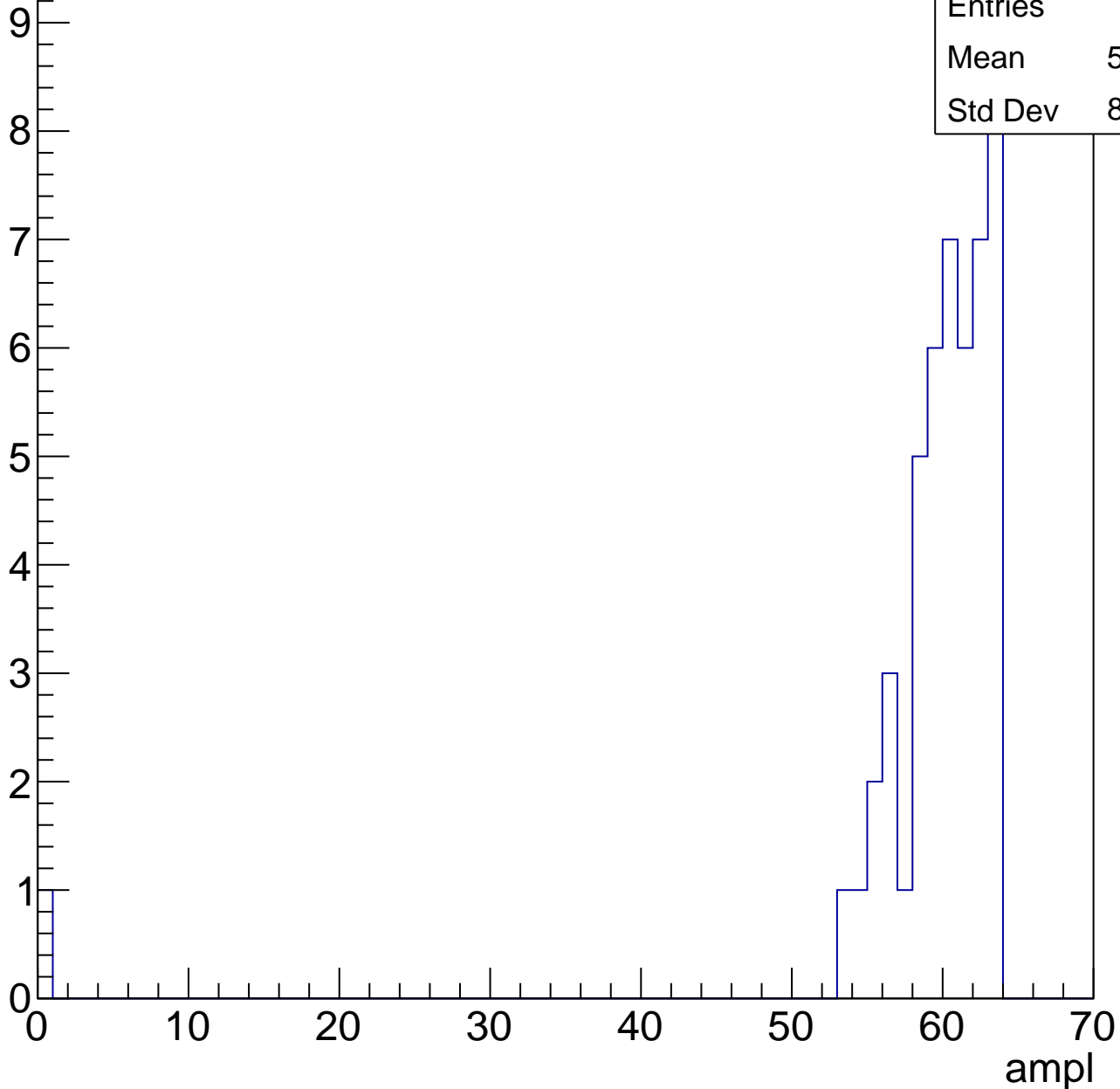


B0L102S, U5-ch127, adc5

calib_packv5_042523_0143.root, FC#12, port B1

Entry

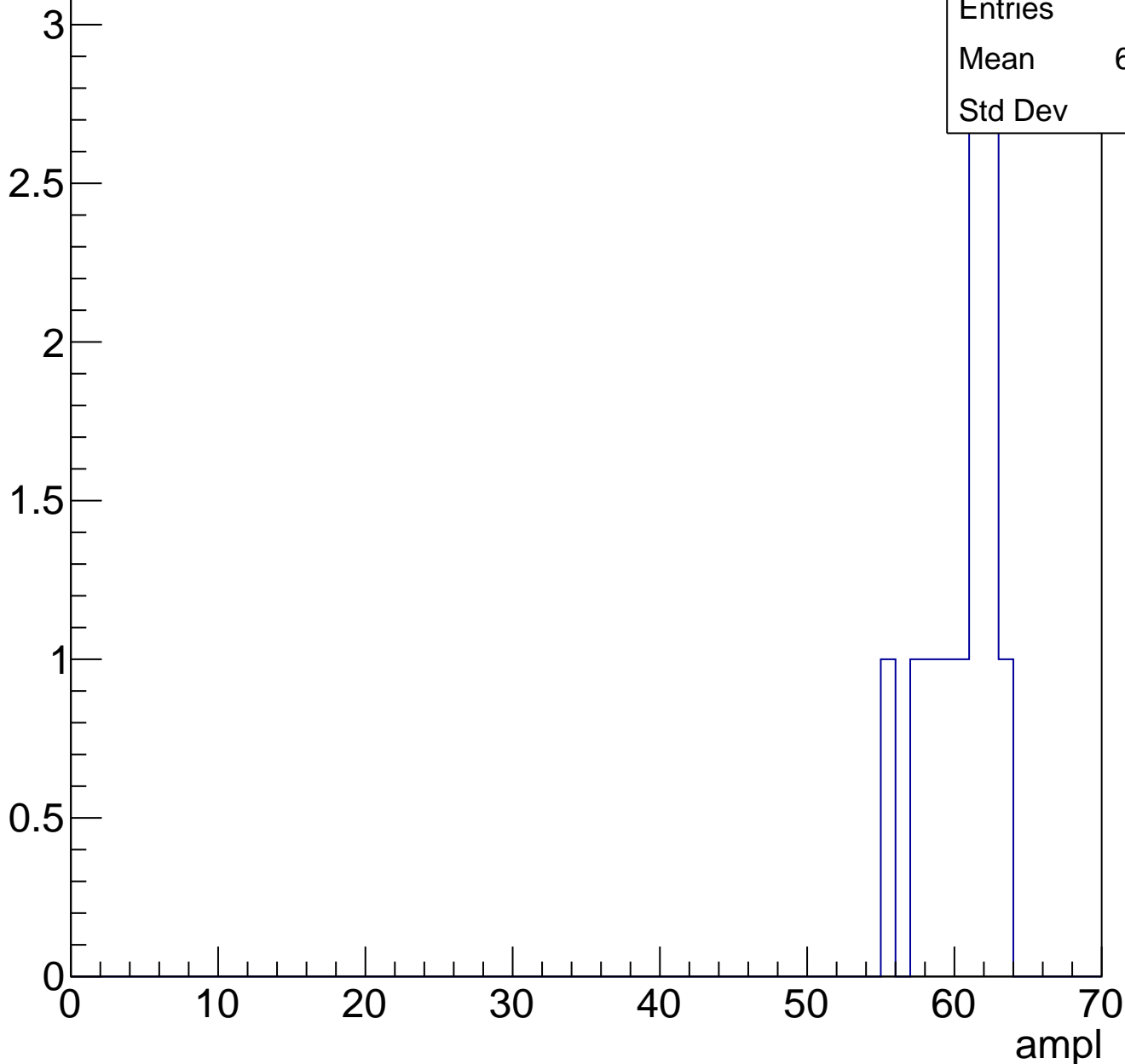
Entries	49
Mean	58.63
Std Dev	8.854



B0L102S, U5-ch127, adc6

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	12
Mean	60.08
Std Dev	2.29

B0L102S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0

B0L102S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#12, port B1

Entry



Entries	0
Mean	0
Std Dev	0