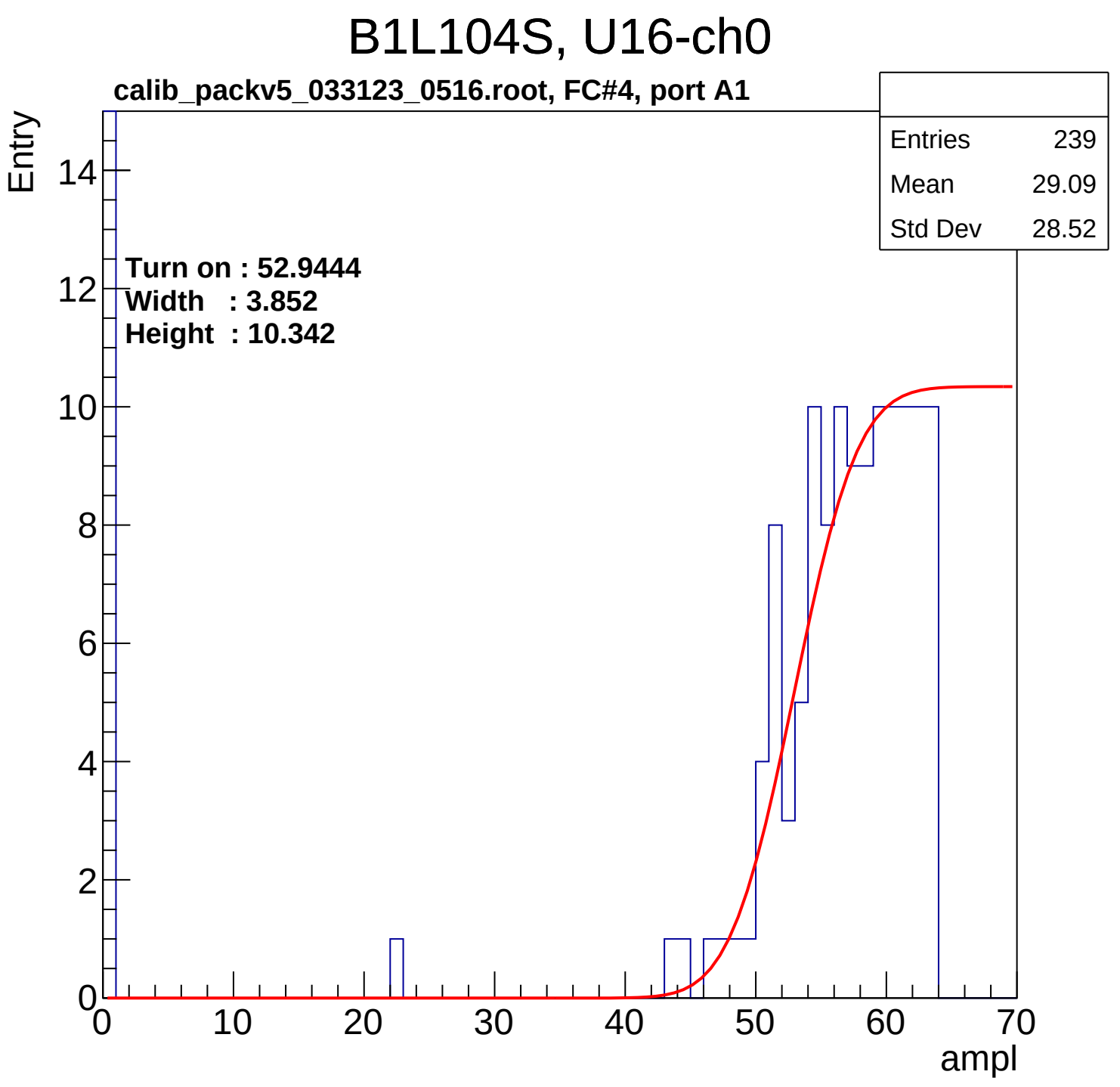
B1L104S, U16-ch0
calib_packv5_033123_0516.root, FC#4, port A1
Entries 239
Mean 29.09
Std Dev 28.52
Turn on : 52.9444
Width : 3.852
Height : 10.342
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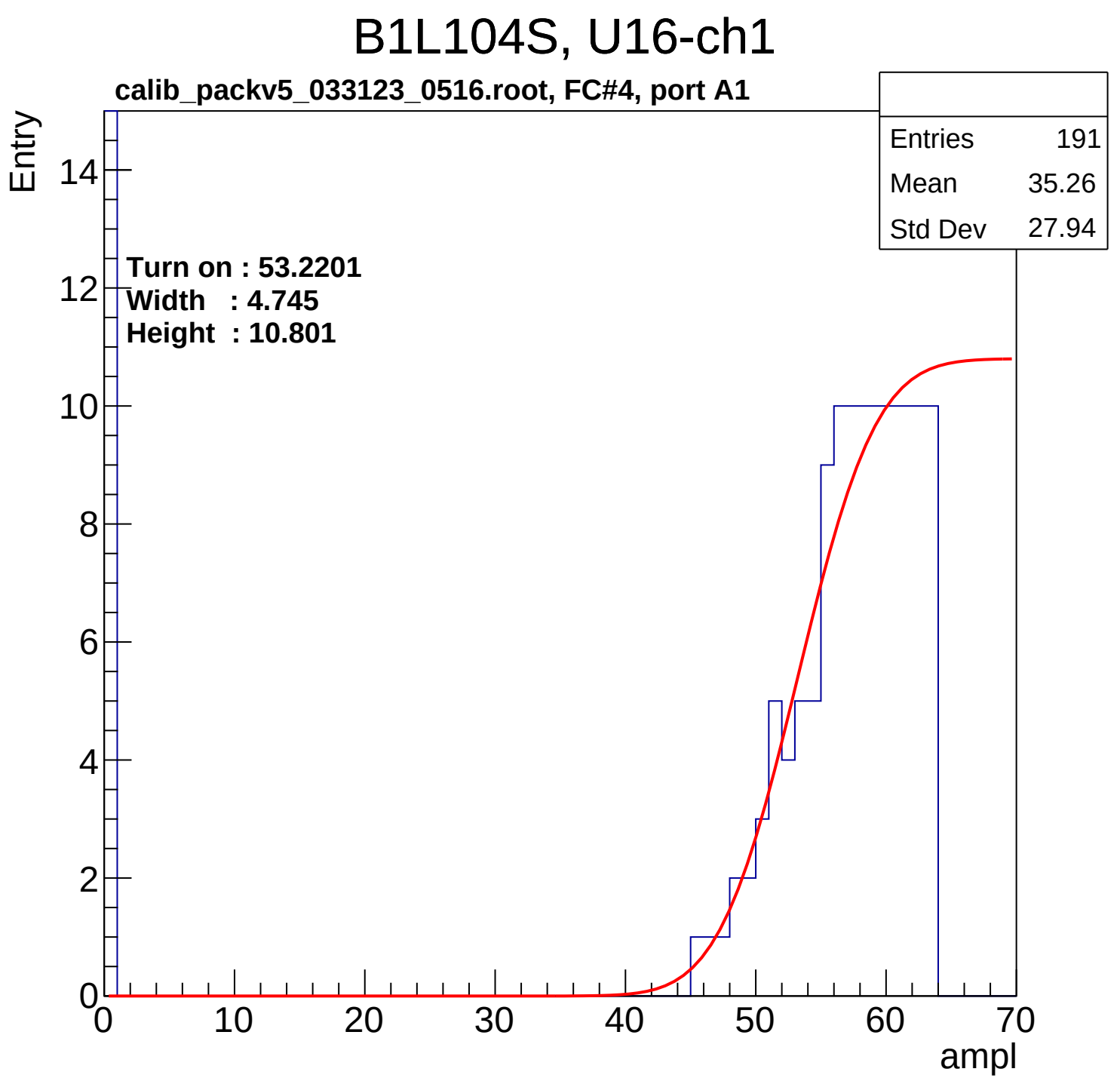
B1L104S, U16-ch1
calib_packv5_033123_0516.root, FC#4, port A1
Entries 191
Mean 35.26
Std Dev 27.94
Turn on : 53.2201
Width : 4.745
Height : 10.801
Entry
ampl
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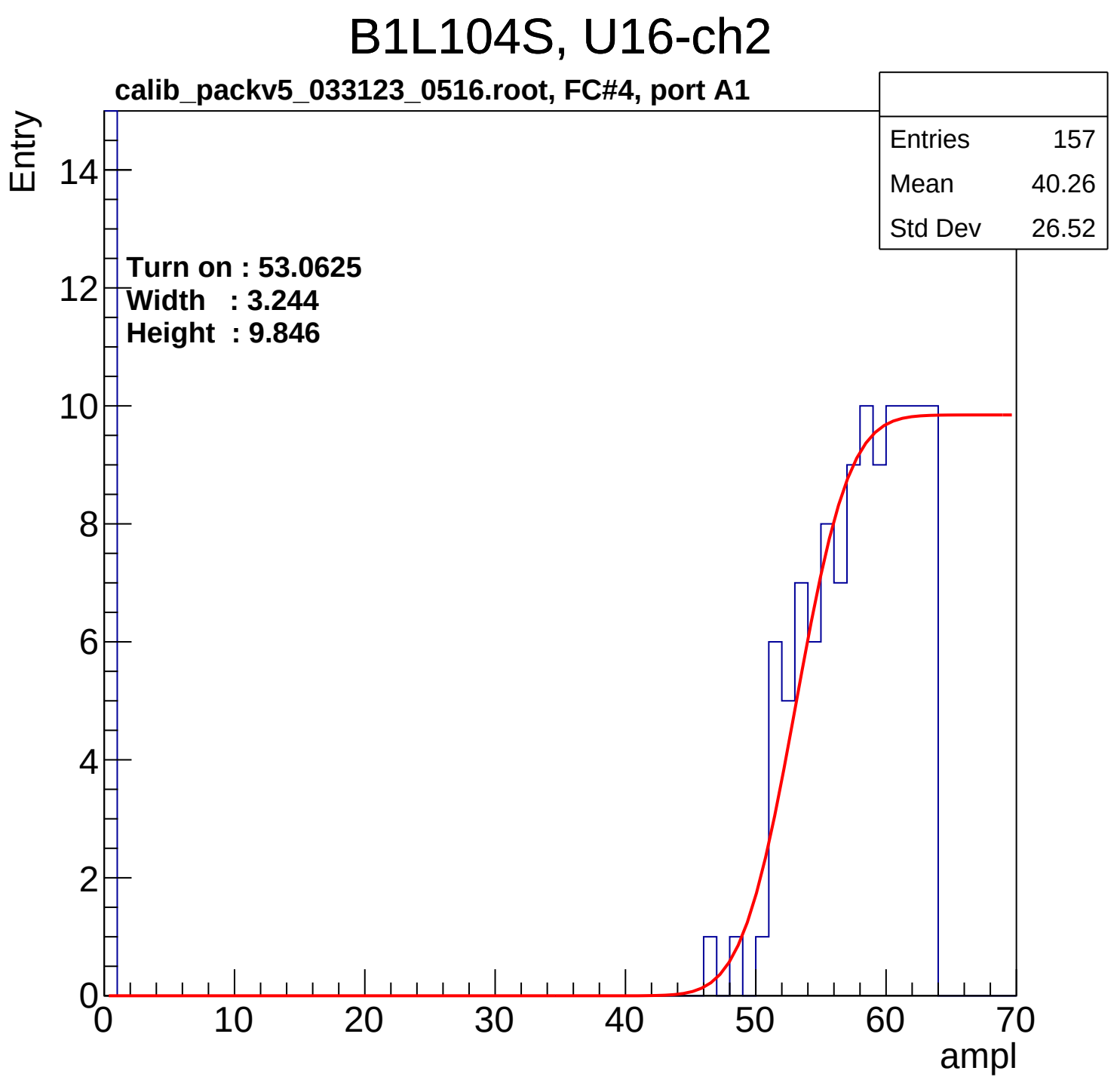
B1L104S, U16-ch2
calib_packv5_033123_0516.root, FC#4, port A1
Entries 157
Mean 40.26
Std Dev 26.52
Turn on : 53.0625
Width : 3.244
Height : 9.846
Entry
ampl
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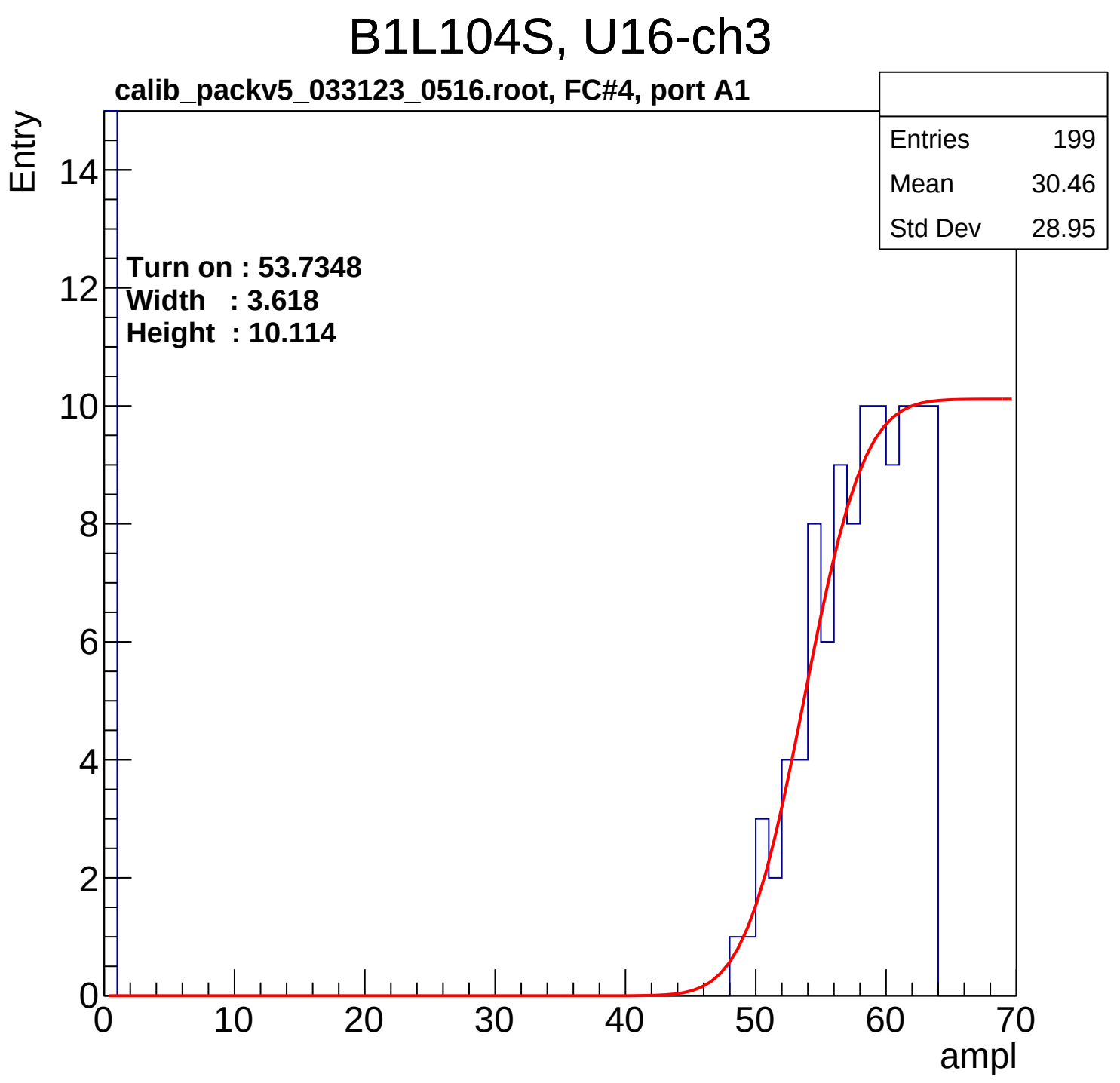
B1L104S, U16-ch3
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 30.46
Std Dev 28.95
Turn on : 53.7348
Width : 3.618
Height : 10.114
Entry
ampl
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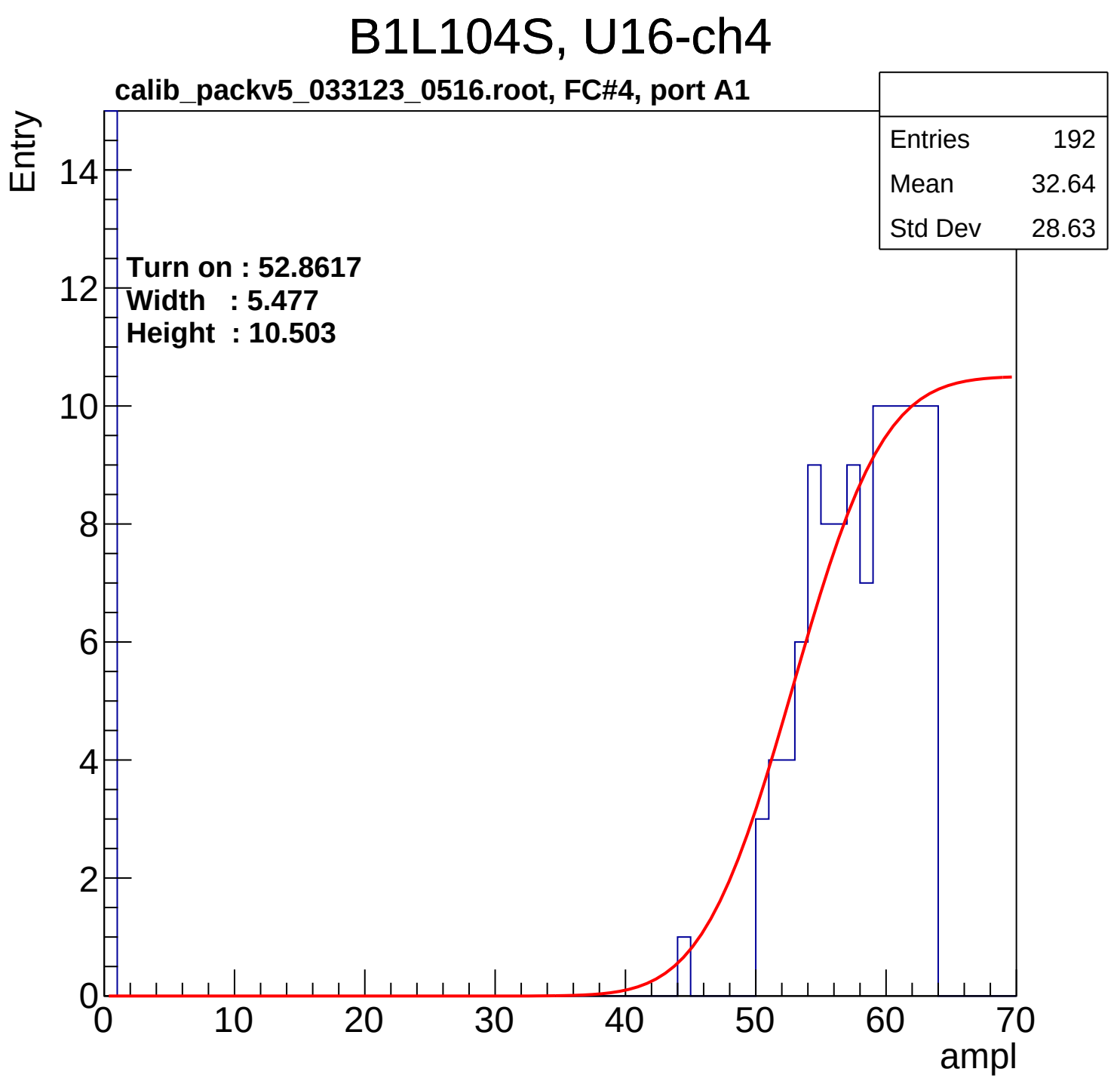
B1L104S, U16-ch4
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 32.64
Std Dev 28.63
Turn on : 52.8617
Width : 5.477
Height : 10.503
Entry
ampl
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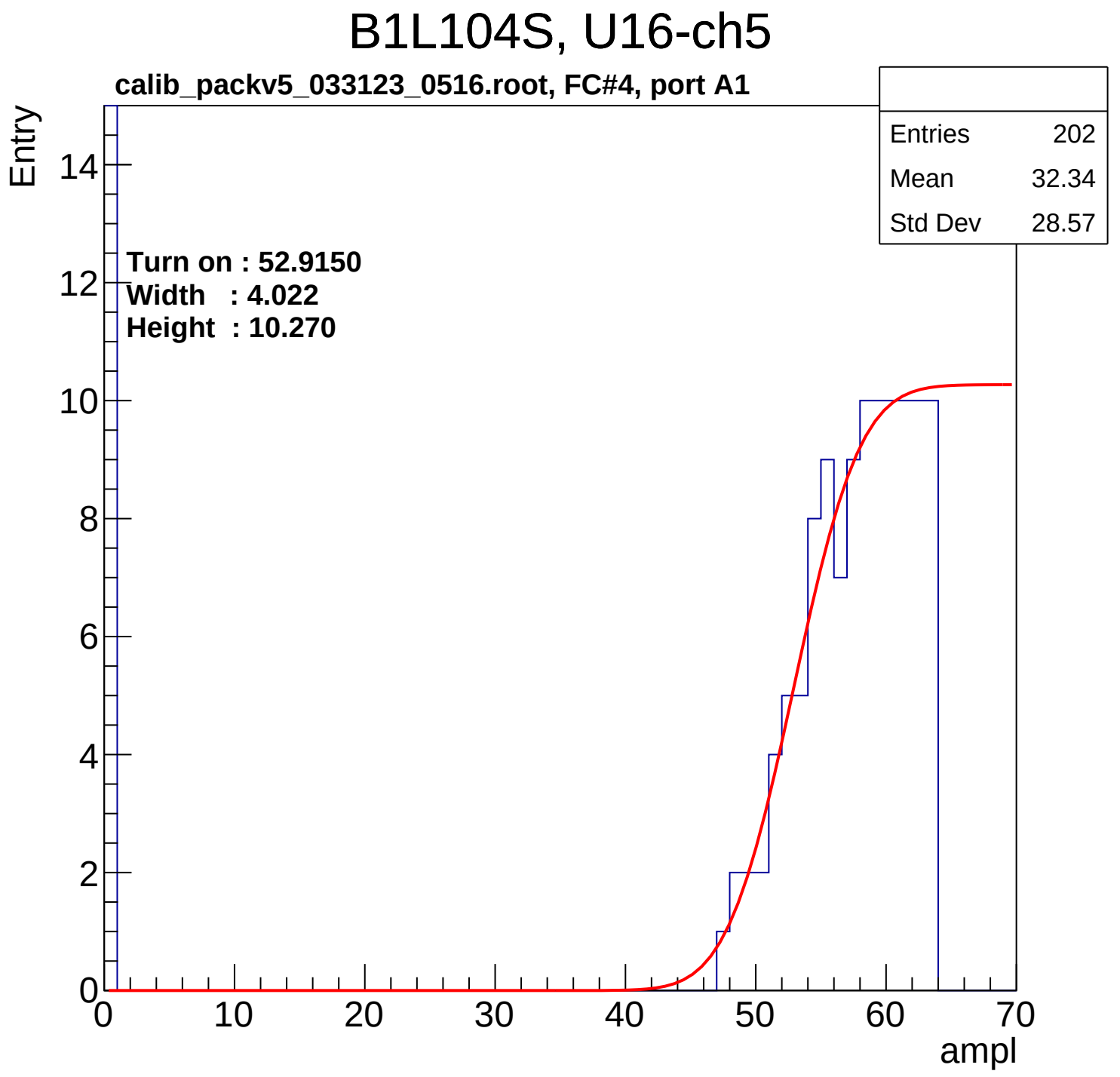
B1L104S, U16-ch5
calib_packv5_033123_0516.root, FC#4, port A1
Entries 202
Mean 32.34
Std Dev 28.57
Turn on : 52.9150
Width : 4.022
Height : 10.270
Entry
ampl
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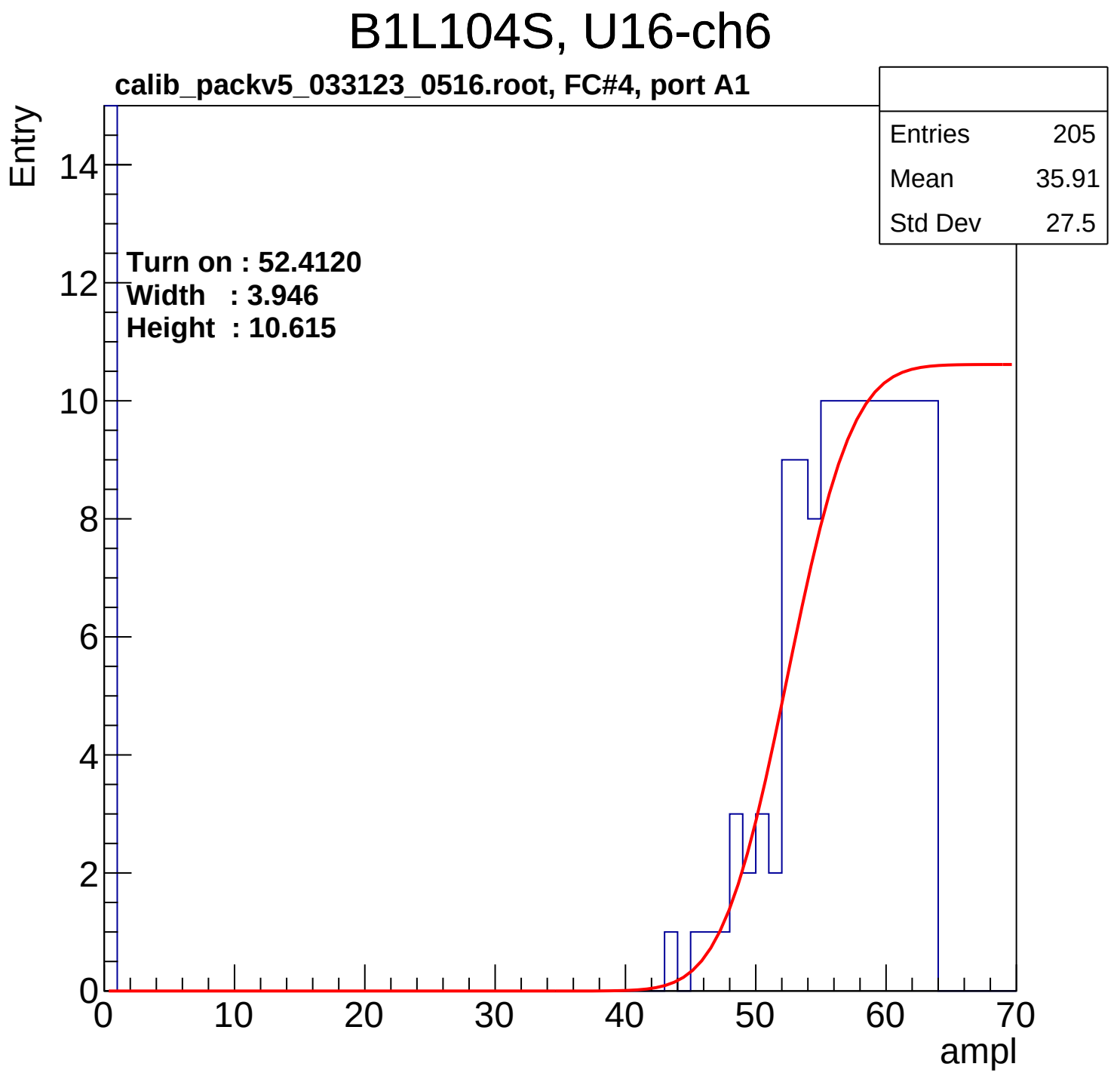
B1L104S, U16-ch6
calib_packv5_033123_0516.root, FC#4, port A1
Entries 205
Mean 35.91
Std Dev 27.5
Turn on : 52.4120
Width : 3.946
Height : 10.615
Entry
ampl
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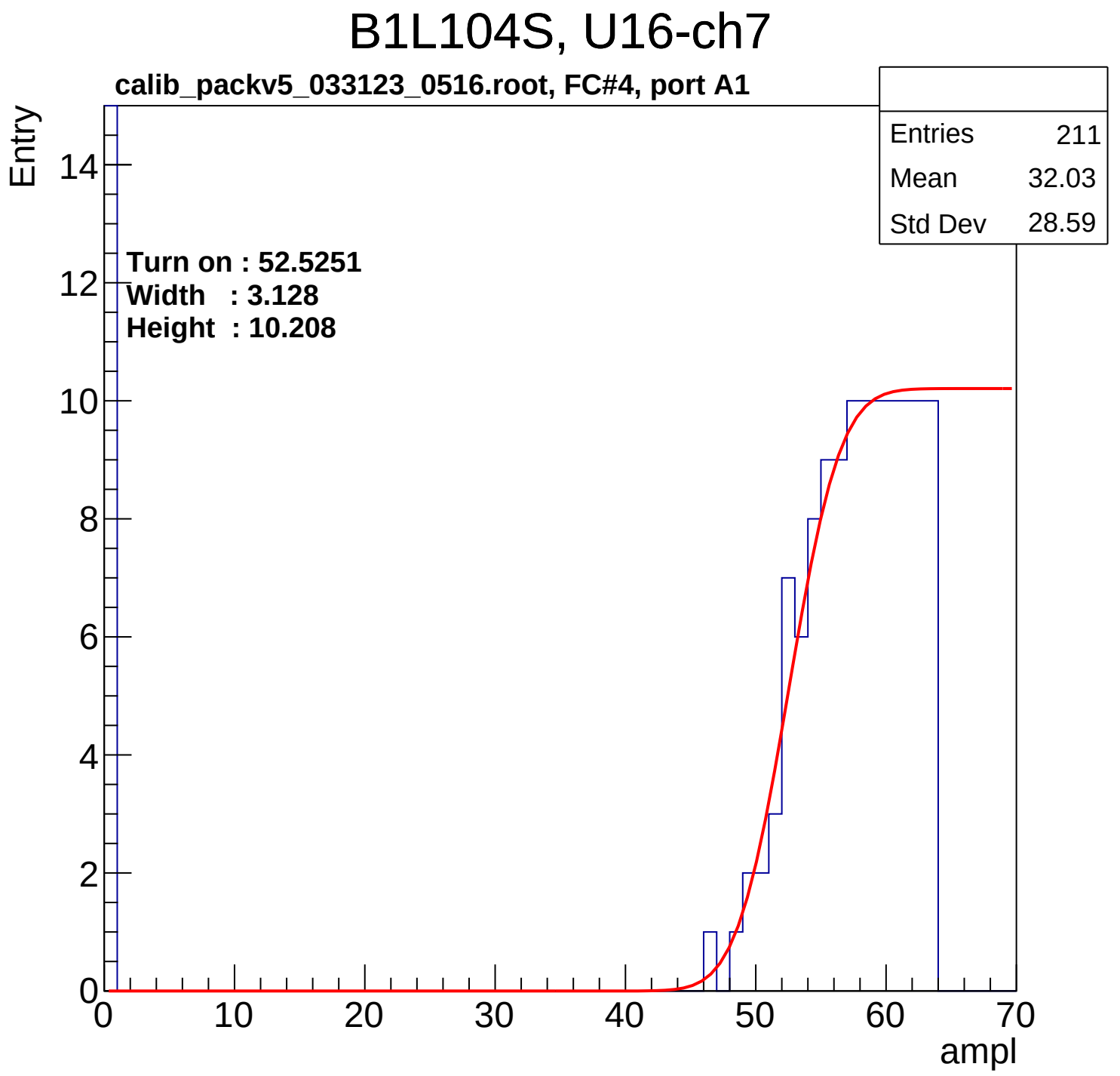
B1L104S, U16-ch7
calib_packv5_033123_0516.root, FC#4, port A1
Entries 211
Mean 32.03
Std Dev 28.59
Turn on : 52.5251
Width : 3.128
Height : 10.208
Entry
ampl
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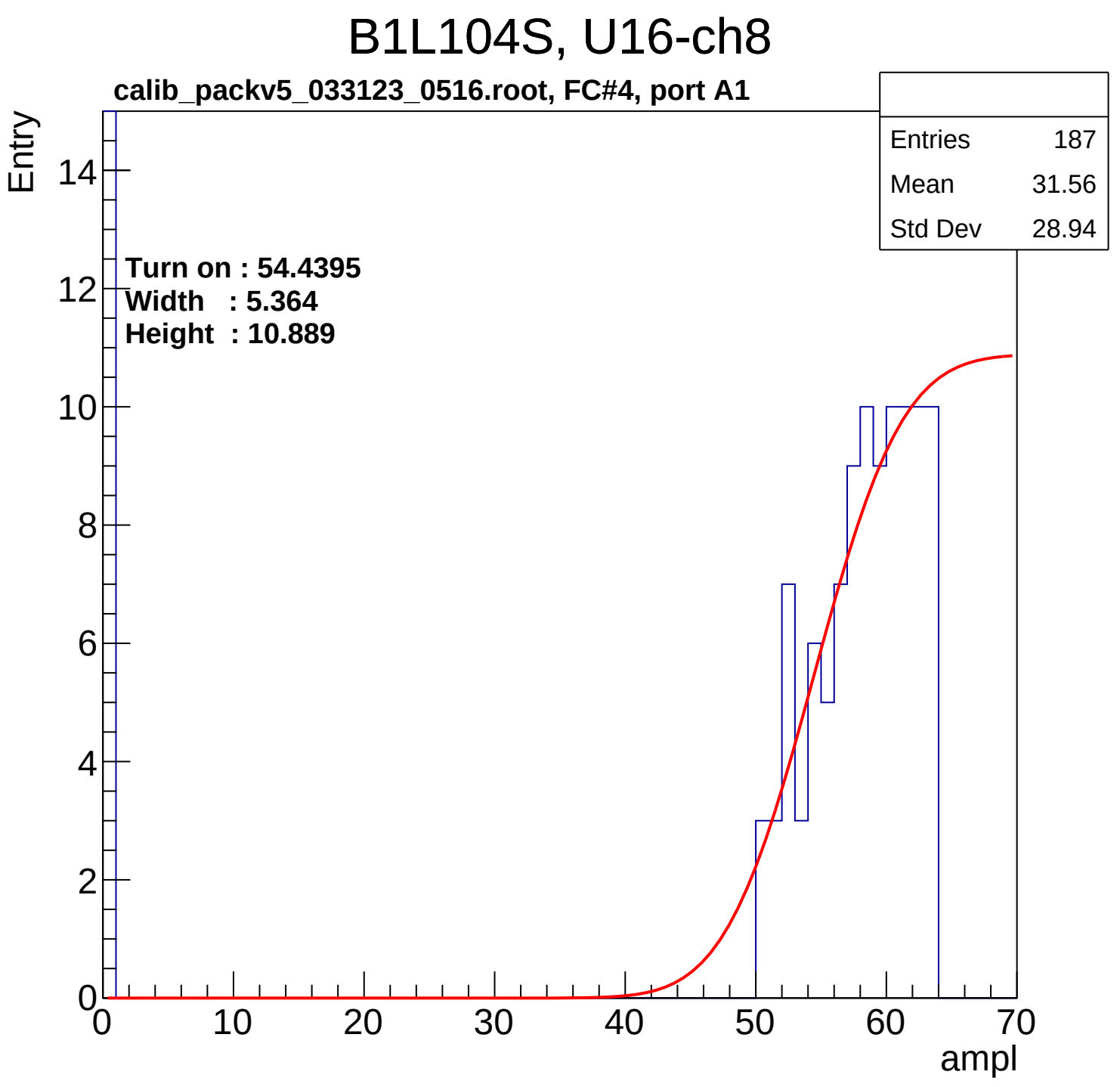
B1L104S, U16-ch8
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 31.56
Std Dev 28.94
Turn on : 54.4395
Width : 5.364
Height : 10.889
Entry
ampl
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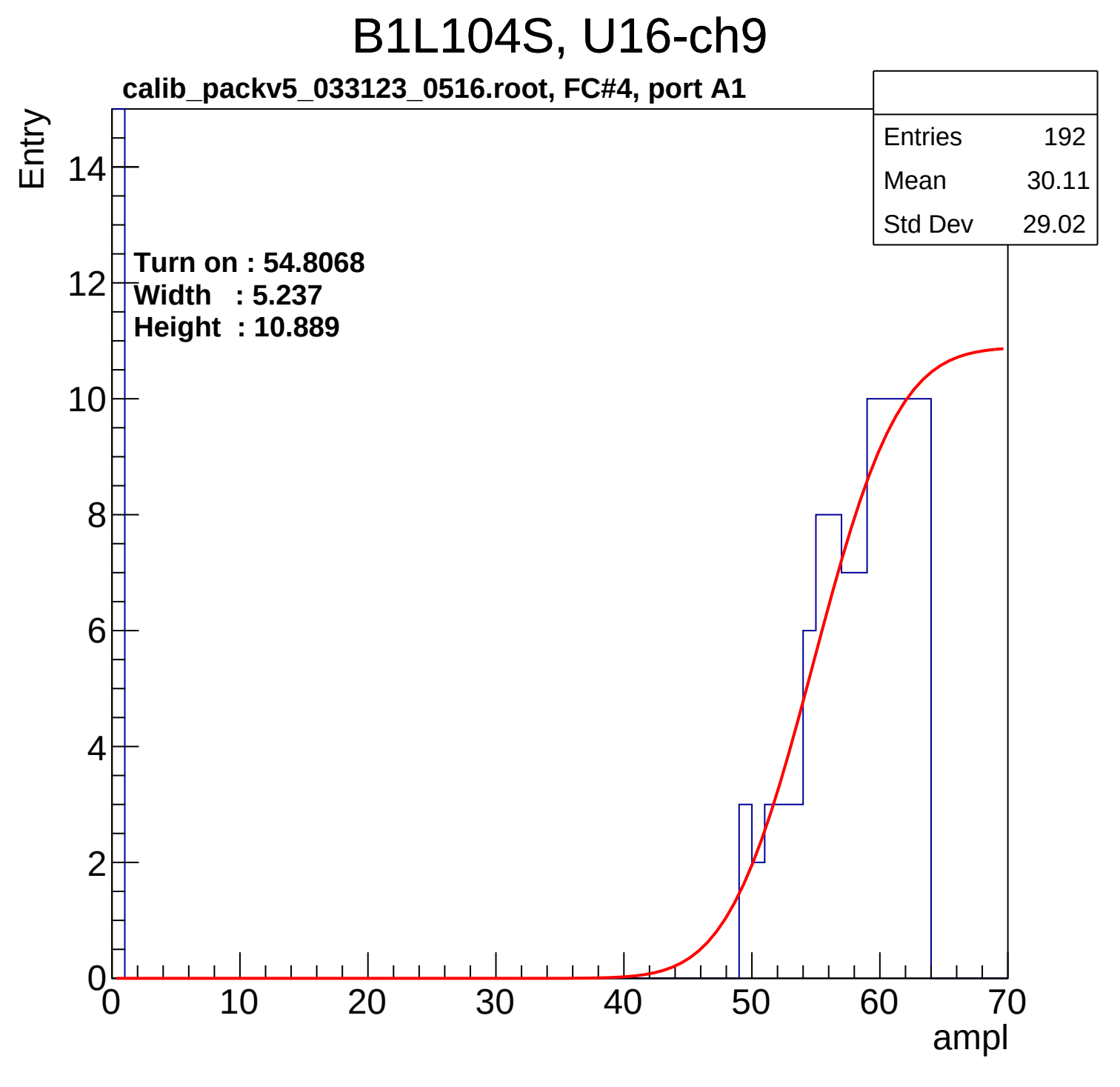
B1L104S, U16-ch9
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 30.11
Std Dev 29.02
Turn on : 54.8068
Width : 5.237
Height : 10.889
Entry
ampl
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8
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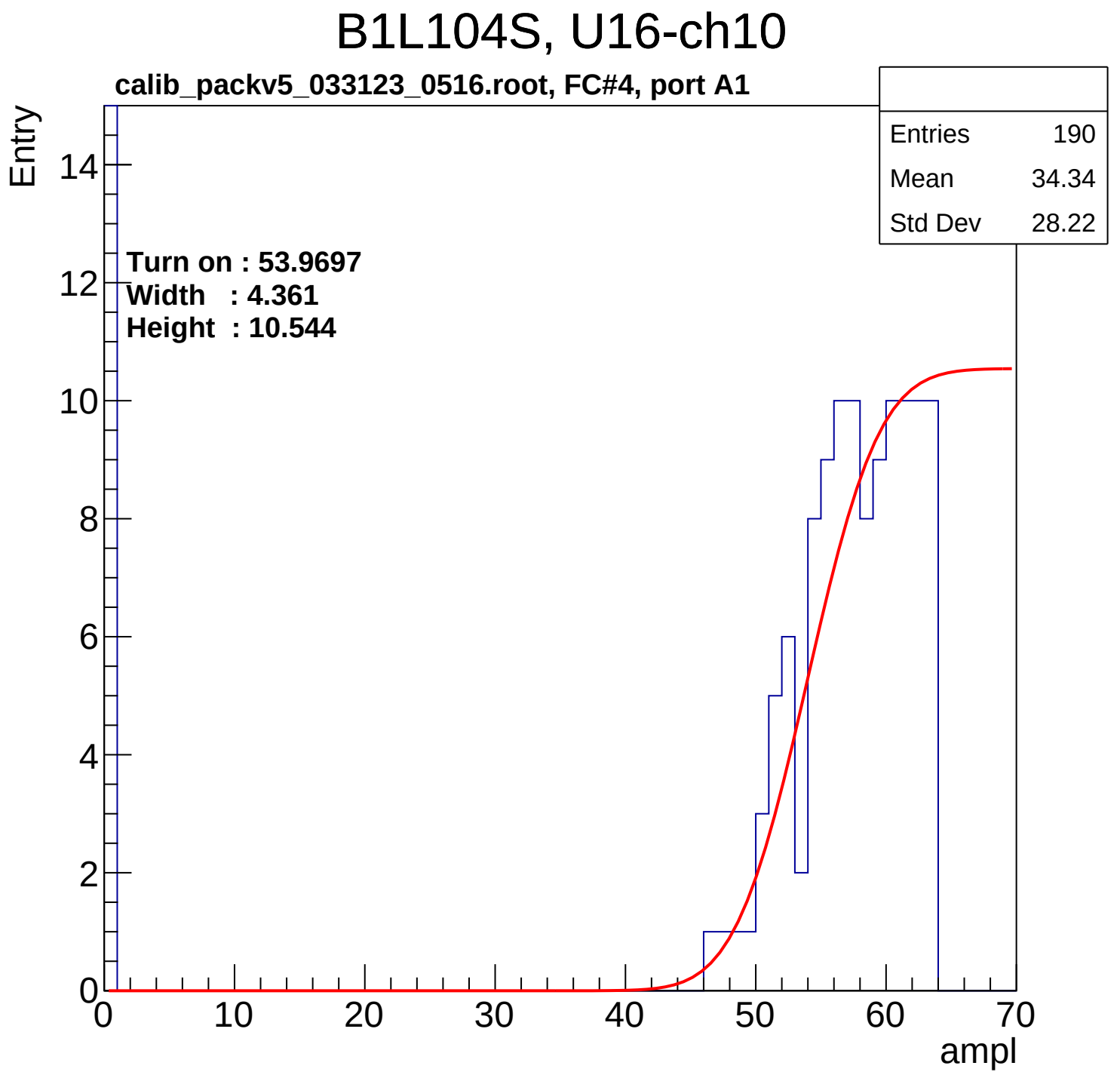
B1L104S, U16-ch10
calib_packv5_033123_0516.root, FC#4, port A1
Entries 190
Mean 34.34
Std Dev 28.22
Turn on : 53.9697
Width : 4.361
Height : 10.544
Entry
ampl
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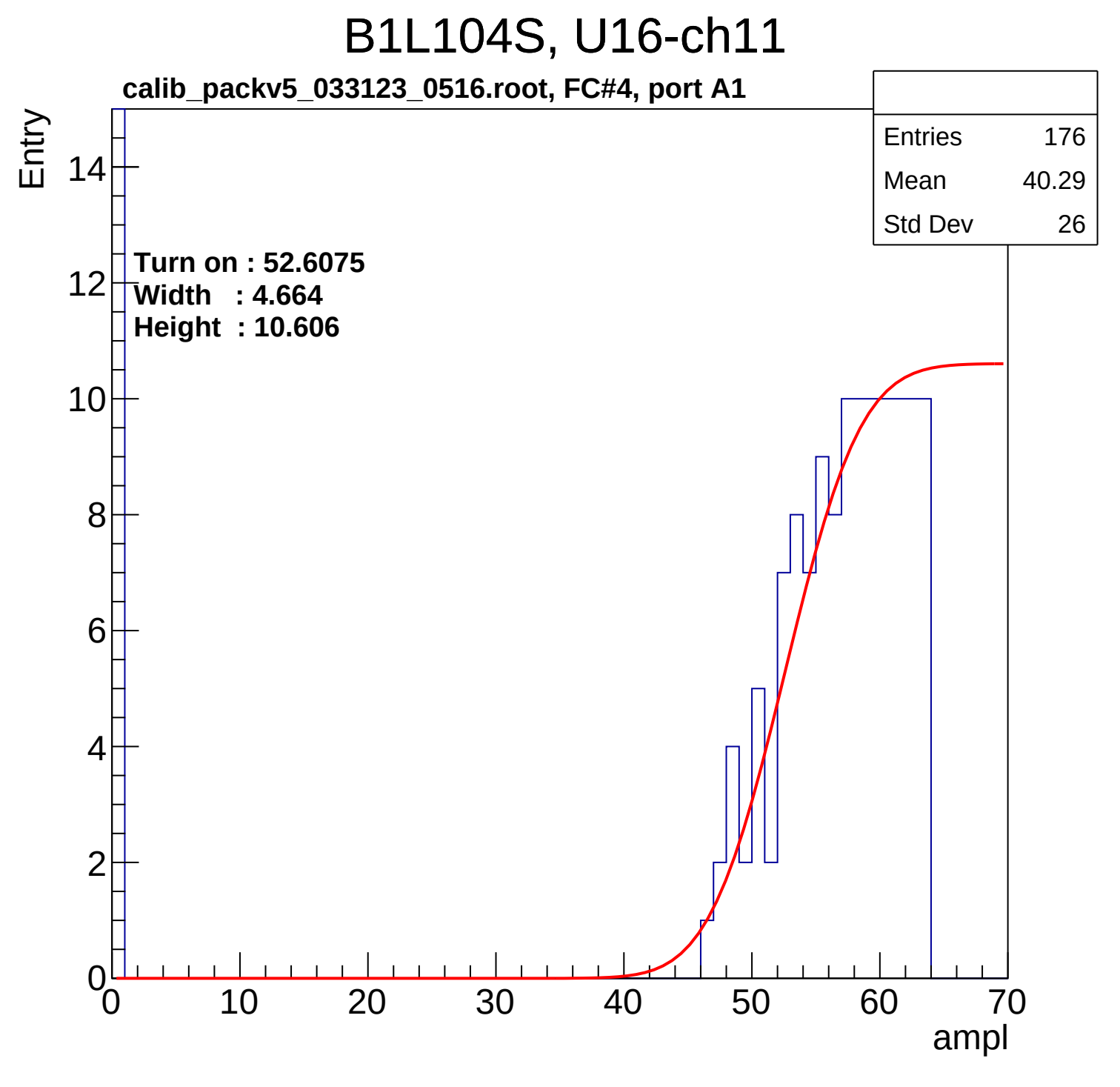
B1L104S, U16-ch11
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 40.29
Std Dev 26
Turn on : 52.6075
Width : 4.664
Height : 10.606
Entry
ampl
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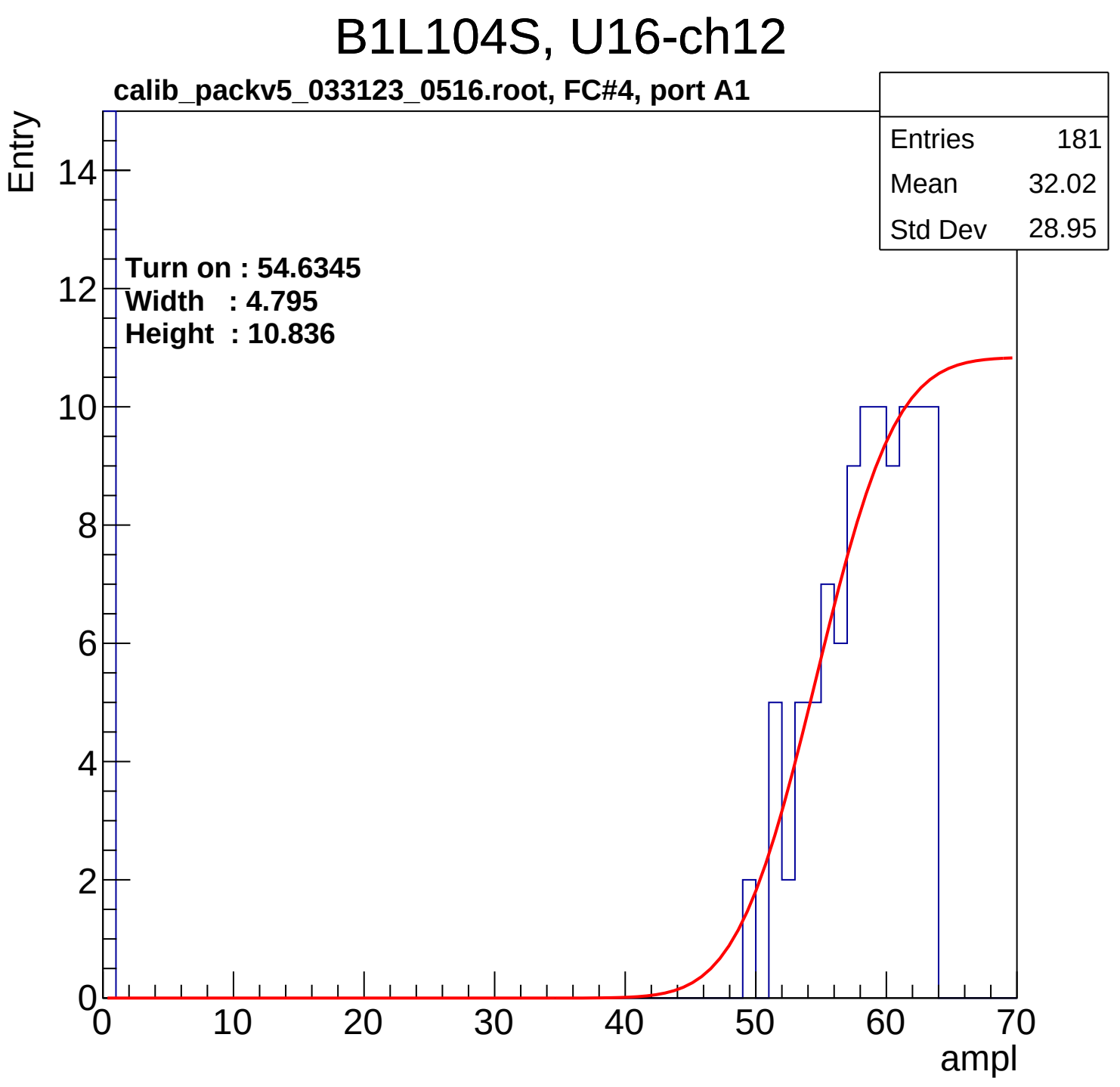
B1L104S, U16-ch12
calib_packv5_033123_0516.root, FC#4, port A1
Entries 181
Mean 32.02
Std Dev 28.95
Turn on : 54.6345
Width : 4.795
Height : 10.836
Entry
ampl
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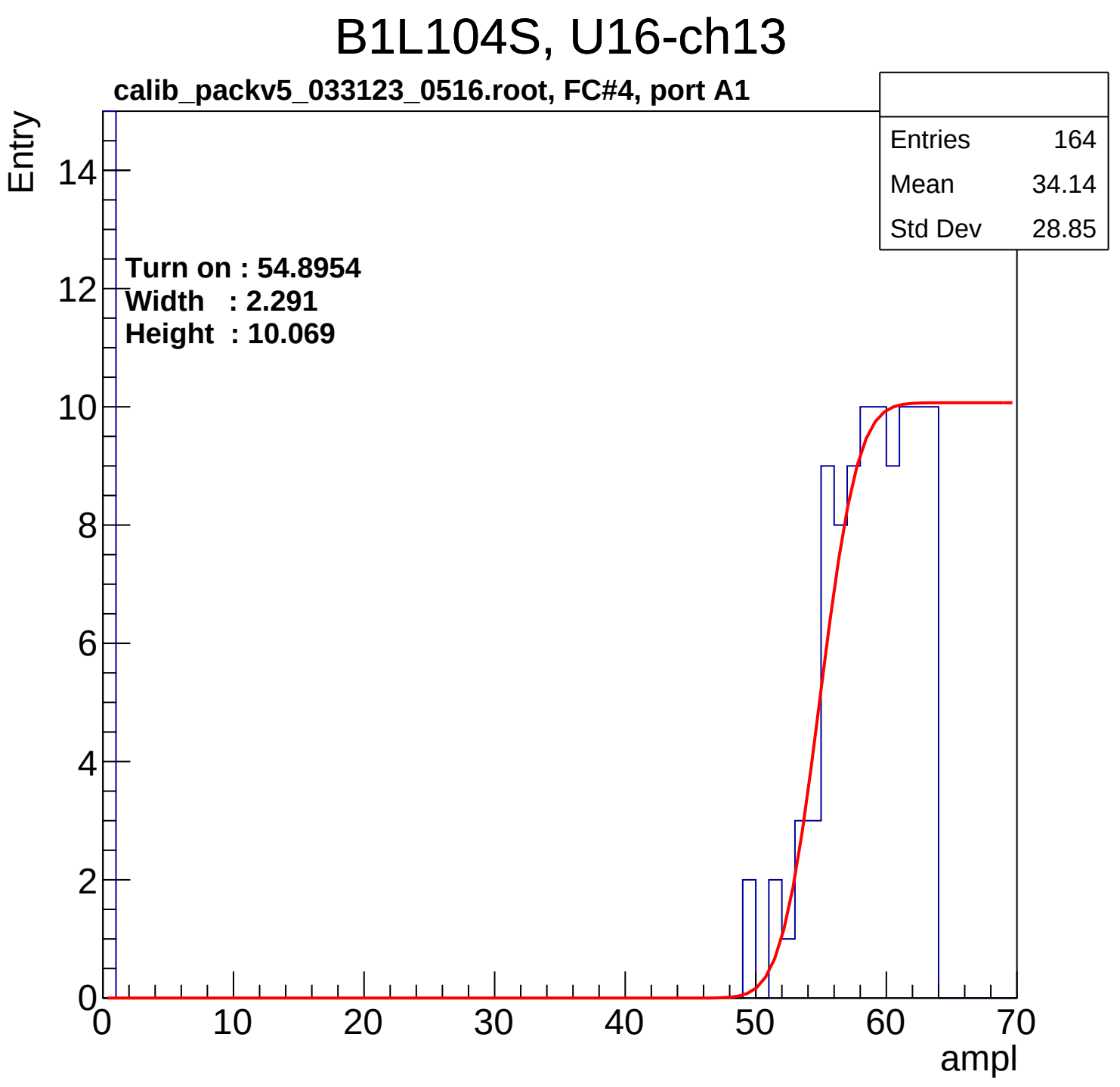
B1L104S, U16-ch13
calib_packv5_033123_0516.root, FC#4, port A1
Entries 164
Mean 34.14
Std Dev 28.85
Turn on : 54.8954
Width : 2.291
Height : 10.069
Entry
ampl
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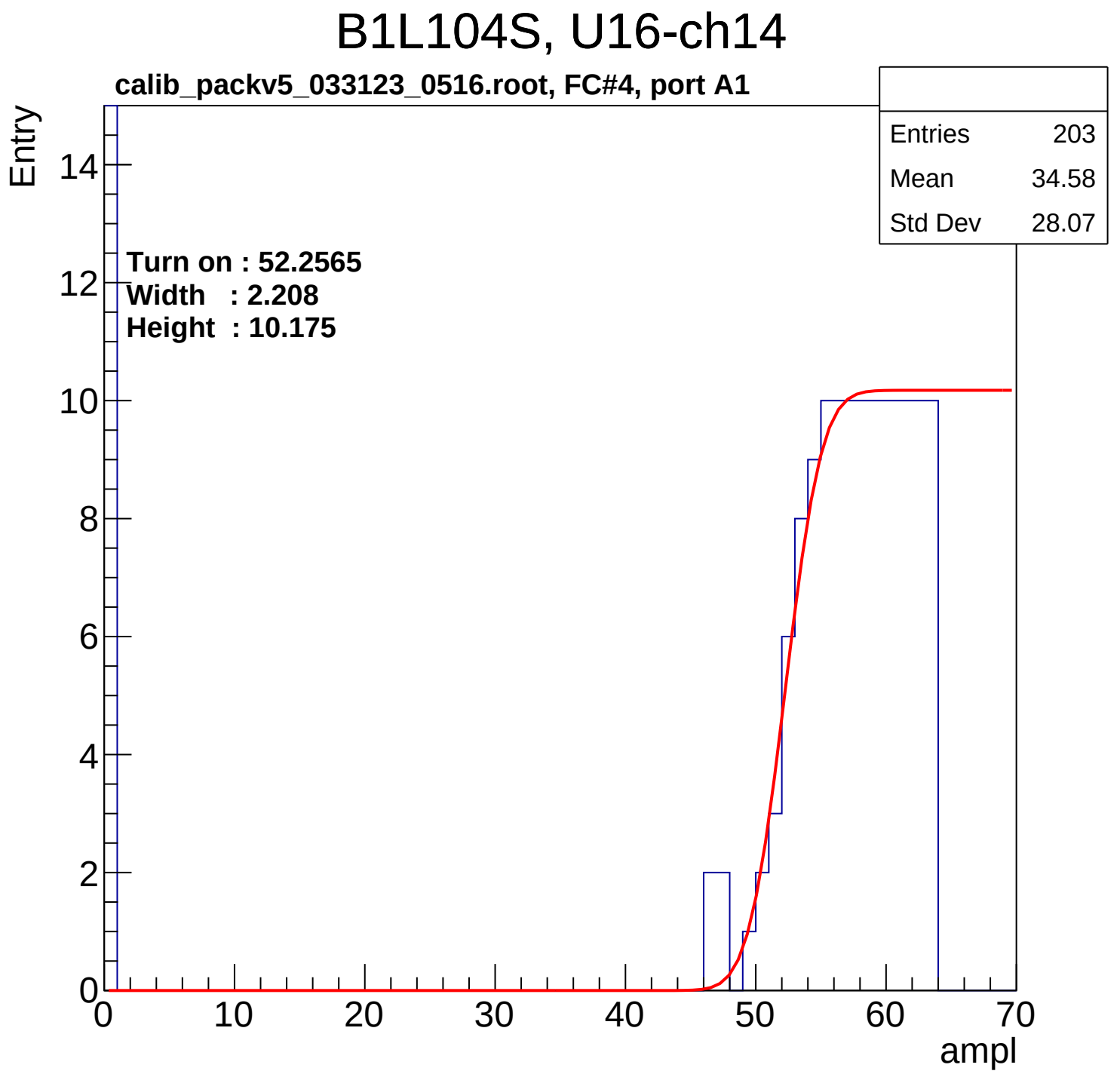
B1L104S, U16-ch14
calib_packv5_033123_0516.root, FC#4, port A1
Entries 203
Mean 34.58
Std Dev 28.07
Turn on : 52.2565
Width : 2.208
Height : 10.175
Entry
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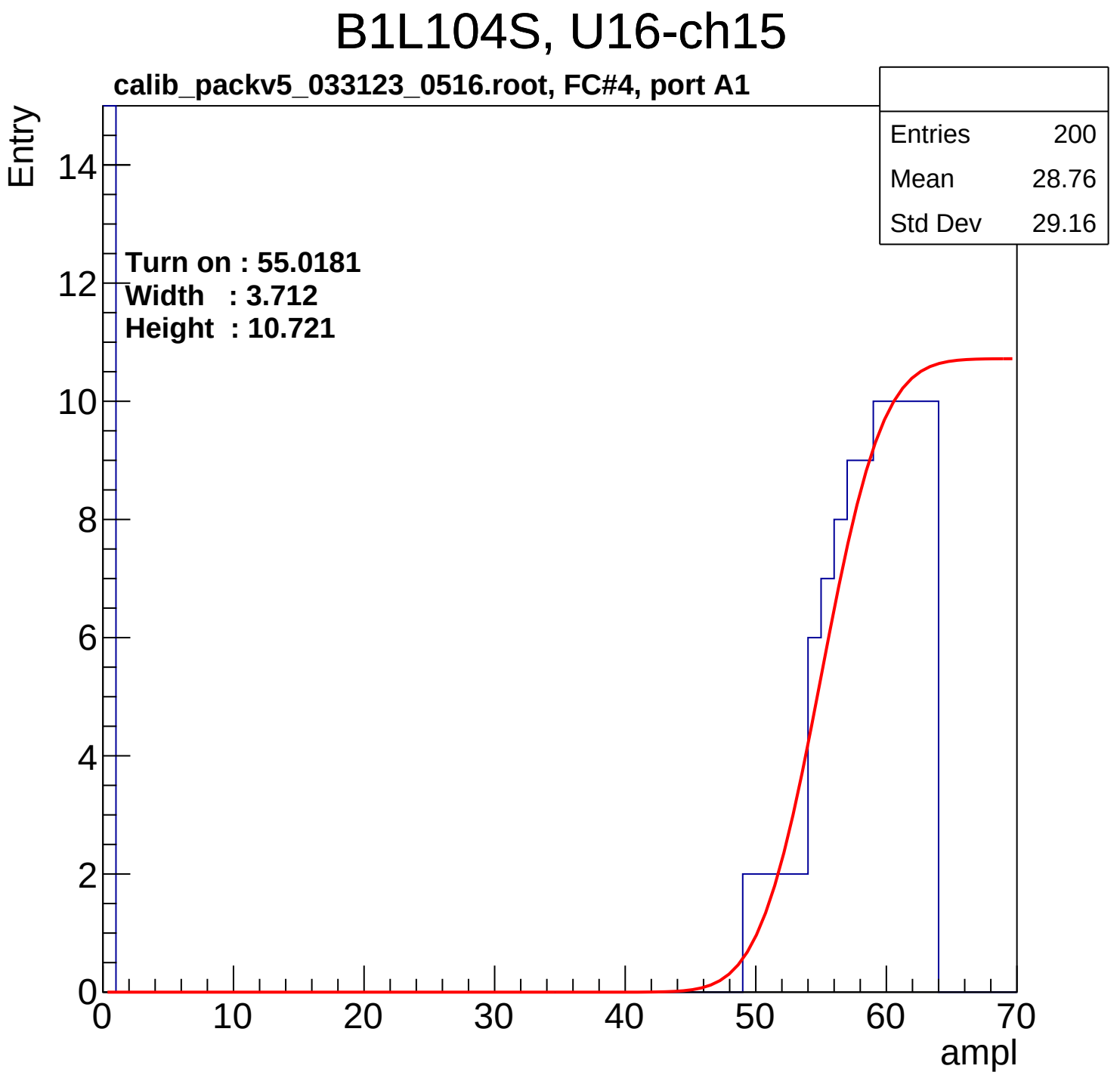
B1L104S, U16-ch15
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 28.76
Std Dev 29.16
Turn on : 55.0181
Width : 3.712
Height : 10.721
Entry
ampl
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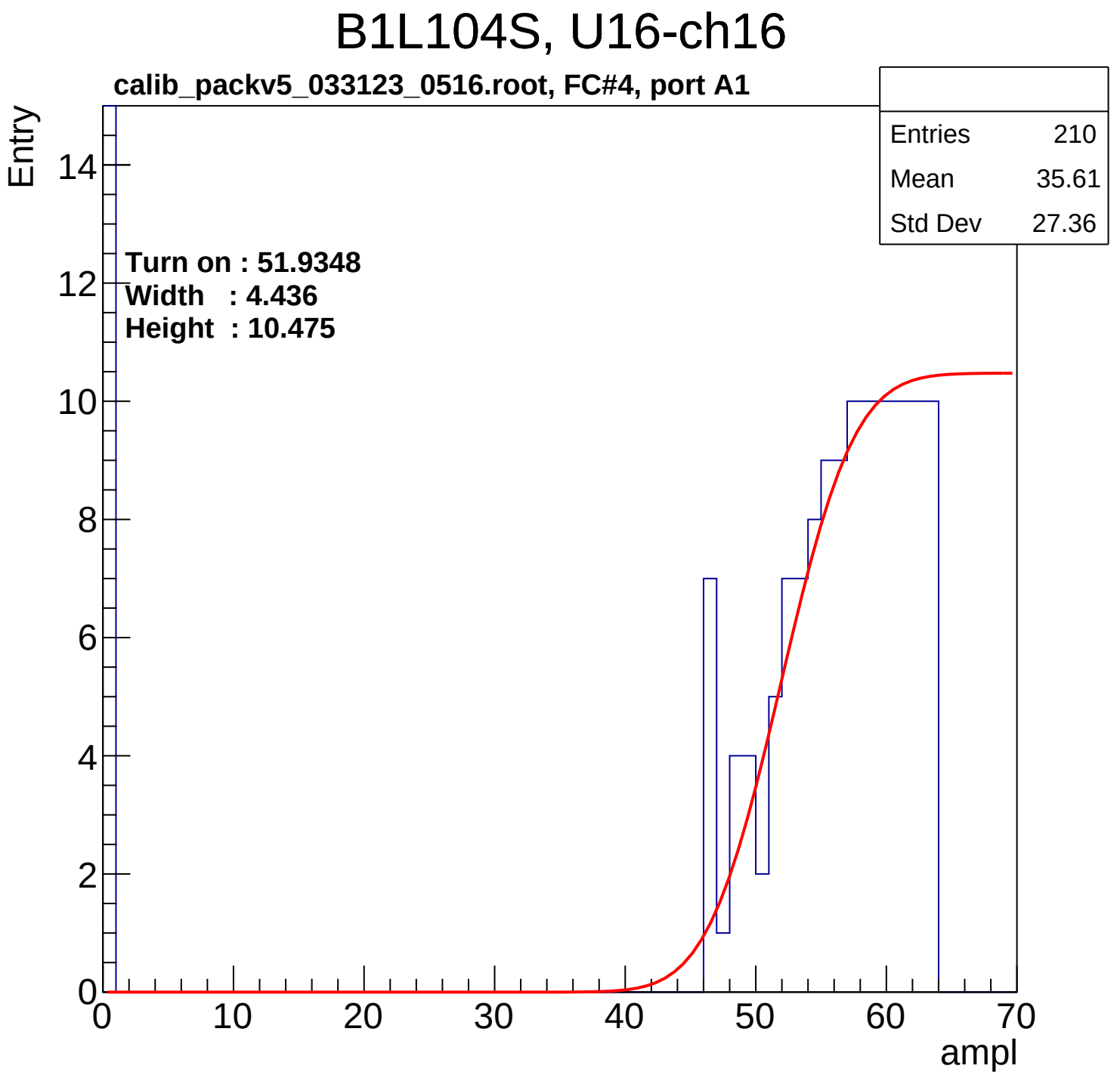
B1L104S, U16-ch16
calib_packv5_033123_0516.root, FC#4, port A1
Entries 210
Mean 35.61
Std Dev 27.36
Turn on : 51.9348
Width : 4.436
Height : 10.475
Entry
ampl
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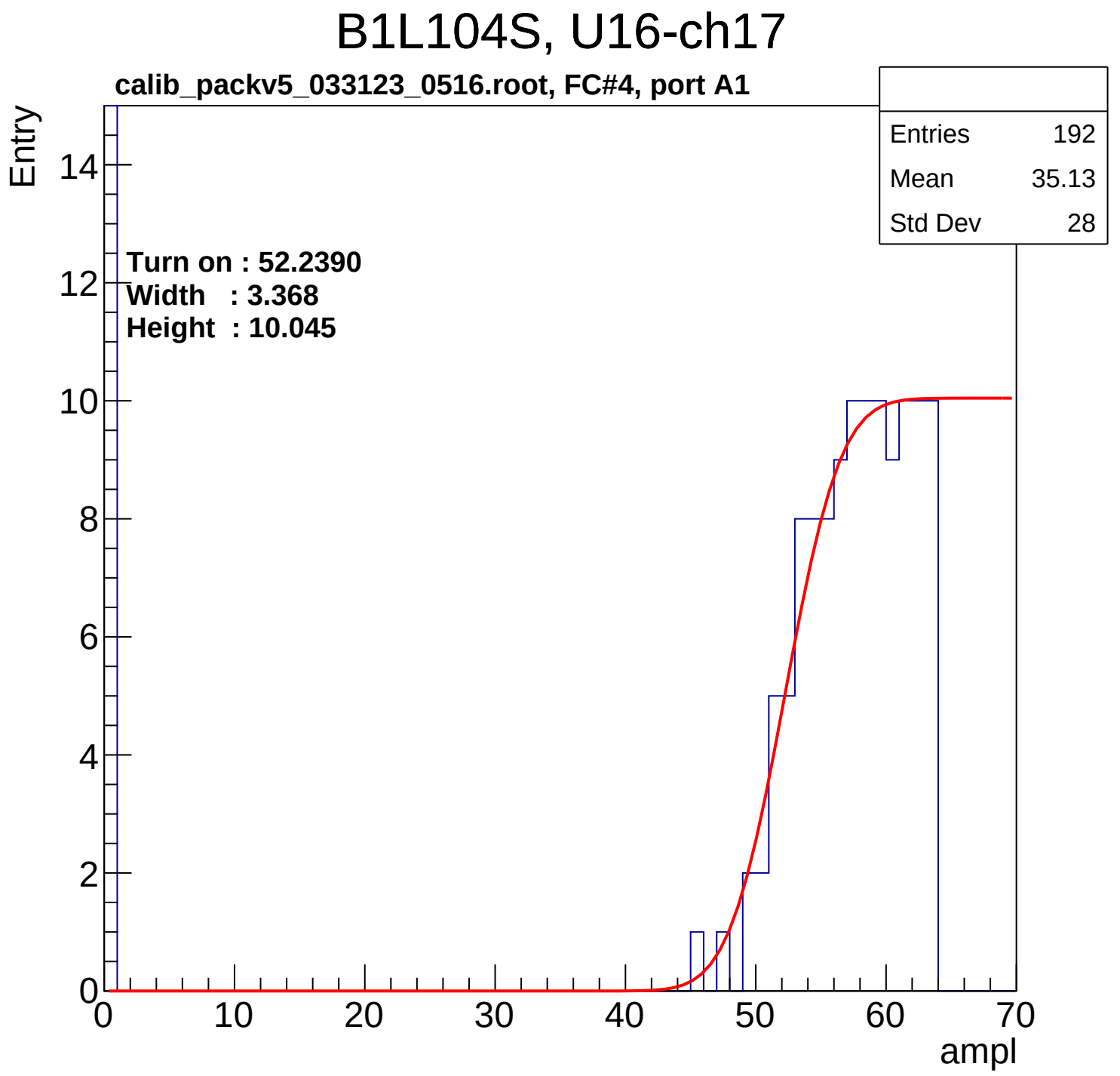
B1L104S, U16-ch17
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 35.13
Std Dev 28
Turn on : 52.2390
Width : 3.368
Height : 10.045
Entry
ampl
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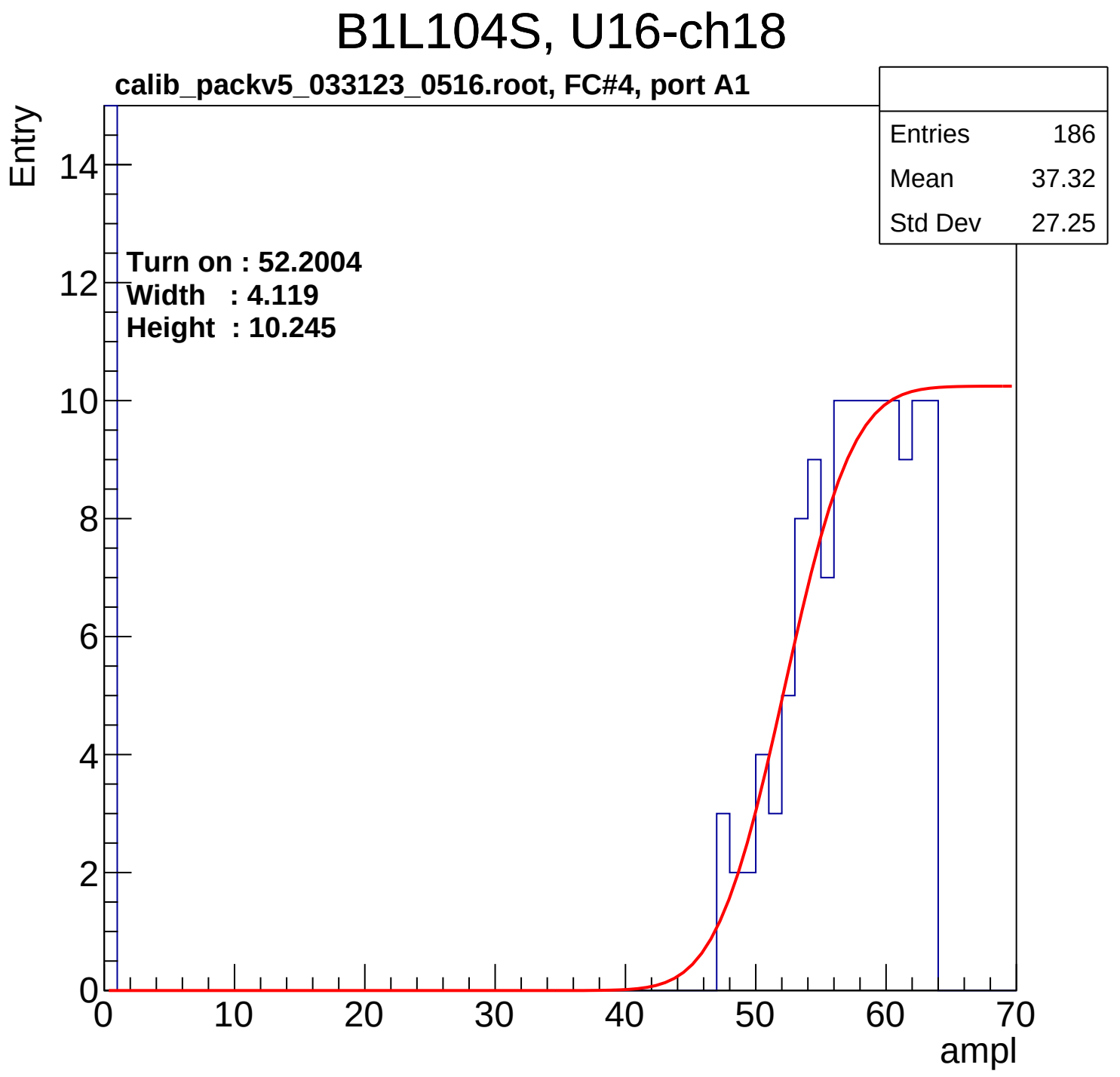
B1L104S, U16-ch18
calib_packv5_033123_0516.root, FC#4, port A1
Entries 186
Mean 37.32
Std Dev 27.25
Turn on : 52.2004
Width : 4.119
Height : 10.245
Entry
ampl
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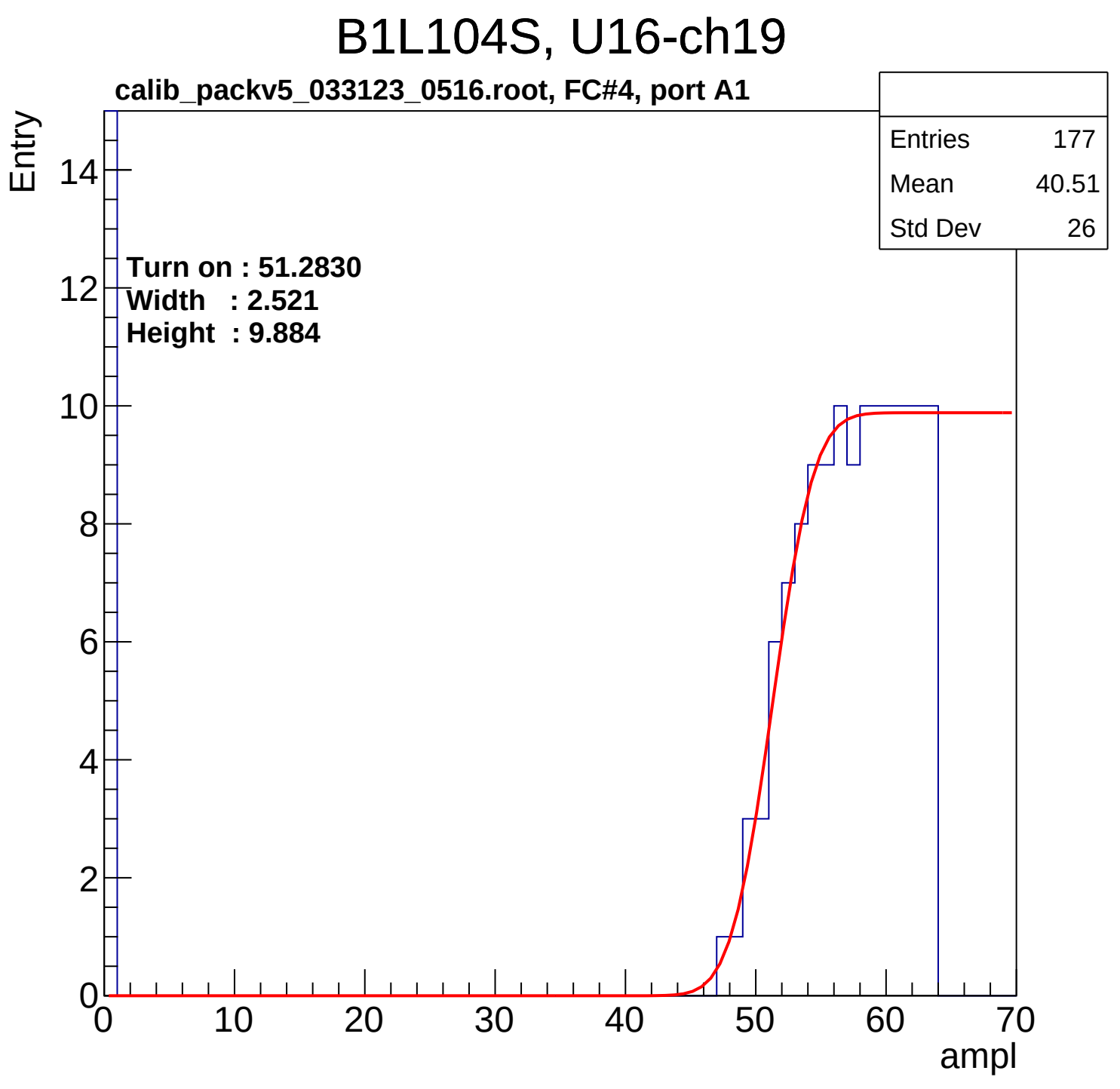
B1L104S, U16-ch19
calib_packv5_033123_0516.root, FC#4, port A1
Entries 177
Mean 40.51
Std Dev 26
Turn on : 51.2830
Width : 2.521
Height : 9.884
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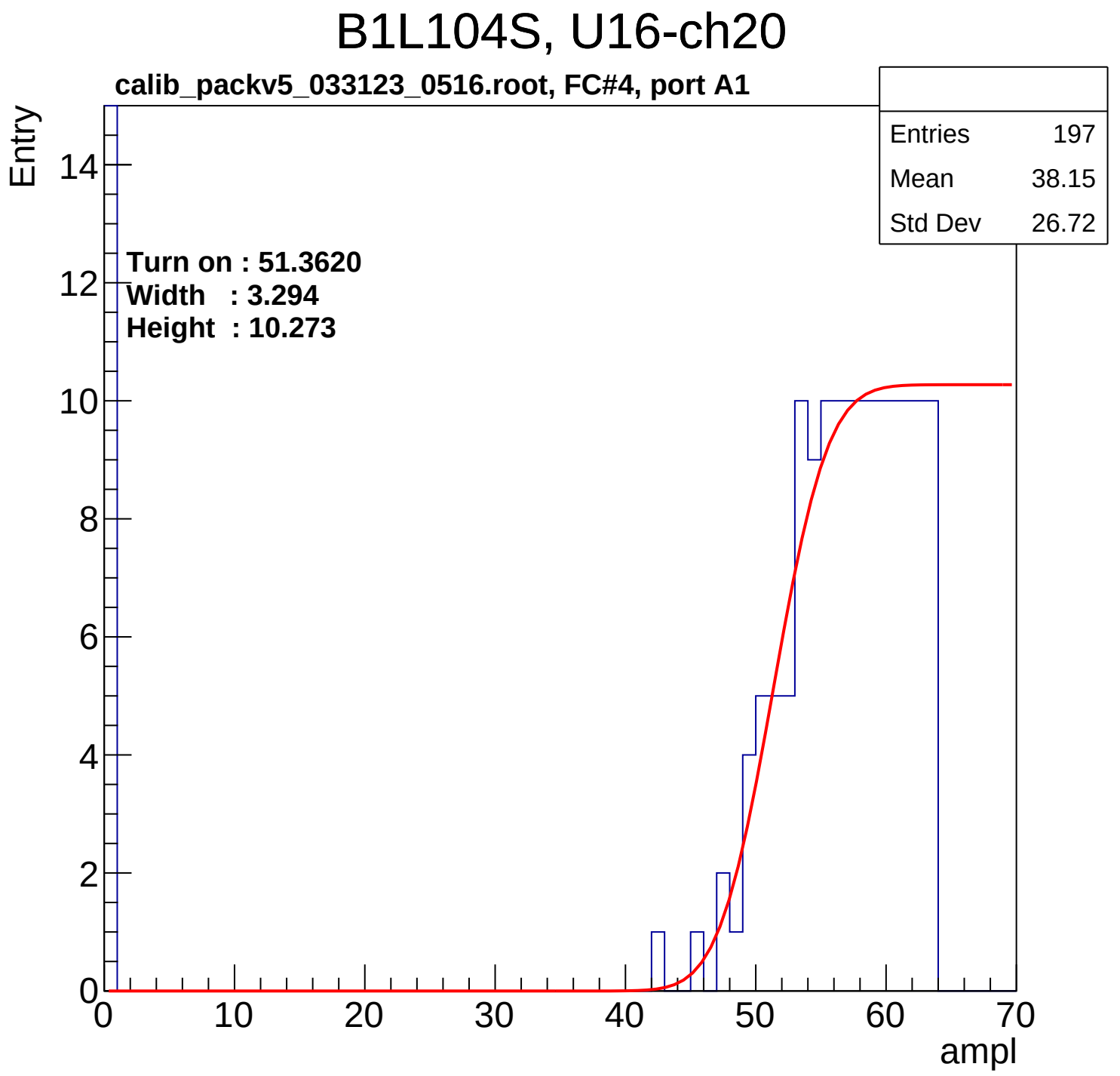
B1L104S, U16-ch20
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 38.15
Std Dev 26.72
Turn on : 51.3620
Width : 3.294
Height : 10.273
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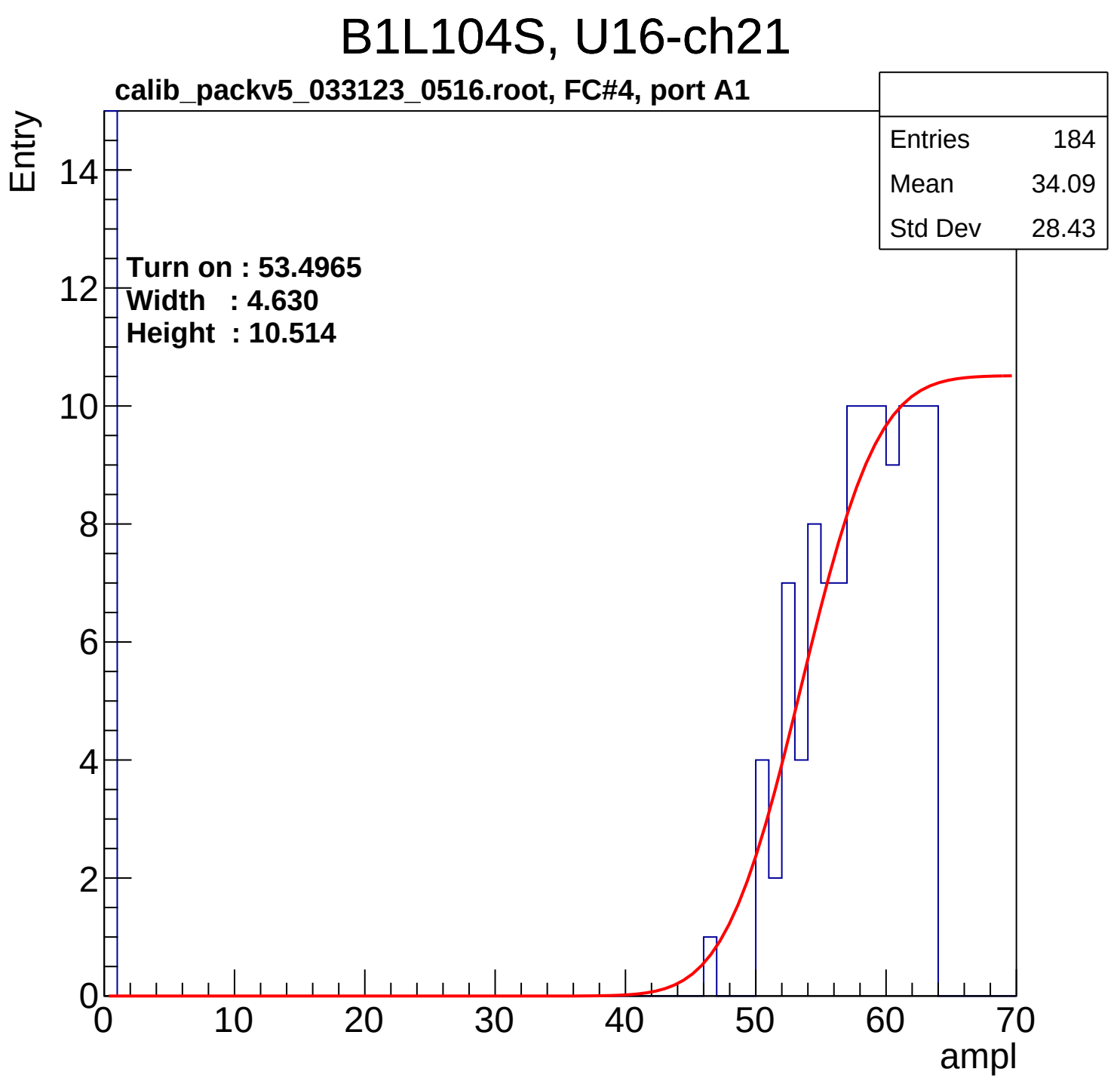
B1L104S, U16-ch21
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 34.09
Std Dev 28.43
Turn on : 53.4965
Width : 4.630
Height : 10.514
Entry
ampl
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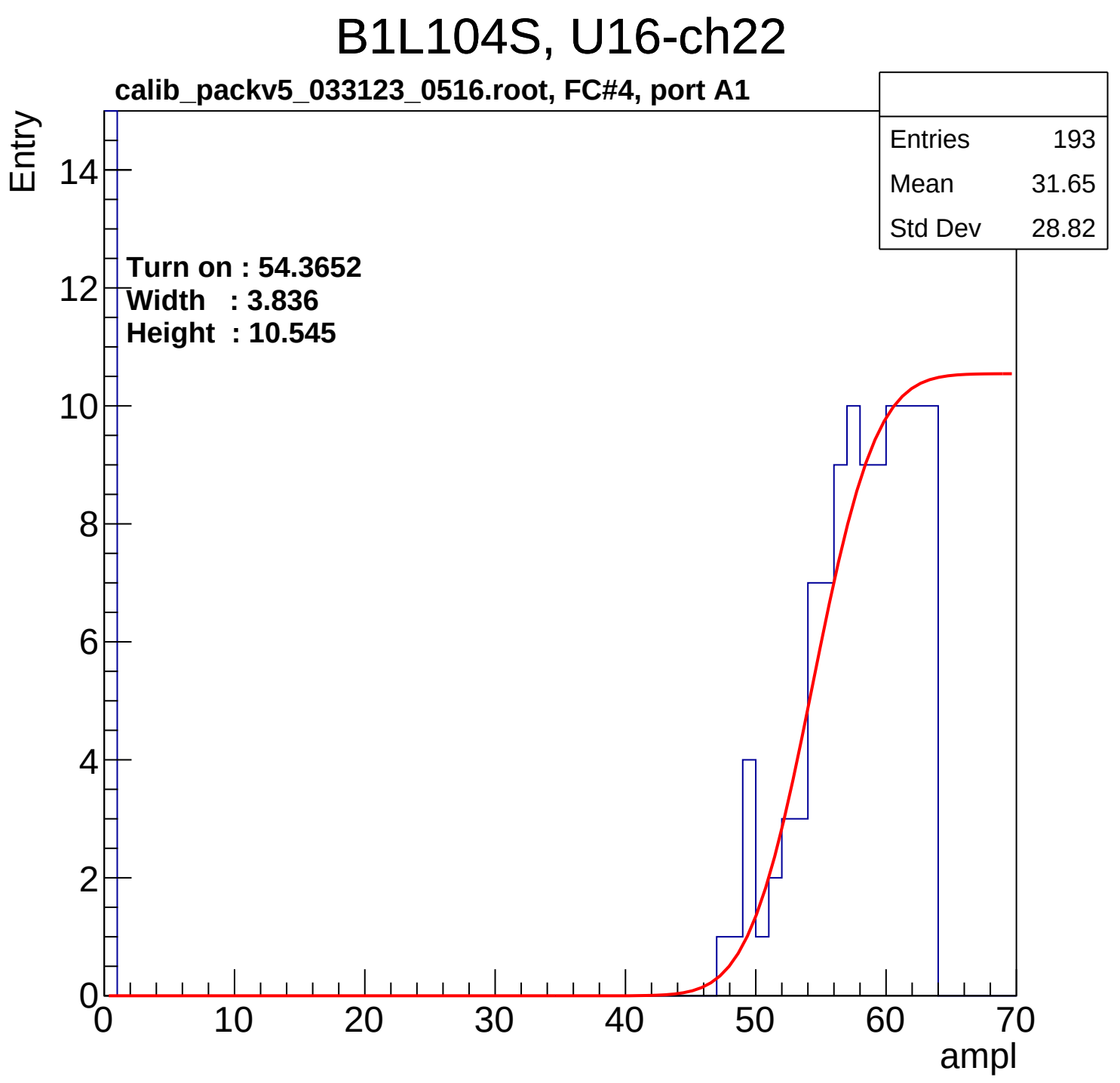
B1L104S, U16-ch22
calib_packv5_033123_0516.root, FC#4, port A1
Entries 193
Mean 31.65
Std Dev 28.82
Turn on : 54.3652
Width : 3.836
Height : 10.545
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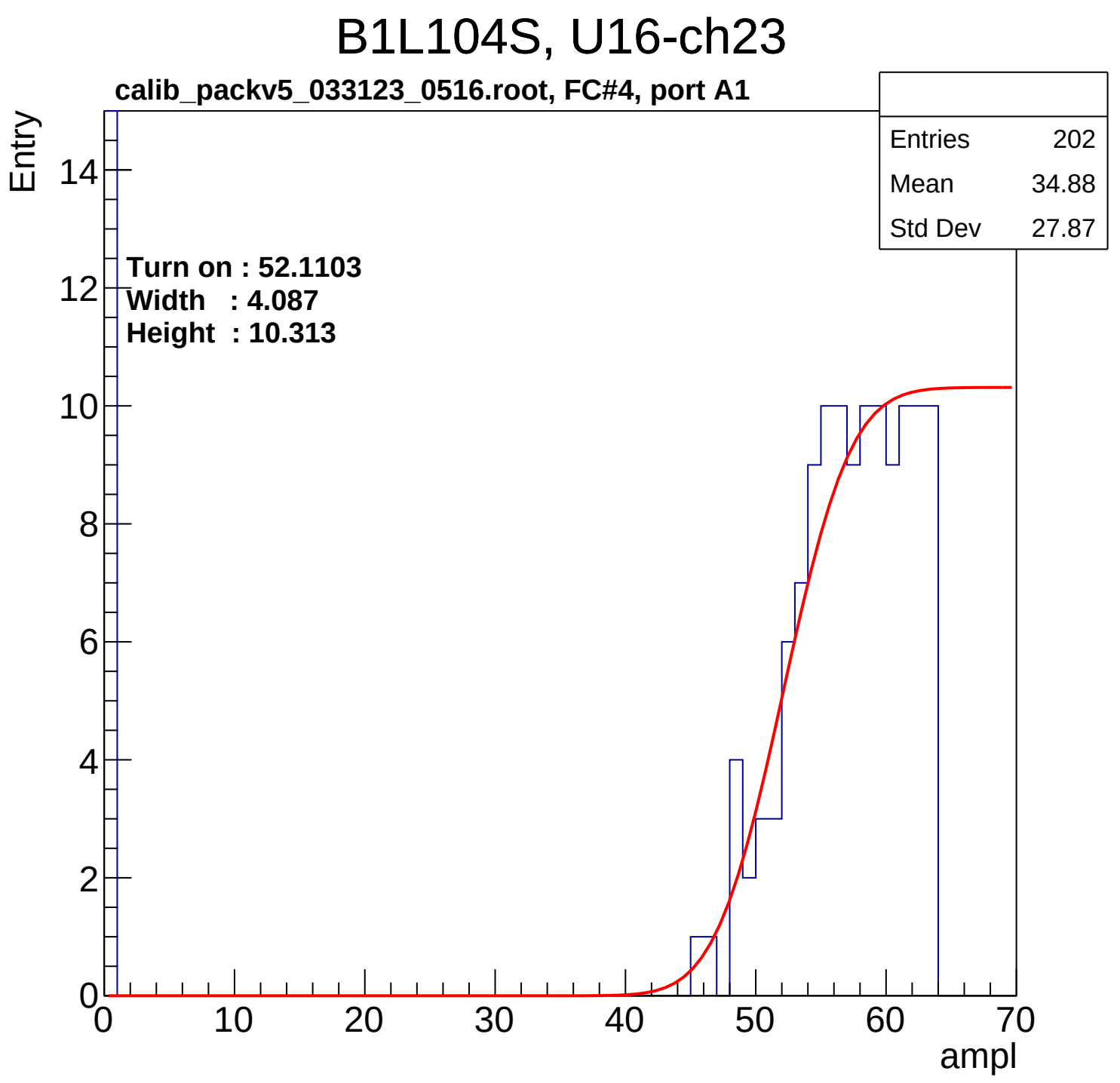
B1L104S, U16-ch23
calib_packv5_033123_0516.root, FC#4, port A1
Entries 202
Mean 34.88
Std Dev 27.87
Turn on : 52.1103
Width : 4.087
Height : 10.313
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ampl
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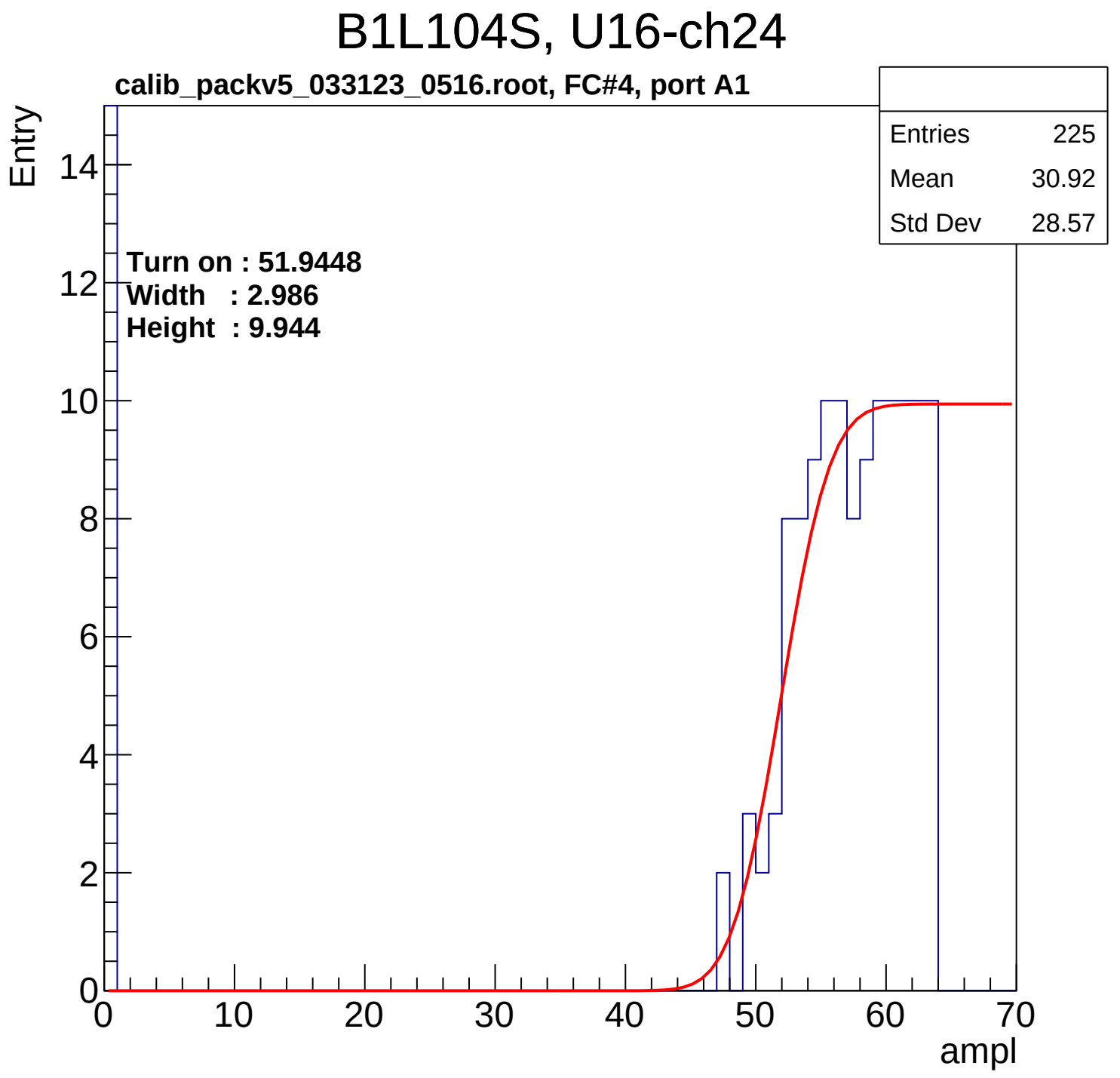
B1L104S, U16-ch24
calib_packv5_033123_0516.root, FC#4, port A1
Entries 225
Mean 30.92
Std Dev 28.57
Turn on : 51.9448
Width : 2.986
Height : 9.944
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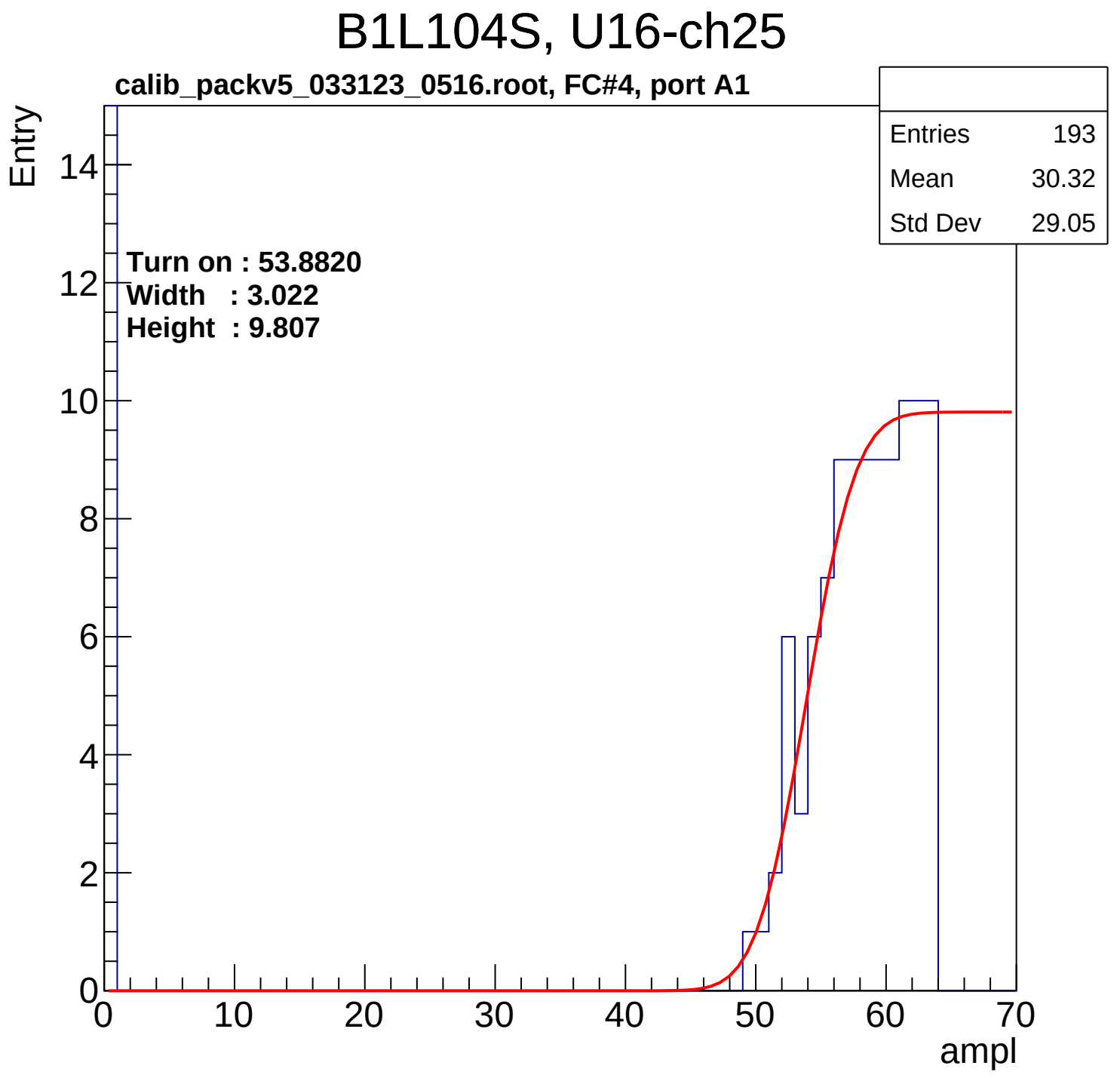
B1L104S, U16-ch25
calib_packv5_033123_0516.root, FC#4, port A1
Entries 193
Mean 30.32
Std Dev 29.05
Turn on : 53.8820
Width : 3.022
Height : 9.807
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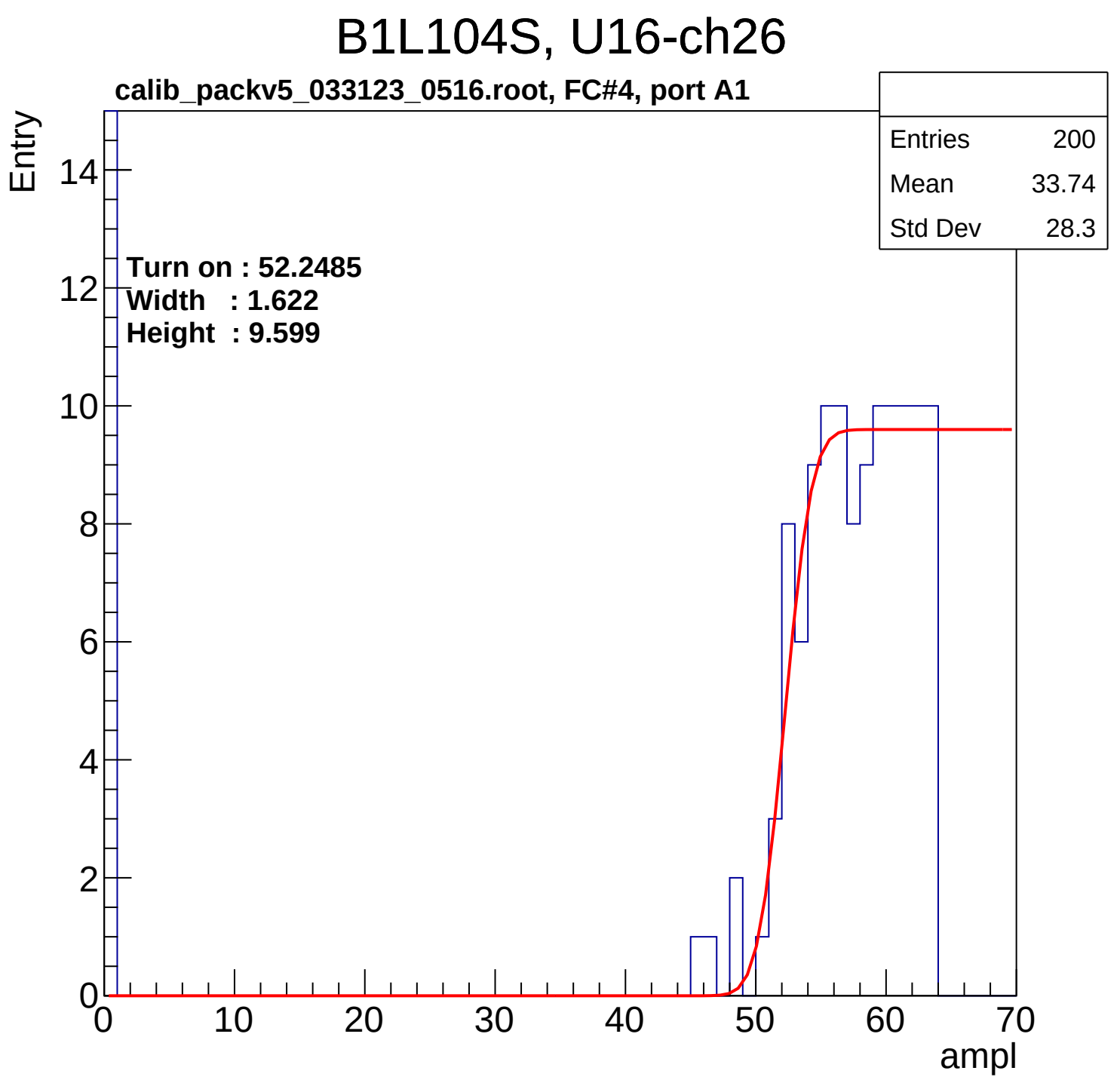
B1L104S, U16-ch26
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 33.74
Std Dev 28.3
Turn on : 52.2485
Width : 1.622
Height : 9.599
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ampl
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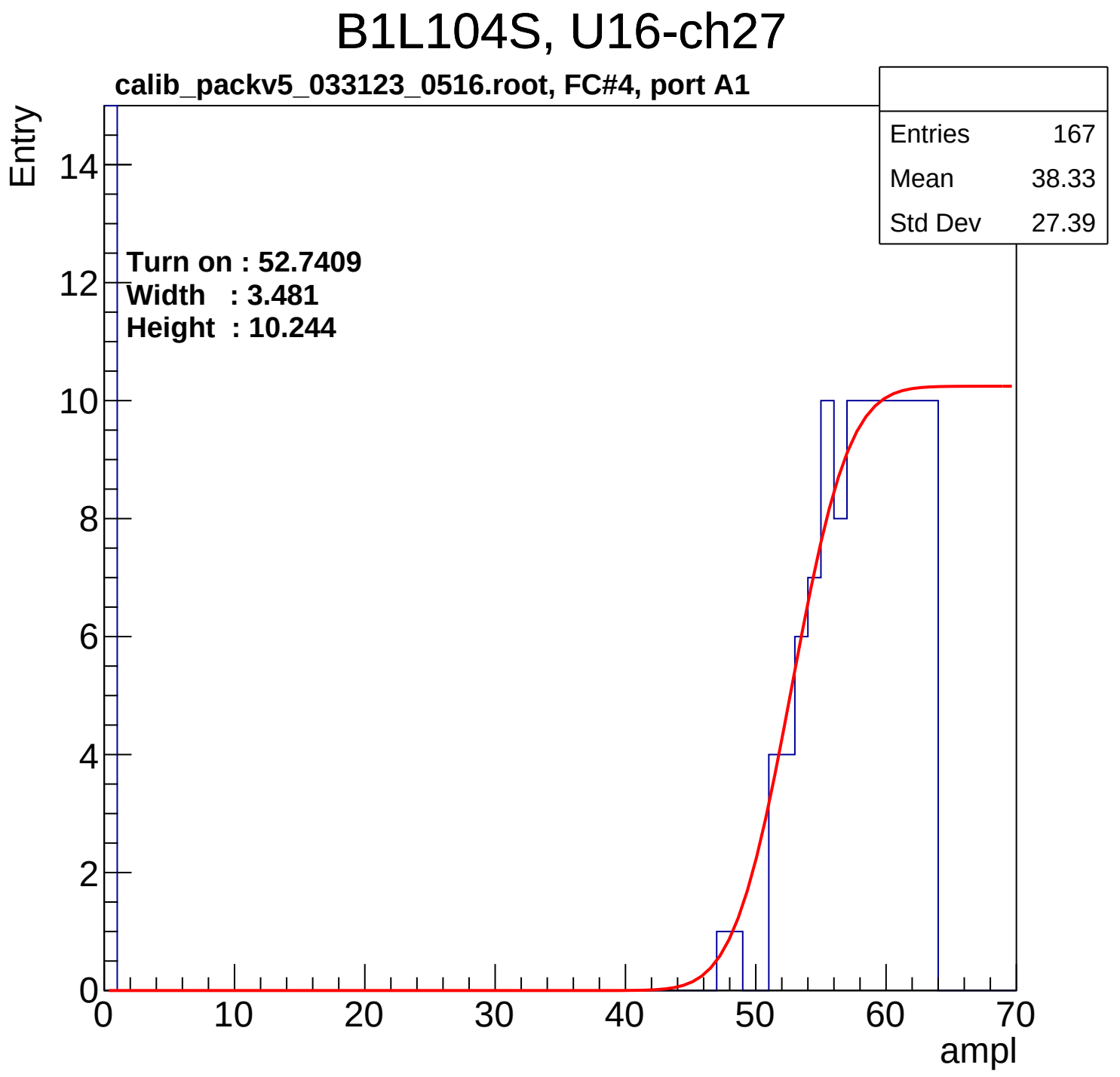
B1L104S, U16-ch27
calib_packv5_033123_0516.root, FC#4, port A1
Entries 167
Mean 38.33
Std Dev 27.39
Turn on : 52.7409
Width : 3.481
Height : 10.244
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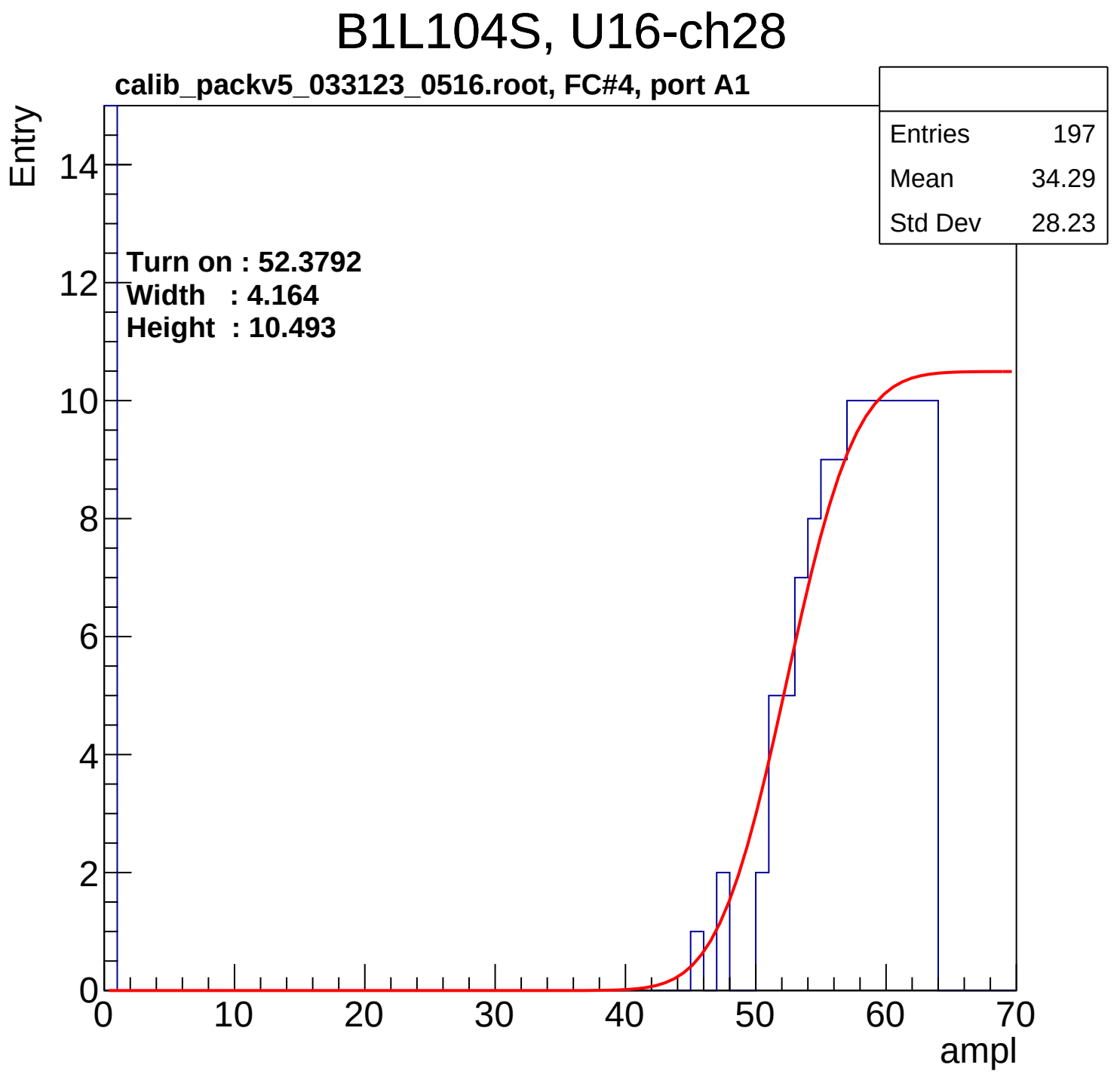
B1L104S, U16-ch28
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 34.29
Std Dev 28.23
Turn on : 52.3792
Width : 4.164
Height : 10.493
Entry
ampl
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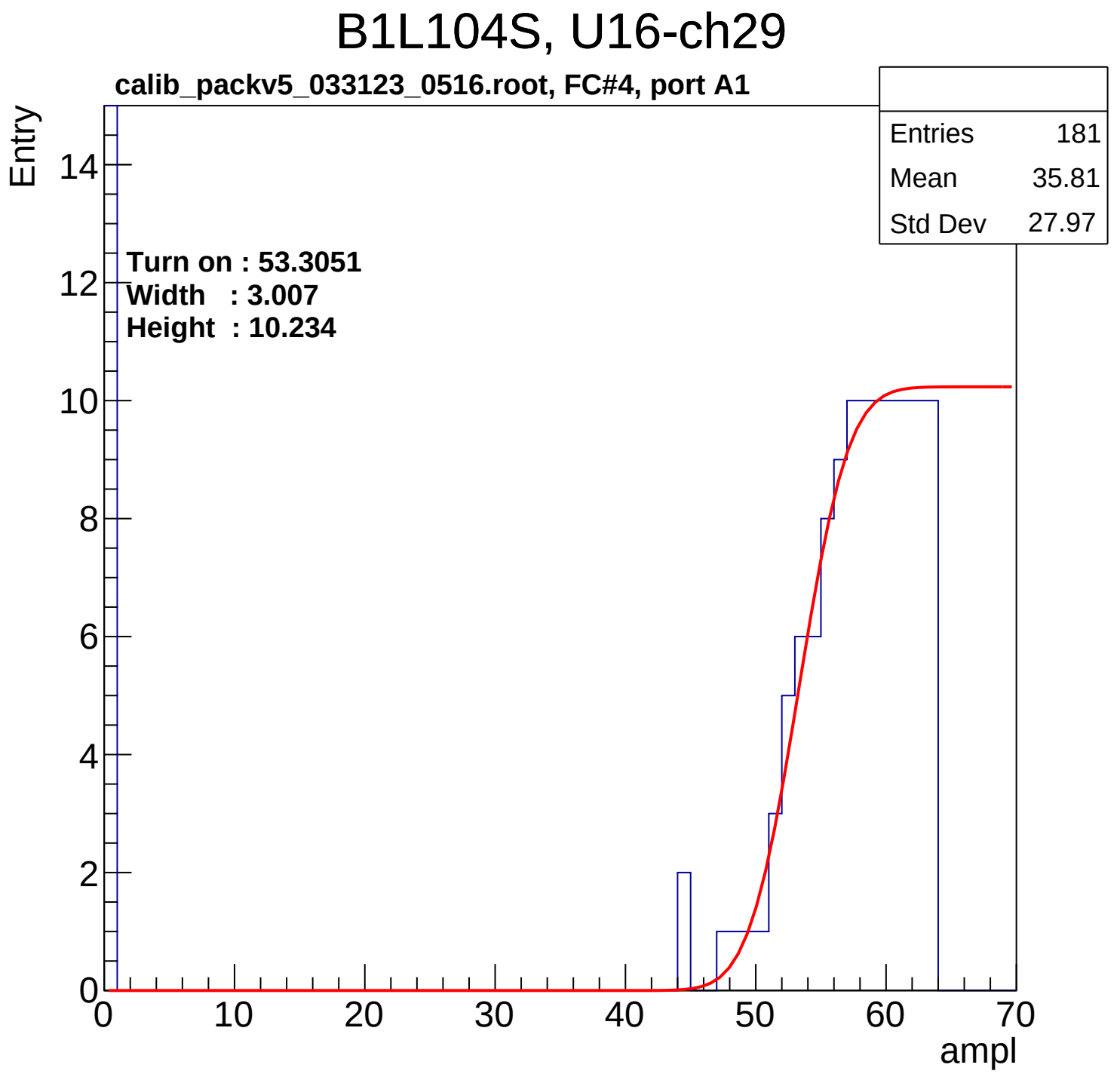
B1L104S, U16-ch29
calib_packv5_033123_0516.root, FC#4, port A1
Entries 181
Mean 35.81
Std Dev 27.97
Turn on : 53.3051
Width : 3.007
Height : 10.234
Entry
ampl
0
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4
6
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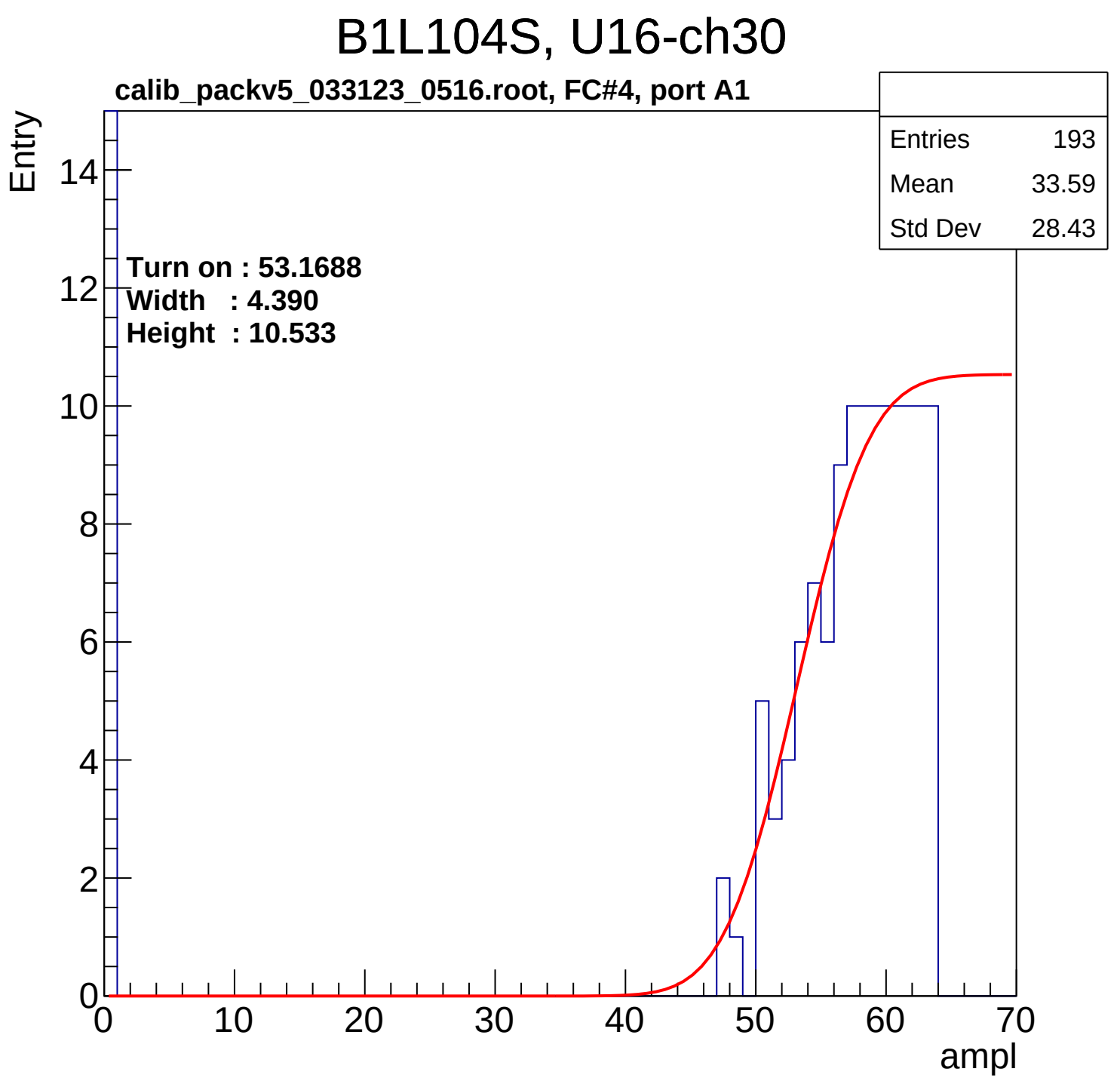
B1L104S, U16-ch30
calib_packv5_033123_0516.root, FC#4, port A1
Entries 193
Mean 33.59
Std Dev 28.43
Turn on : 53.1688
Width : 4.390
Height : 10.533
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ampl
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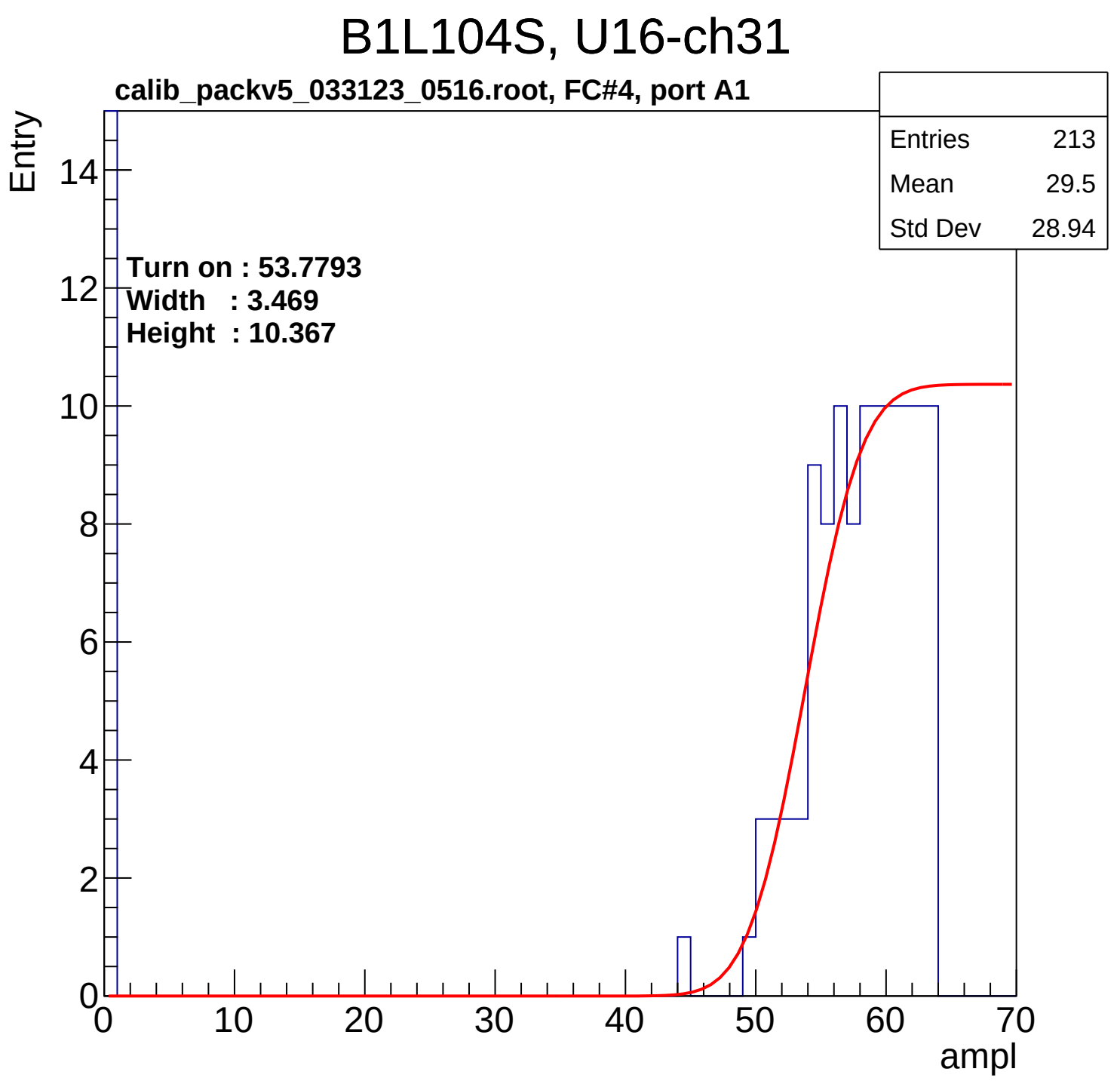
B1L104S, U16-ch31
calib_packv5_033123_0516.root, FC#4, port A1
Entries 213
Mean 29.5
Std Dev 28.94
Turn on : 53.7793
Width : 3.469
Height : 10.367
Entry
ampl
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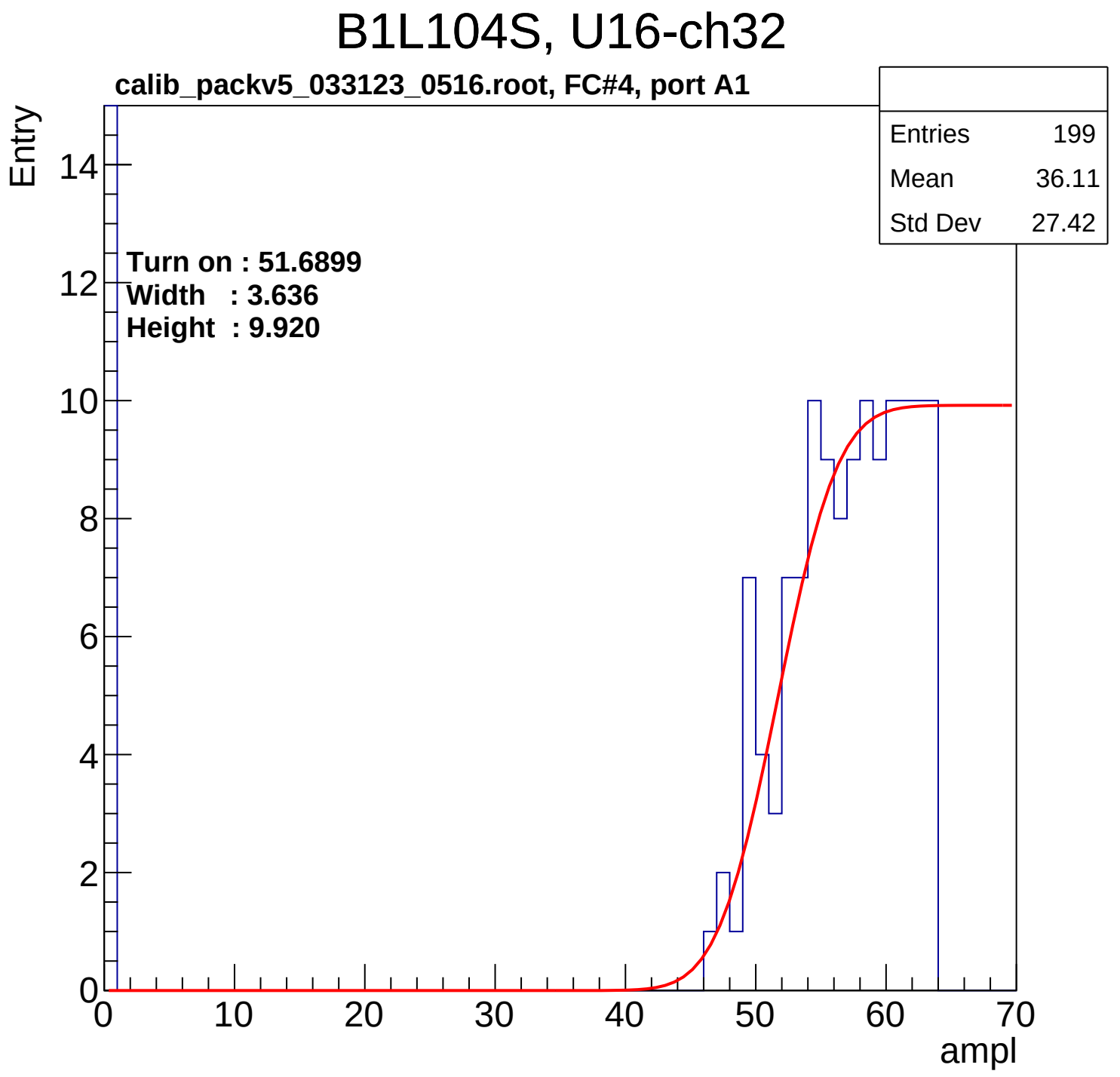
B1L104S, U16-ch32
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 36.11
Std Dev 27.42
Turn on : 51.6899
Width : 3.636
Height : 9.920
Entry
ampl
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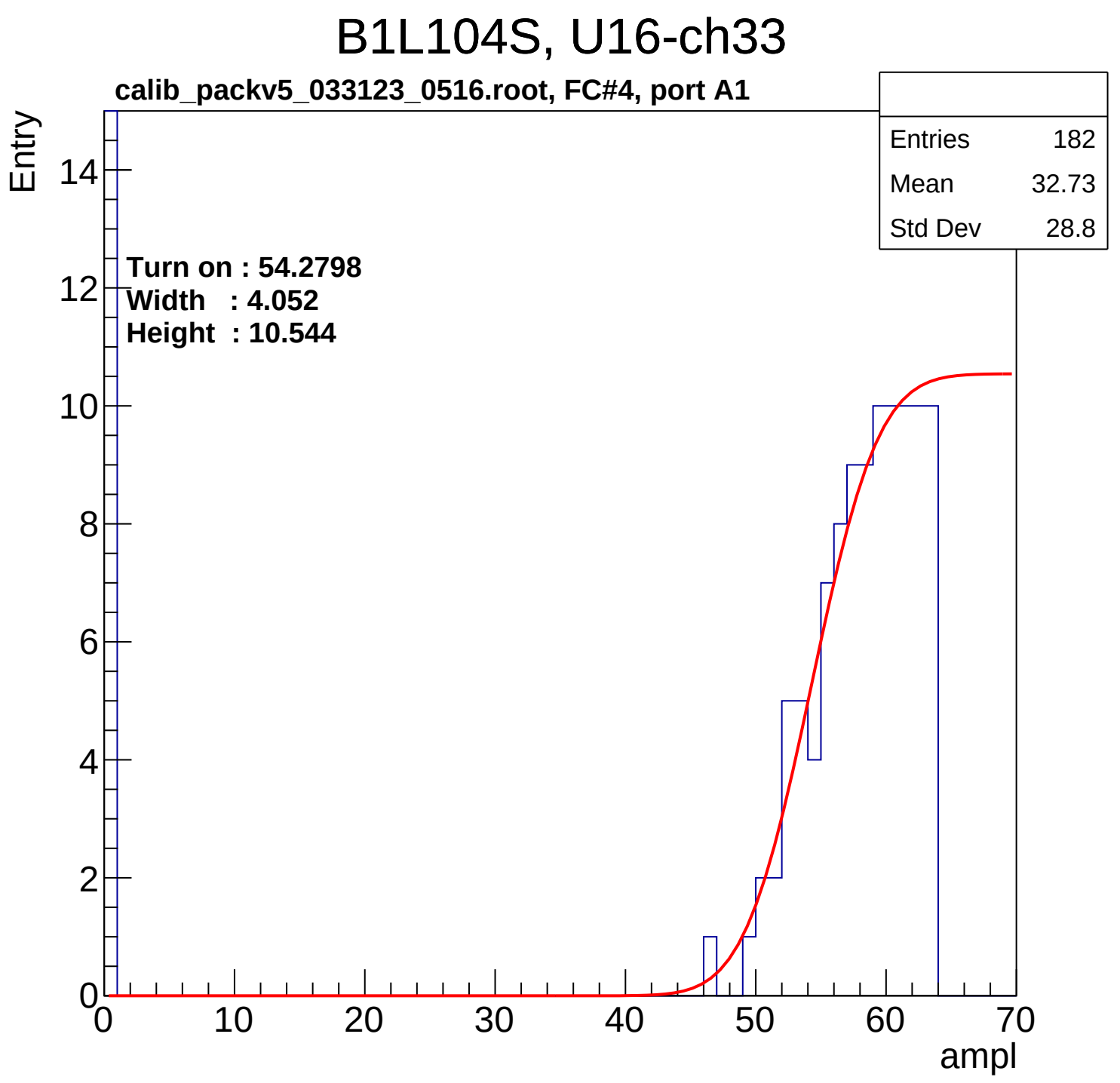
B1L104S, U16-ch33
calib_packv5_033123_0516.root, FC#4, port A1
Entries 182
Mean 32.73
Std Dev 28.8
Turn on : 54.2798
Width : 4.052
Height : 10.544
Entry
ampl
0
2
4
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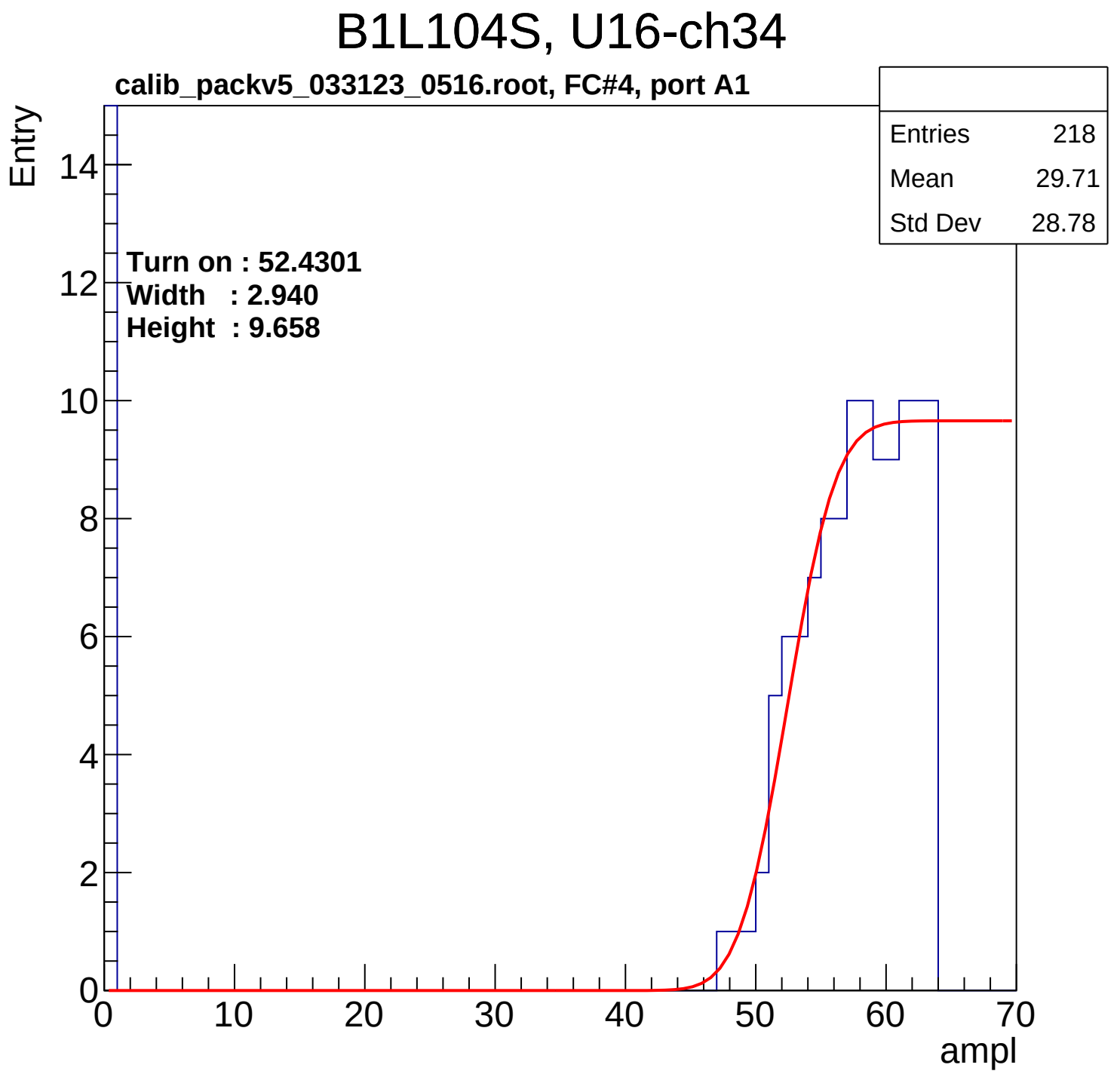
B1L104S, U16-ch34
calib_packv5_033123_0516.root, FC#4, port A1
Entries 218
Mean 29.71
Std Dev 28.78
Turn on : 52.4301
Width : 2.940
Height : 9.658
Entry
ampl
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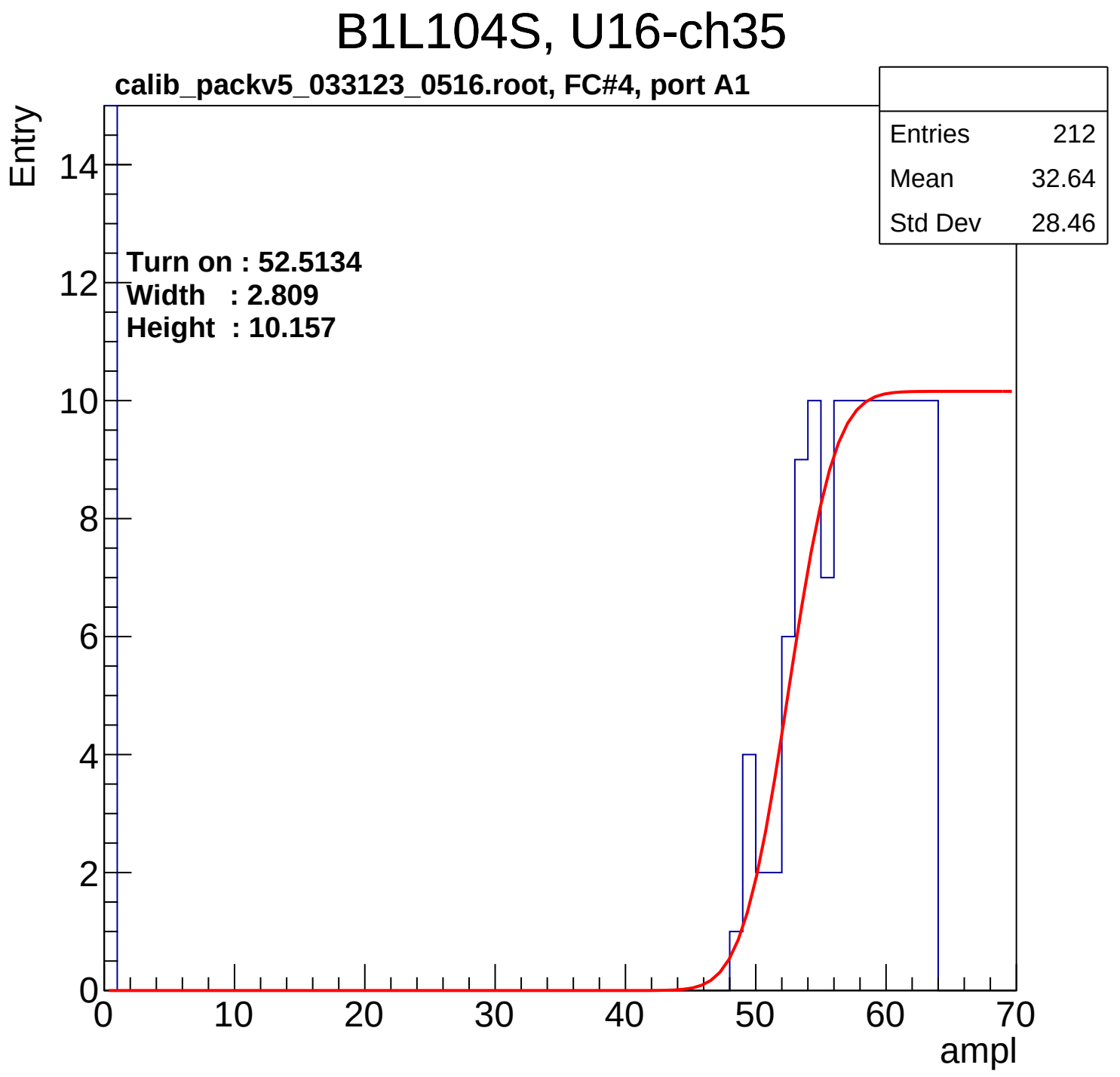
B1L104S, U16-ch35
calib_packv5_033123_0516.root, FC#4, port A1
Entries 212
Mean 32.64
Std Dev 28.46
Turn on : 52.5134
Width : 2.809
Height : 10.157
Entry
ampl
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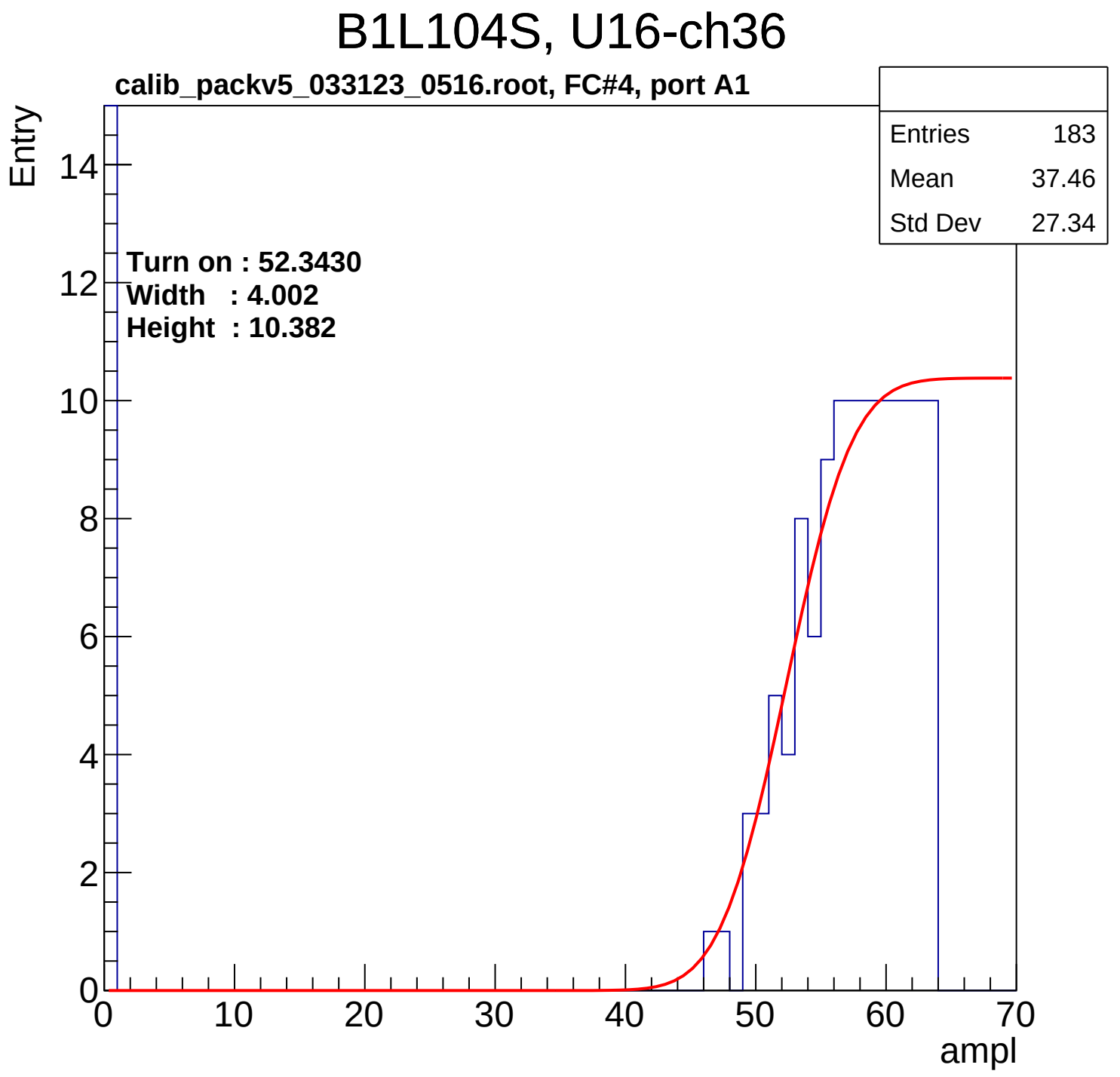
B1L104S, U16-ch36
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 37.46
Std Dev 27.34
Turn on : 52.3430
Width : 4.002
Height : 10.382
Entry
ampl
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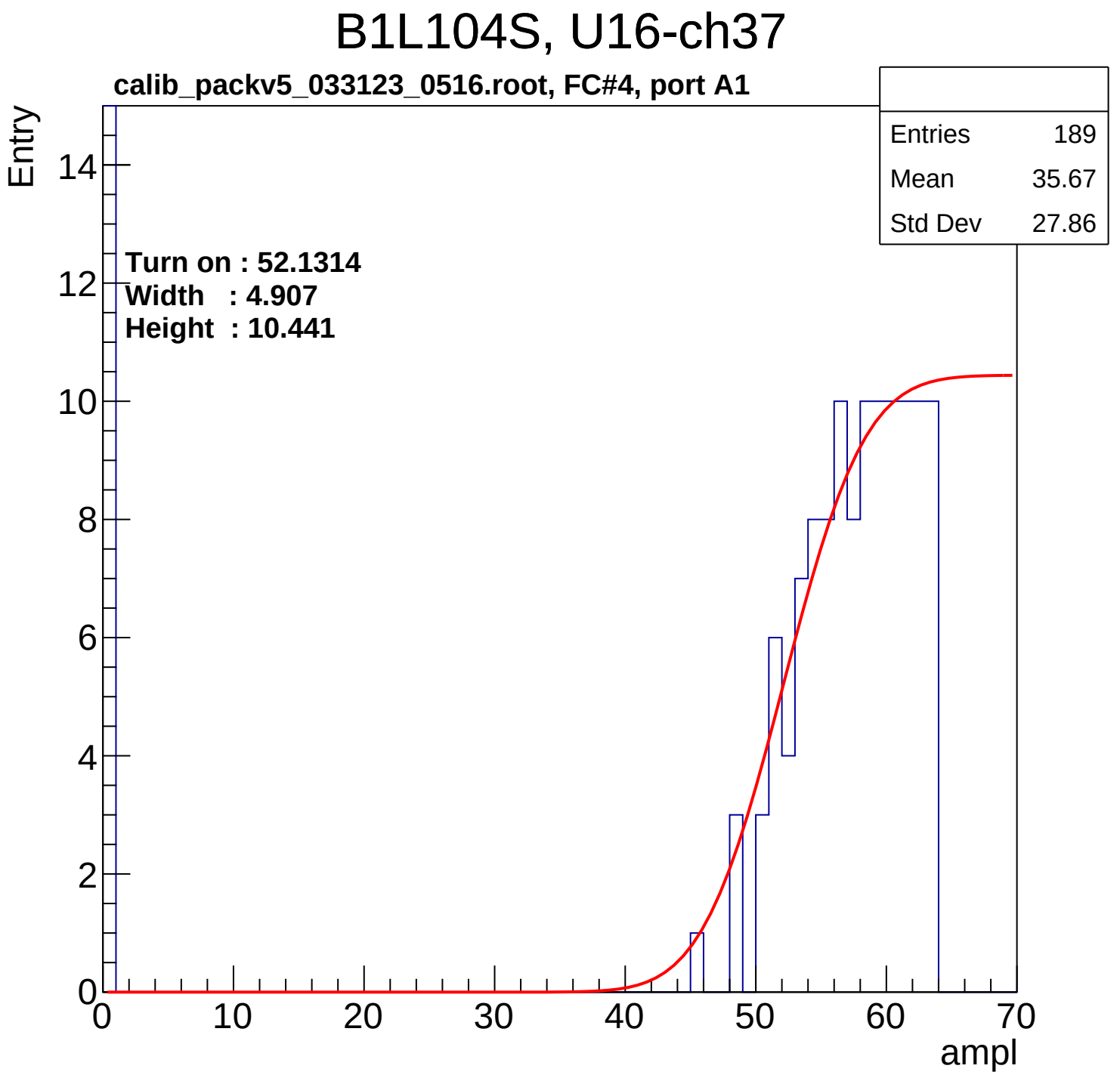
B1L104S, U16-ch37
calib_packv5_033123_0516.root, FC#4, port A1
Entries 189
Mean 35.67
Std Dev 27.86
Turn on : 52.1314
Width : 4.907
Height : 10.441
Entry
ampl
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8
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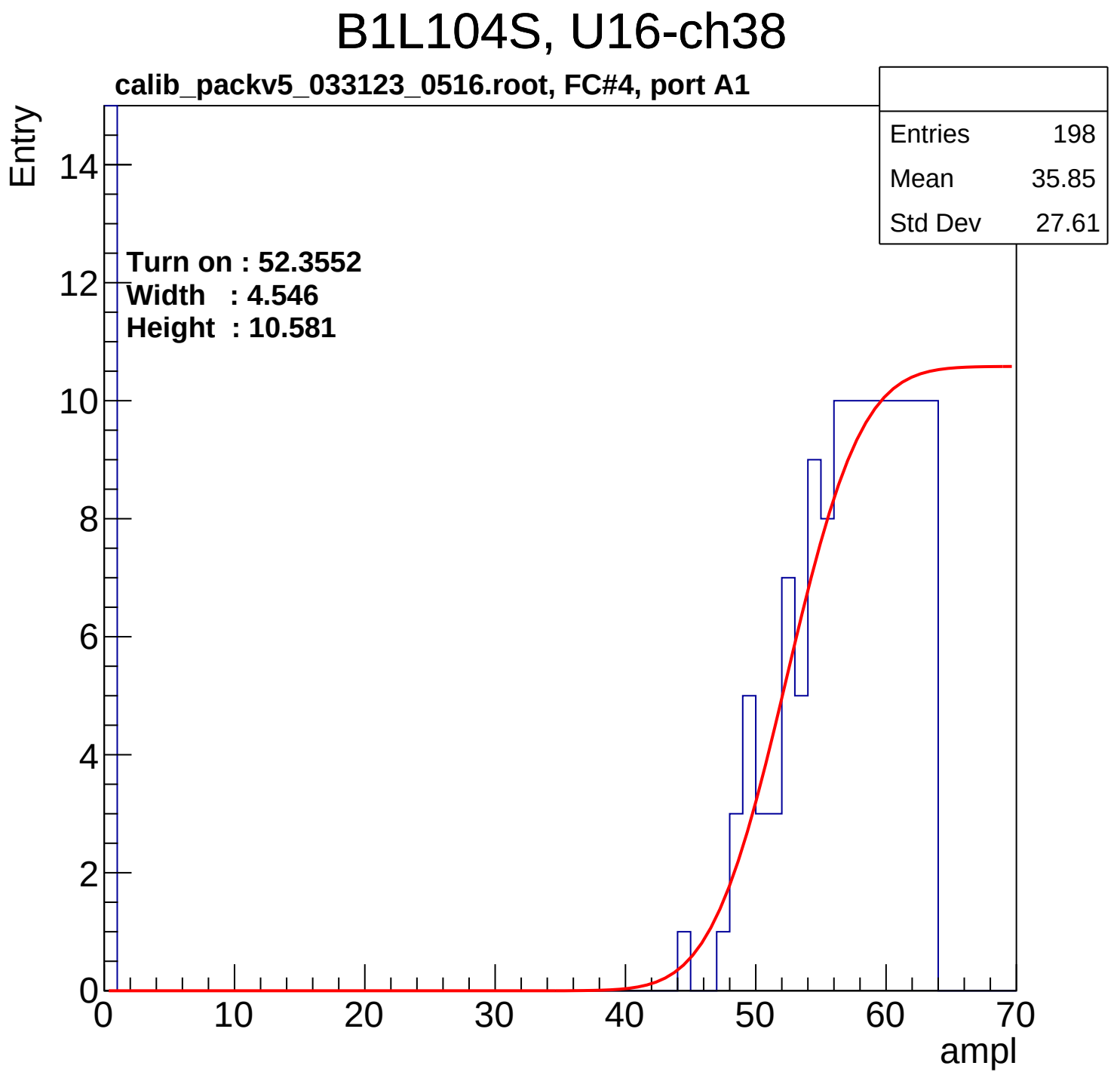
B1L104S, U16-ch38
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 35.85
Std Dev 27.61
Turn on : 52.3552
Width : 4.546
Height : 10.581
Entry
ampl
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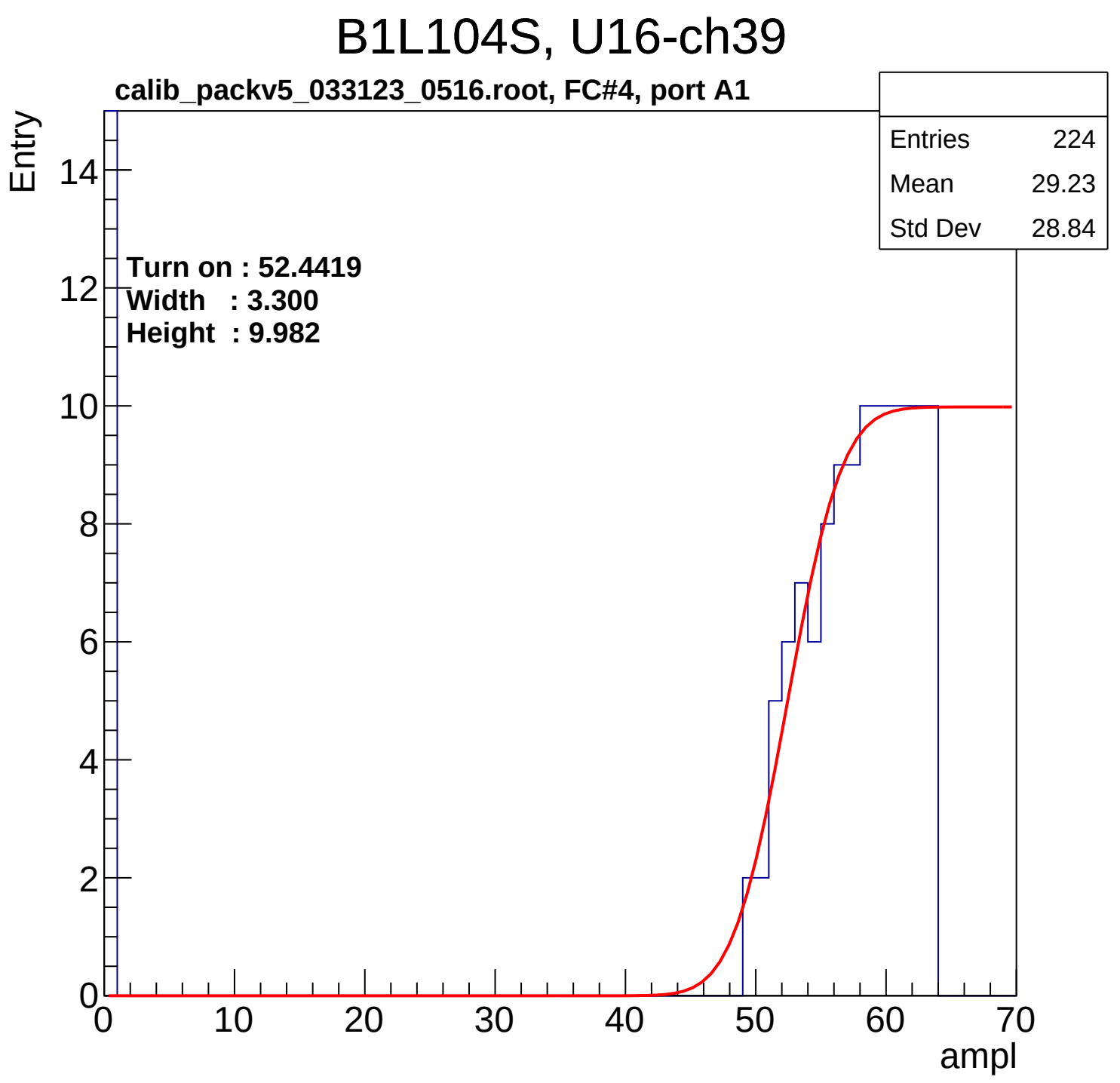
B1L104S, U16-ch39
calib_packv5_033123_0516.root, FC#4, port A1
Entries 224
Mean 29.23
Std Dev 28.84
Turn on : 52.4419
Width : 3.300
Height : 9.982
Entry
ampl
0
2
4
6
8
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12
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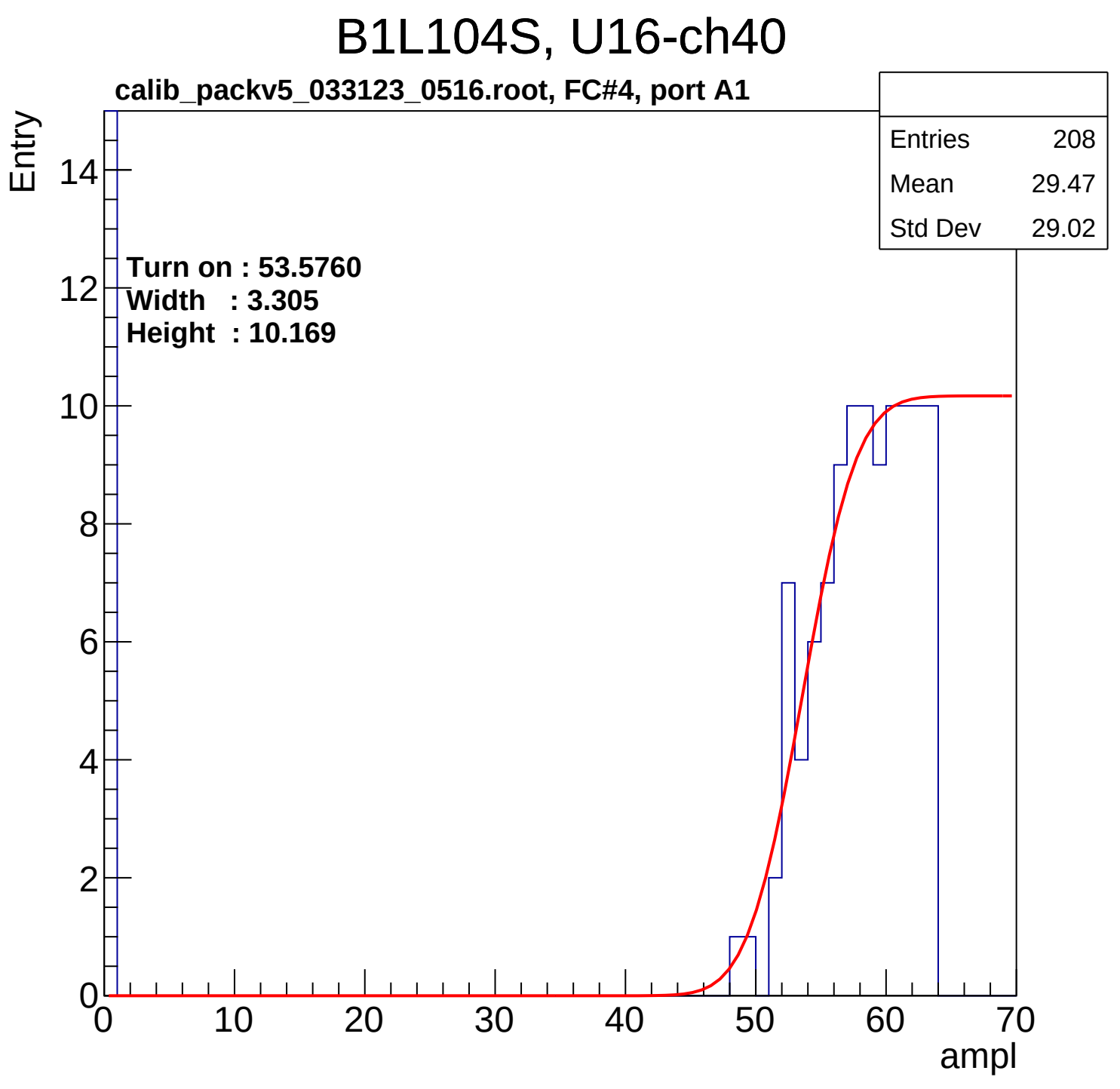
B1L104S, U16-ch40
calib_packv5_033123_0516.root, FC#4, port A1
Entries 208
Mean 29.47
Std Dev 29.02
Turn on : 53.5760
Width : 3.305
Height : 10.169
Entry
ampl
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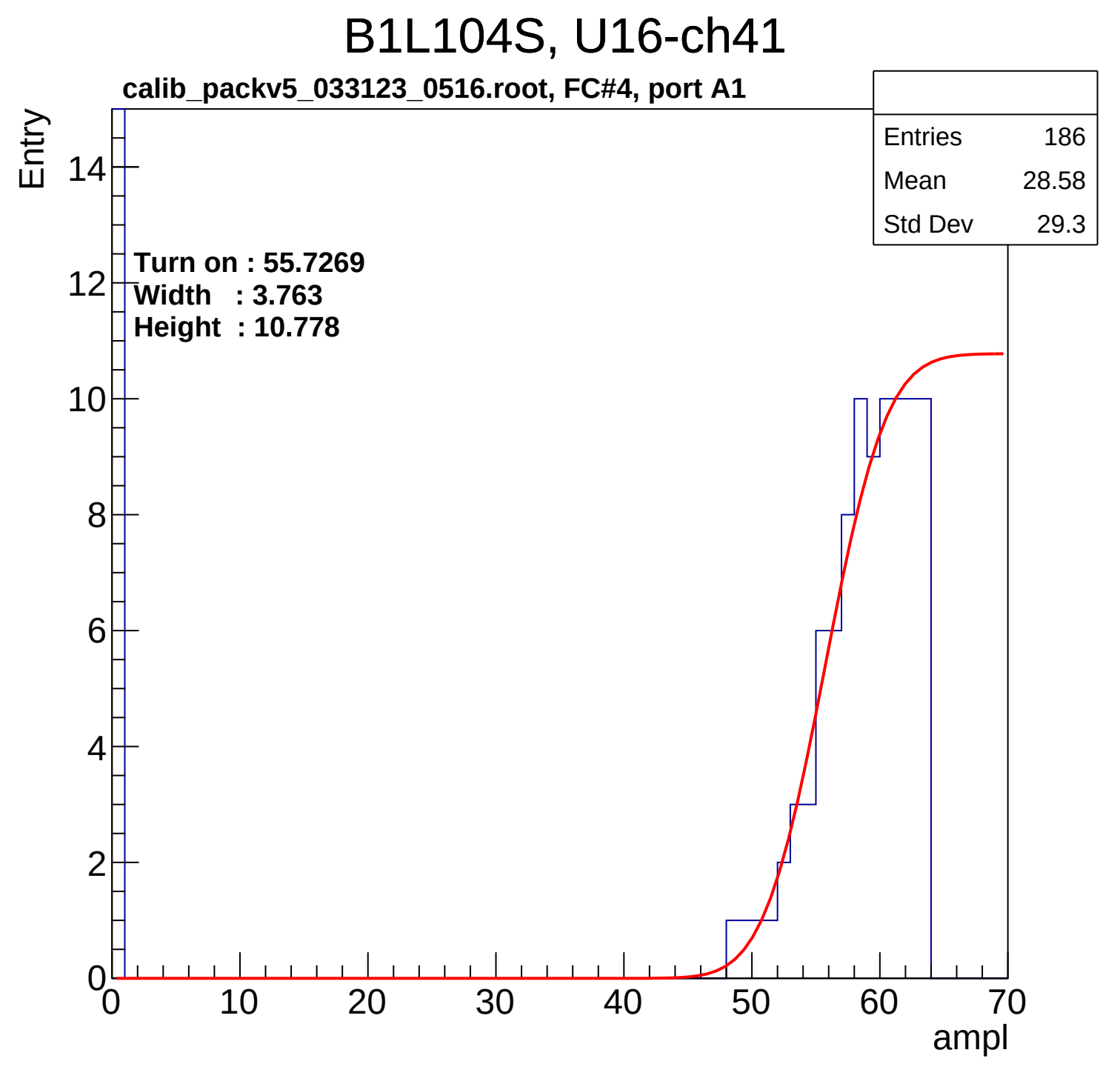
B1L104S, U16-ch41
calib_packv5_033123_0516.root, FC#4, port A1
Entries 186
Mean 28.58
Std Dev 29.3
Turn on : 55.7269
Width : 3.763
Height : 10.778
Entry
ampl
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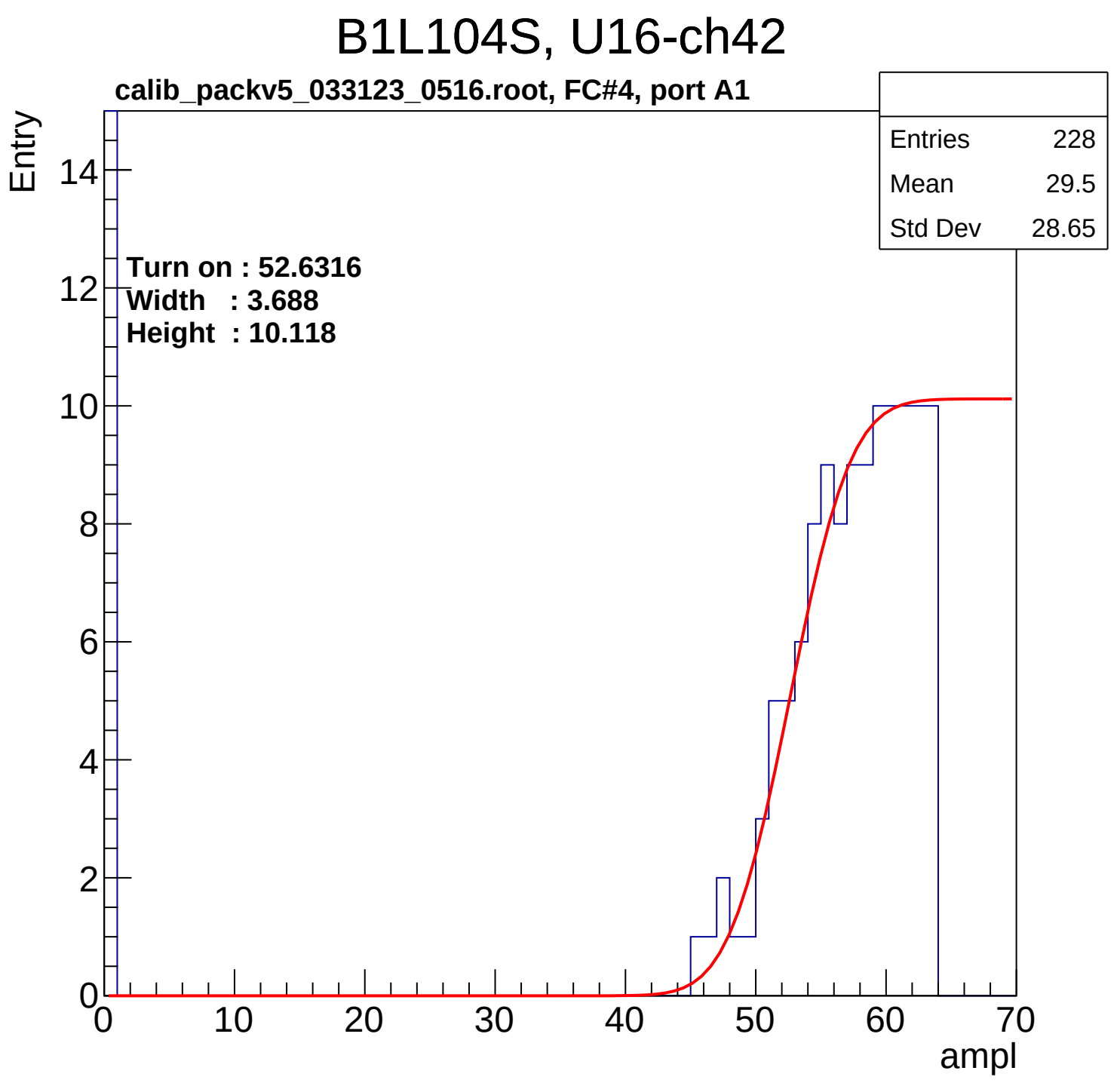
B1L104S, U16-ch42
calib_packv5_033123_0516.root, FC#4, port A1
Entries 228
Mean 29.5
Std Dev 28.65
Turn on : 52.6316
Width : 3.688
Height : 10.118
Entry
ampl
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6
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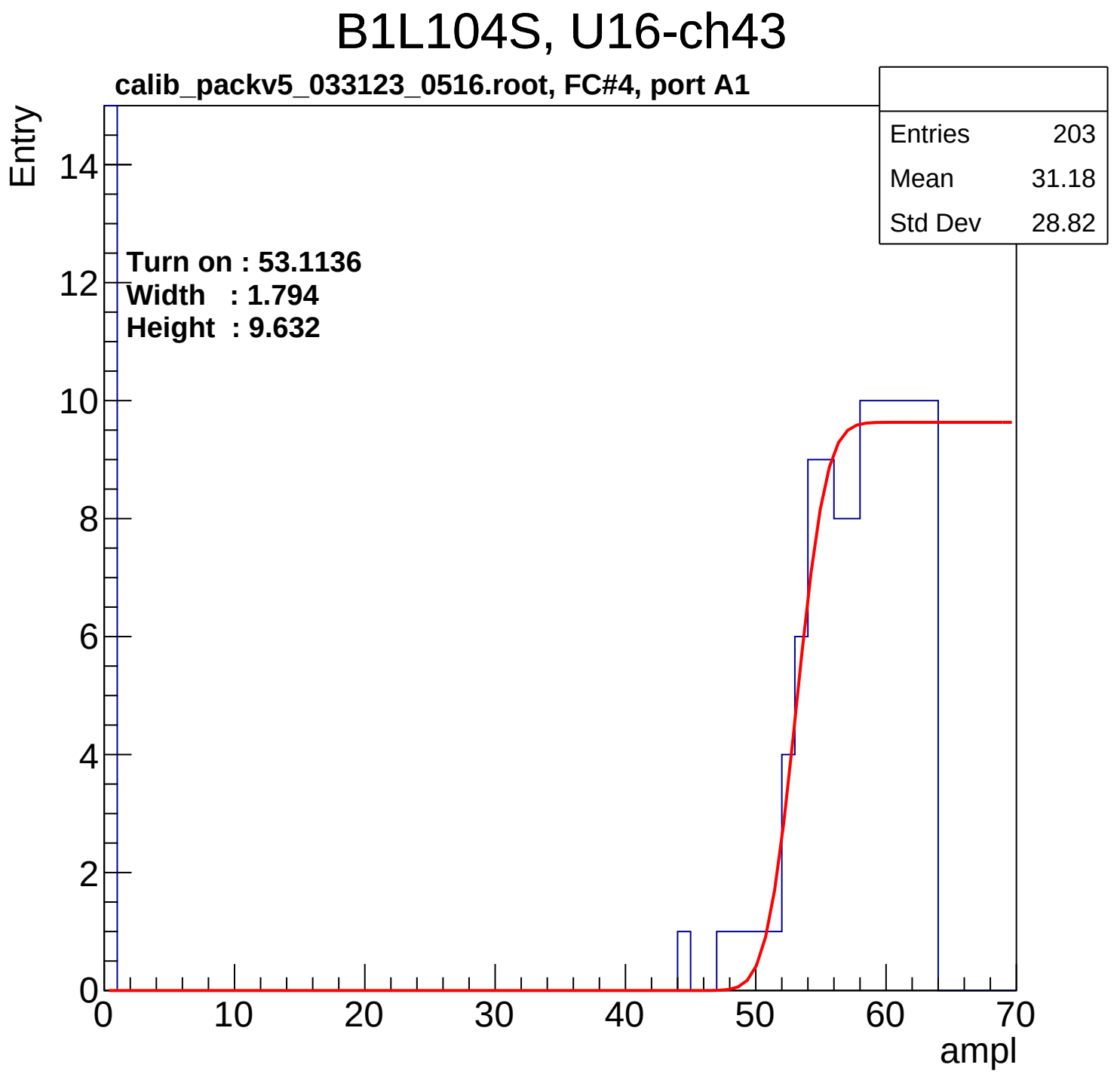
B1L104S, U16-ch43
calib_packv5_033123_0516.root, FC#4, port A1
Entries 203
Mean 31.18
Std Dev 28.82
Turn on : 53.1136
Width : 1.794
Height : 9.632
Entry
ampl
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2
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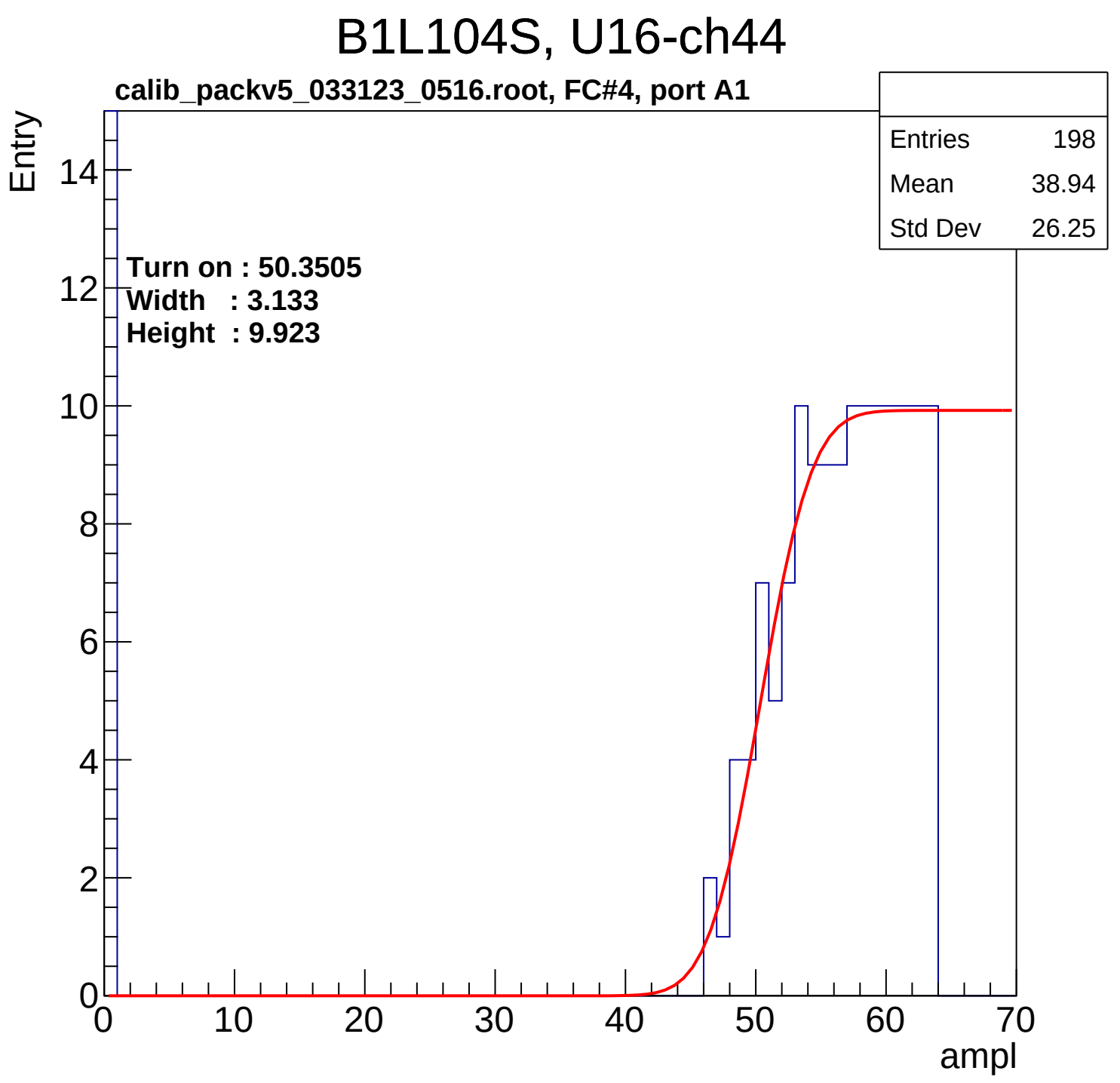
B1L104S, U16-ch44
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 38.94
Std Dev 26.25
Turn on : 50.3505
Width : 3.133
Height : 9.923
Entry
ampl
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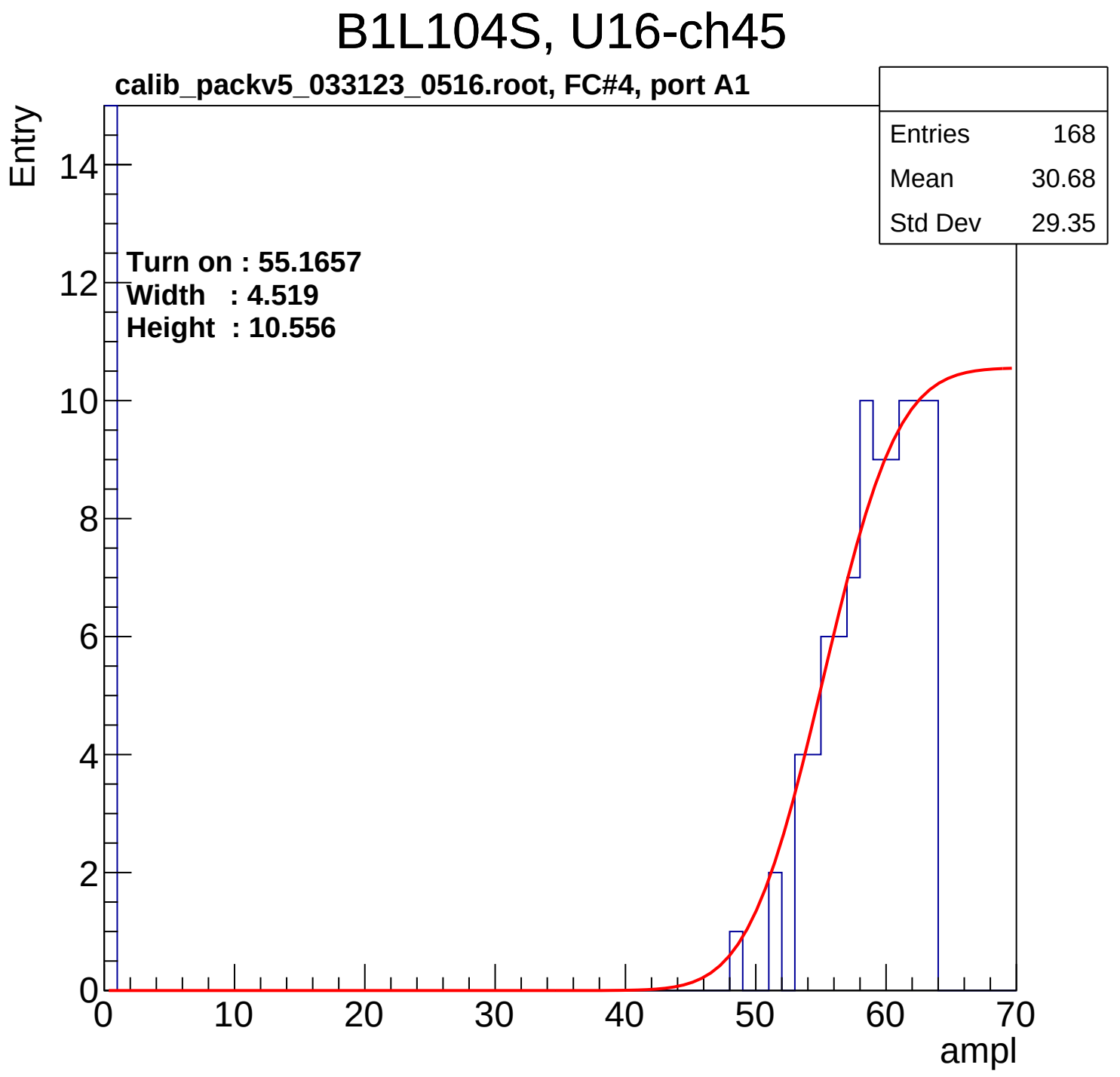
B1L104S, U16-ch45
calib_packv5_033123_0516.root, FC#4, port A1
Entries 168
Mean 30.68
Std Dev 29.35
Turn on : 55.1657
Width : 4.519
Height : 10.556
Entry
ampl
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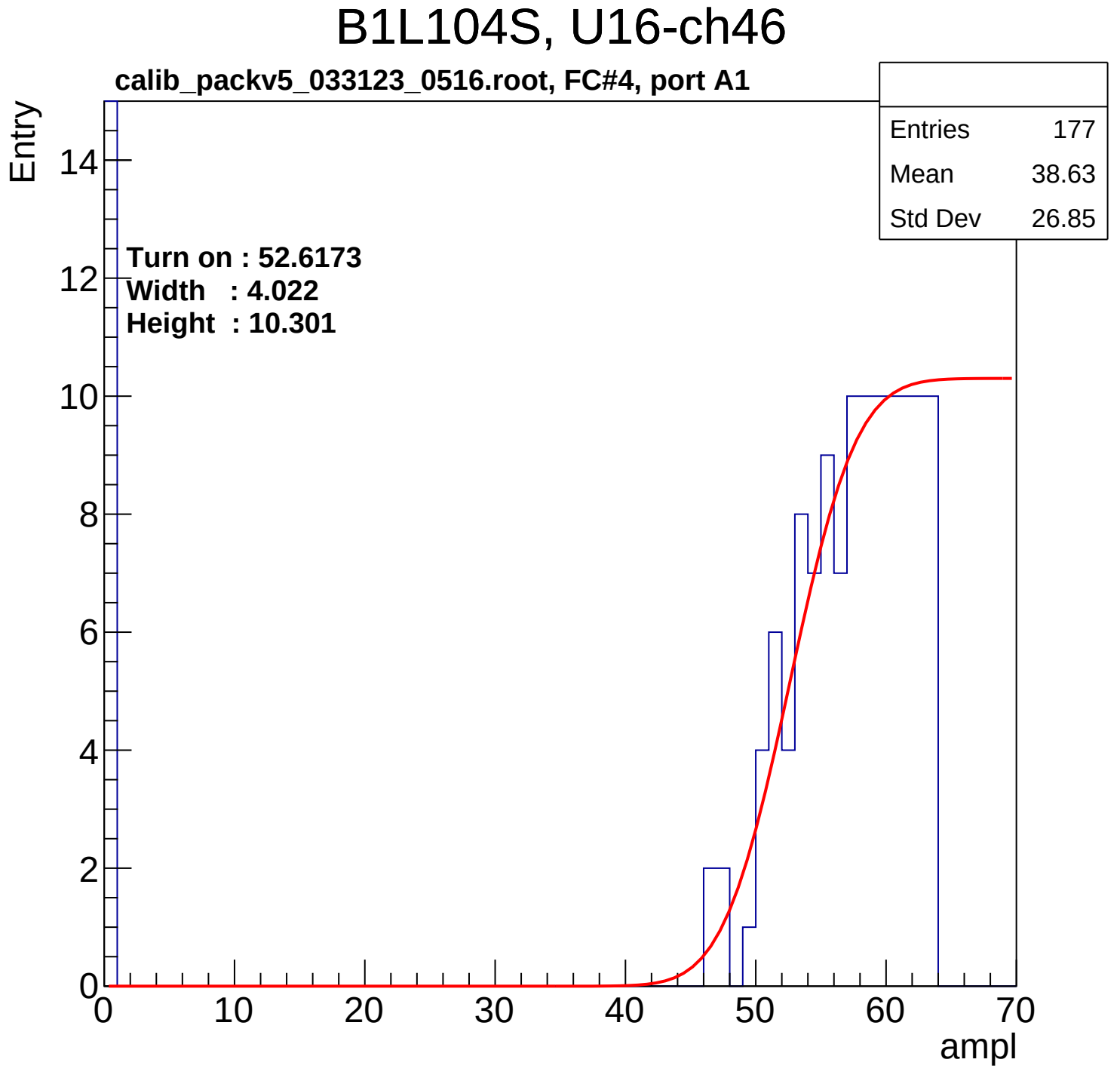
B1L104S, U16-ch46
calib_packv5_033123_0516.root, FC#4, port A1
Entries 177
Mean 38.63
Std Dev 26.85
Turn on : 52.6173
Width : 4.022
Height : 10.301
Entry
ampl
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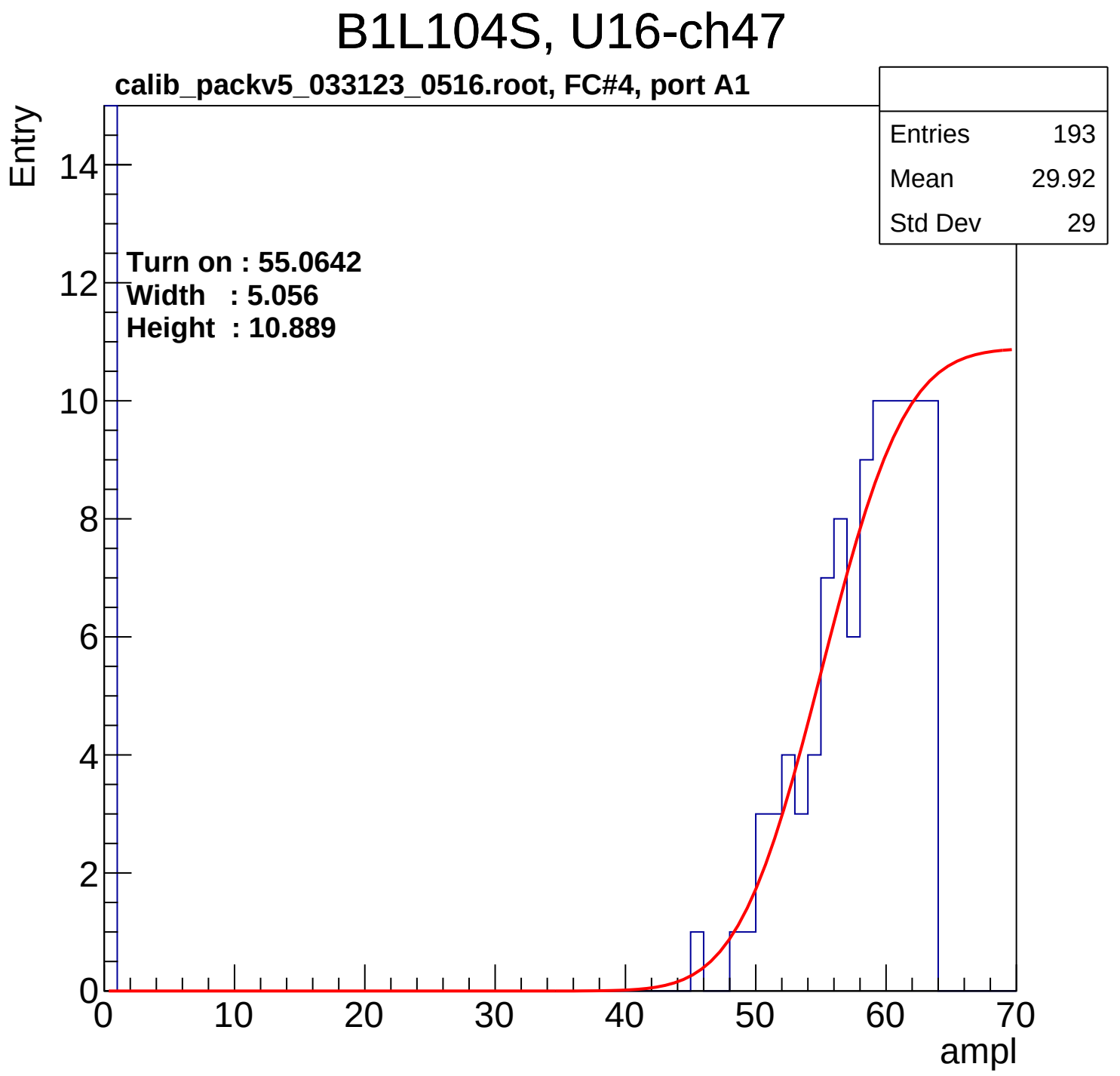
B1L104S, U16-ch47
calib_packv5_033123_0516.root, FC#4, port A1
Entries 193
Mean 29.92
Std Dev 29
Turn on : 55.0642
Width : 5.056
Height : 10.889
Entry
ampl
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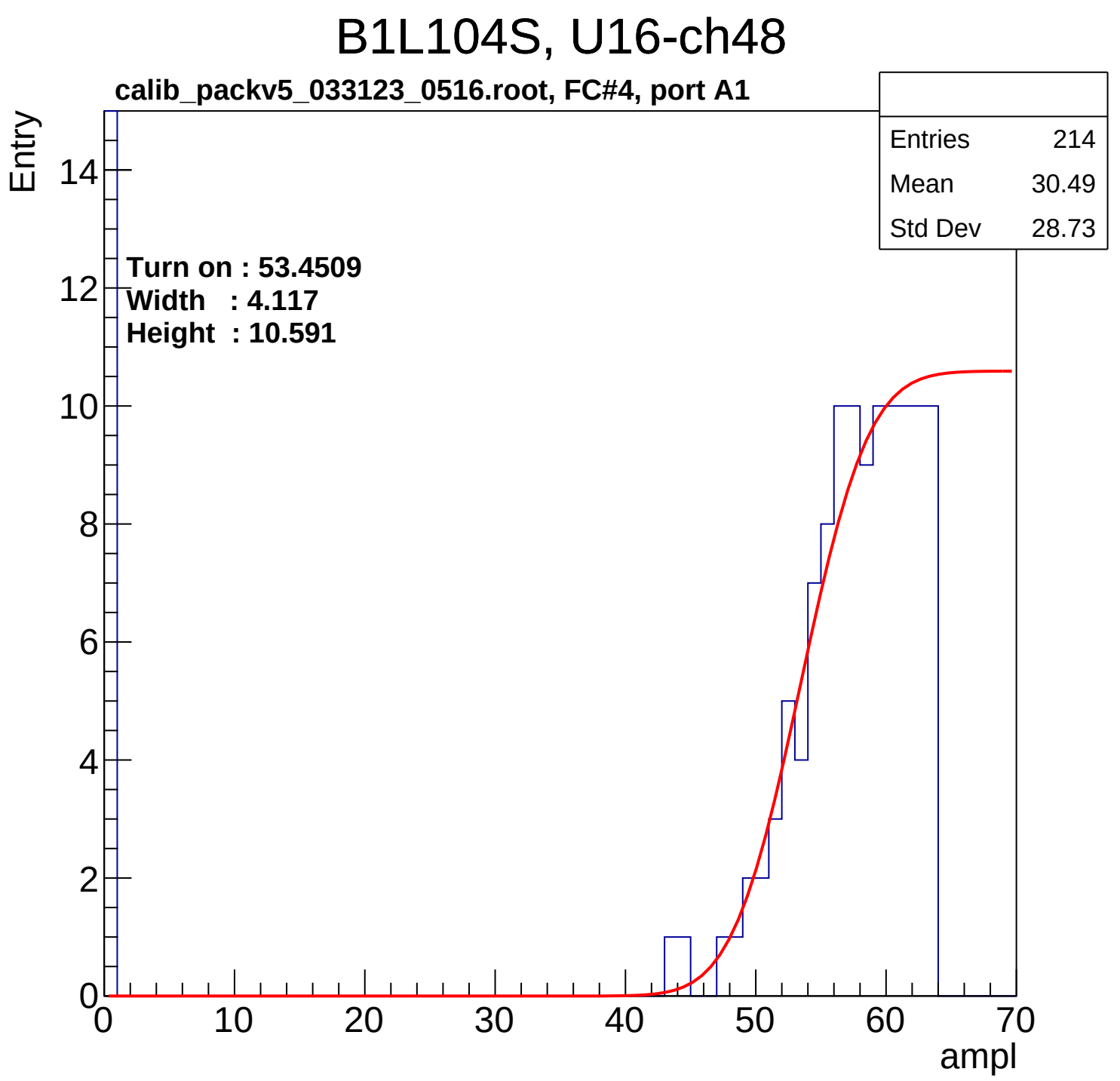
B1L104S, U16-ch48
calib_packv5_033123_0516.root, FC#4, port A1
Entries 214
Mean 30.49
Std Dev 28.73
Turn on : 53.4509
Width : 4.117
Height : 10.591
Entry
ampl
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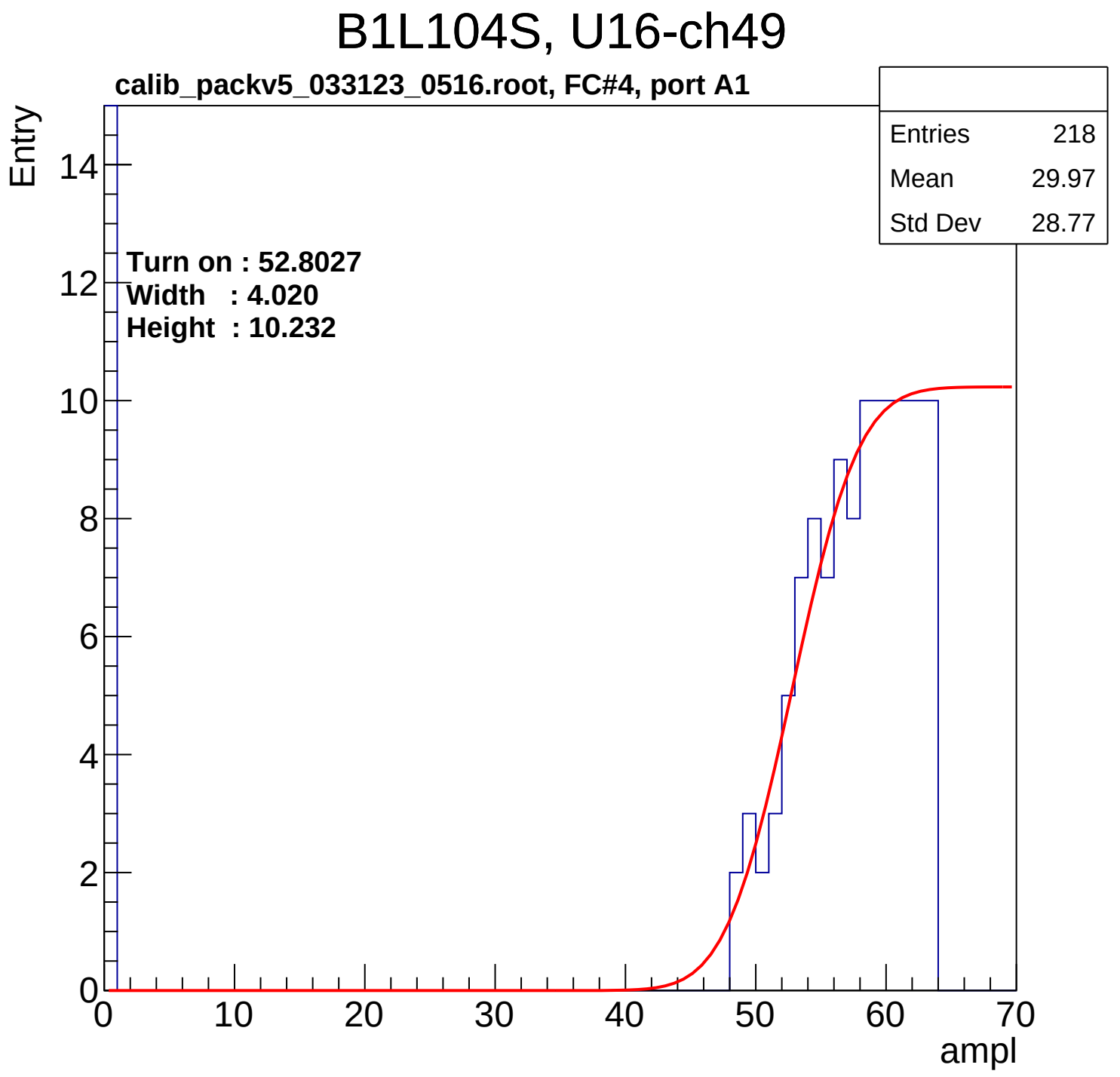
B1L104S, U16-ch49
calib_packv5_033123_0516.root, FC#4, port A1
Entries 218
Mean 29.97
Std Dev 28.77
Turn on : 52.8027
Width : 4.020
Height : 10.232
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ampl
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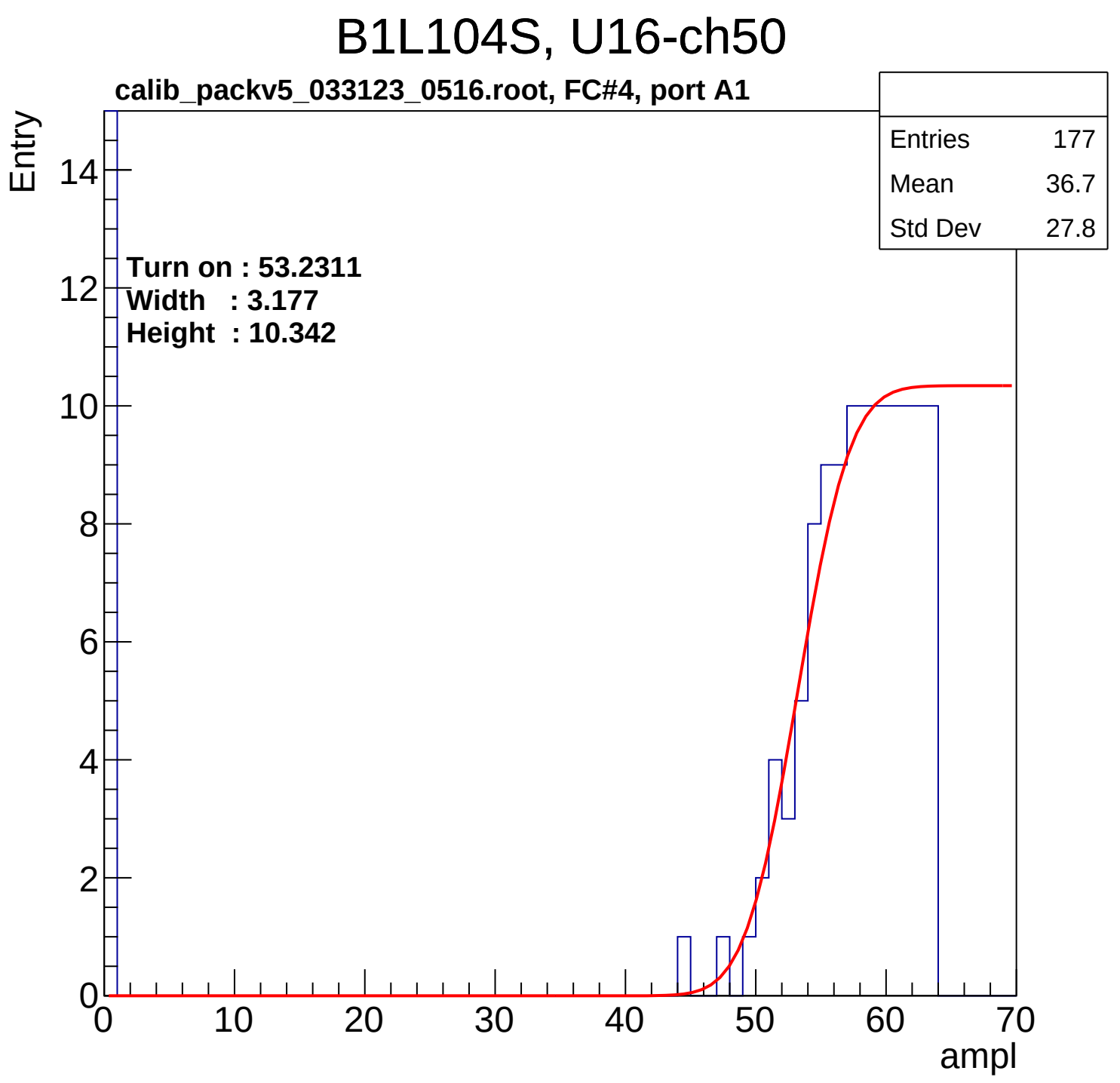
B1L104S, U16-ch50
calib_packv5_033123_0516.root, FC#4, port A1
Entries 177
Mean 36.7
Std Dev 27.8
Turn on : 53.2311
Width : 3.177
Height : 10.342
Entry
ampl
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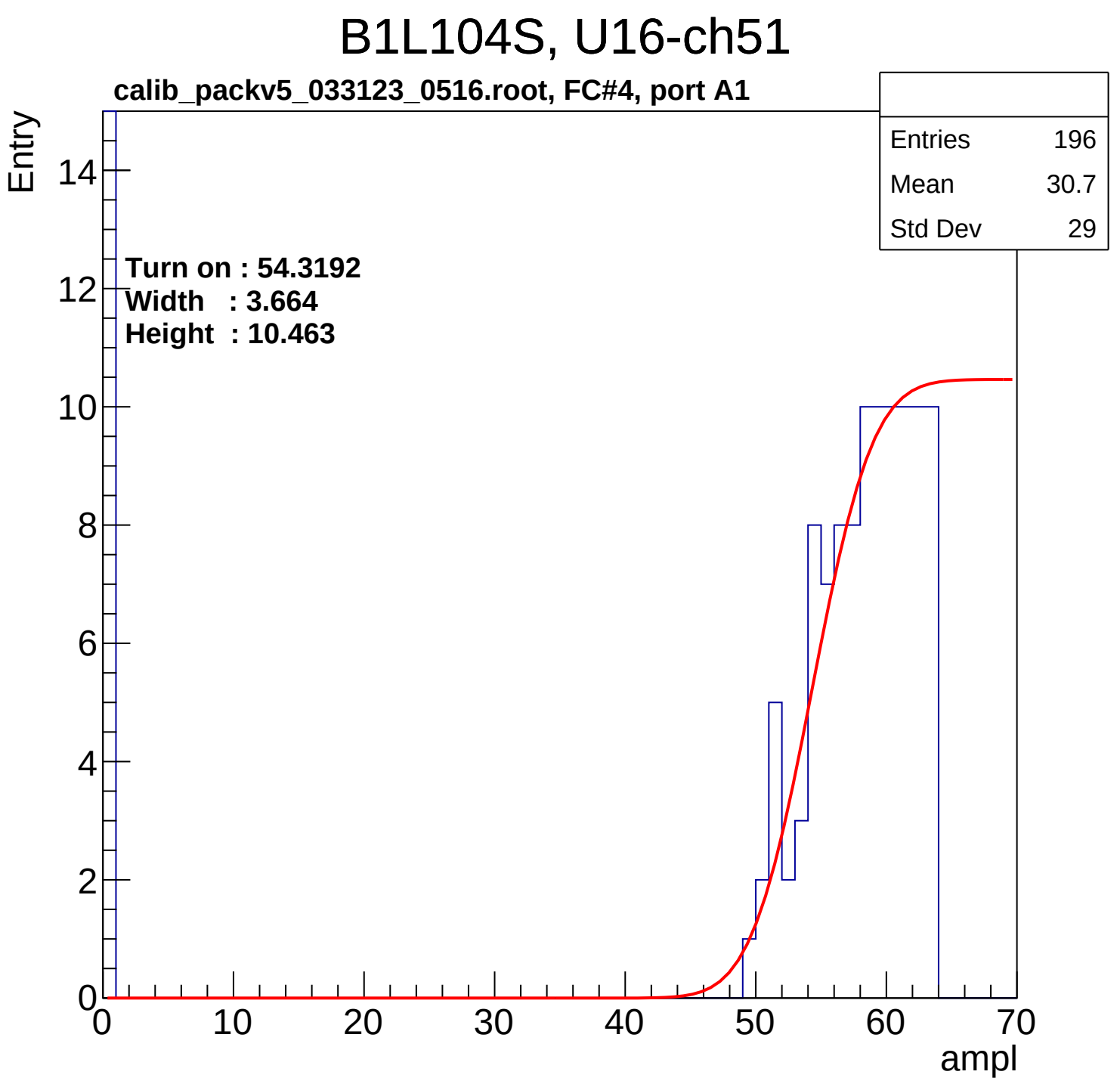
B1L104S, U16-ch51
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 30.7
Std Dev 29
Turn on : 54.3192
Width : 3.664
Height : 10.463
Entry
ampl
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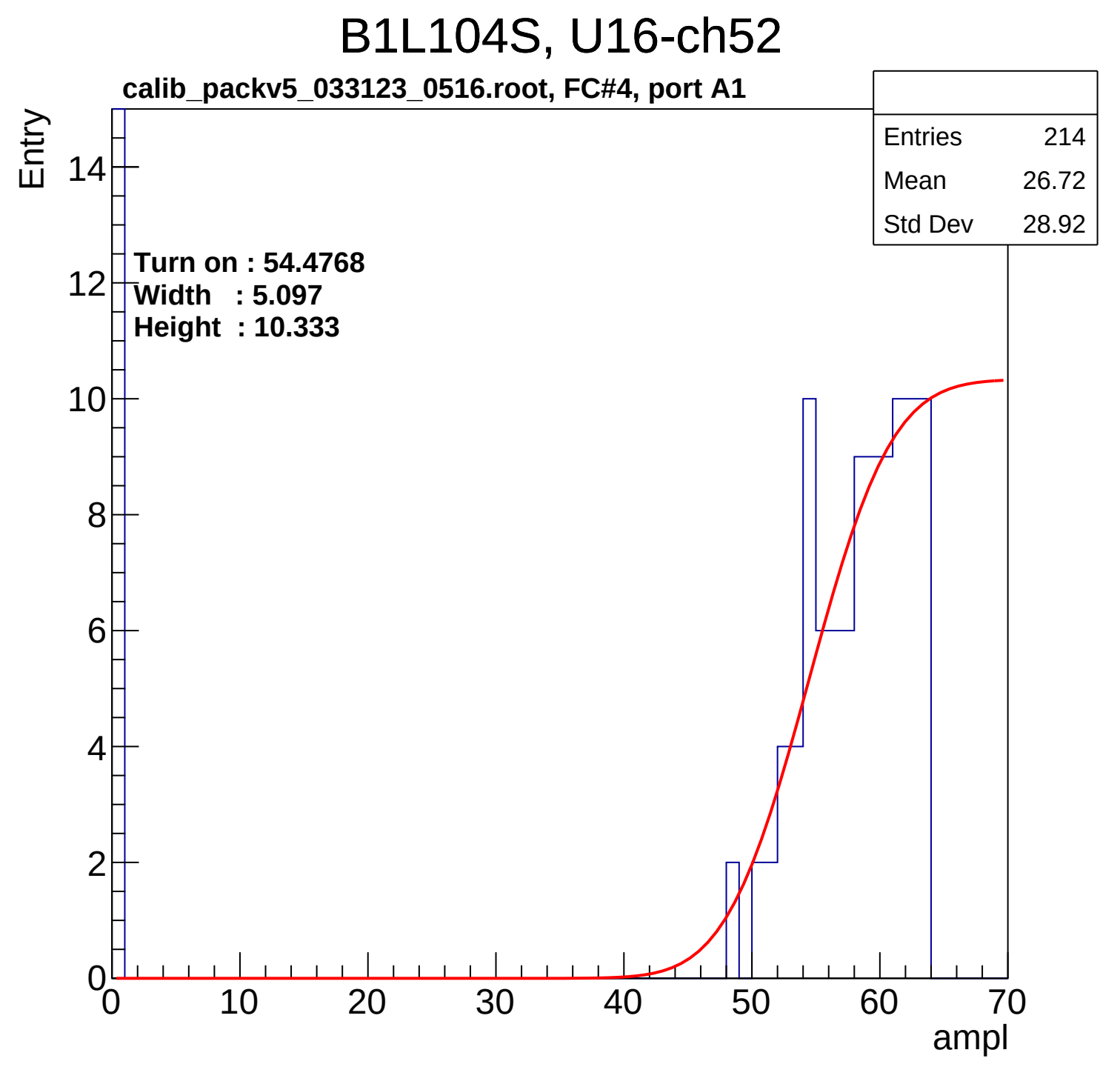
B1L104S, U16-ch52
calib_packv5_033123_0516.root, FC#4, port A1
Entries 214
Mean 26.72
Std Dev 28.92
Turn on : 54.4768
Width : 5.097
Height : 10.333
Entry
ampl
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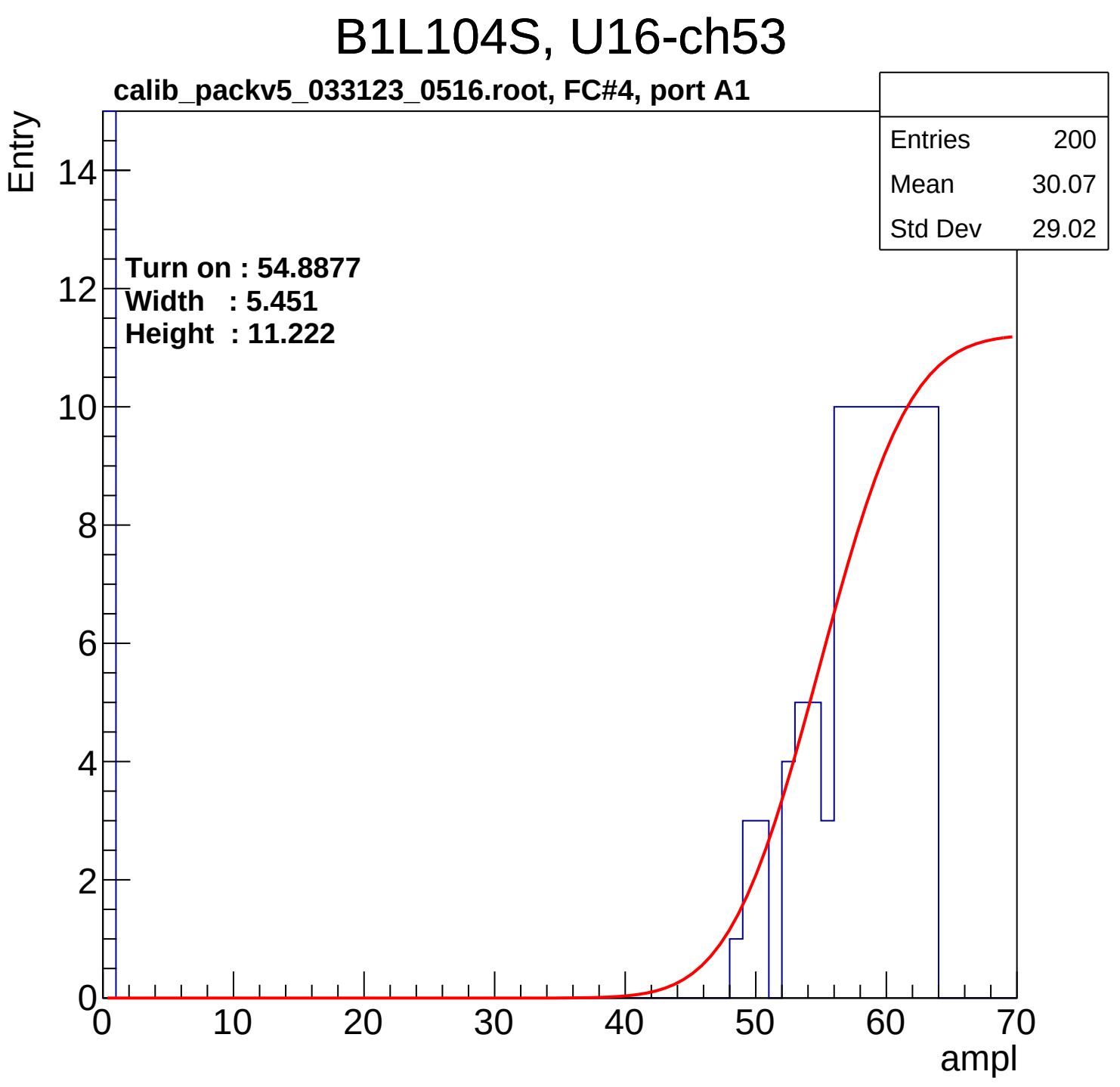
B1L104S, U16-ch53
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 30.07
Std Dev 29.02
Turn on : 54.8877
Width : 5.451
Height : 11.222
Entry
ampl
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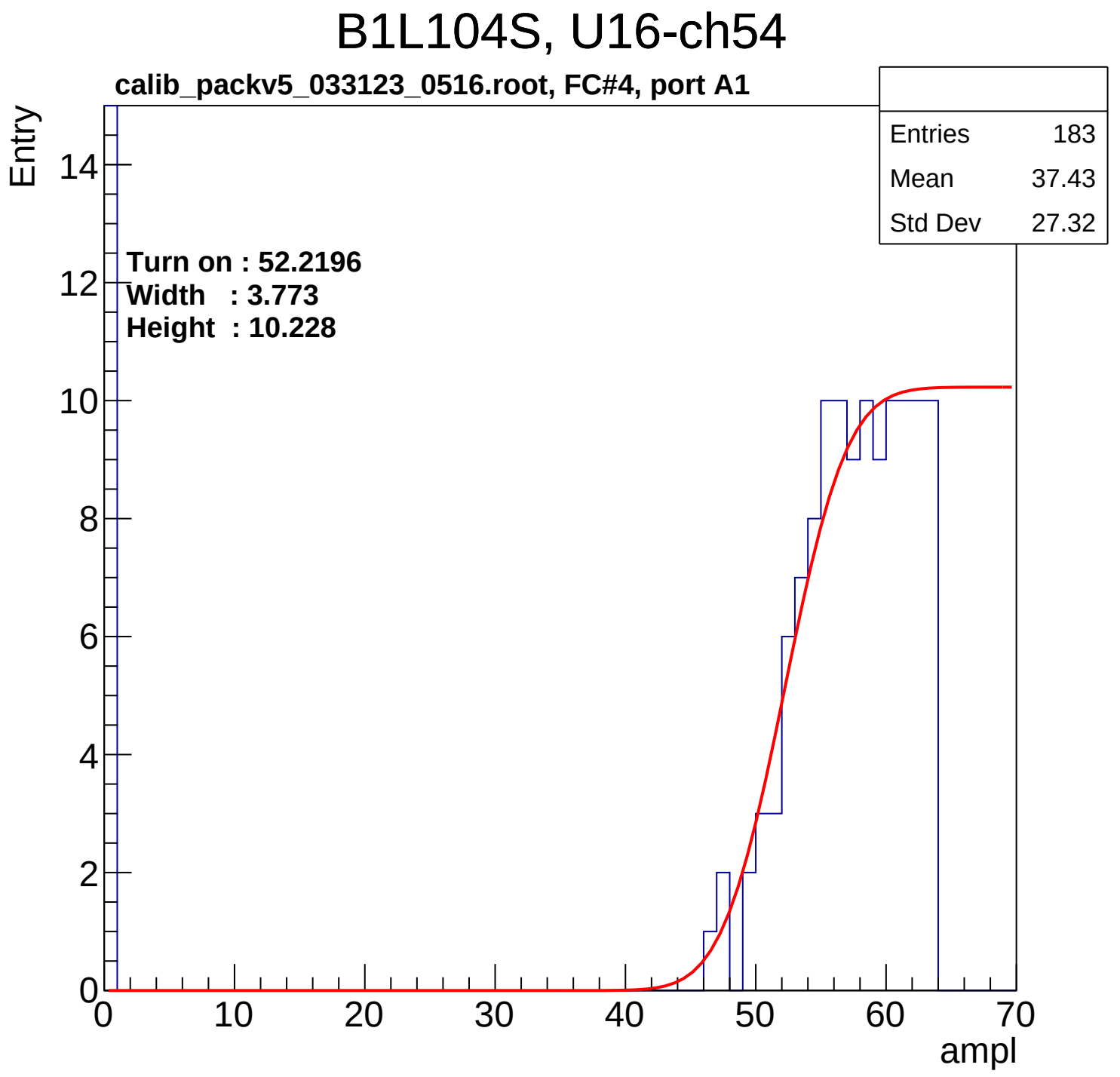
B1L104S, U16-ch54
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 37.43
Std Dev 27.32
Turn on : 52.2196
Width : 3.773
Height : 10.228
Entry
ampl
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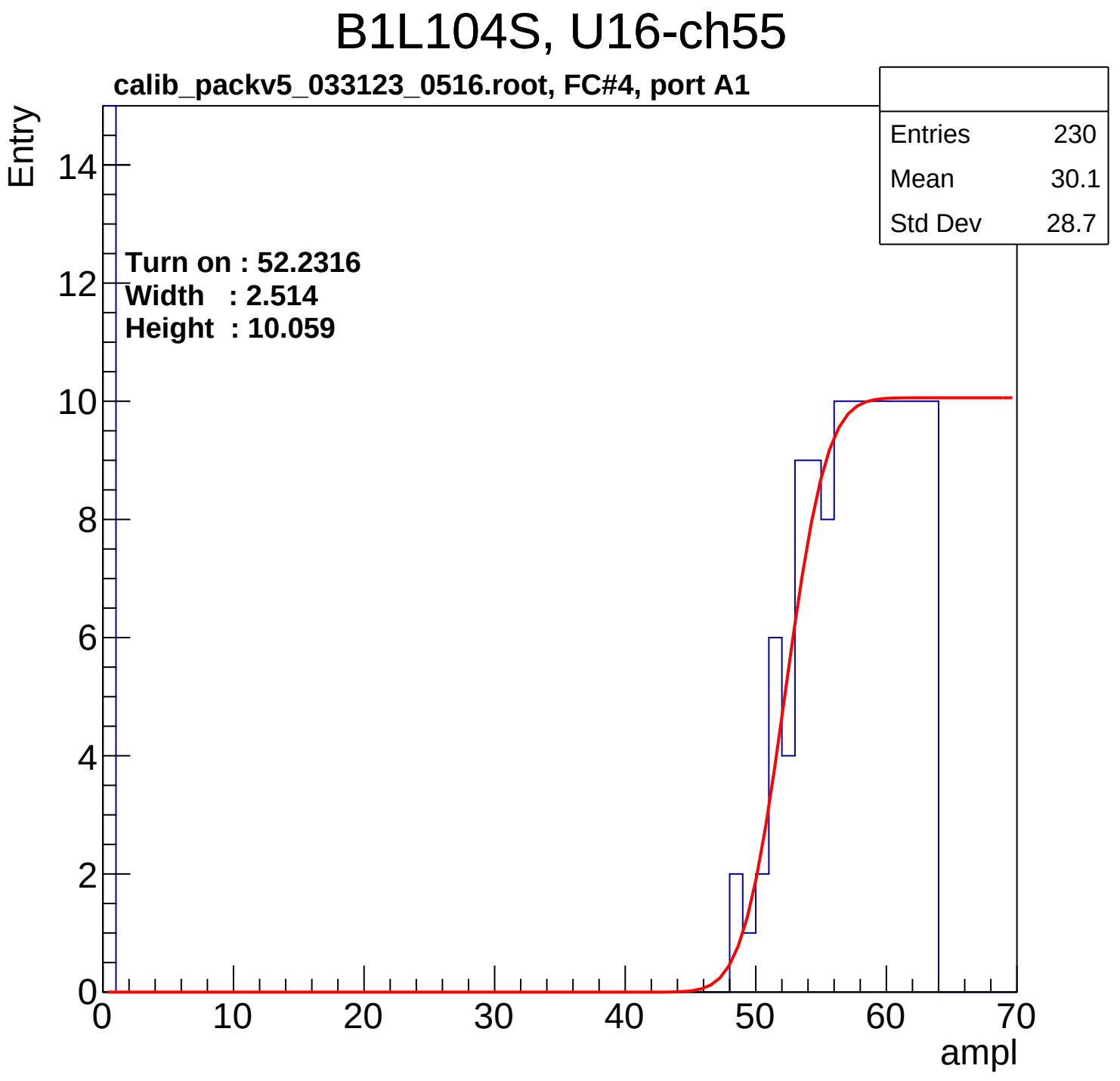
B1L104S, U16-ch55
calib_packv5_033123_0516.root, FC#4, port A1
Entries 230
Mean 30.1
Std Dev 28.7
Turn on : 52.2316
Width : 2.514
Height : 10.059
Entry
ampl
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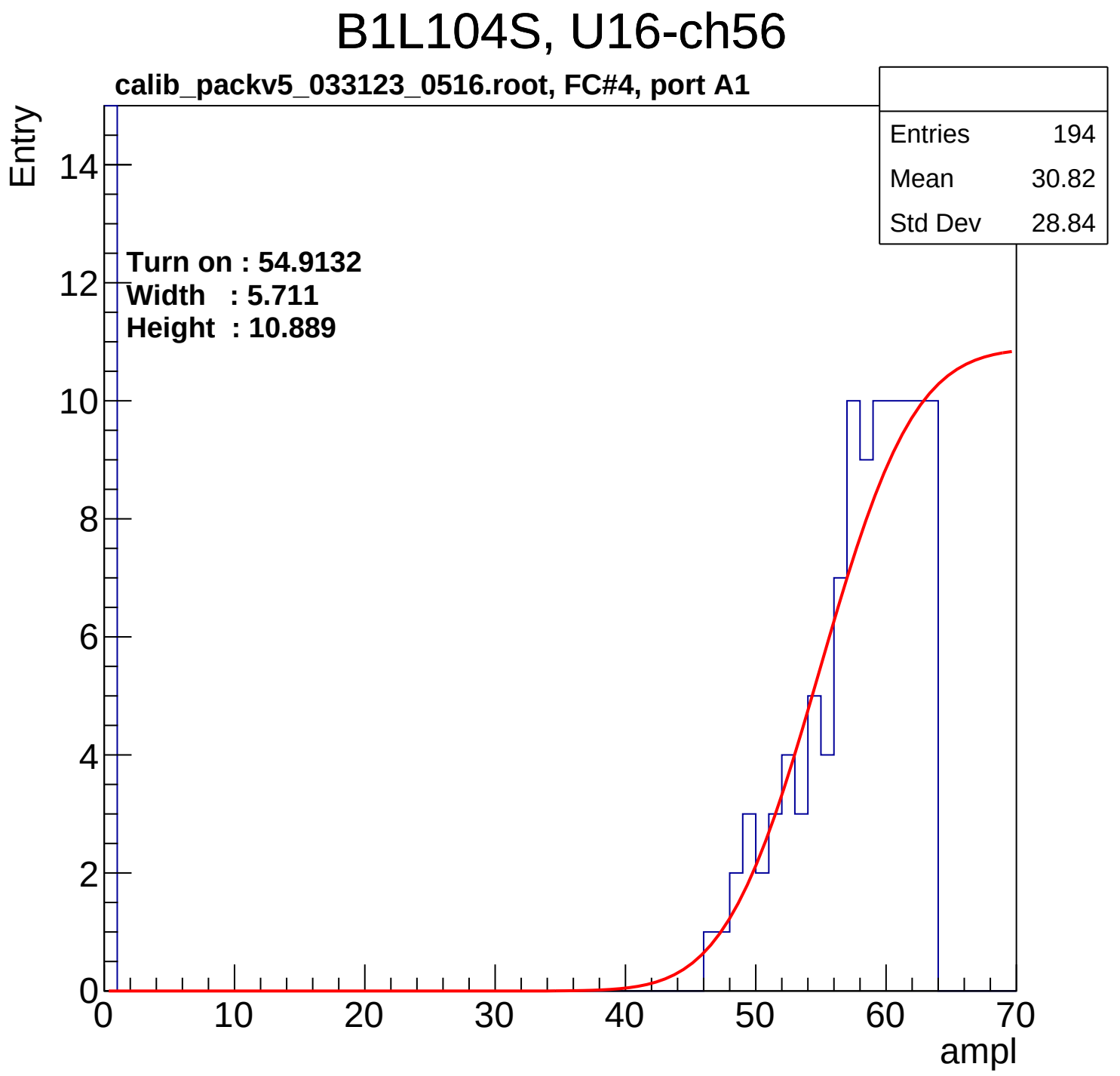
B1L104S, U16-ch56
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 30.82
Std Dev 28.84
Turn on : 54.9132
Width : 5.711
Height : 10.889
Entry
ampl
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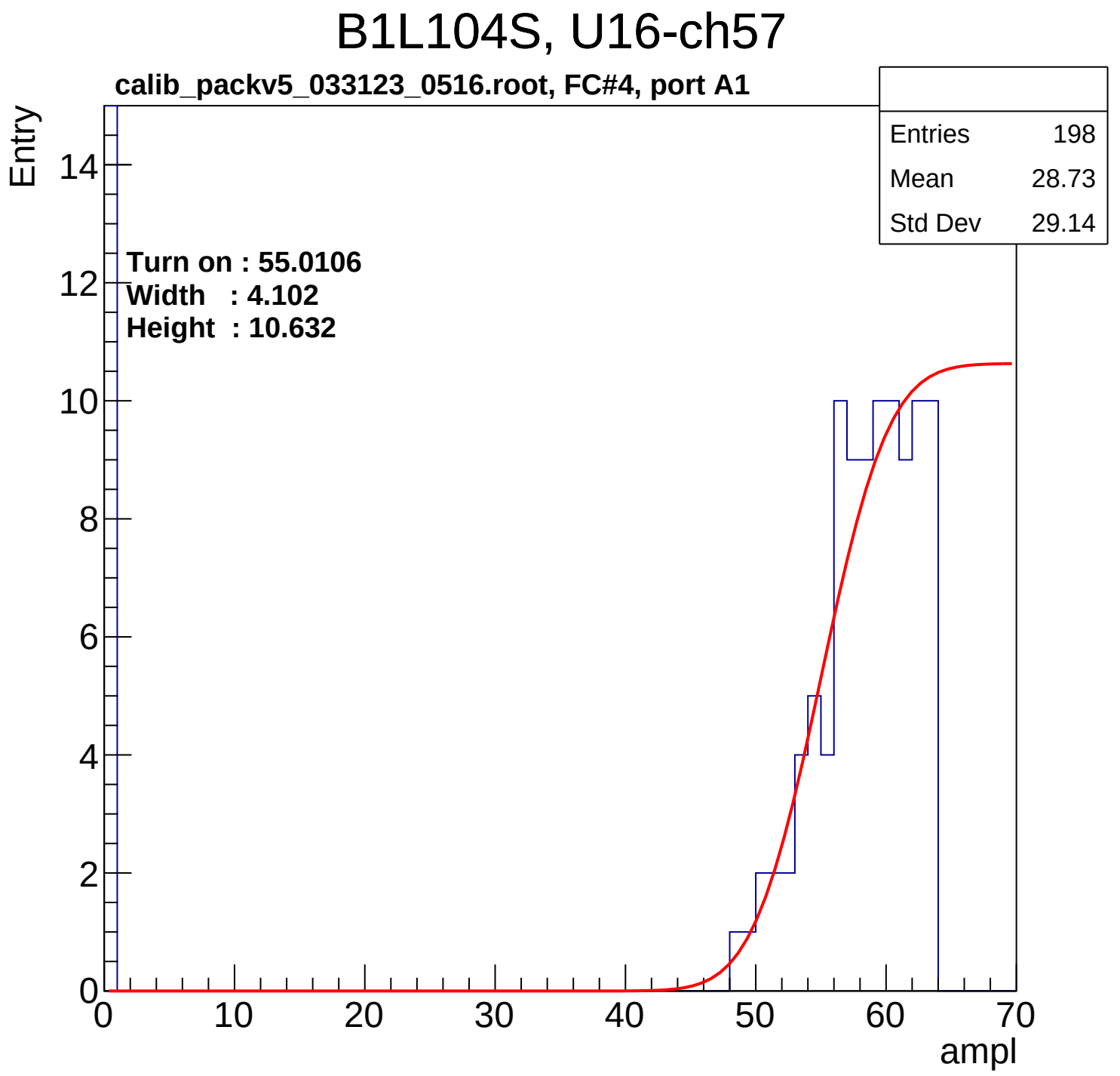
B1L104S, U16-ch57
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 28.73
Std Dev 29.14
Turn on : 55.0106
Width : 4.102
Height : 10.632
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ampl
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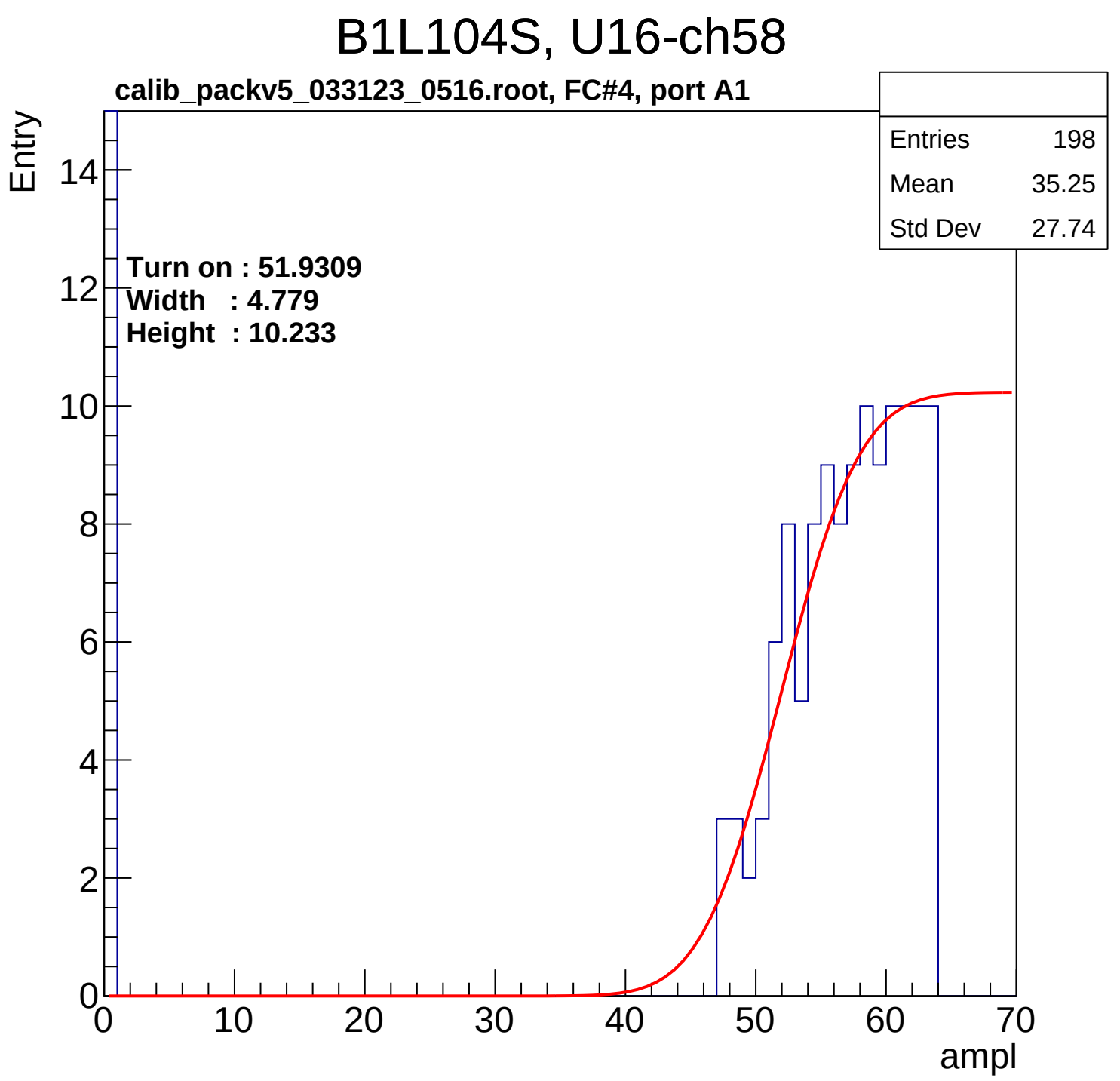
B1L104S, U16-ch58
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 35.25
Std Dev 27.74
Turn on : 51.9309
Width : 4.779
Height : 10.233
Entry
ampl
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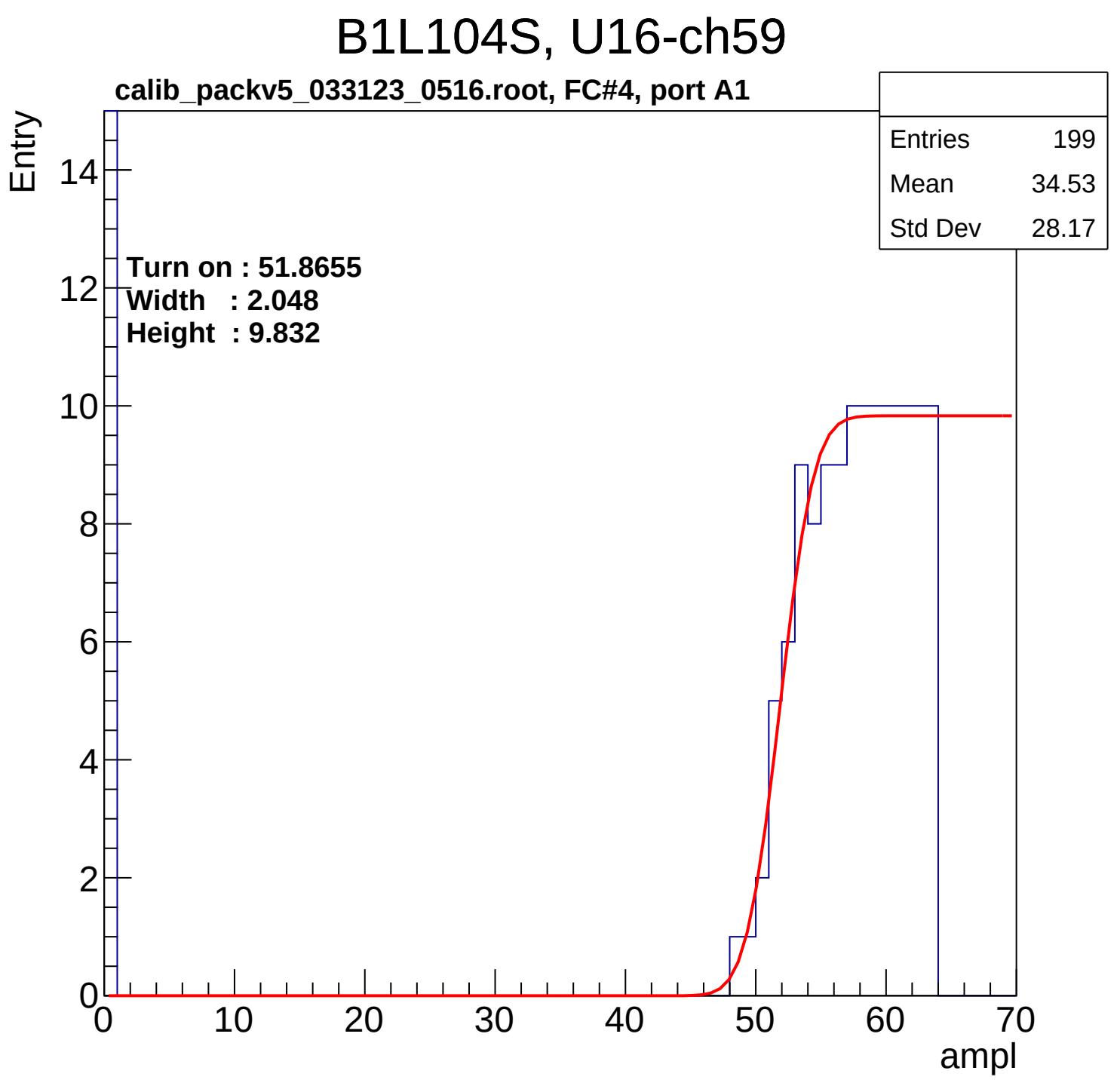
B1L104S, U16-ch59
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 34.53
Std Dev 28.17
Turn on : 51.8655
Width : 2.048
Height : 9.832
Entry
ampl
0
2
4
6
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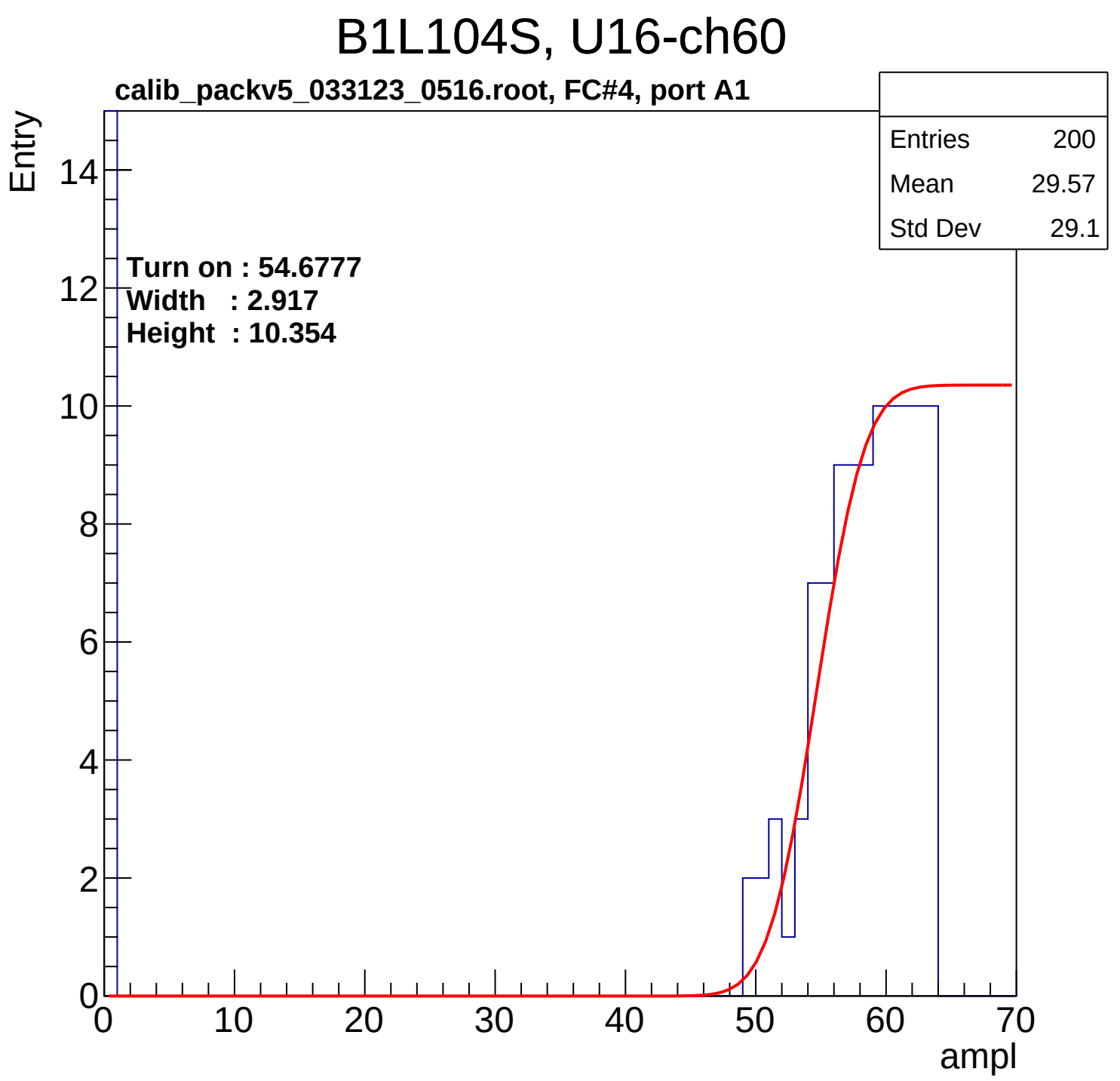
B1L104S, U16-ch60
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 29.57
Std Dev 29.1
Turn on : 54.6777
Width : 2.917
Height : 10.354
Entry
ampl
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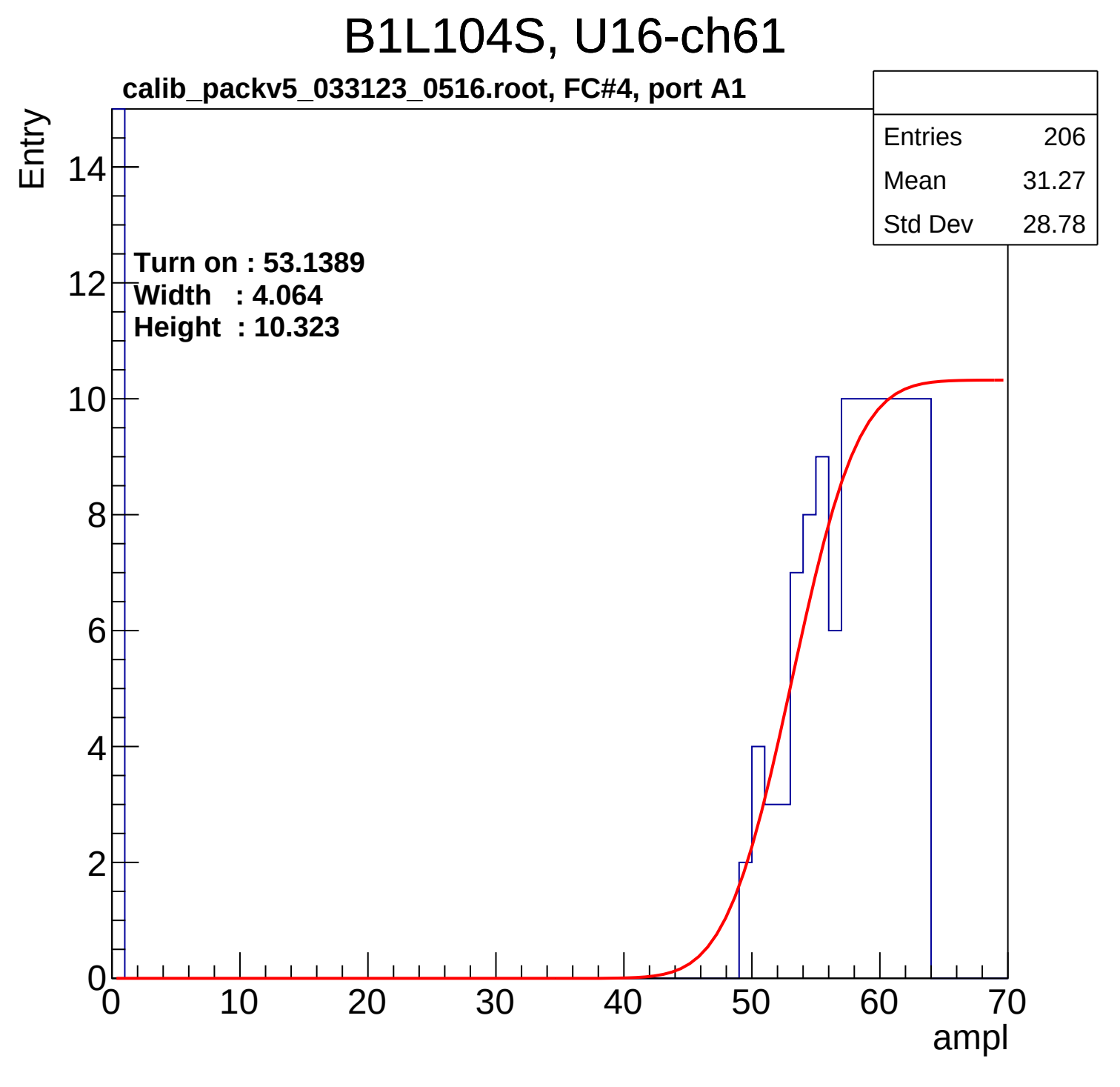
B1L104S, U16-ch61
calib_packv5_033123_0516.root, FC#4, port A1
Entries 206
Mean 31.27
Std Dev 28.78
Turn on : 53.1389
Width : 4.064
Height : 10.323
Entry
ampl
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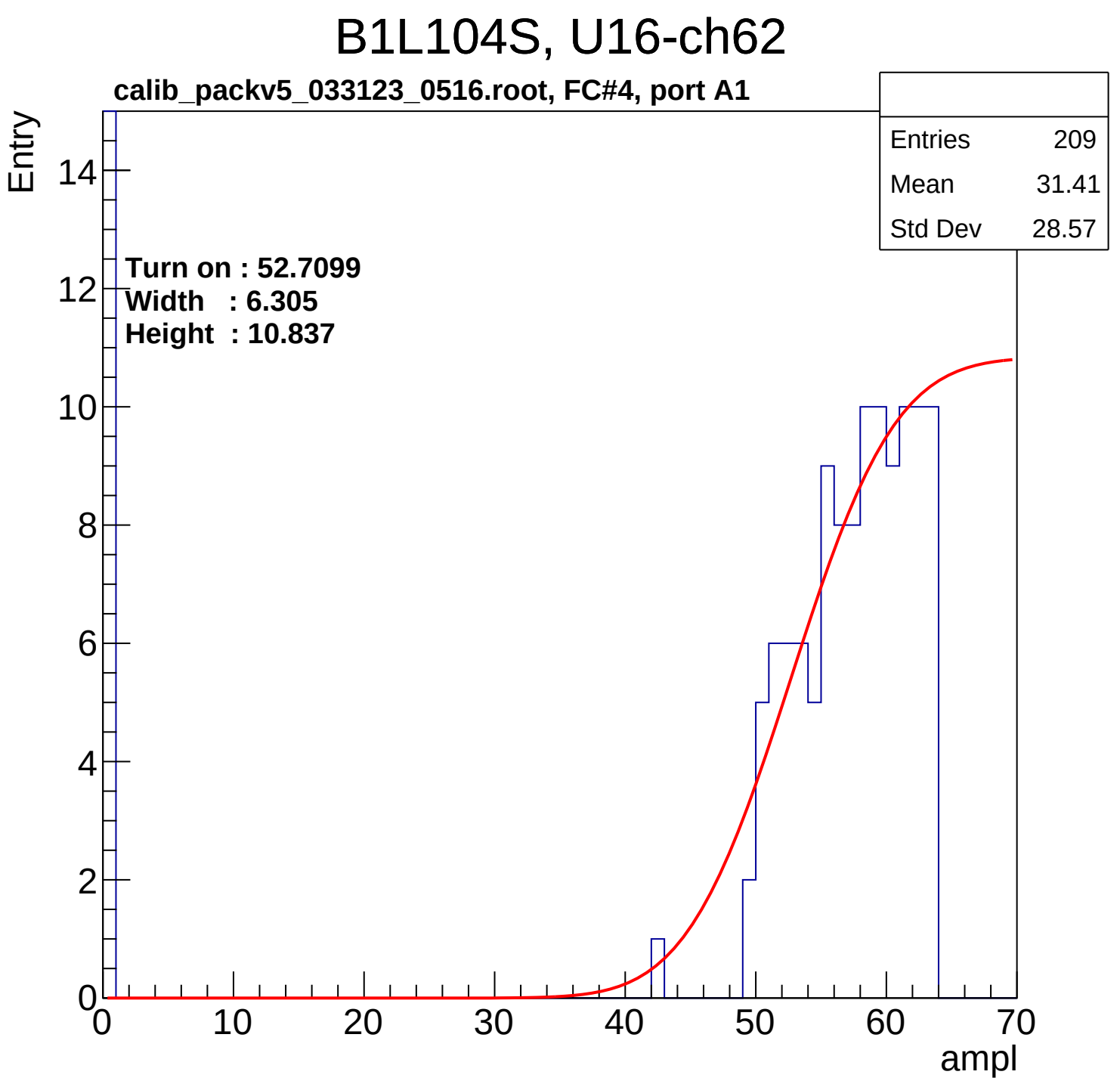
B1L104S, U16-ch62
calib_packv5_033123_0516.root, FC#4, port A1
Entries 209
Mean 31.41
Std Dev 28.57
Turn on : 52.7099
Width : 6.305
Height : 10.837
Entry
ampl
0
2
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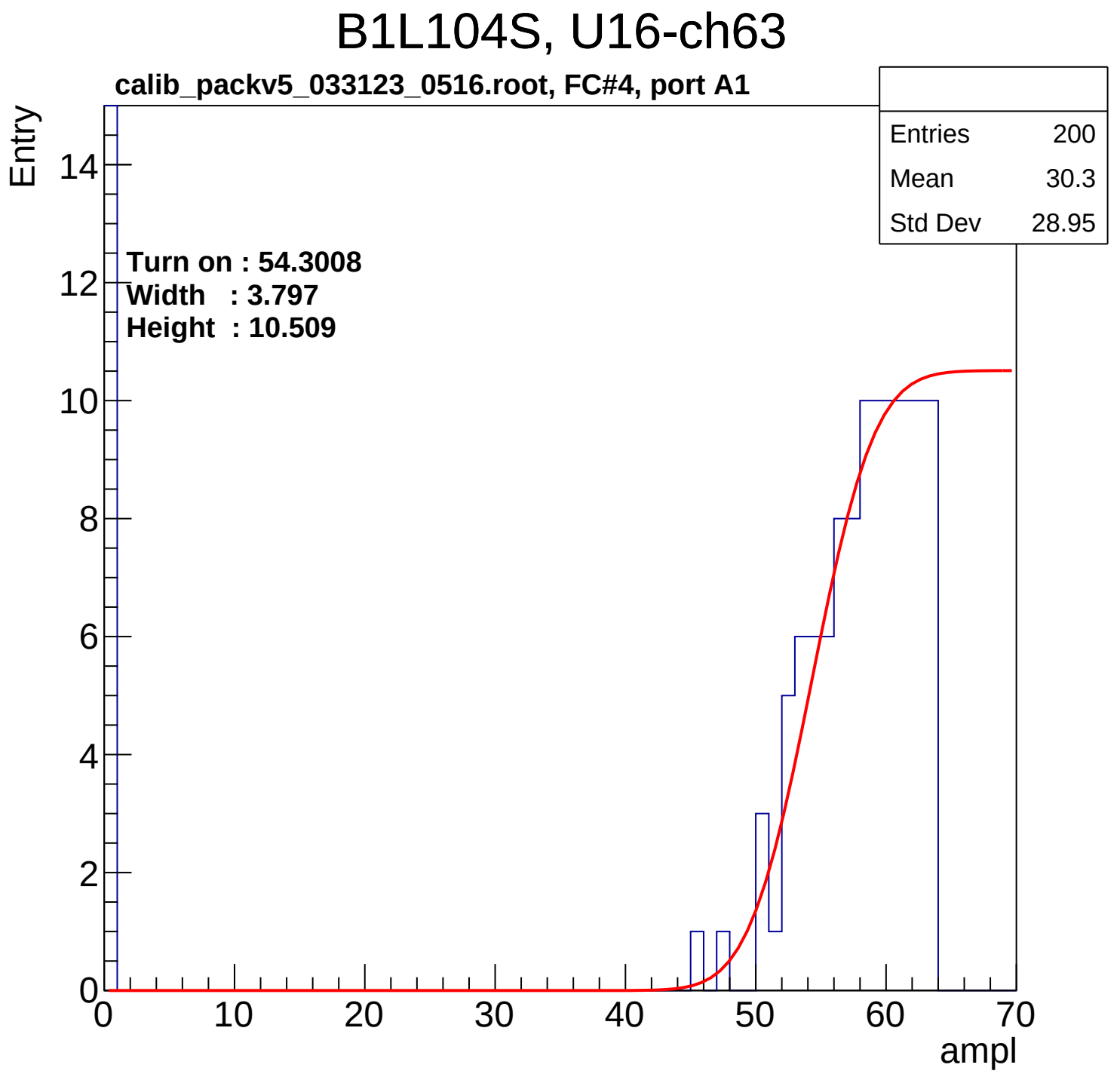
B1L104S, U16-ch63
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 30.3
Std Dev 28.95
Turn on : 54.3008
Width : 3.797
Height : 10.509
Entry
ampl
0
2
4
6
8
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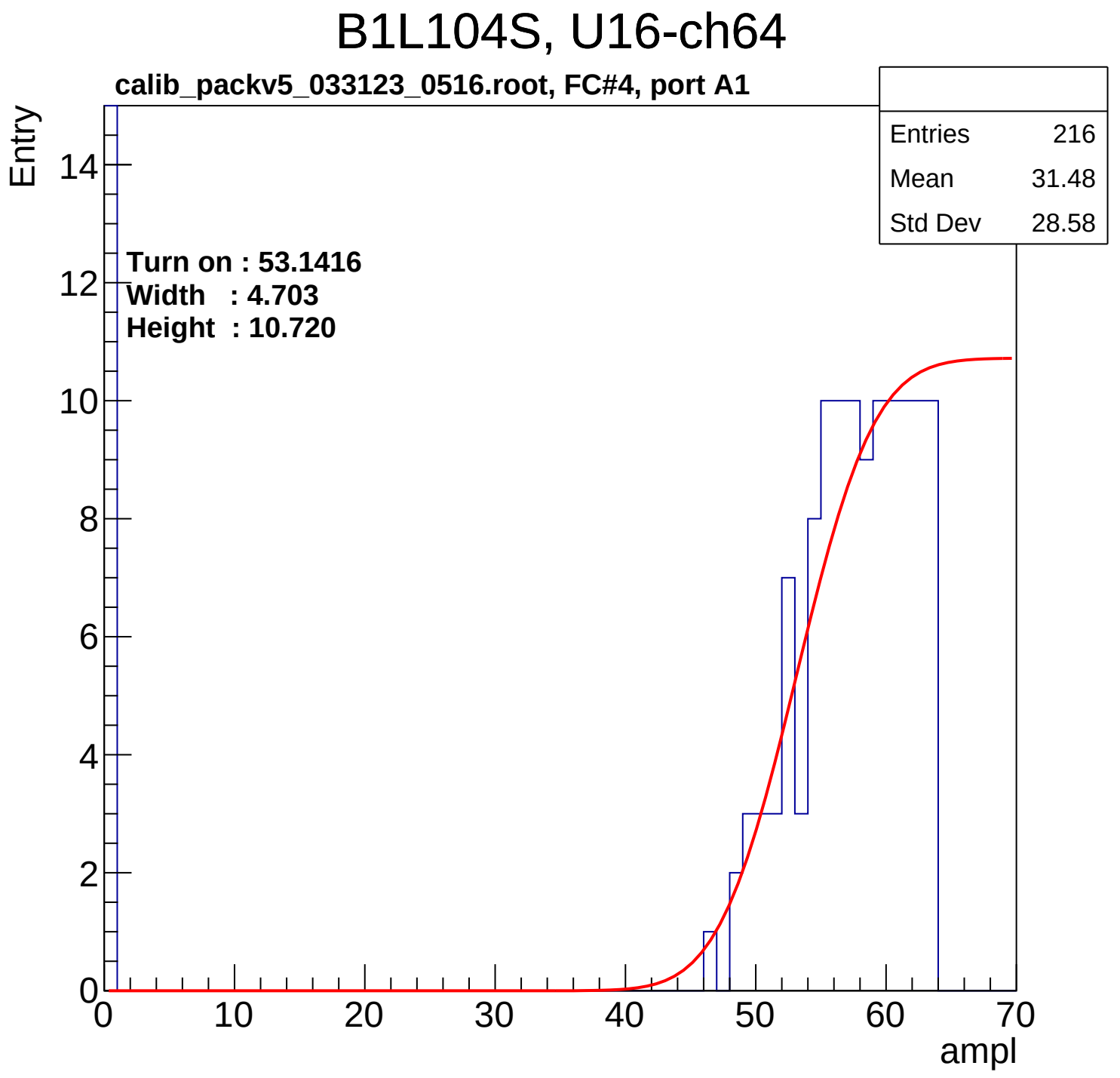
B1L104S, U16-ch64
calib_packv5_033123_0516.root, FC#4, port A1
Entries 216
Mean 31.48
Std Dev 28.58
Turn on : 53.1416
Width : 4.703
Height : 10.720
Entry
ampl
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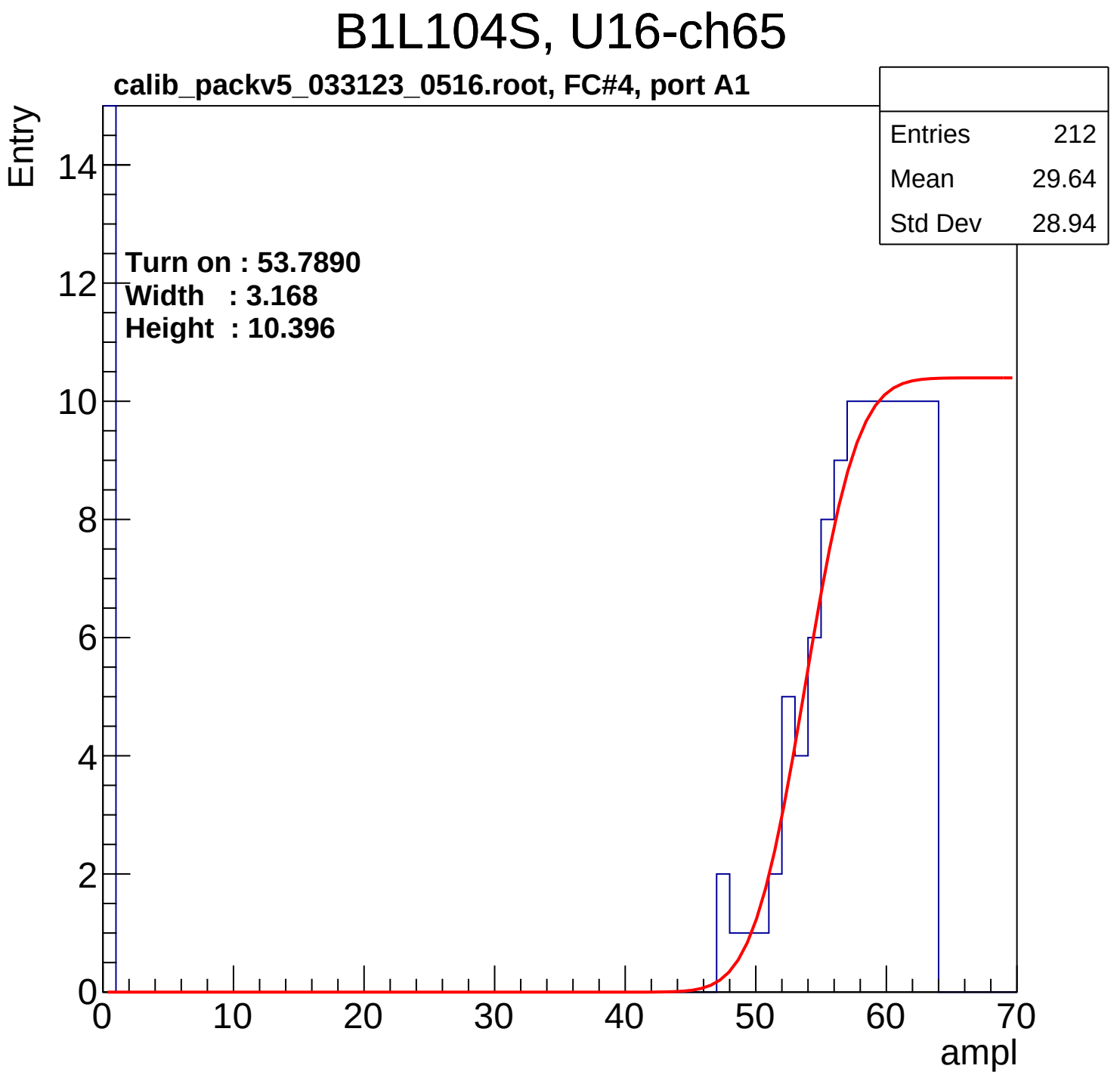
B1L104S, U16-ch65
calib_packv5_033123_0516.root, FC#4, port A1
Entries 212
Mean 29.64
Std Dev 28.94
Turn on : 53.7890
Width : 3.168
Height : 10.396
Entry
ampl
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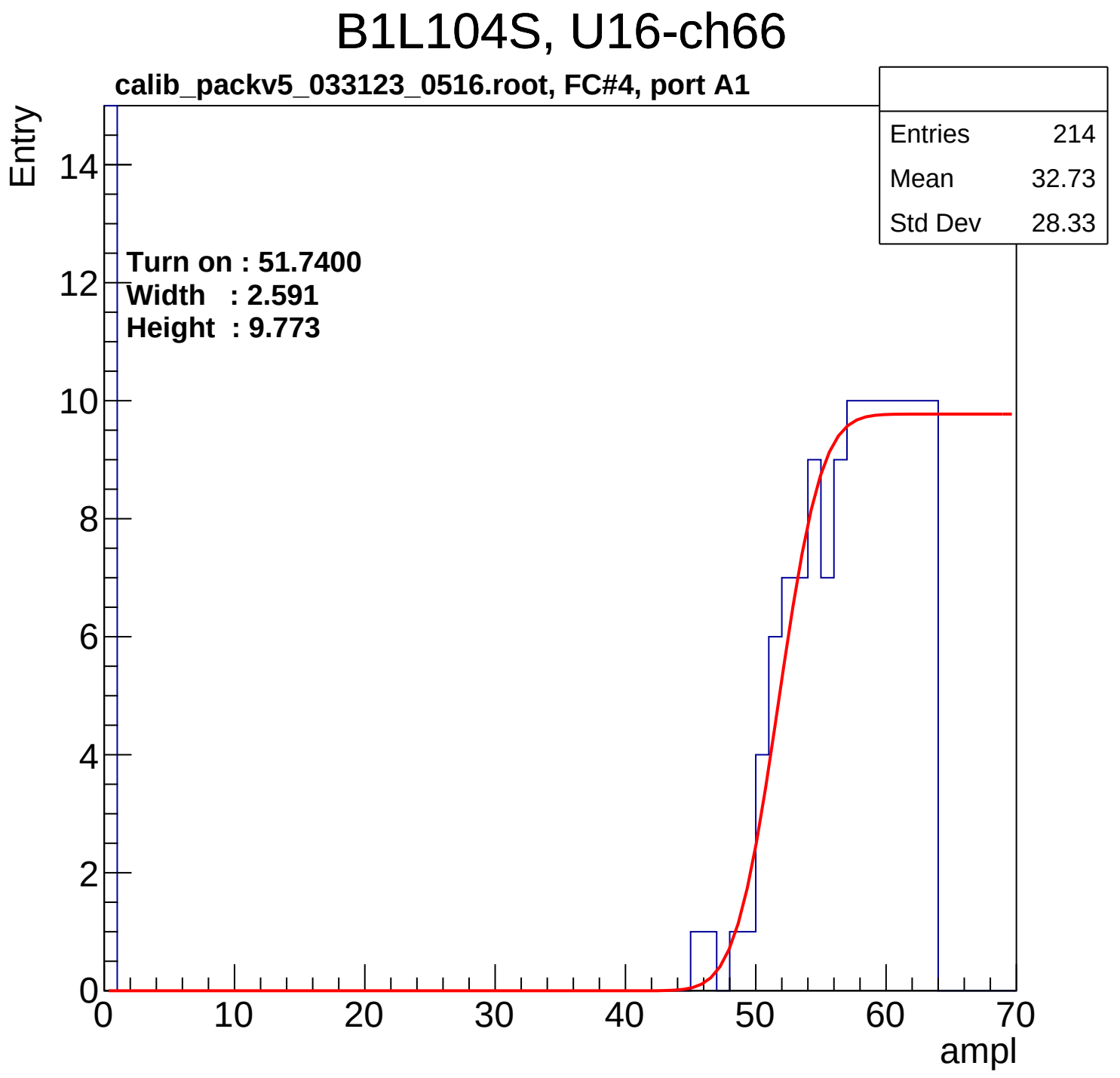
B1L104S, U16-ch66
calib_packv5_033123_0516.root, FC#4, port A1
Entries 214
Mean 32.73
Std Dev 28.33
Turn on : 51.7400
Width : 2.591
Height : 9.773
Entry
ampl
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2
4
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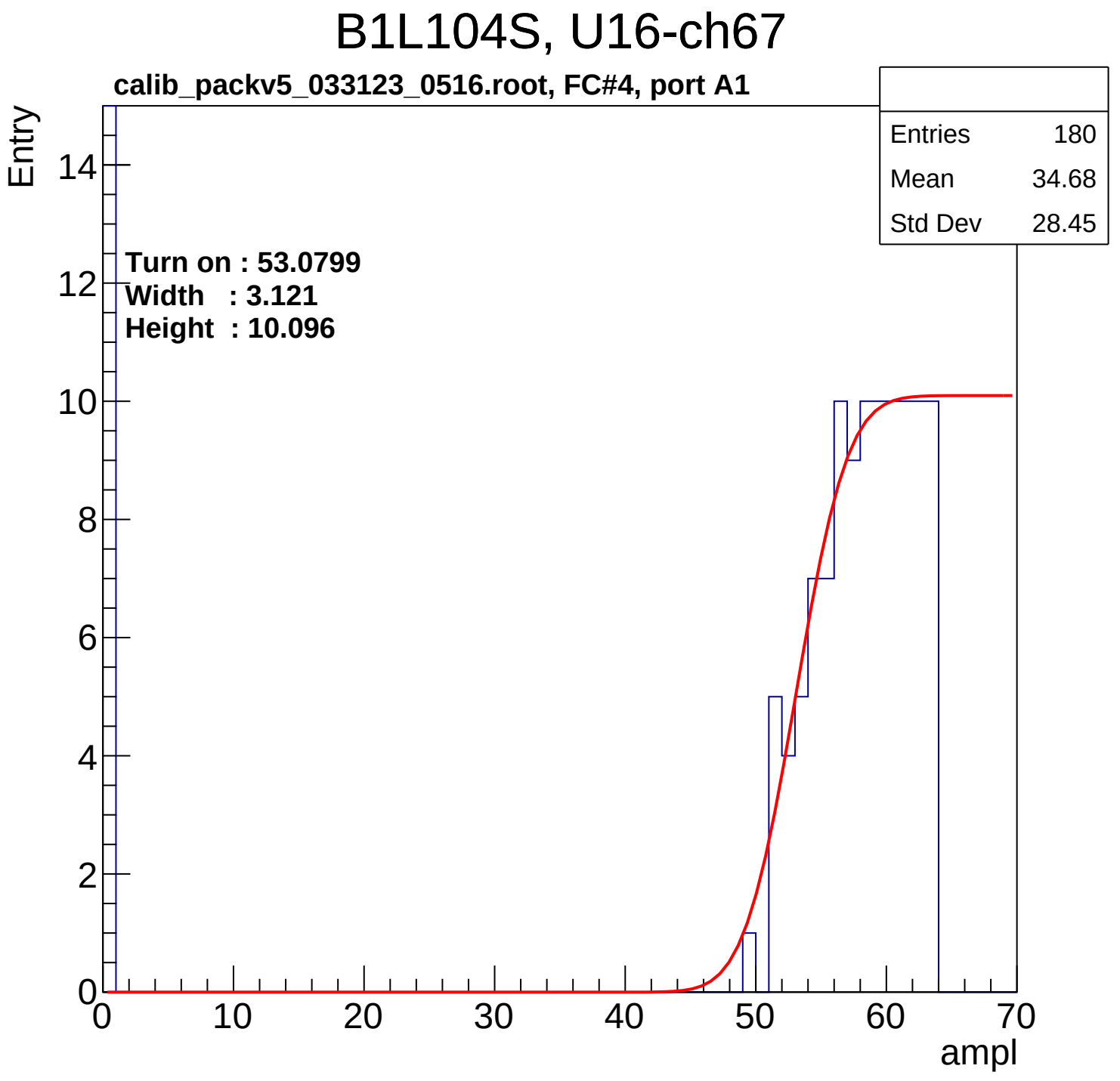
B1L104S, U16-ch67
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 34.68
Std Dev 28.45
Turn on : 53.0799
Width : 3.121
Height : 10.096
Entry
ampl
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4
6
8
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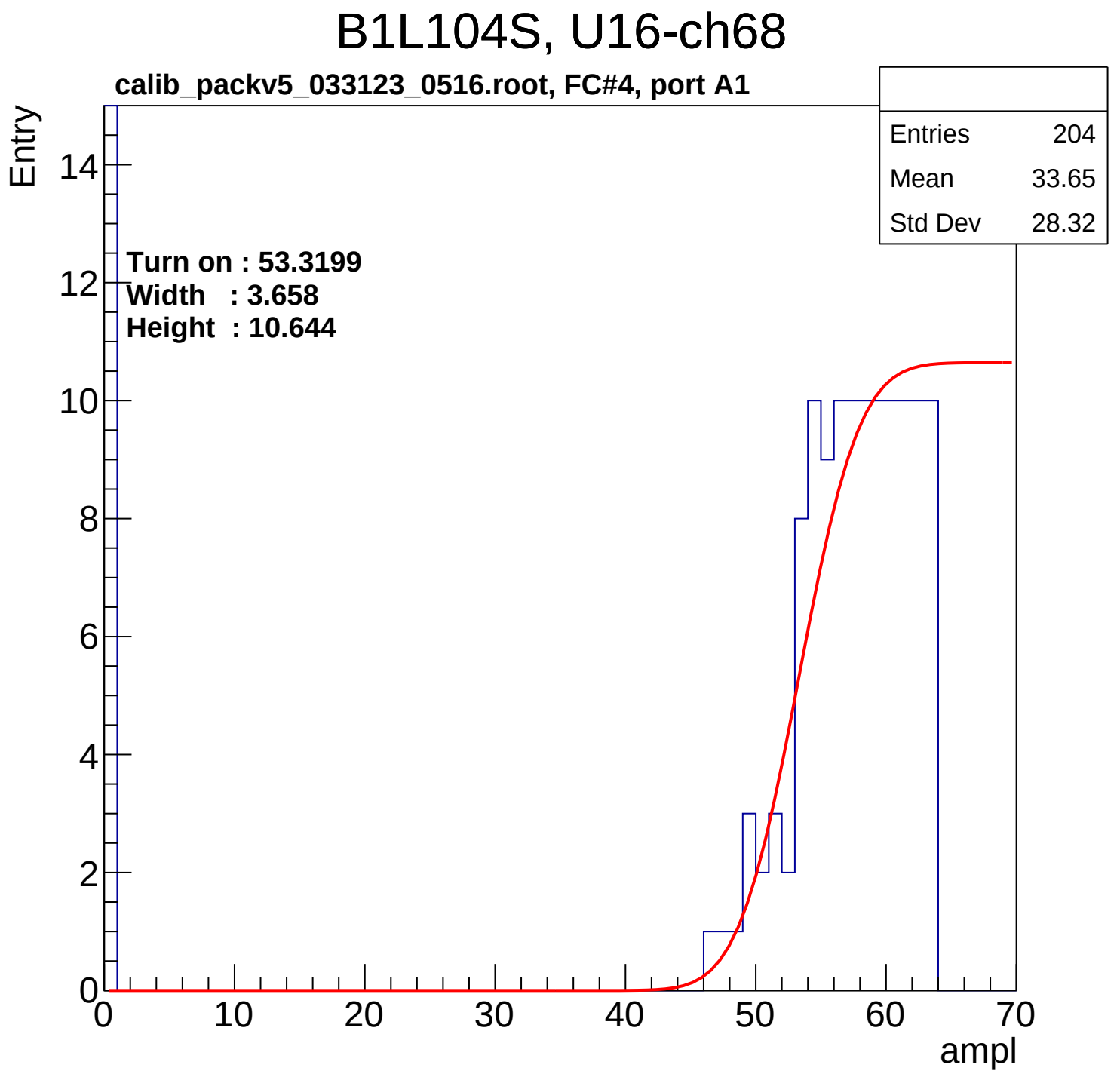
B1L104S, U16-ch68
calib_packv5_033123_0516.root, FC#4, port A1
Entries 204
Mean 33.65
Std Dev 28.32
Turn on : 53.3199
Width : 3.658
Height : 10.644
Entry
ampl
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2
4
6
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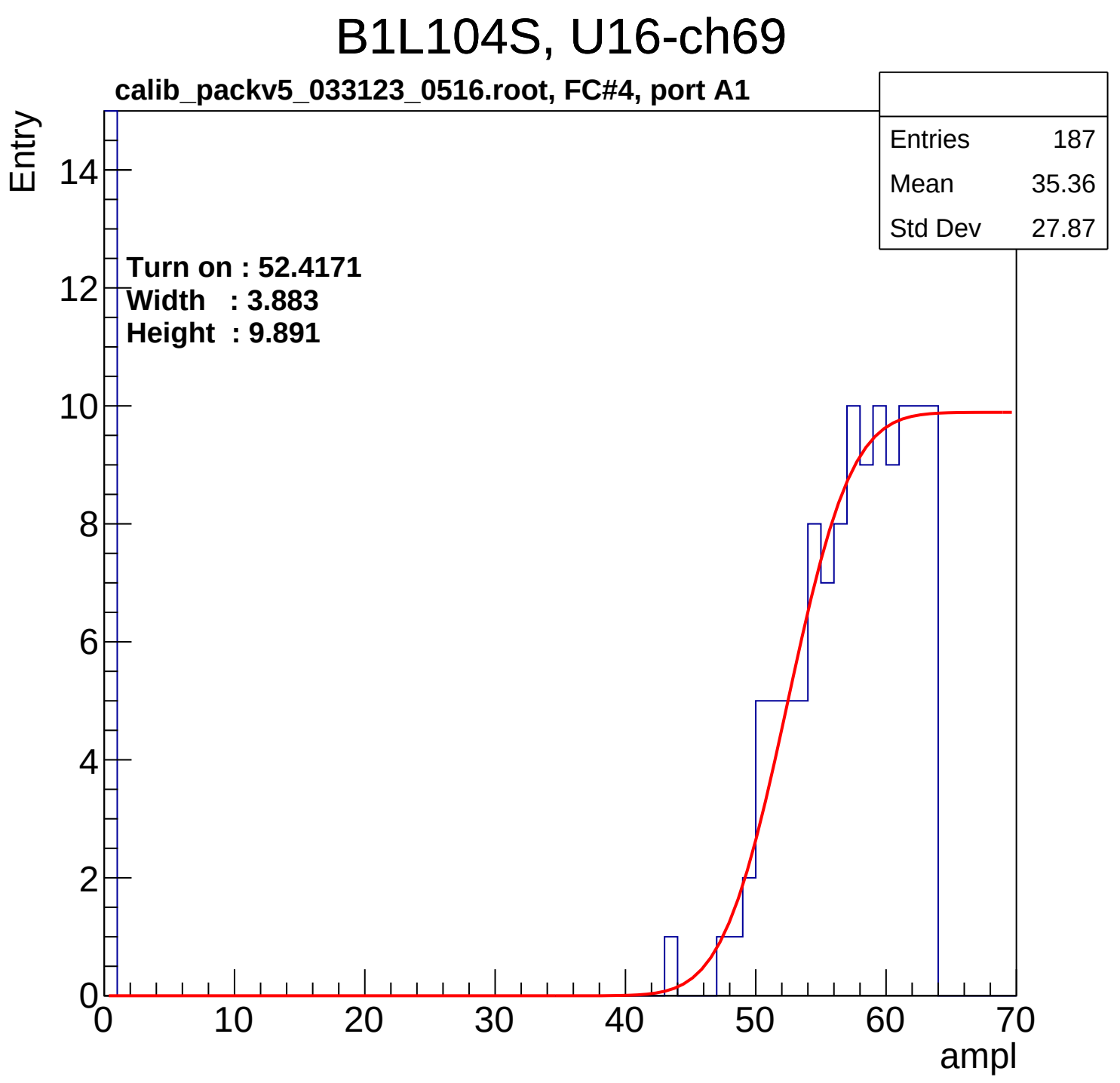
B1L104S, U16-ch69
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 35.36
Std Dev 27.87
Turn on : 52.4171
Width : 3.883
Height : 9.891
Entry
ampl
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4
6
8
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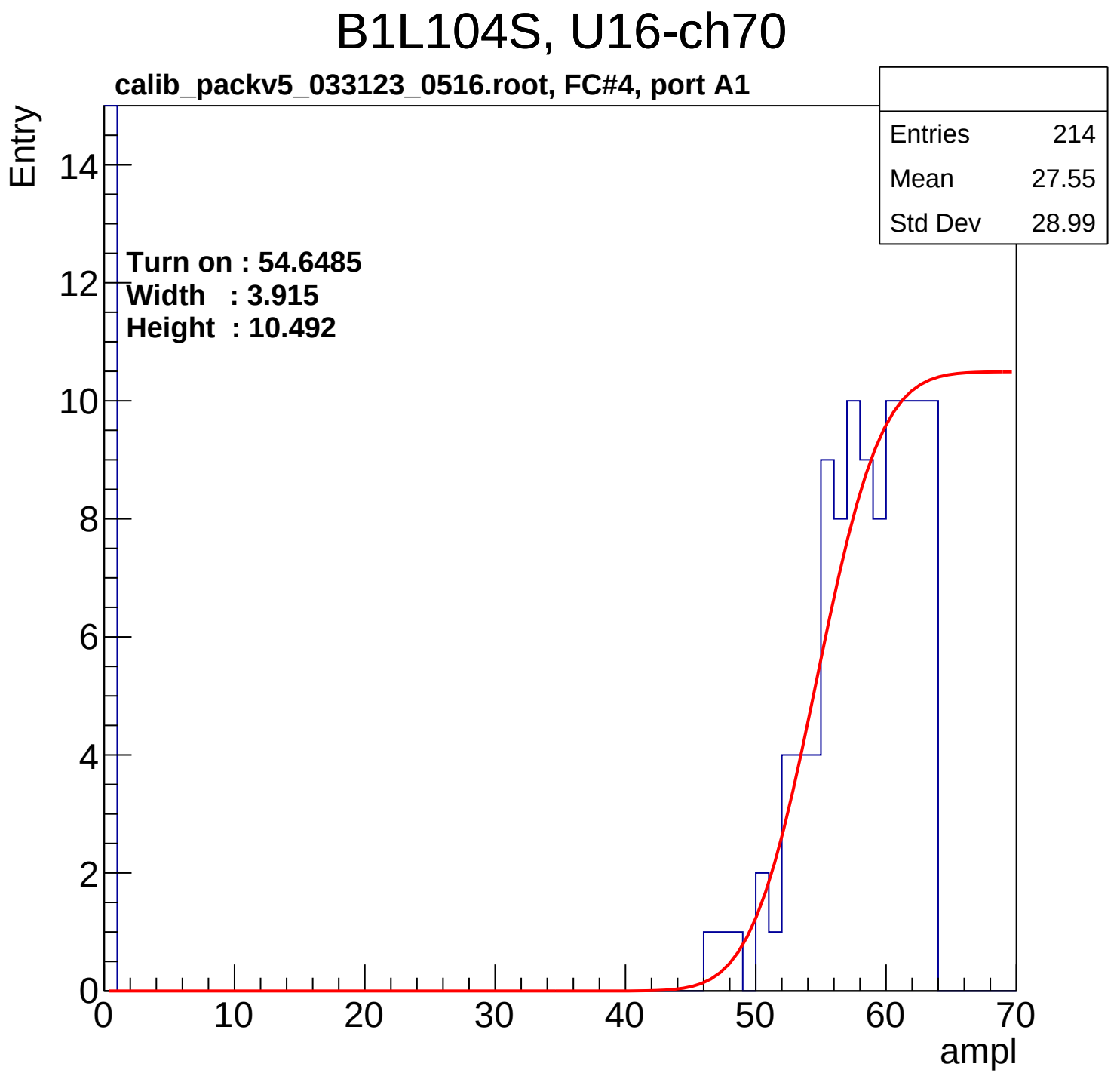
B1L104S, U16-ch70
calib_packv5_033123_0516.root, FC#4, port A1
Entries 214
Mean 27.55
Std Dev 28.99
Turn on : 54.6485
Width : 3.915
Height : 10.492
Entry
ampl
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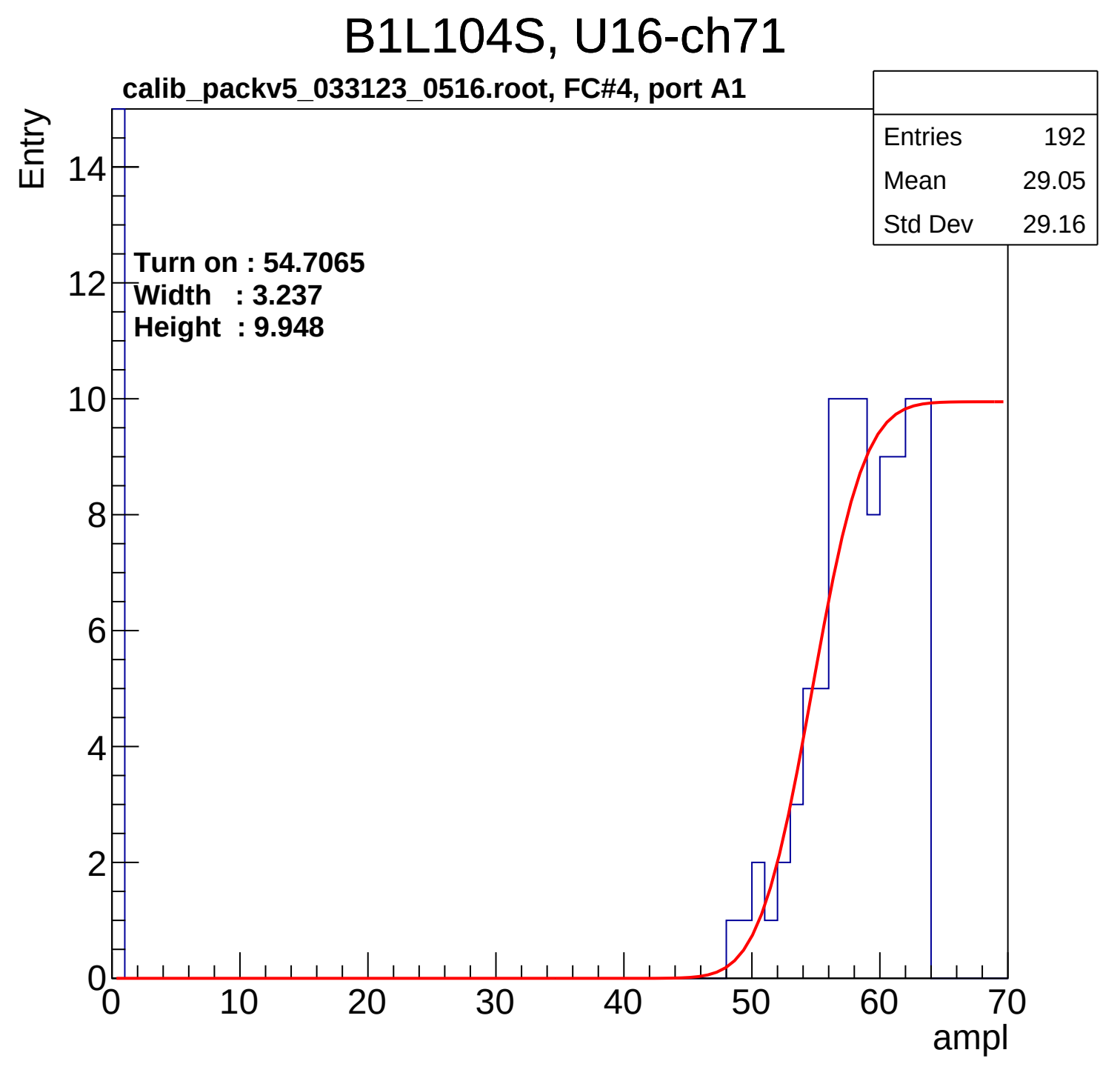
B1L104S, U16-ch71
calib_packv5_033123_0516.root, FC#4, port A1
Entries 192
Mean 29.05
Std Dev 29.16
Turn on : 54.7065
Width : 3.237
Height : 9.948
Entry
ampl
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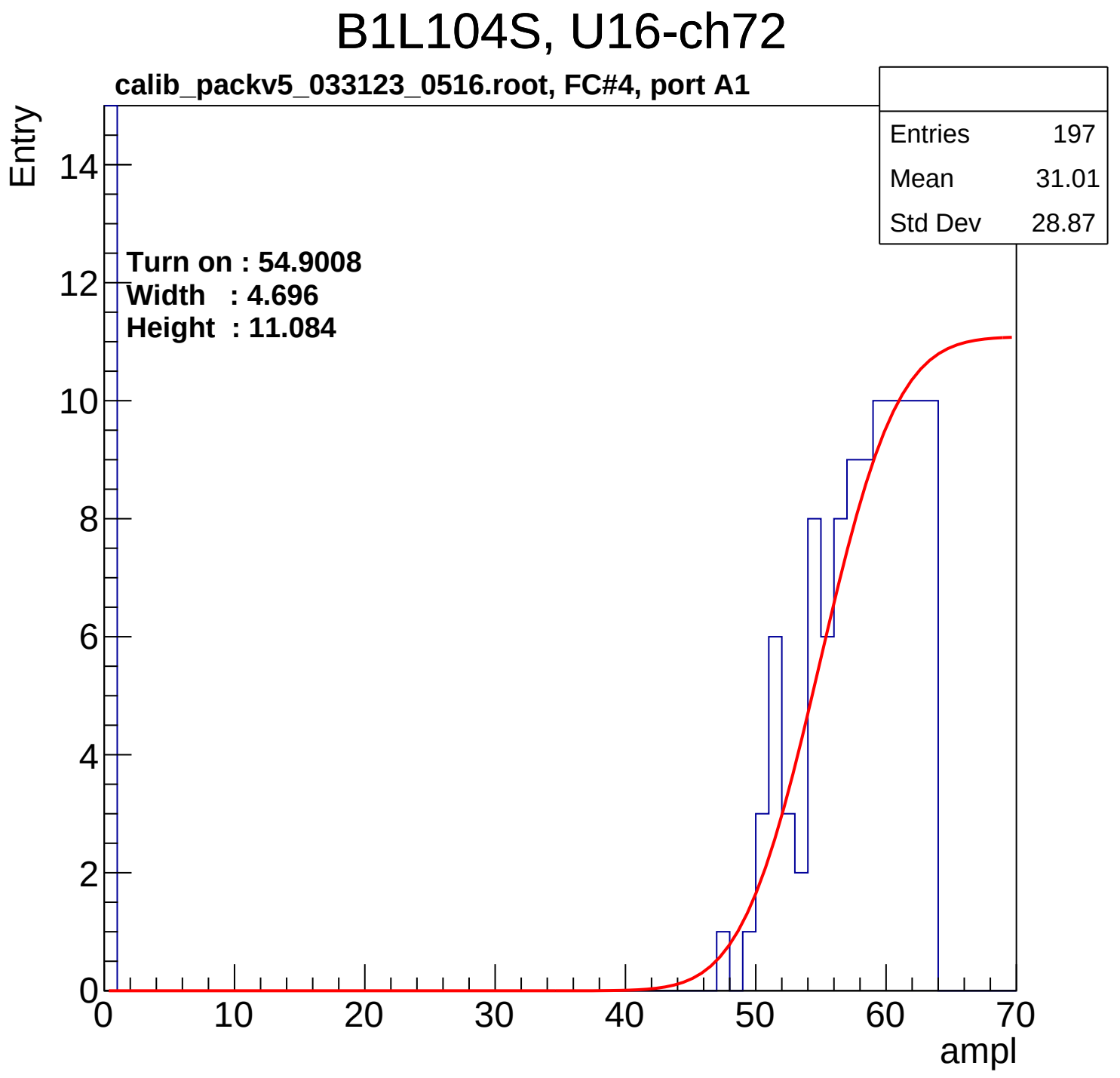
B1L104S, U16-ch72
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 31.01
Std Dev 28.87
Turn on : 54.9008
Width : 4.696
Height : 11.084
Entry
ampl
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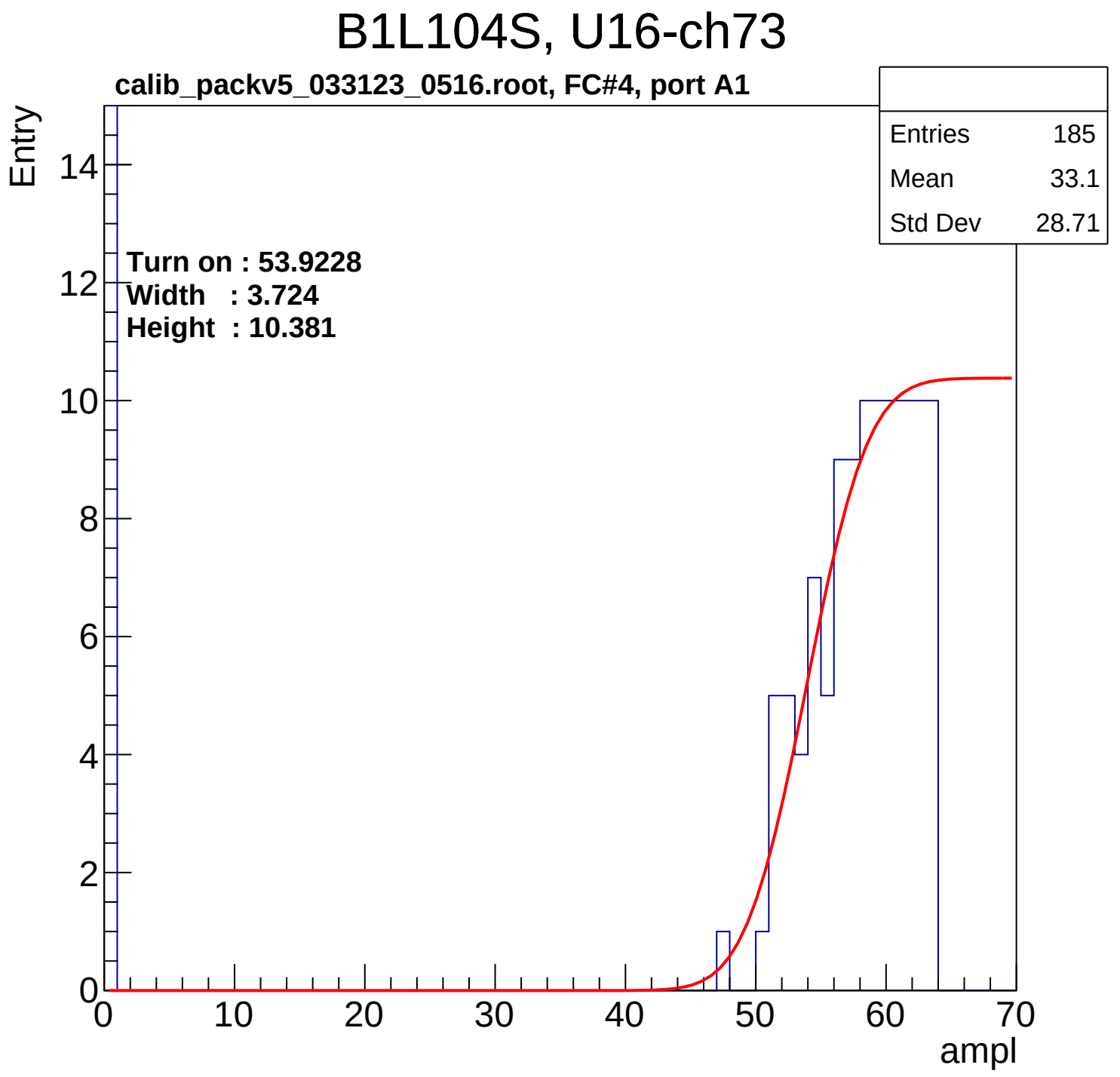
B1L104S, U16-ch73
calib_packv5_033123_0516.root, FC#4, port A1
Entries 185
Mean 33.1
Std Dev 28.71
Turn on : 53.9228
Width : 3.724
Height : 10.381
Entry
ampl
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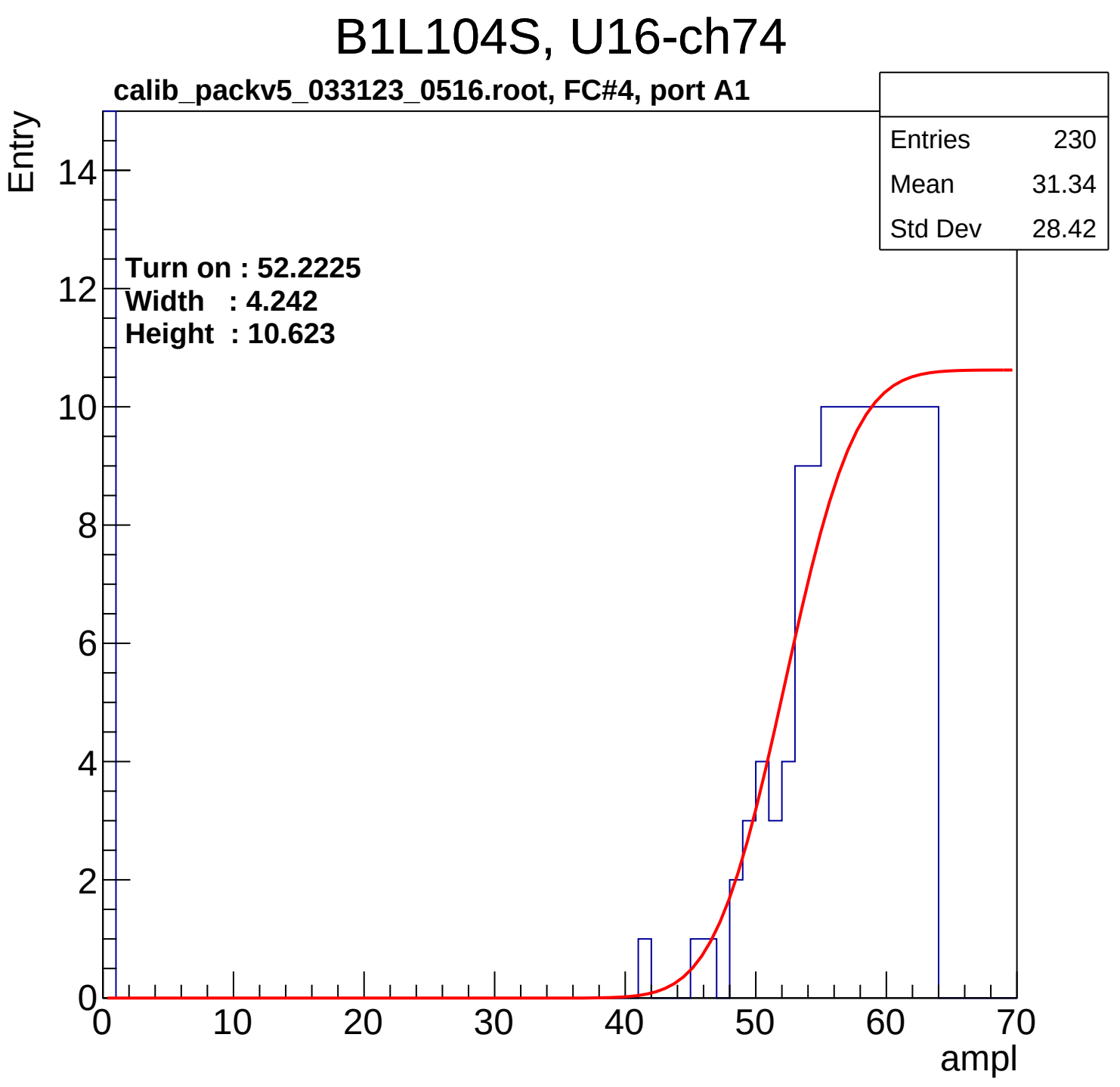
B1L104S, U16-ch74
calib_packv5_033123_0516.root, FC#4, port A1
Entries 230
Mean 31.34
Std Dev 28.42
Turn on : 52.2225
Width : 4.242
Height : 10.623
Entry
ampl
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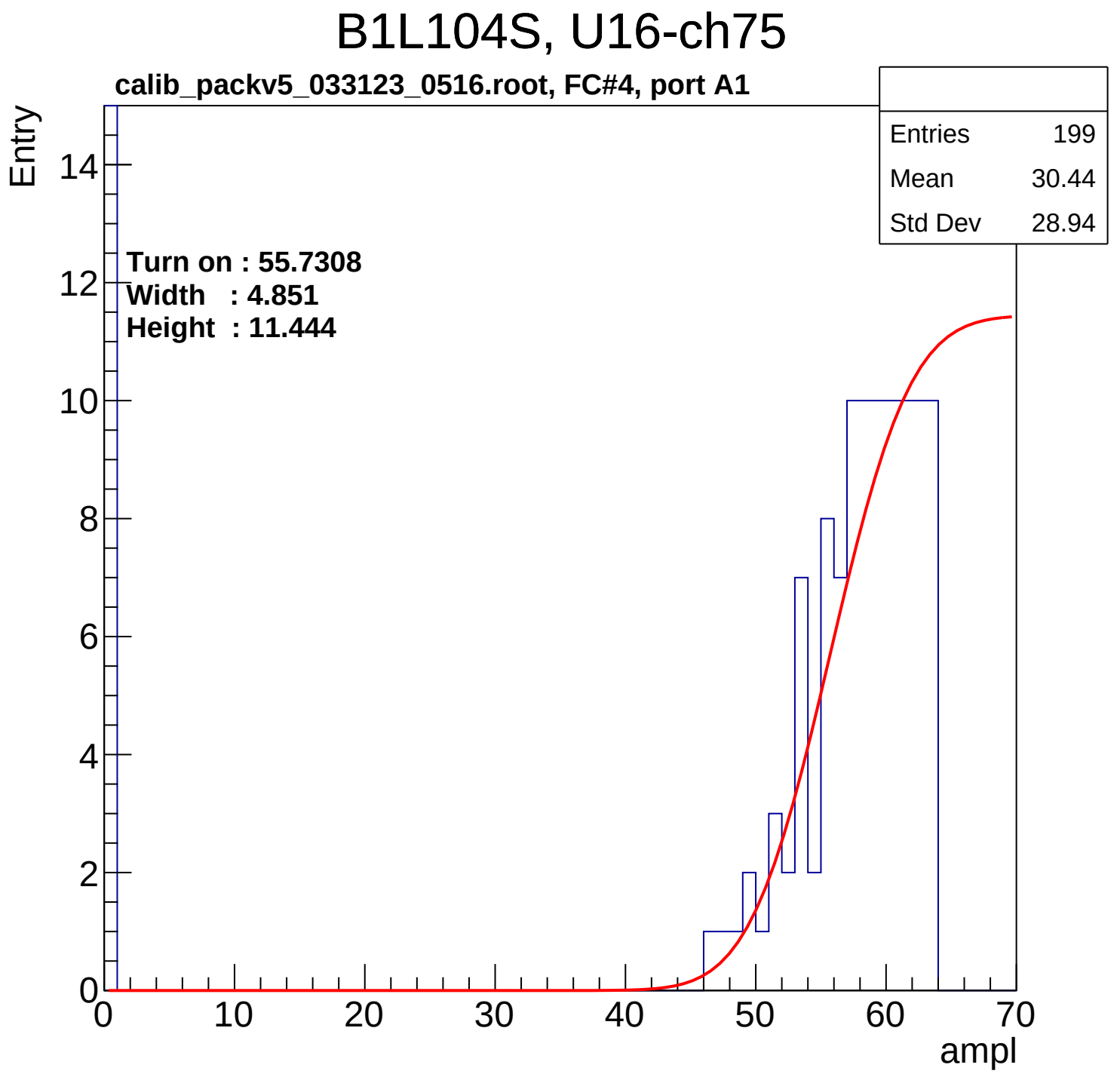
B1L104S, U16-ch75
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 30.44
Std Dev 28.94
Turn on : 55.7308
Width : 4.851
Height : 11.444
Entry
ampl
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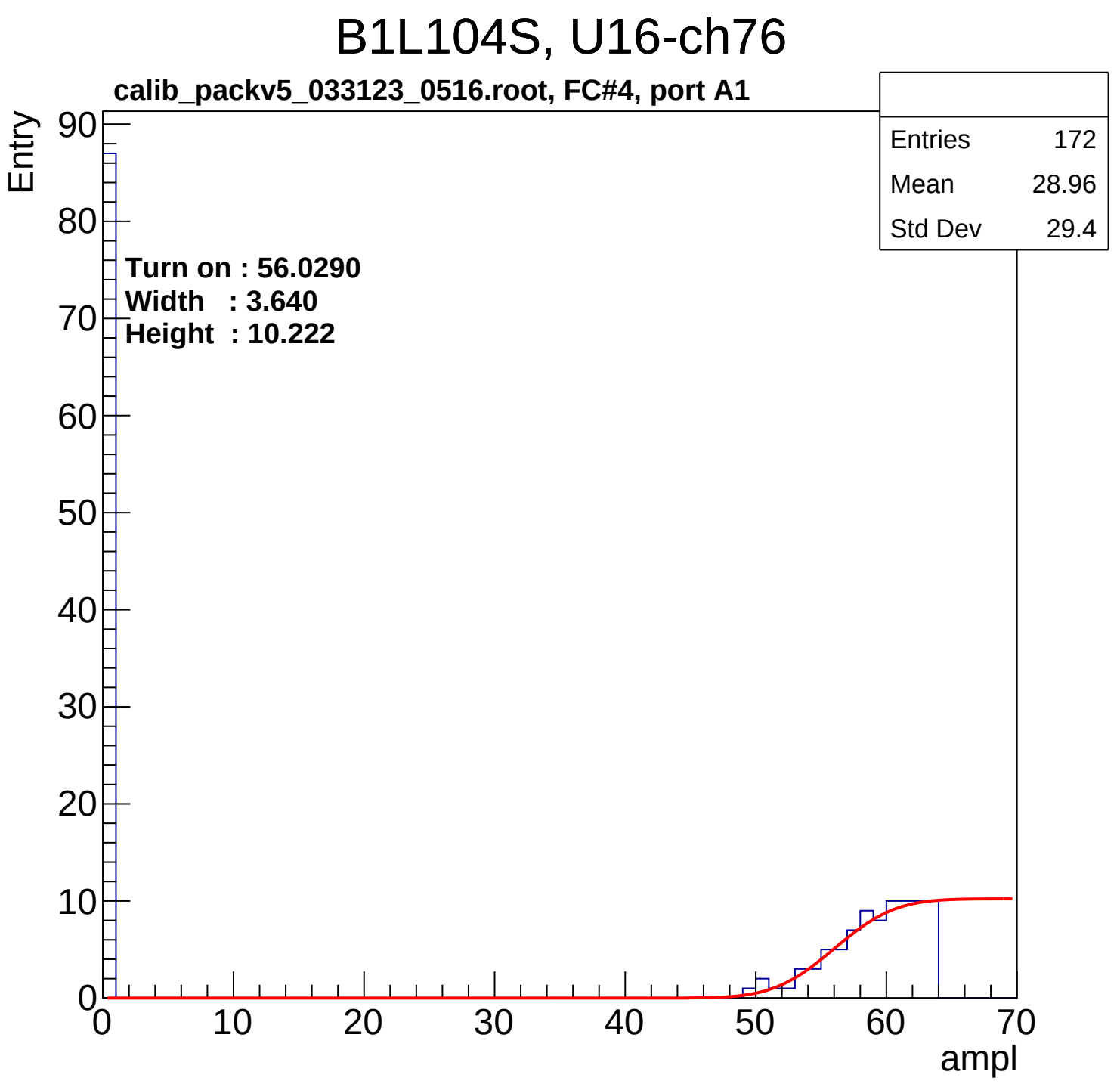
B1L104S, U16-ch76
calib_packv5_033123_0516.root, FC#4, port A1
Entries 172
Mean 28.96
Std Dev 29.4
Turn on : 56.0290
Width : 3.640
Height : 10.222
Entry
ampl
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80
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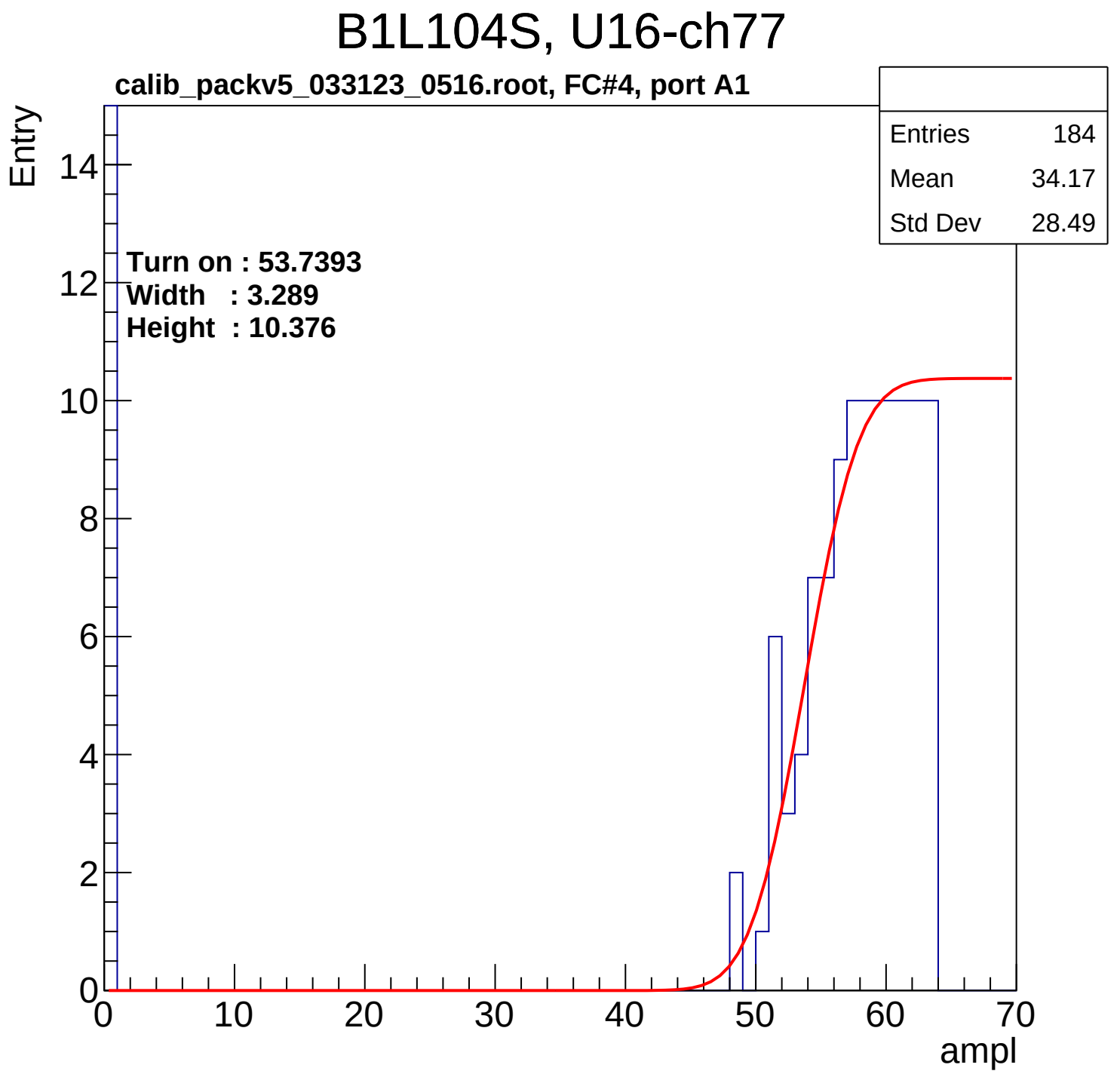
B1L104S, U16-ch77
calib_packv5_033123_0516.root, FC#4, port A1
Entries 184
Mean 34.17
Std Dev 28.49
Turn on : 53.7393
Width : 3.289
Height : 10.376
Entry
ampl
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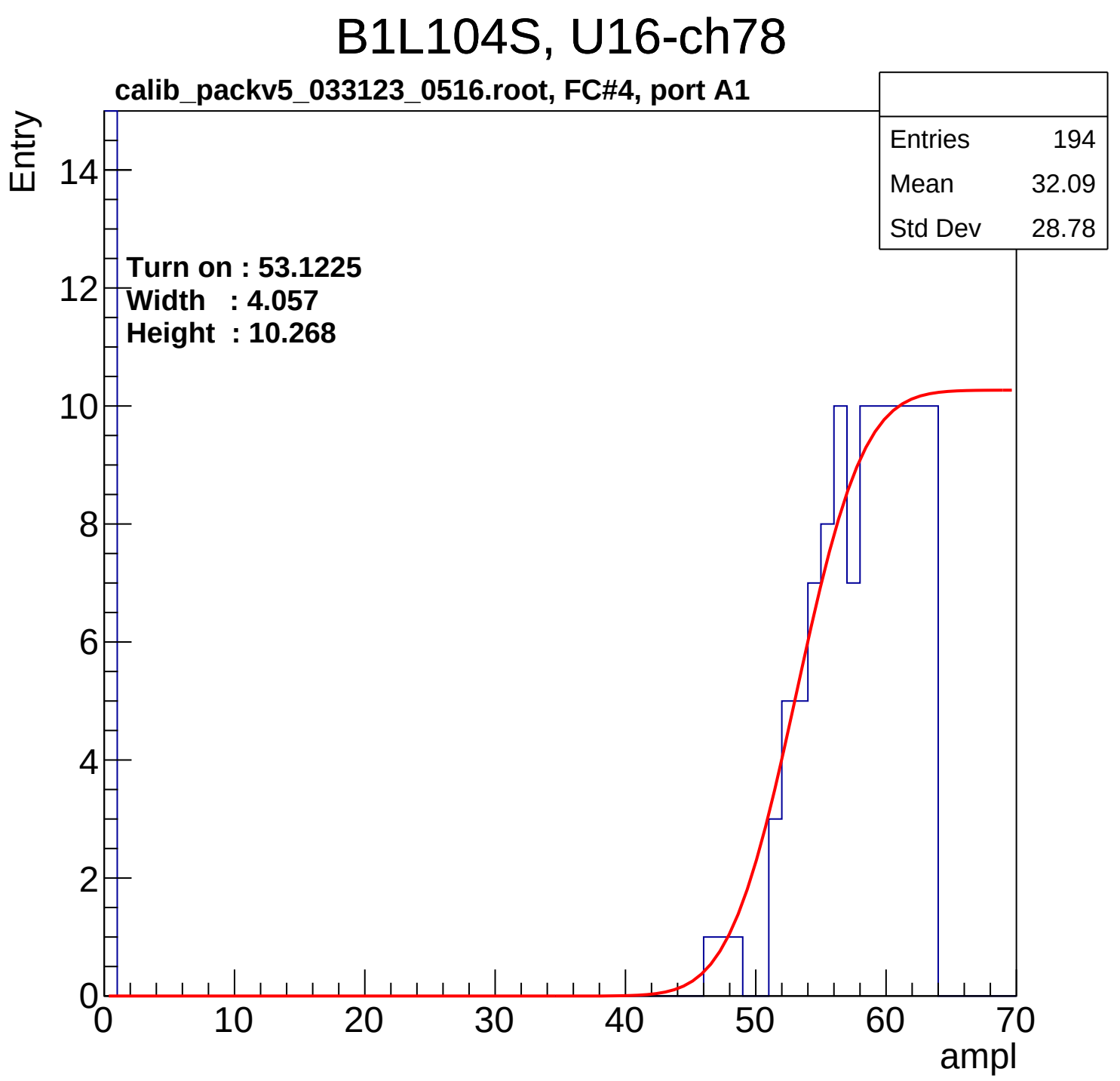
B1L104S, U16-ch78
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 32.09
Std Dev 28.78
Turn on : 53.1225
Width : 4.057
Height : 10.268
Entry
ampl
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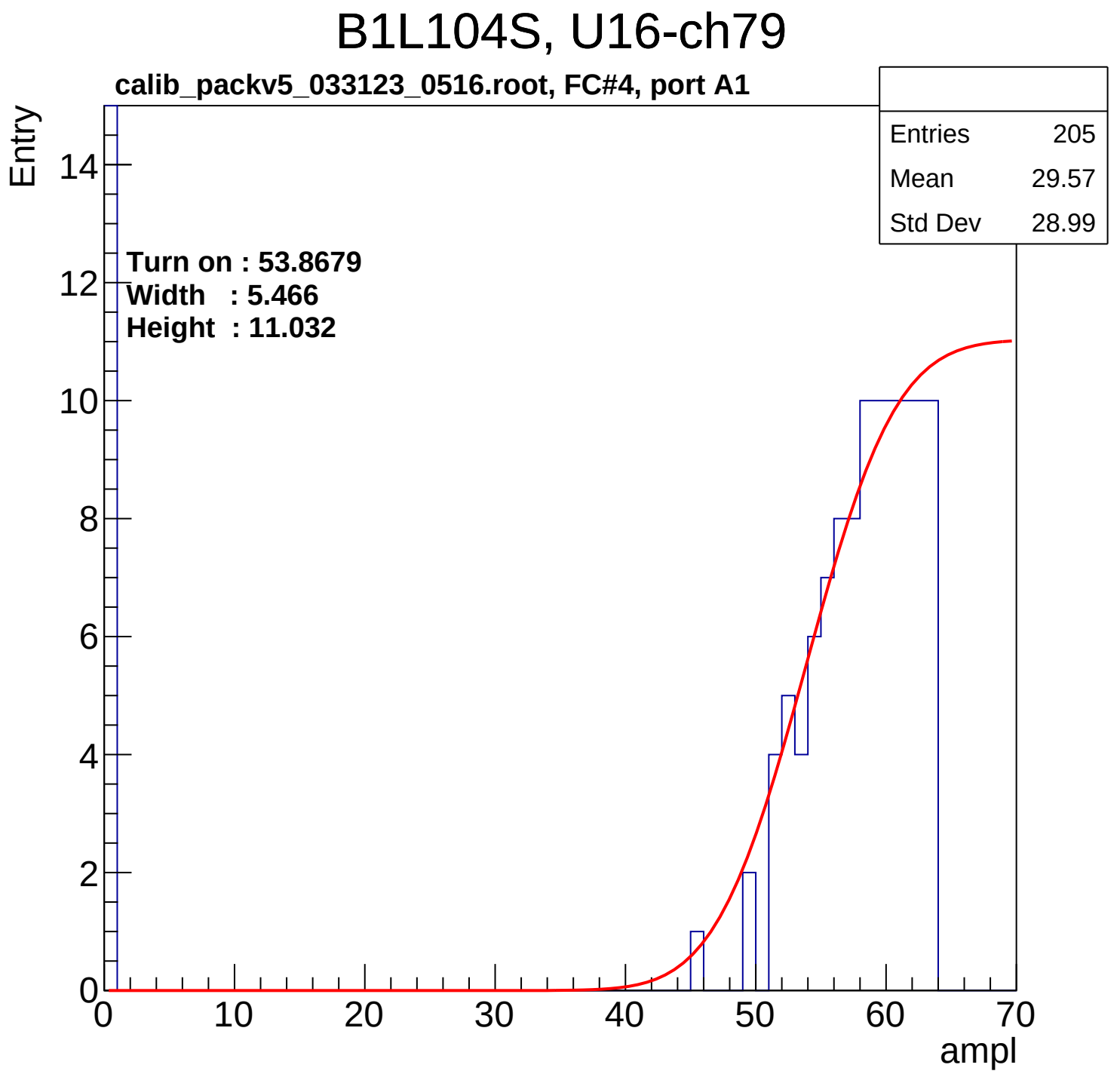
B1L104S, U16-ch79
calib_packv5_033123_0516.root, FC#4, port A1
Entries 205
Mean 29.57
Std Dev 28.99
Turn on : 53.8679
Width : 5.466
Height : 11.032
Entry
ampl
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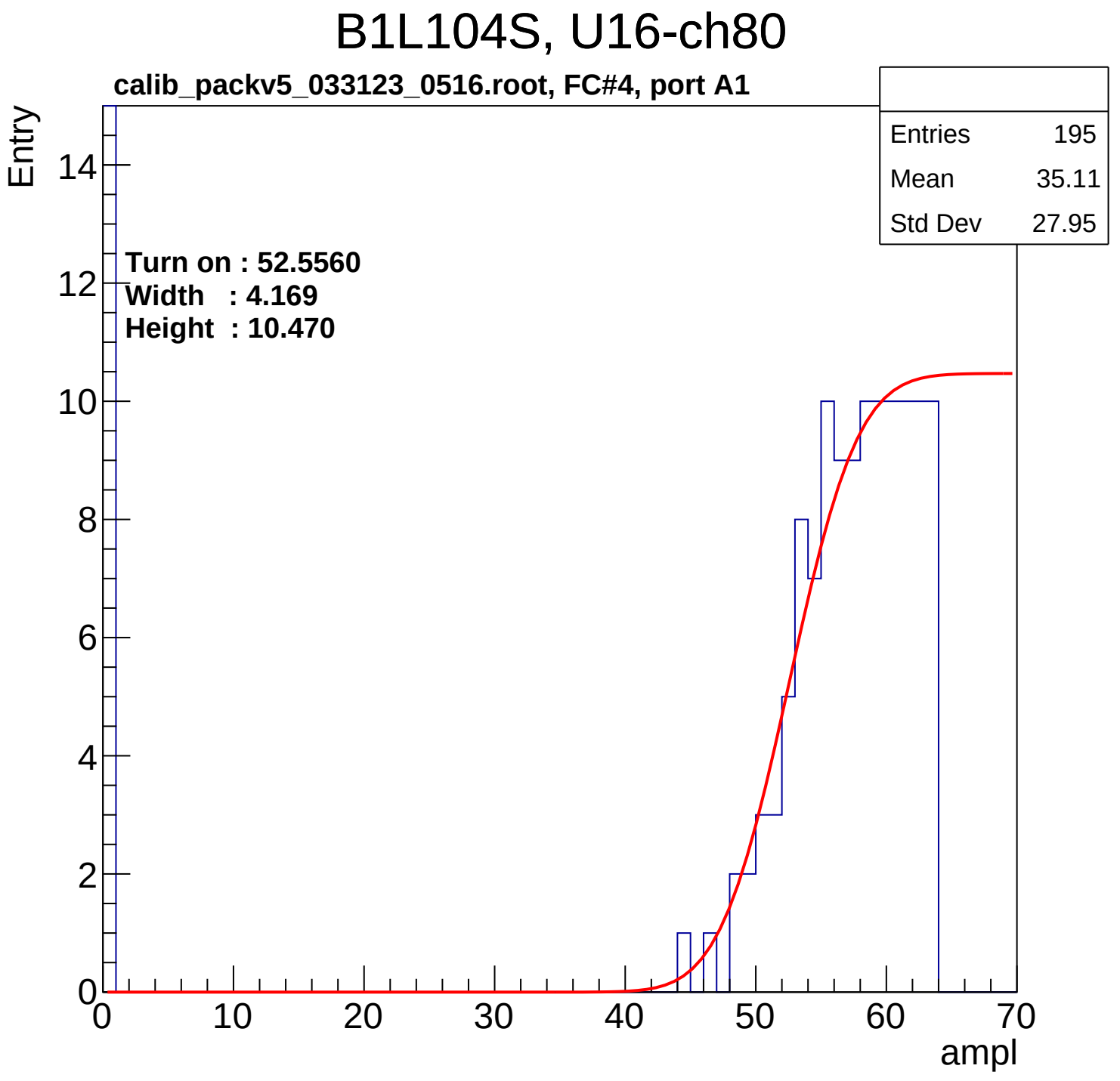
B1L104S, U16-ch80
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 35.11
Std Dev 27.95
Turn on : 52.5560
Width : 4.169
Height : 10.470
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ampl
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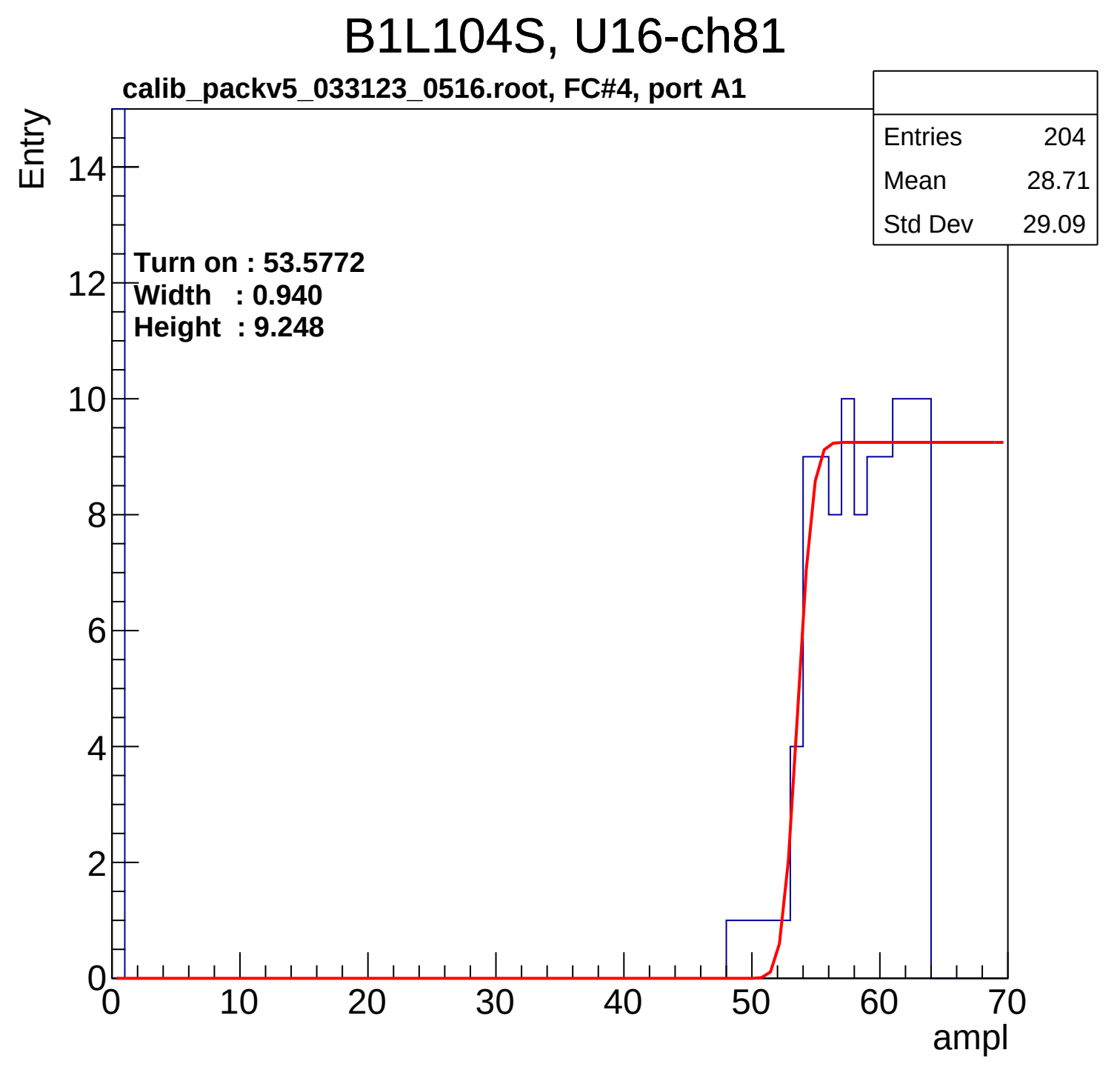
B1L104S, U16-ch81
calib_packv5_033123_0516.root, FC#4, port A1
Entries 204
Mean 28.71
Std Dev 29.09
Turn on : 53.5772
Width : 0.940
Height : 9.248
Entry
ampl
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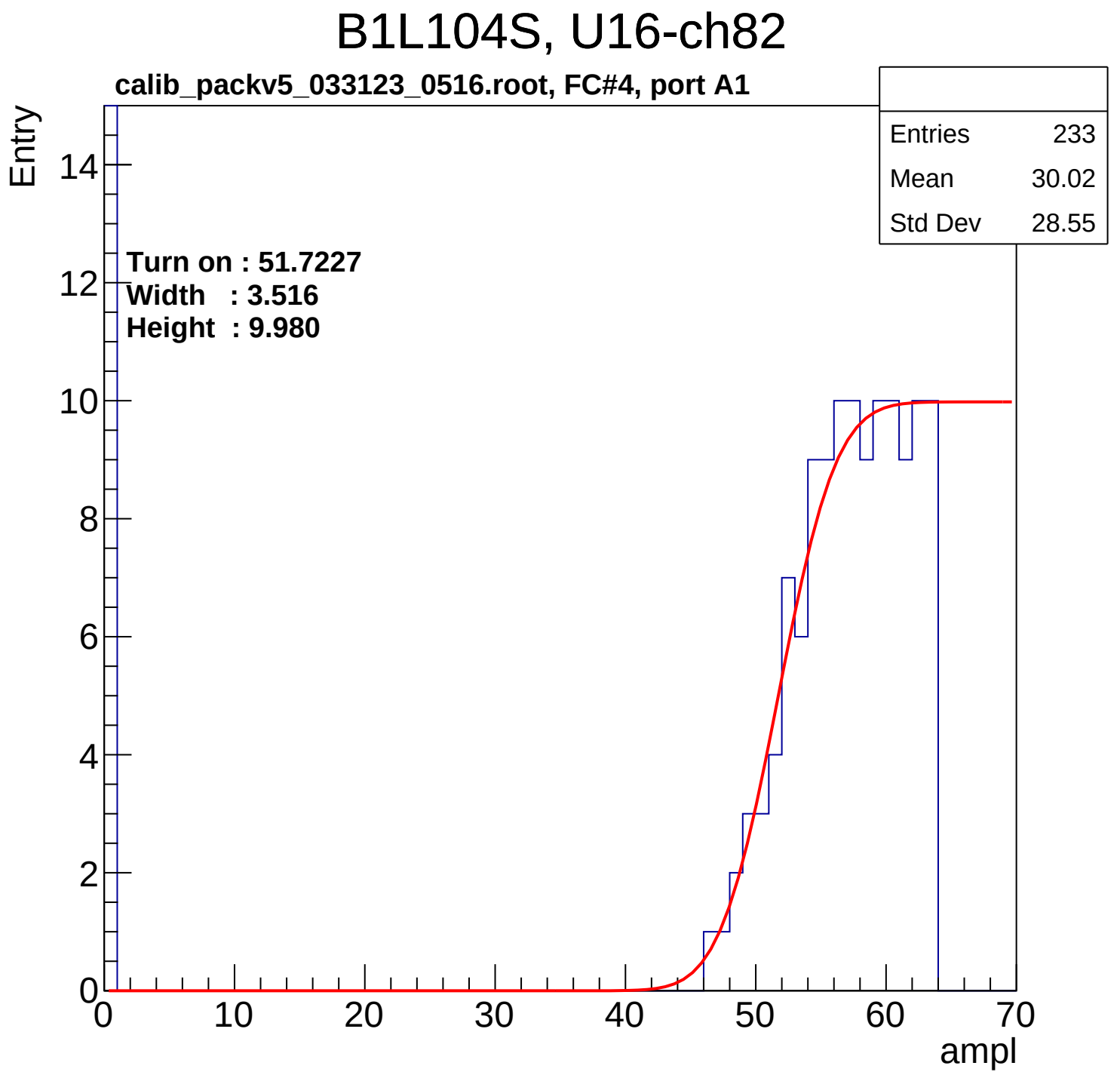
B1L104S, U16-ch82
calib_packv5_033123_0516.root, FC#4, port A1
Entries 233
Mean 30.02
Std Dev 28.55
Turn on : 51.7227
Width : 3.516
Height : 9.980
Entry
ampl
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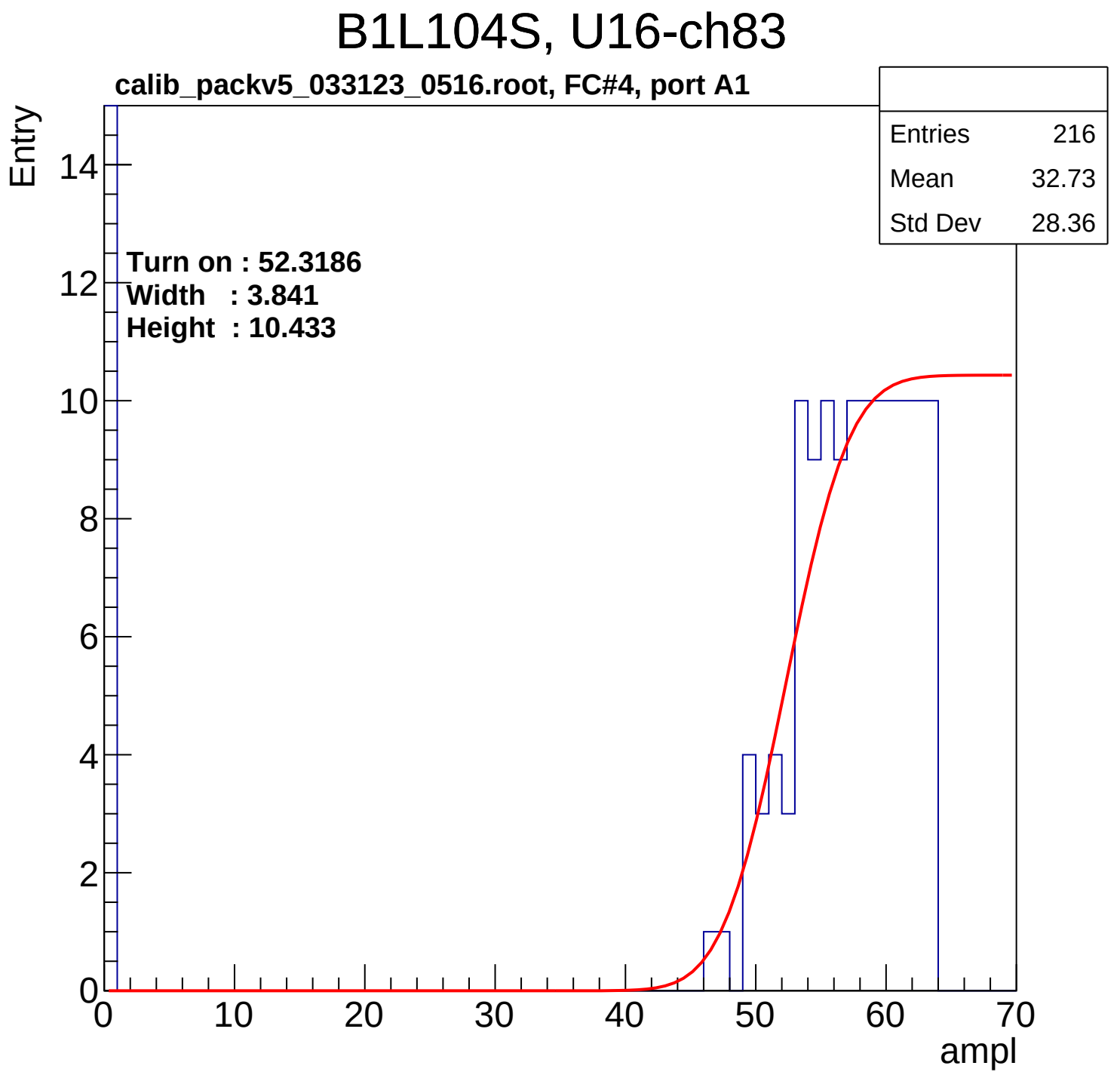
B1L104S, U16-ch83
calib_packv5_033123_0516.root, FC#4, port A1
Entries 216
Mean 32.73
Std Dev 28.36
Turn on : 52.3186
Width : 3.841
Height : 10.433
Entry
ampl
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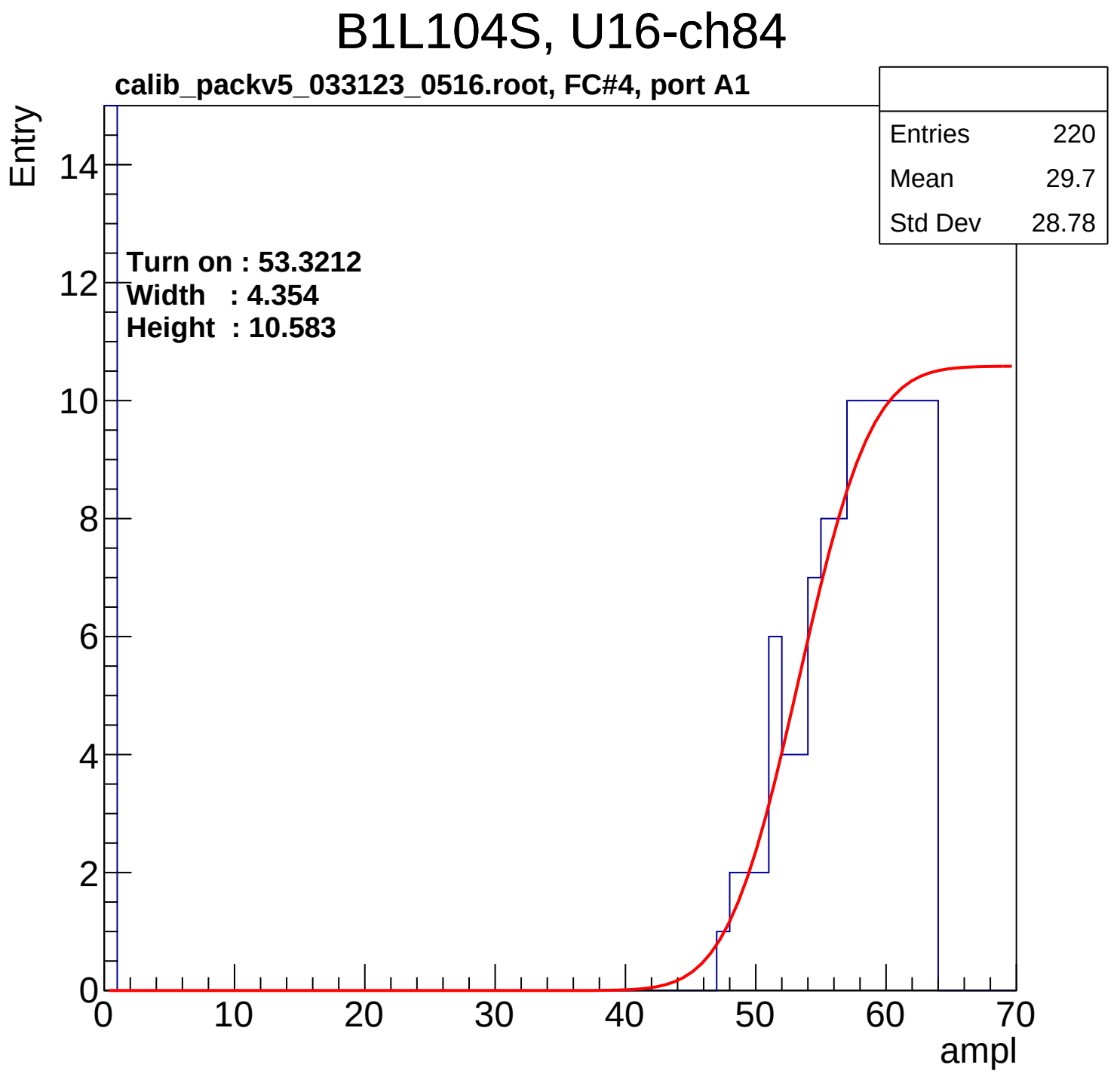
B1L104S, U16-ch84
calib_packv5_033123_0516.root, FC#4, port A1
Entries 220
Mean 29.7
Std Dev 28.78
Turn on : 53.3212
Width : 4.354
Height : 10.583
Entry
ampl
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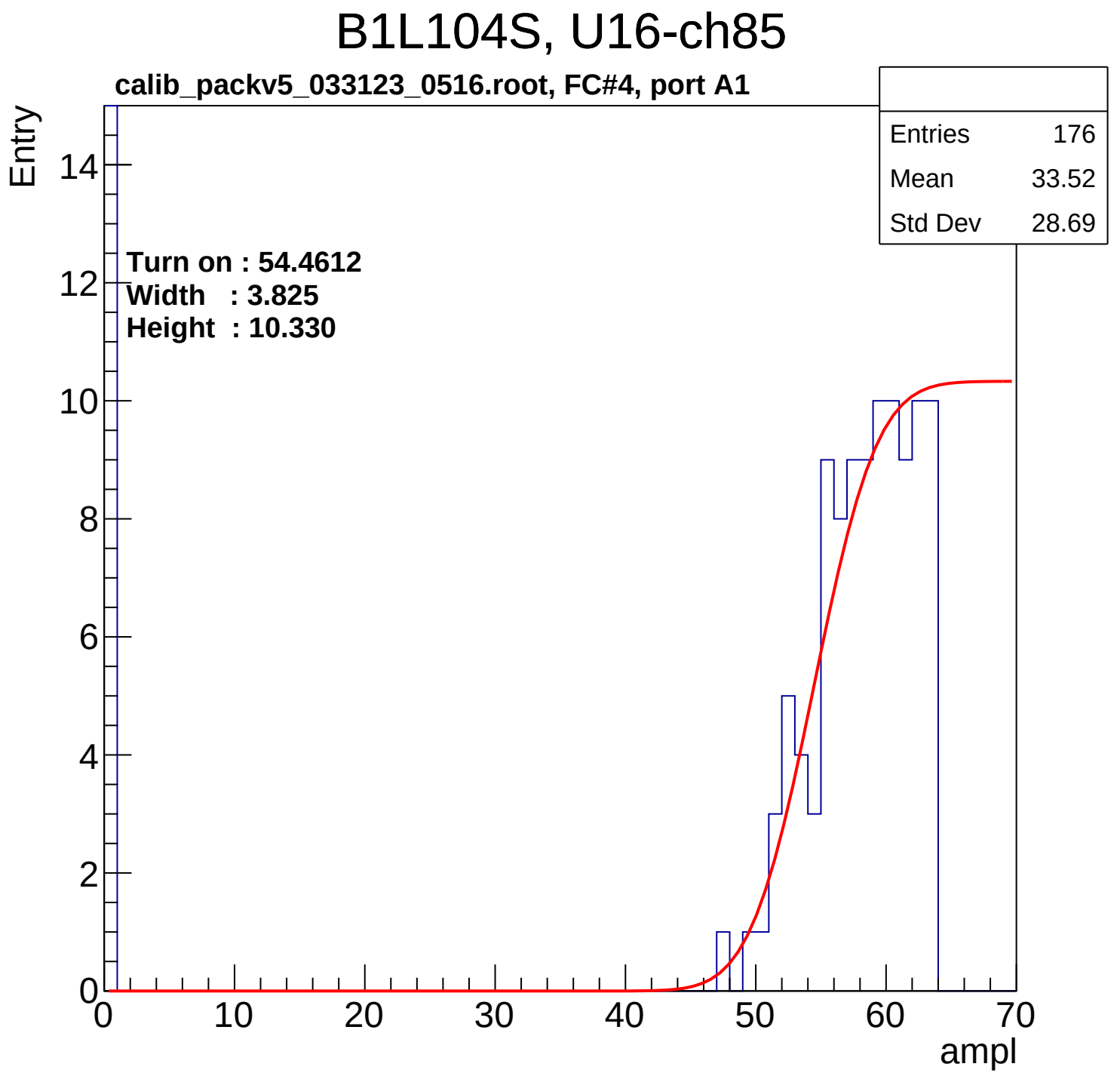
B1L104S, U16-ch85
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 33.52
Std Dev 28.69
Turn on : 54.4612
Width : 3.825
Height : 10.330
Entry
ampl
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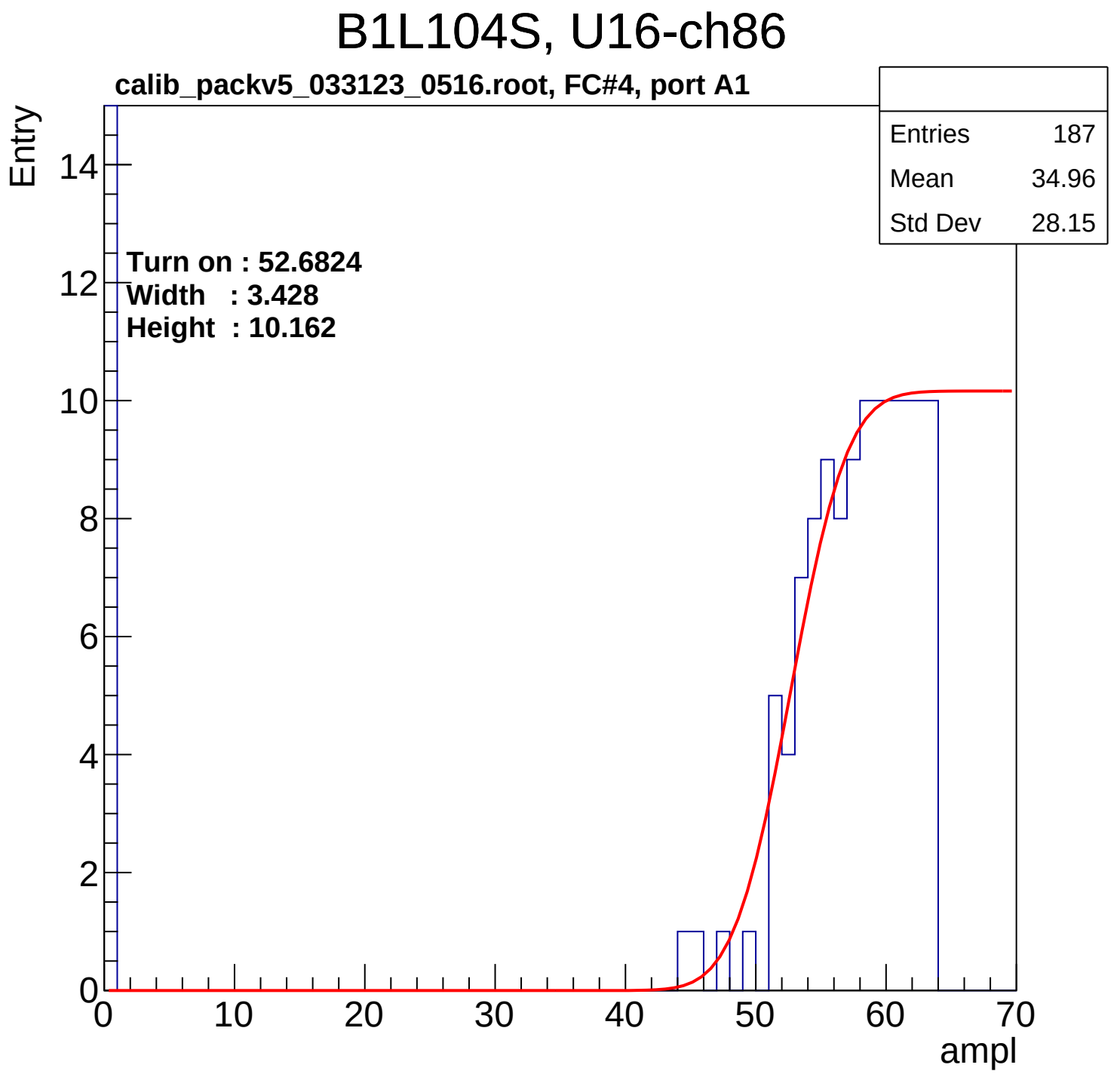
B1L104S, U16-ch86
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 34.96
Std Dev 28.15
Turn on : 52.6824
Width : 3.428
Height : 10.162
Entry
ampl
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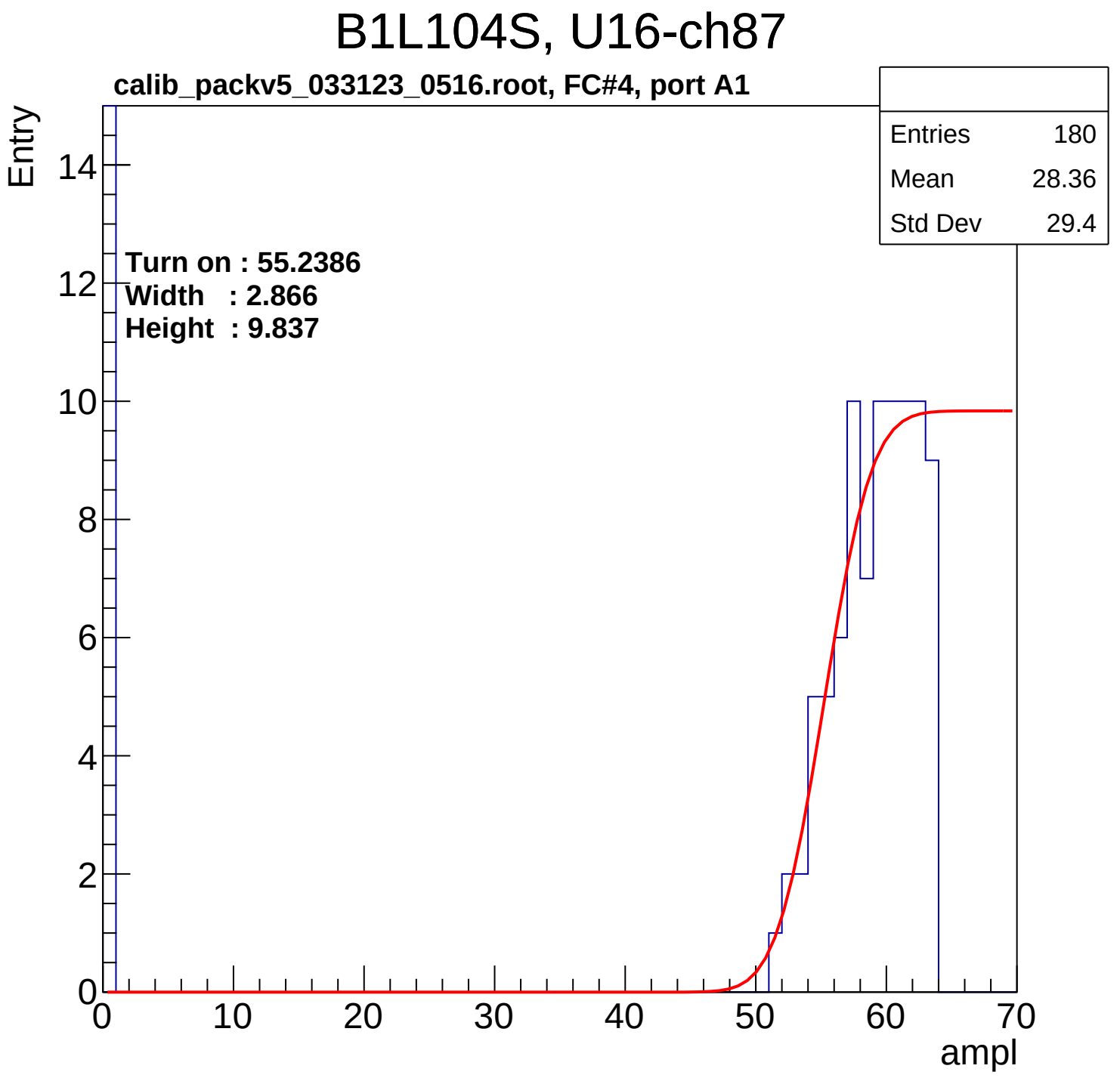
B1L104S, U16-ch87
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 28.36
Std Dev 29.4
Turn on : 55.2386
Width : 2.866
Height : 9.837
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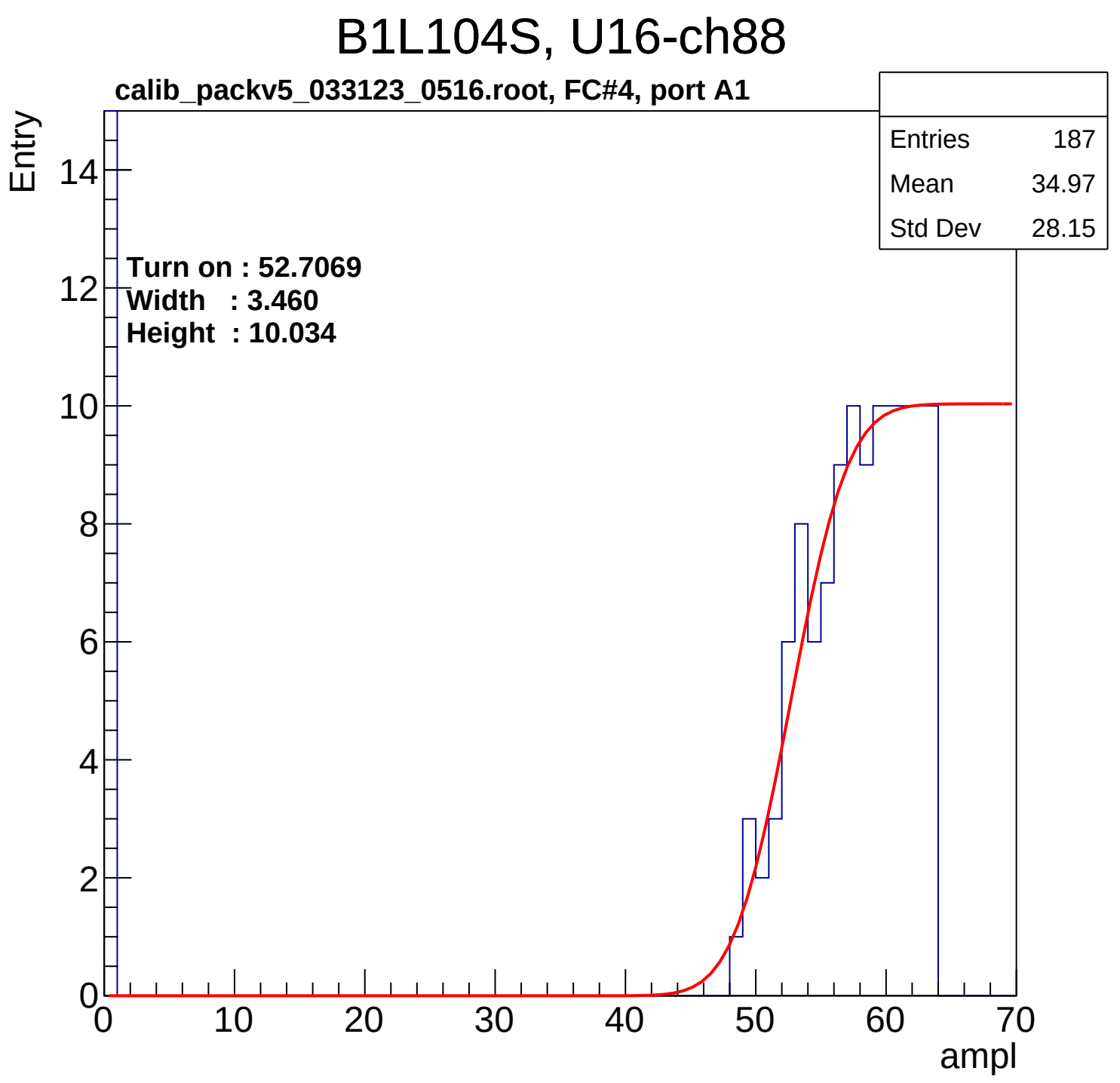
B1L104S, U16-ch88
calib_packv5_033123_0516.root, FC#4, port A1
Entries 187
Mean 34.97
Std Dev 28.15
Turn on : 52.7069
Width : 3.460
Height : 10.034
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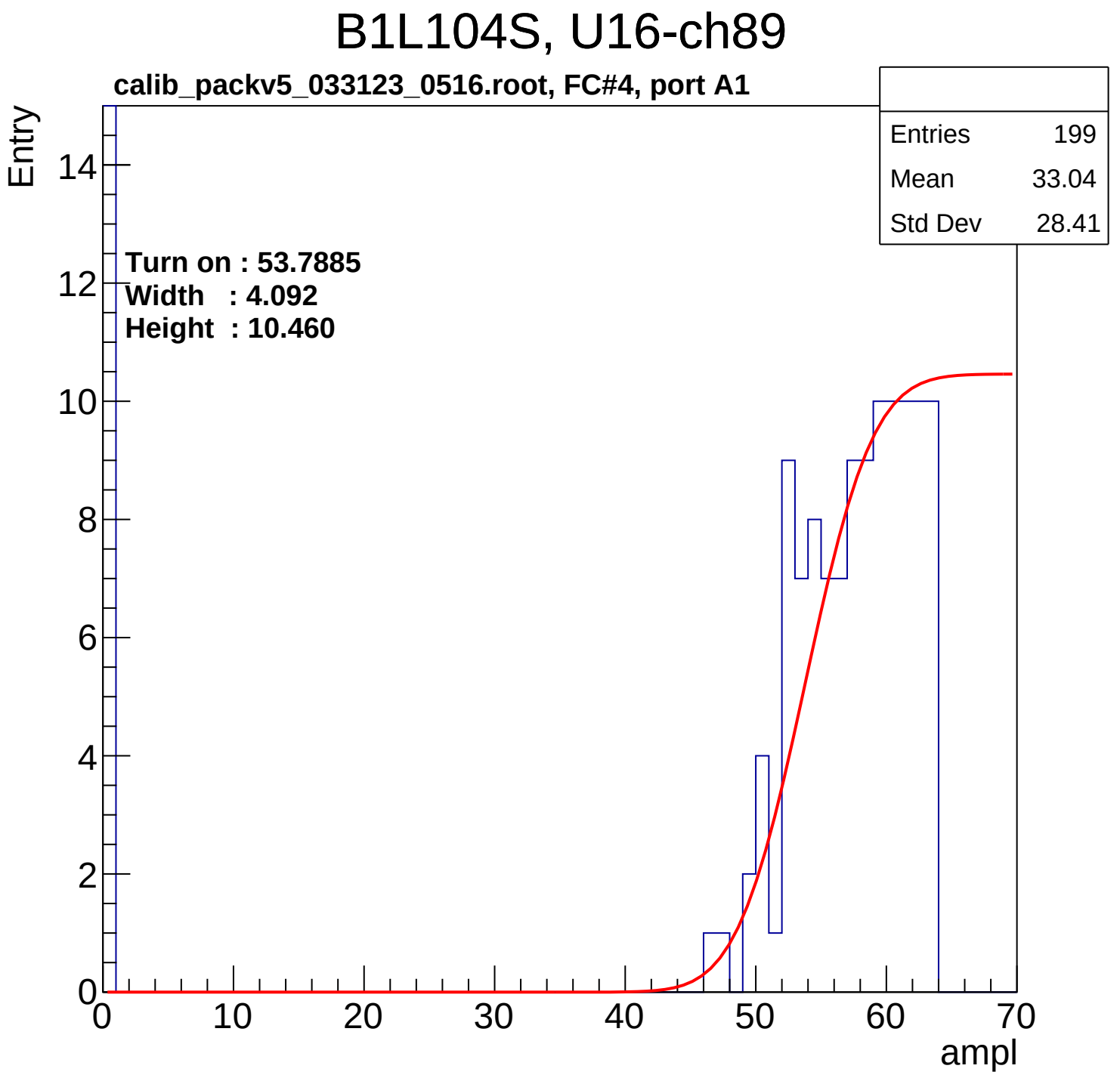
B1L104S, U16-ch89
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 33.04
Std Dev 28.41
Turn on : 53.7885
Width : 4.092
Height : 10.460
Entry
ampl
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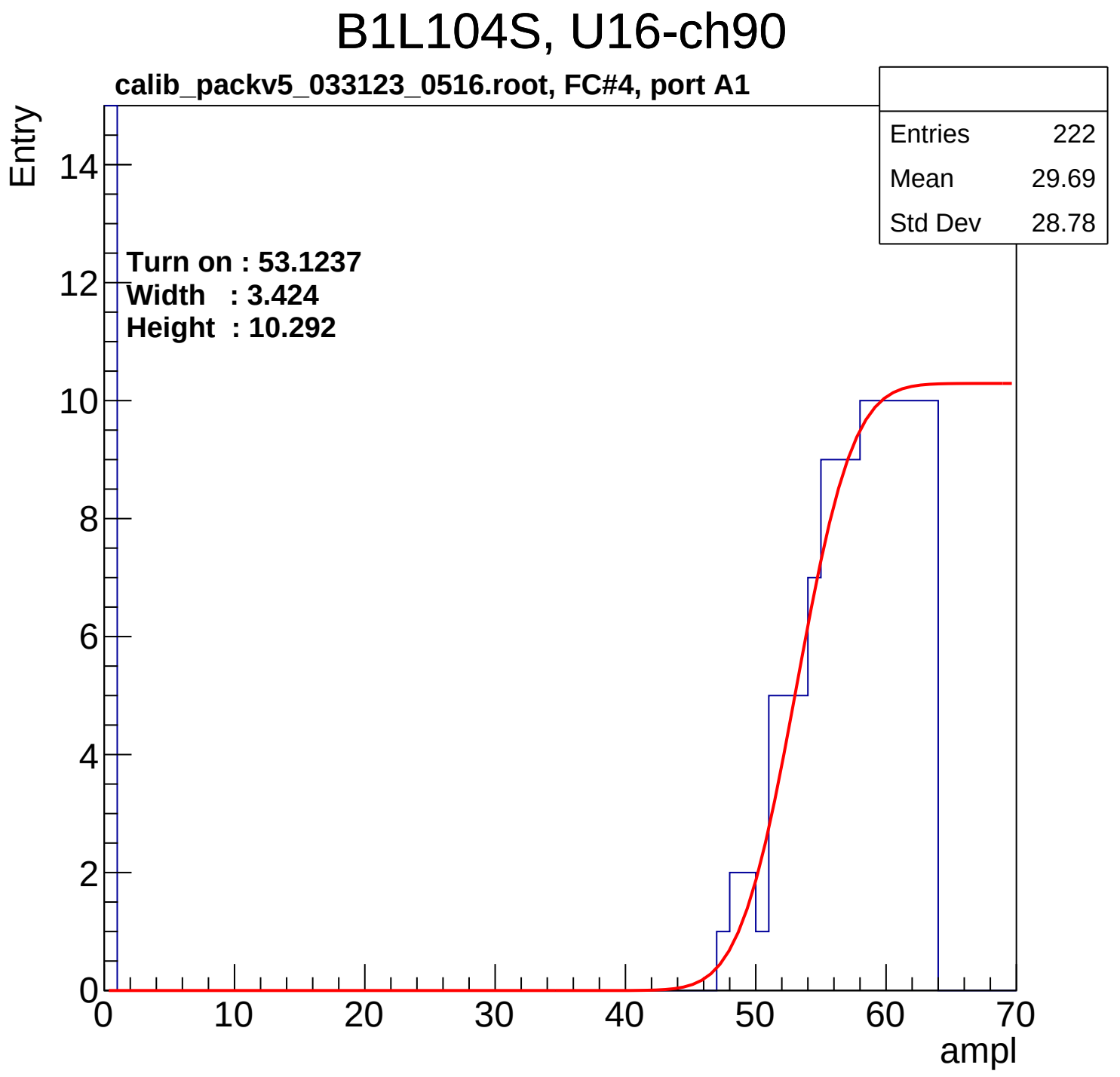
B1L104S, U16-ch90
calib_packv5_033123_0516.root, FC#4, port A1
Entries 222
Mean 29.69
Std Dev 28.78
Turn on : 53.1237
Width : 3.424
Height : 10.292
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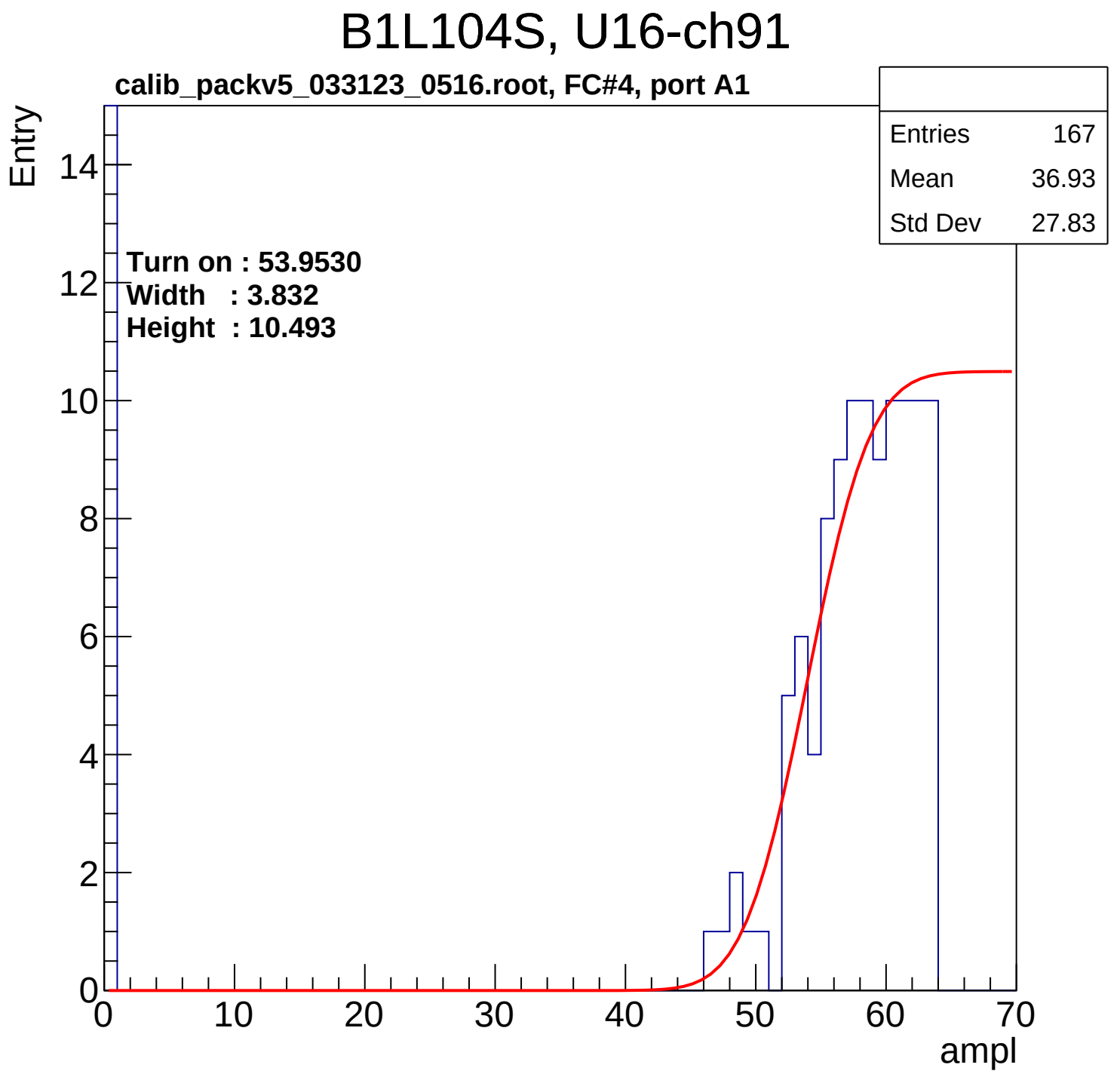
B1L104S, U16-ch91
calib_packv5_033123_0516.root, FC#4, port A1
Entries 167
Mean 36.93
Std Dev 27.83
Turn on : 53.9530
Width : 3.832
Height : 10.493
Entry
ampl
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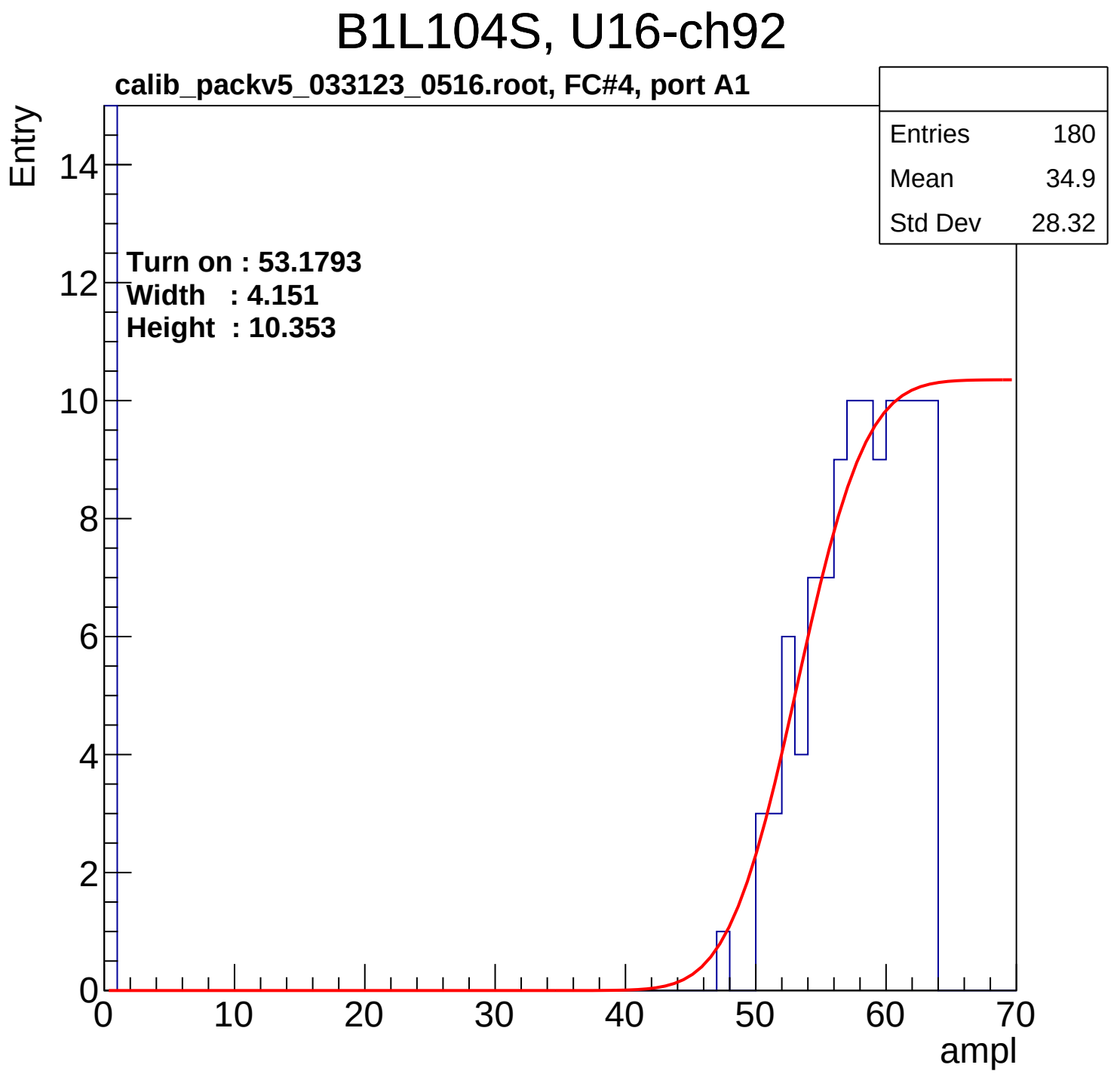
B1L104S, U16-ch92
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 34.9
Std Dev 28.32
Turn on : 53.1793
Width : 4.151
Height : 10.353
Entry
ampl
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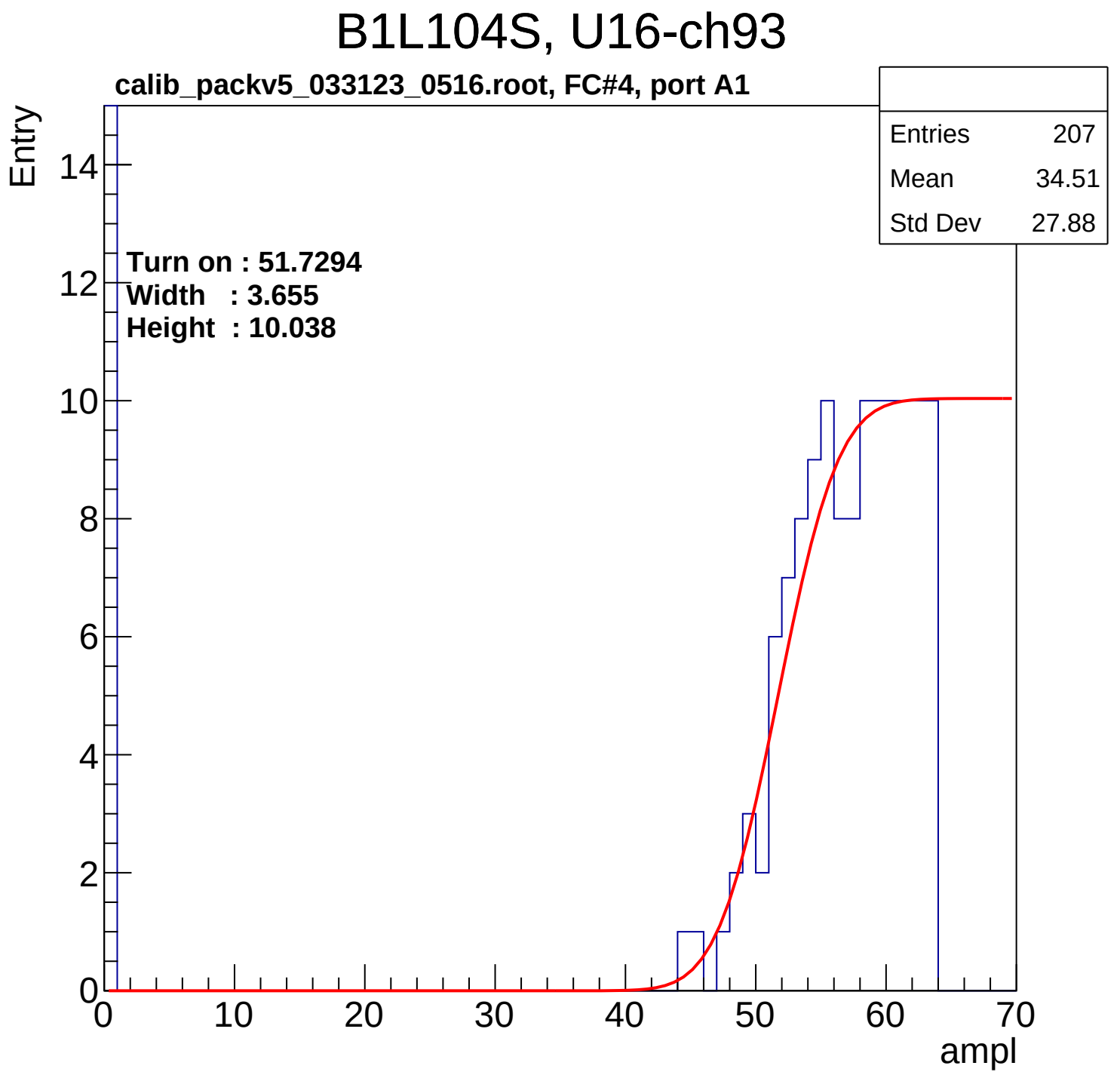
B1L104S, U16-ch93
calib_packv5_033123_0516.root, FC#4, port A1
Entries 207
Mean 34.51
Std Dev 27.88
Turn on : 51.7294
Width : 3.655
Height : 10.038
Entry
ampl
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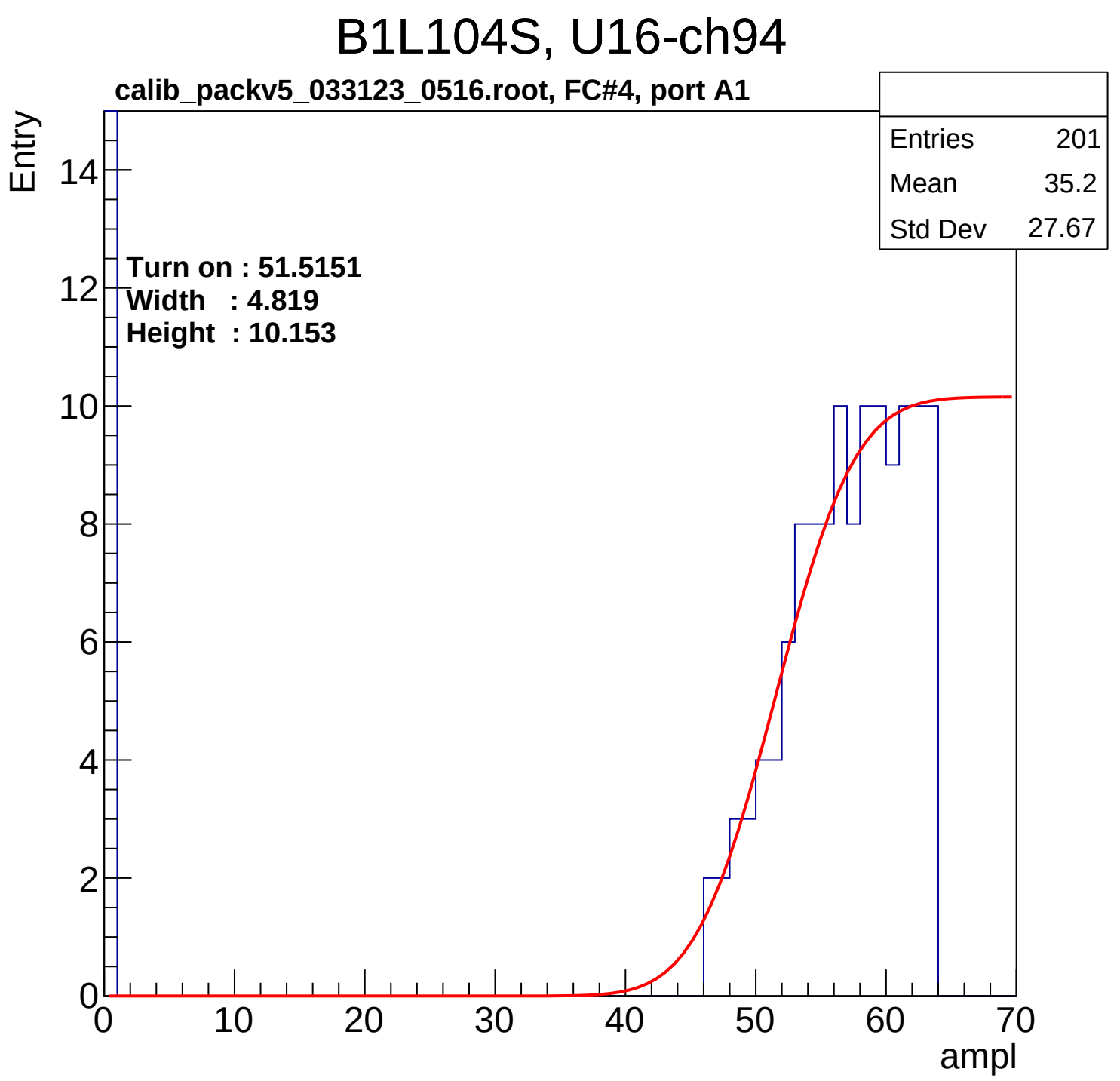
B1L104S, U16-ch94
calib_packv5_033123_0516.root, FC#4, port A1
Entries 201
Mean 35.2
Std Dev 27.67
Turn on : 51.5151
Width : 4.819
Height : 10.153
Entry
ampl
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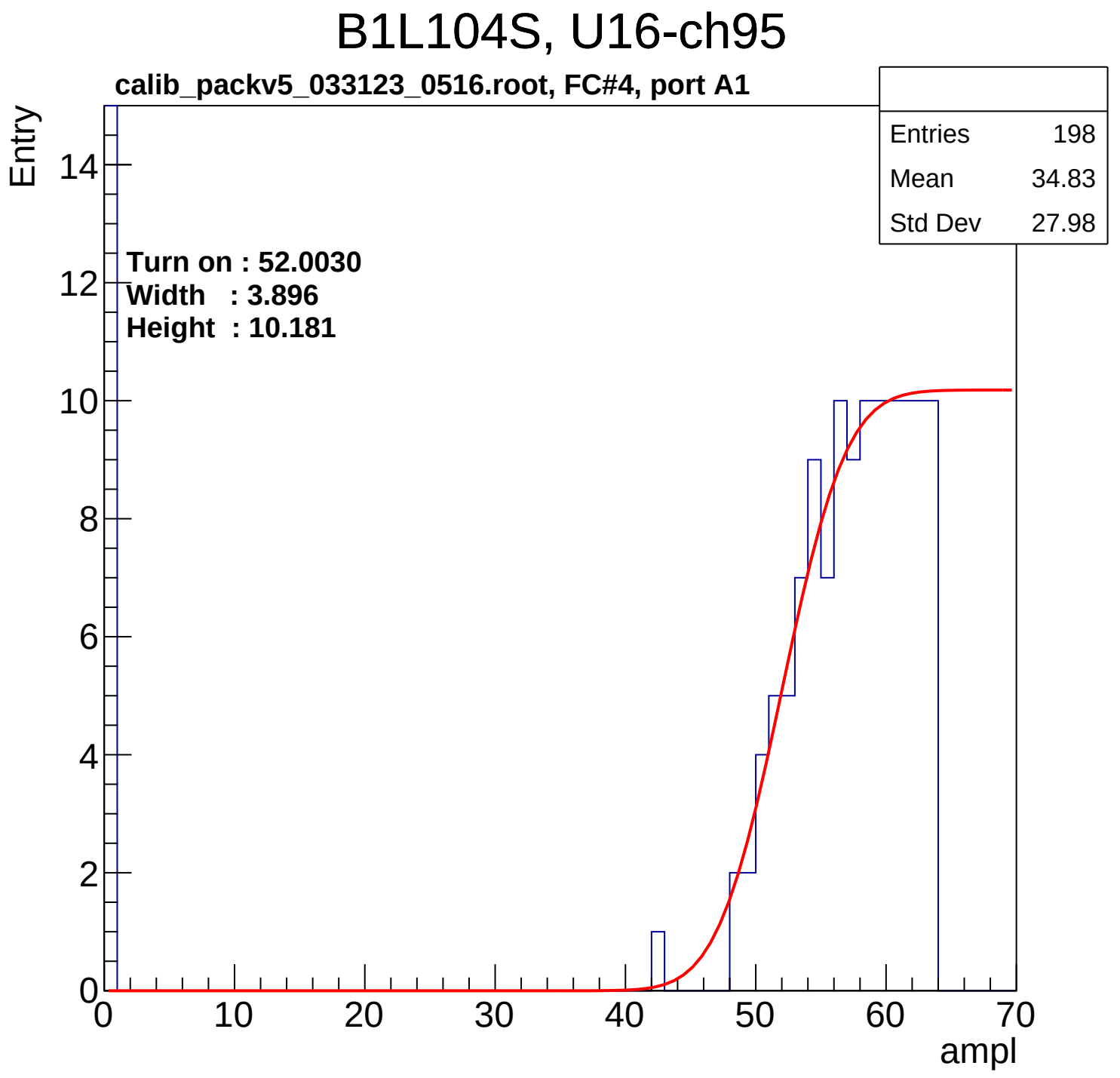
B1L104S, U16-ch95
calib_packv5_033123_0516.root, FC#4, port A1
Entries 198
Mean 34.83
Std Dev 27.98
Turn on : 52.0030
Width : 3.896
Height : 10.181
Entry
ampl
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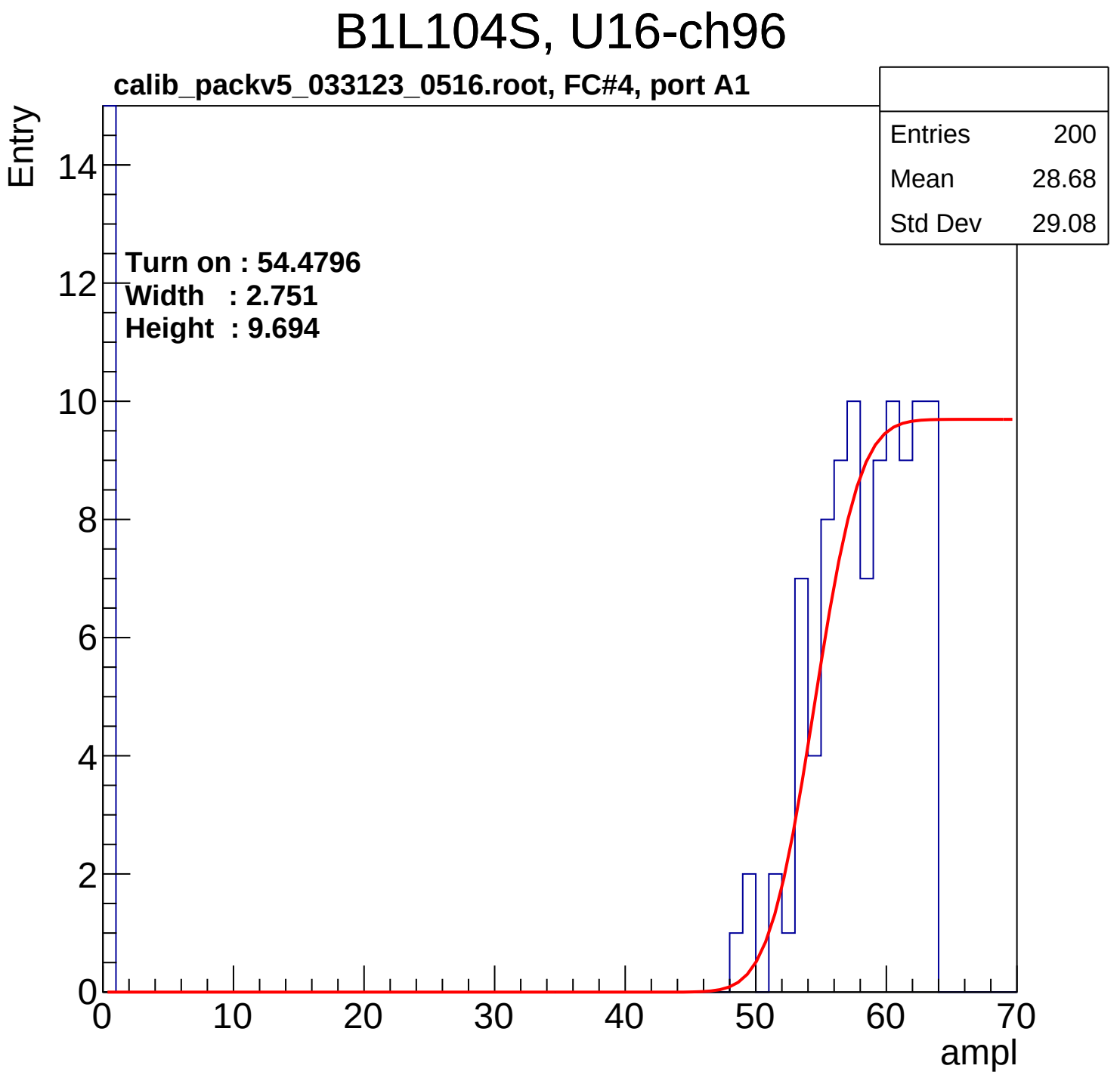
B1L104S, U16-ch96
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 28.68
Std Dev 29.08
Turn on : 54.4796
Width : 2.751
Height : 9.694
Entry
ampl
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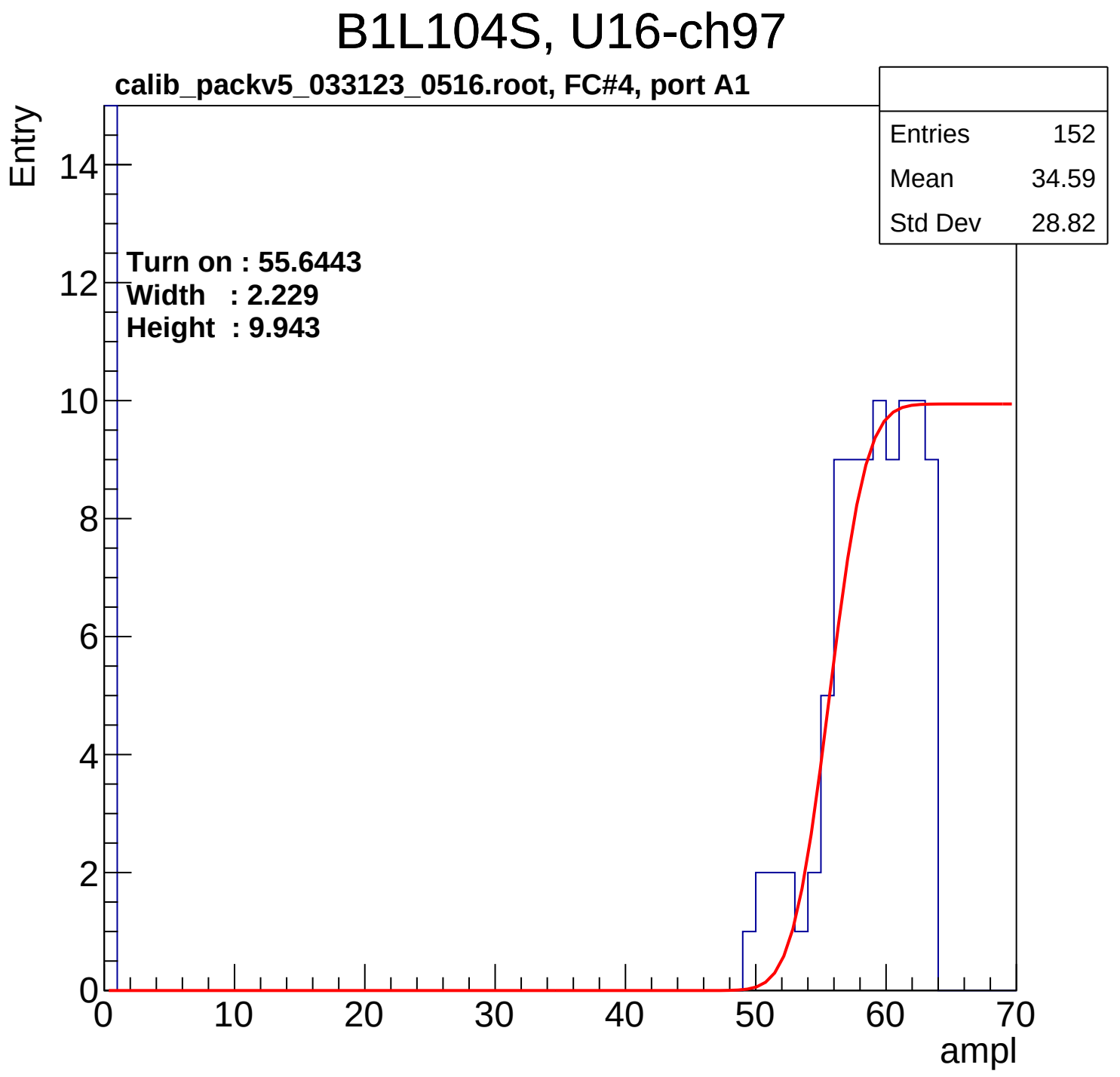
B1L104S, U16-ch97
calib_packv5_033123_0516.root, FC#4, port A1
Entries 152
Mean 34.59
Std Dev 28.82
Turn on : 55.6443
Width : 2.229
Height : 9.943
Entry
ampl
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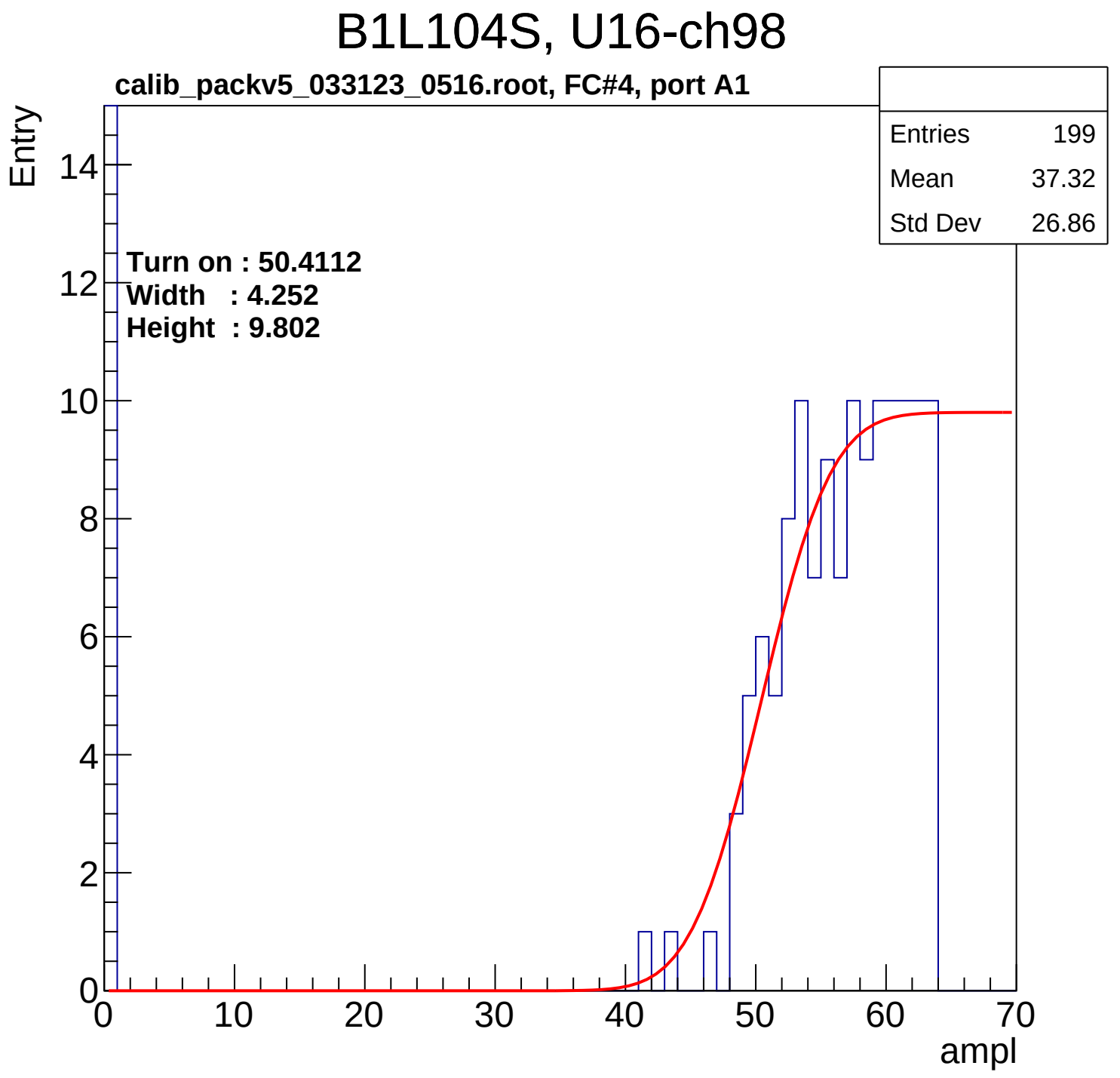
B1L104S, U16-ch98
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 37.32
Std Dev 26.86
Turn on : 50.4112
Width : 4.252
Height : 9.802
Entry
ampl
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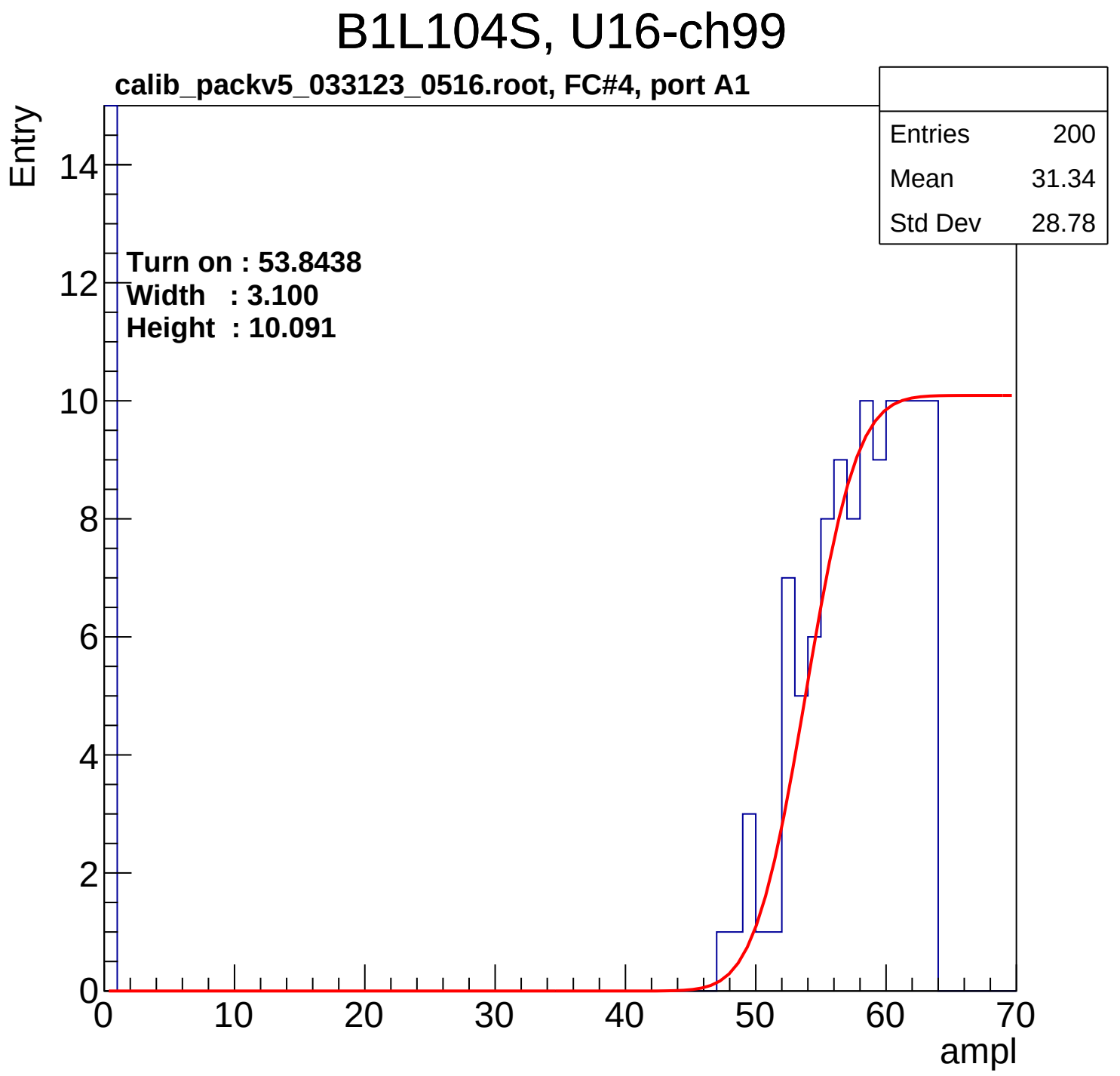
B1L104S, U16-ch99
calib_packv5_033123_0516.root, FC#4, port A1
Entries 200
Mean 31.34
Std Dev 28.78
Turn on : 53.8438
Width : 3.100
Height : 10.091
Entry
ampl
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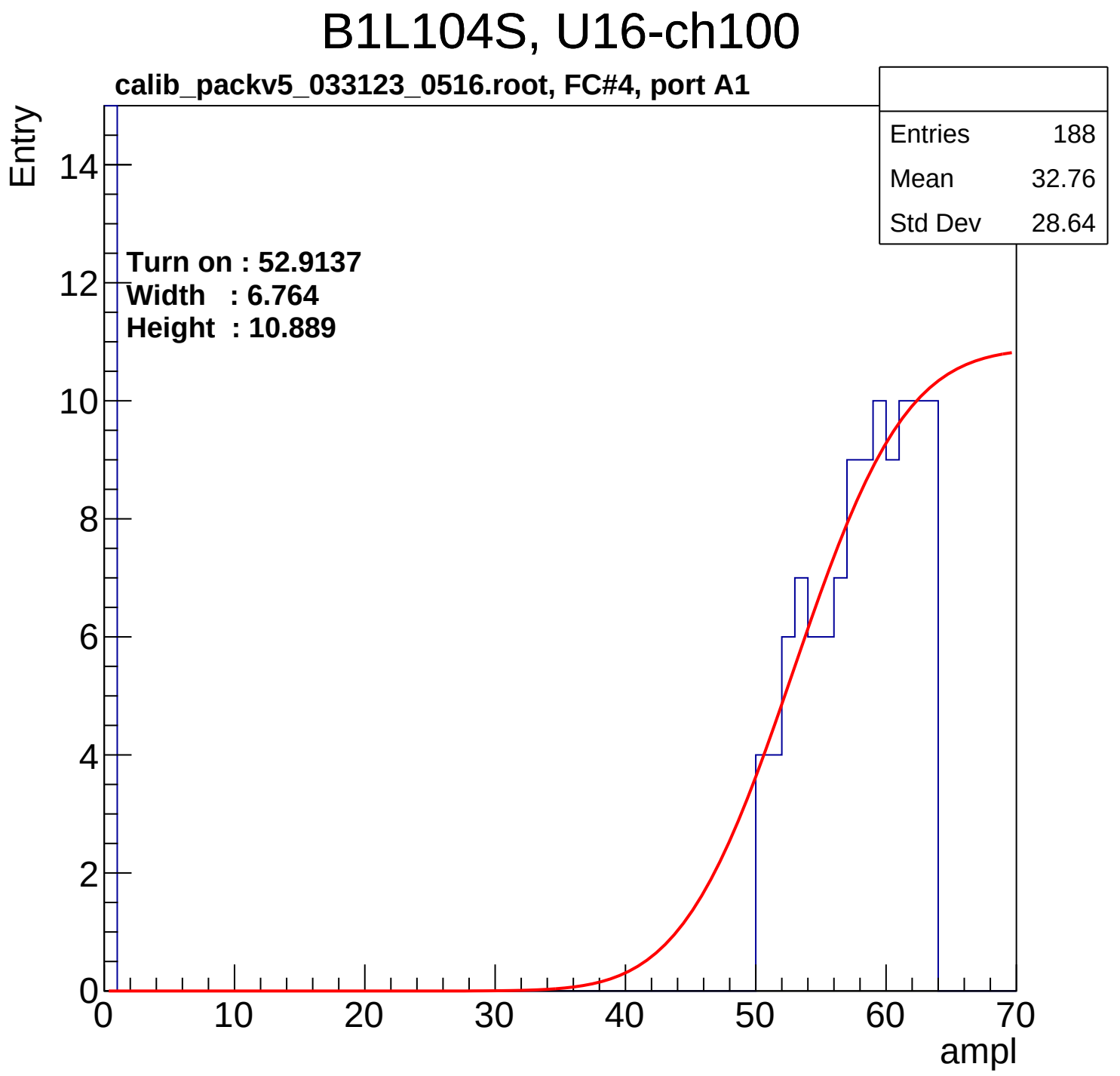
B1L104S, U16-ch100
calib_packv5_033123_0516.root, FC#4, port A1
Entries 188
Mean 32.76
Std Dev 28.64
Turn on : 52.9137
Width : 6.764
Height : 10.889
Entry
ampl
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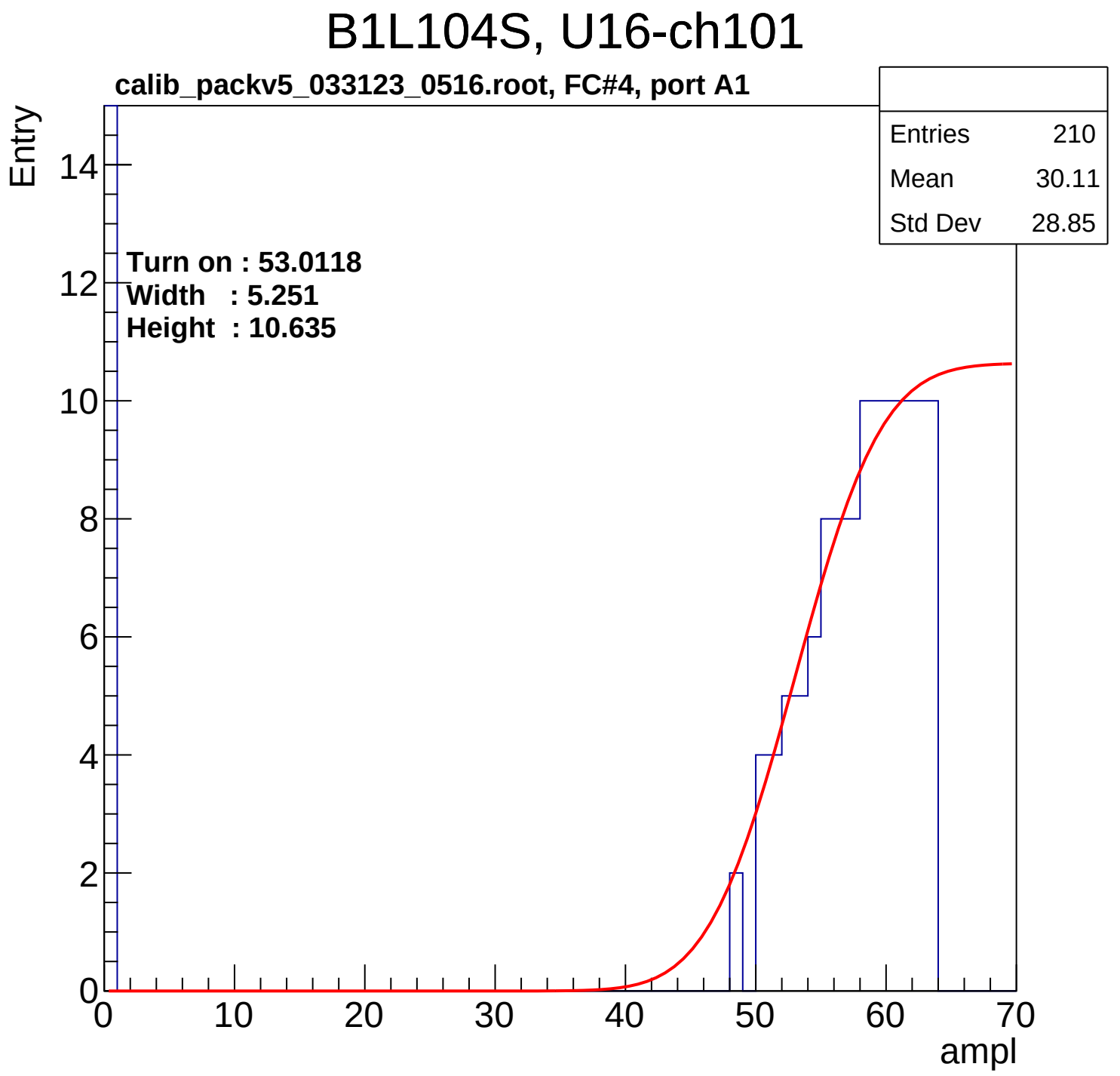
B1L104S, U16-ch101
calib_packv5_033123_0516.root, FC#4, port A1
Entries 210
Mean 30.11
Std Dev 28.85
Turn on : 53.0118
Width : 5.251
Height : 10.635
Entry
ampl
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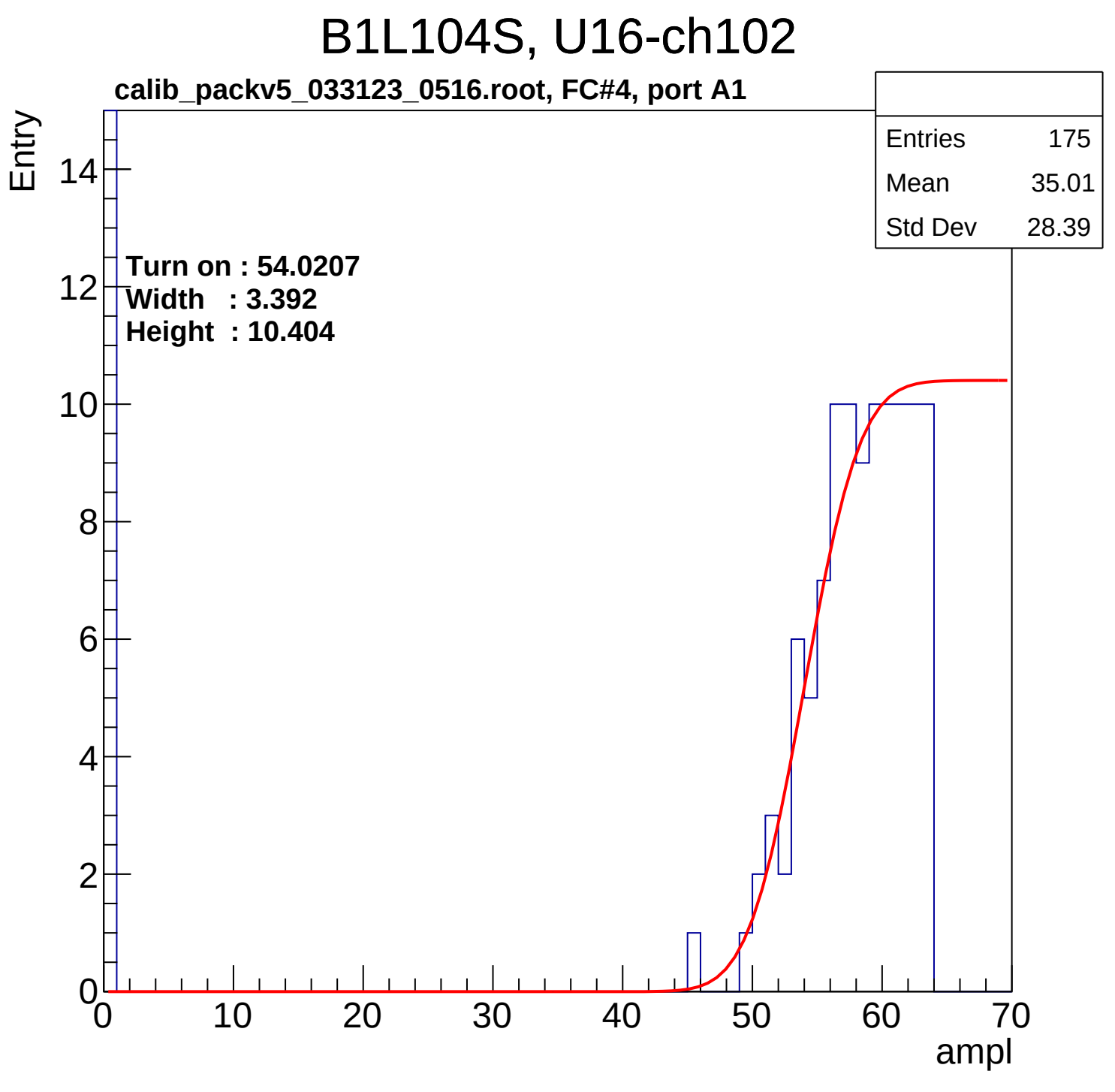
B1L104S, U16-ch102
calib_packv5_033123_0516.root, FC#4, port A1
Entries 175
Mean 35.01
Std Dev 28.39
Turn on : 54.0207
Width : 3.392
Height : 10.404
Entry
ampl
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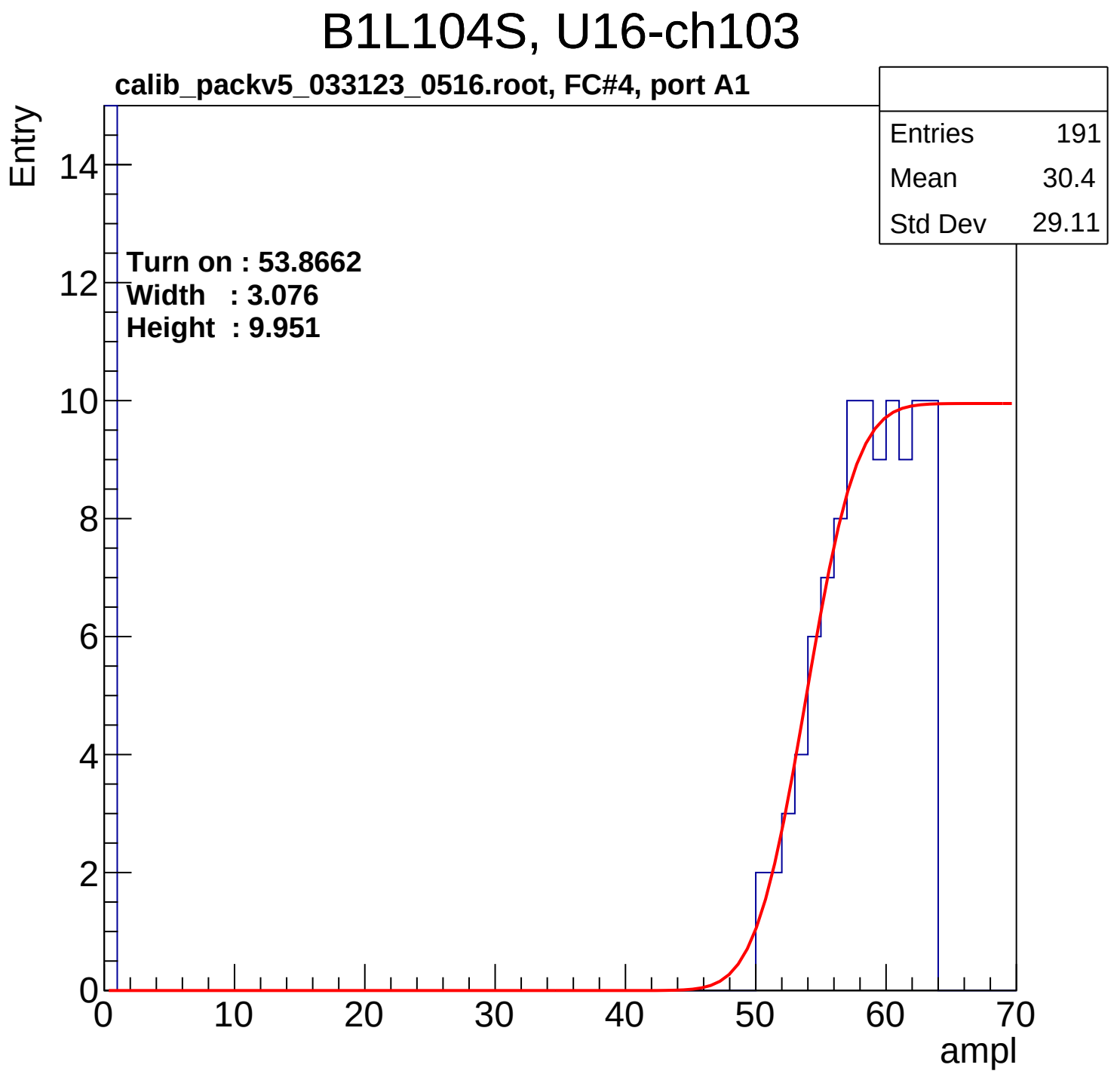
B1L104S, U16-ch103
calib_packv5_033123_0516.root, FC#4, port A1
Entries 191
Mean 30.4
Std Dev 29.11
Turn on : 53.8662
Width : 3.076
Height : 9.951
Entry
ampl
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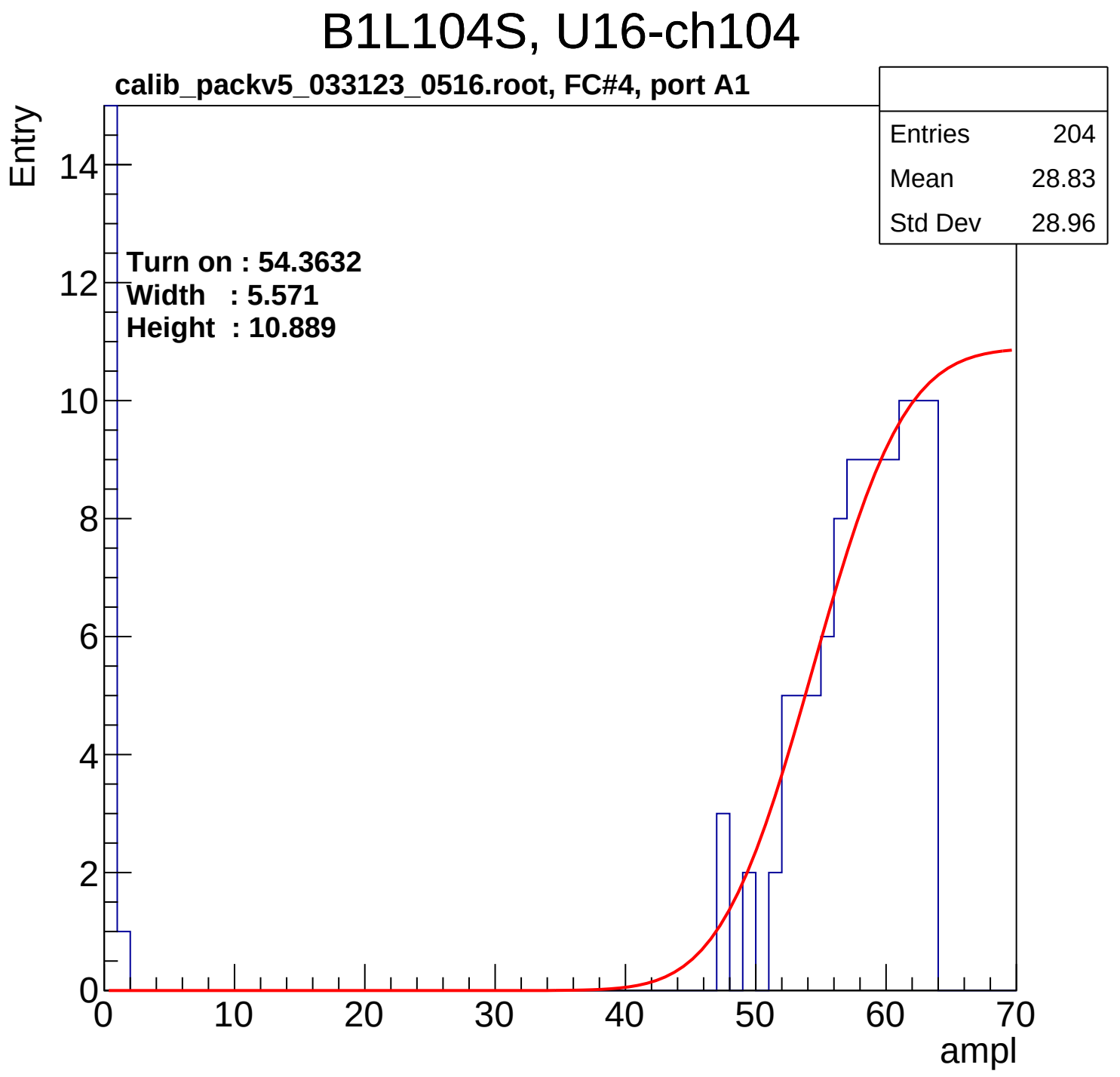
B1L104S, U16-ch104
calib_packv5_033123_0516.root, FC#4, port A1
Entries 204
Mean 28.83
Std Dev 28.96
Turn on : 54.3632
Width : 5.571
Height : 10.889
Entry
ampl
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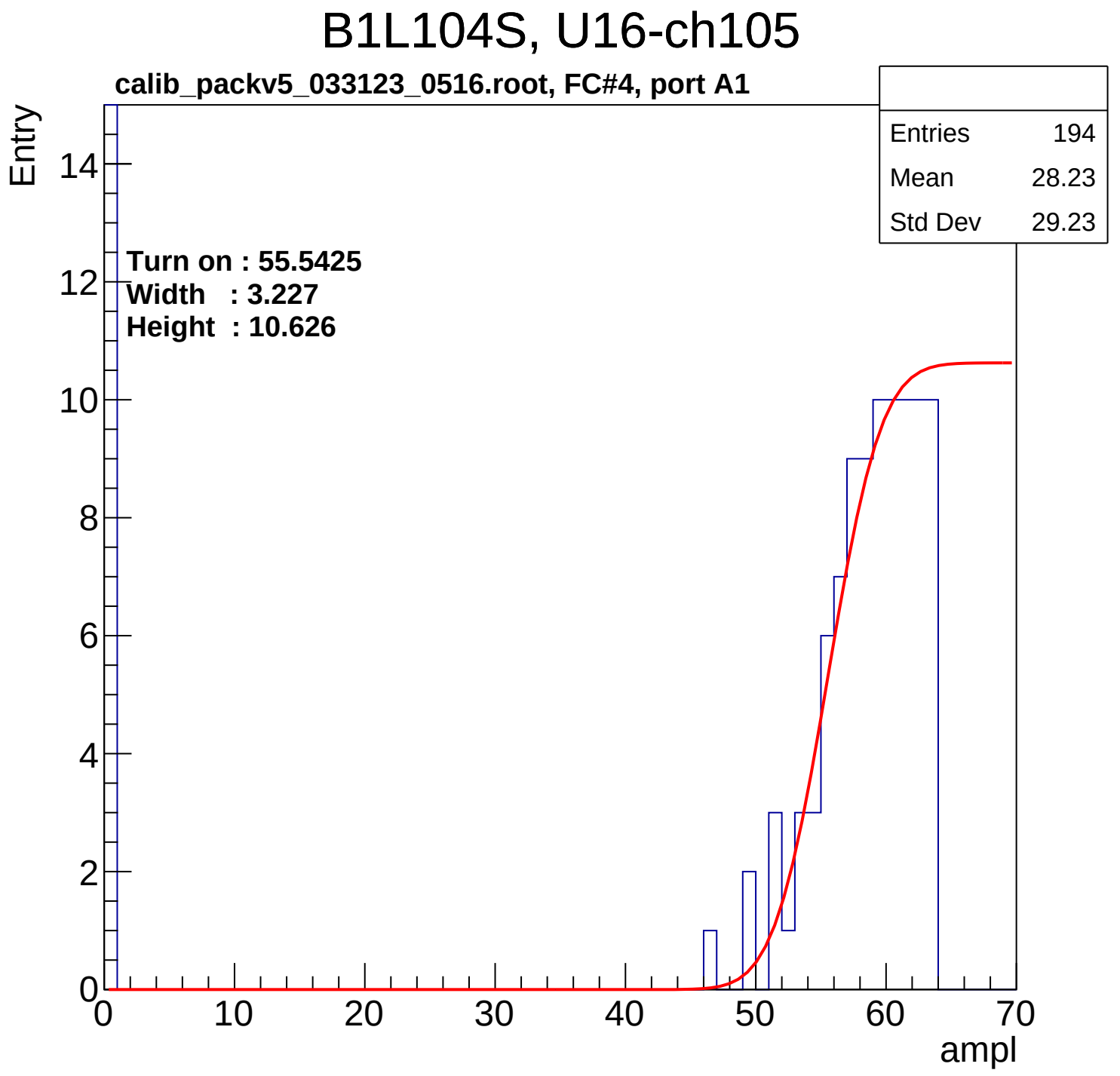
B1L104S, U16-ch105
calib_packv5_033123_0516.root, FC#4, port A1
Entries 194
Mean 28.23
Std Dev 29.23
Turn on : 55.5425
Width : 3.227
Height : 10.626
Entry
ampl
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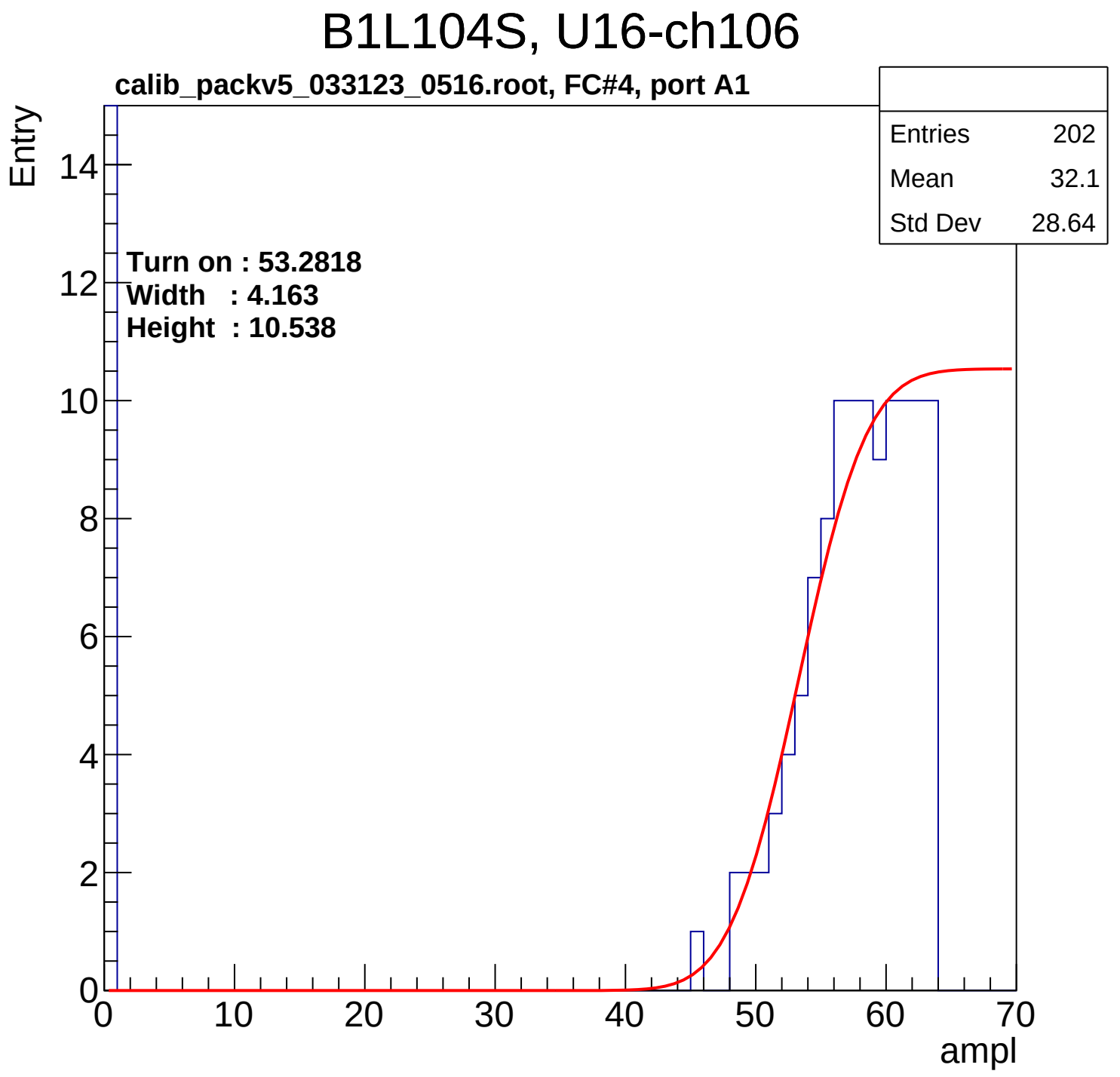
B1L104S, U16-ch106
calib_packv5_033123_0516.root, FC#4, port A1
Entries 202
Mean 32.1
Std Dev 28.64
Turn on : 53.2818
Width : 4.163
Height : 10.538
Entry
ampl
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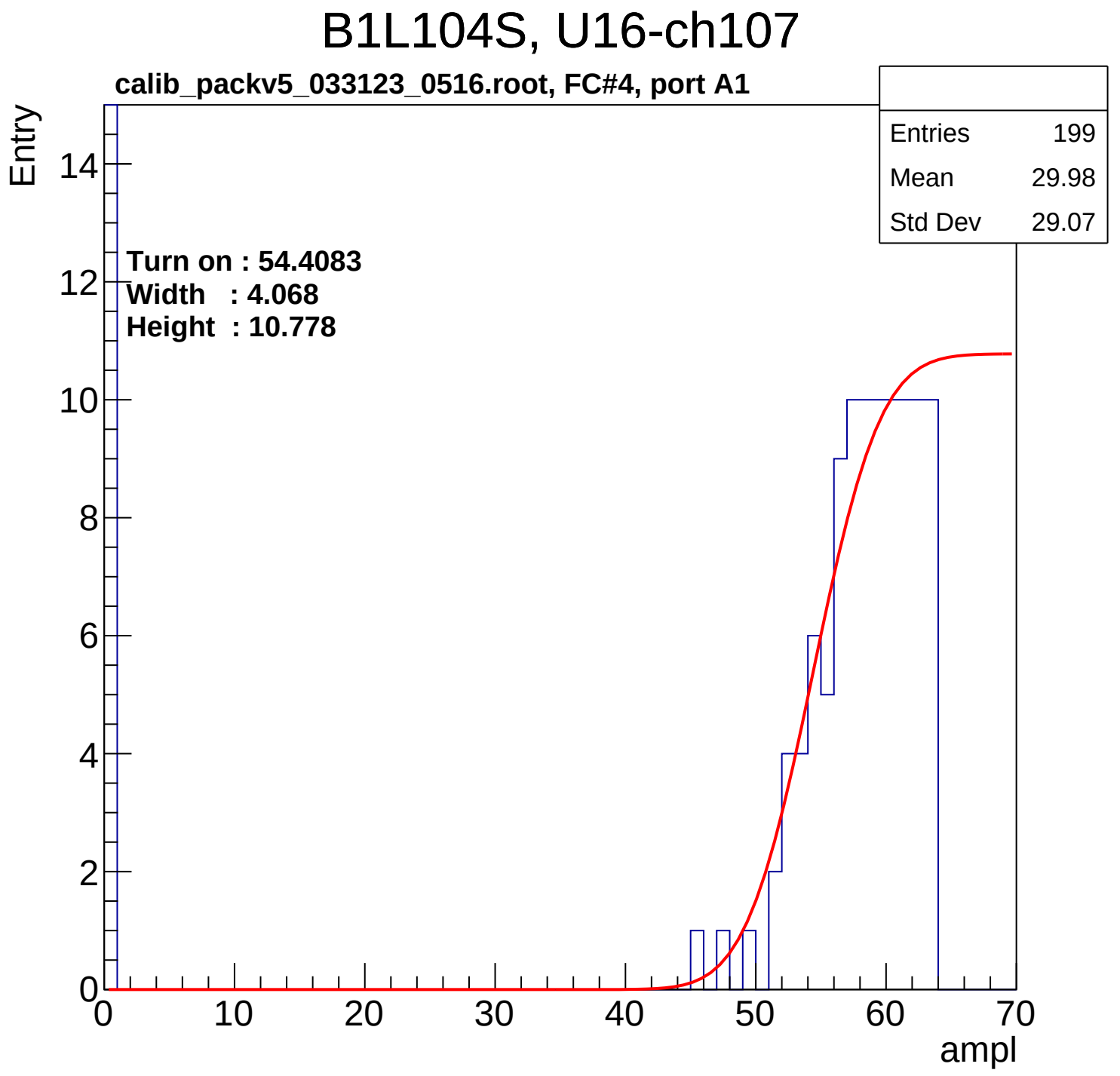
B1L104S, U16-ch107
calib_packv5_033123_0516.root, FC#4, port A1
Entries 199
Mean 29.98
Std Dev 29.07
Turn on : 54.4083
Width : 4.068
Height : 10.778
Entry
ampl
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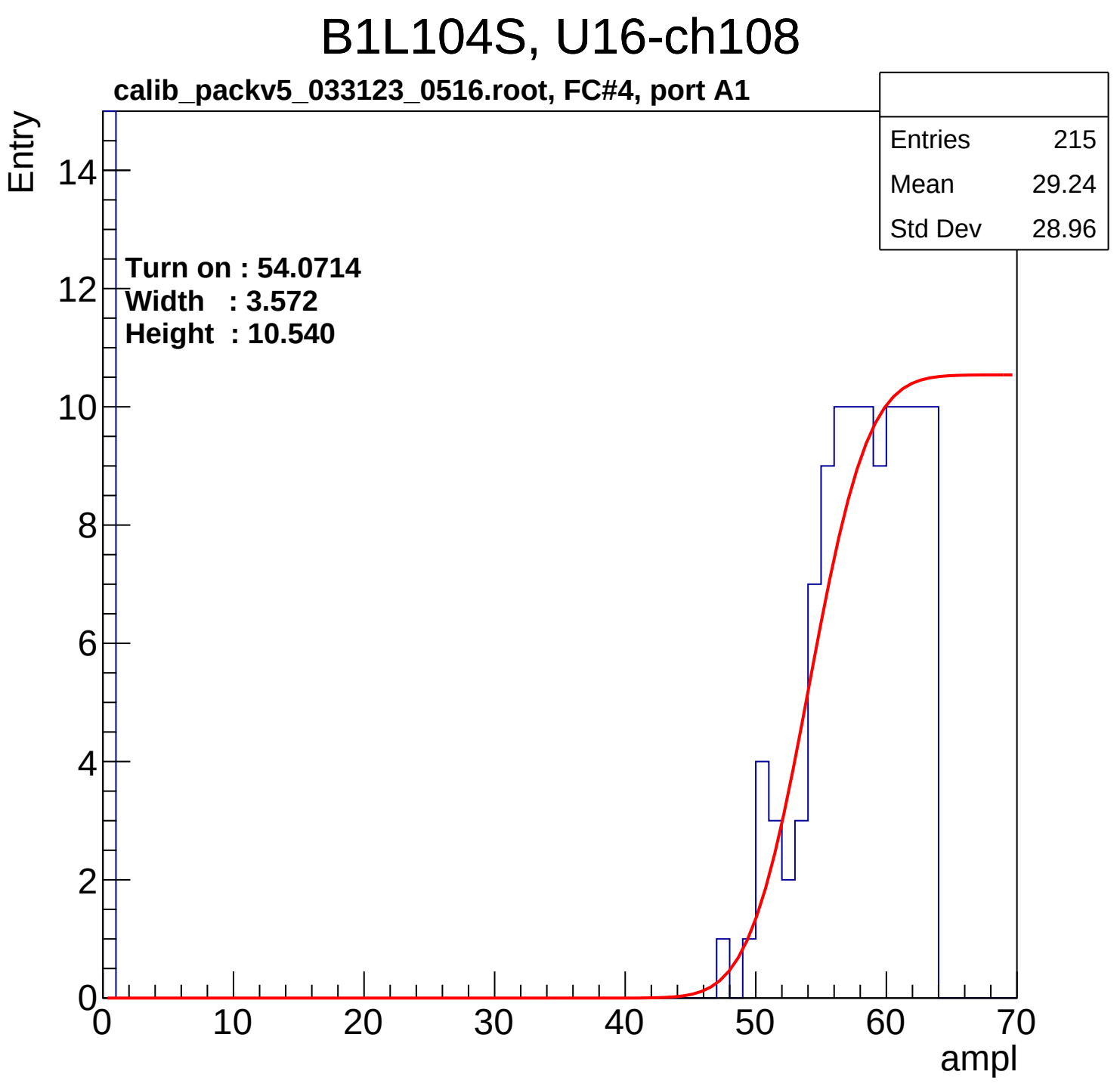
B1L104S, U16-ch108
calib_packv5_033123_0516.root, FC#4, port A1
Entries 215
Mean 29.24
Std Dev 28.96
Turn on : 54.0714
Width : 3.572
Height : 10.540
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ampl
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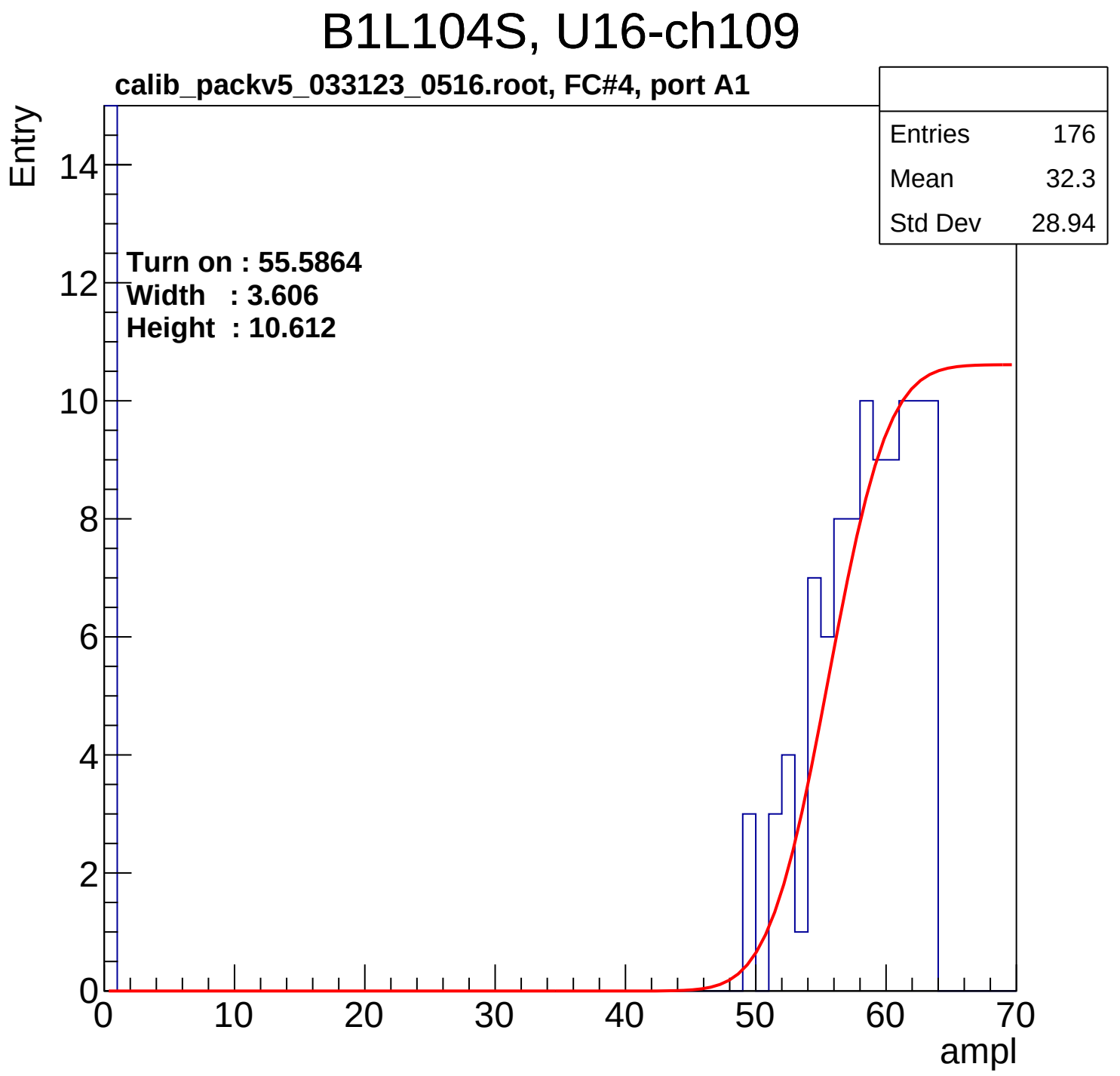
B1L104S, U16-ch109
calib_packv5_033123_0516.root, FC#4, port A1
Entries 176
Mean 32.3
Std Dev 28.94
Turn on : 55.5864
Width : 3.606
Height : 10.612
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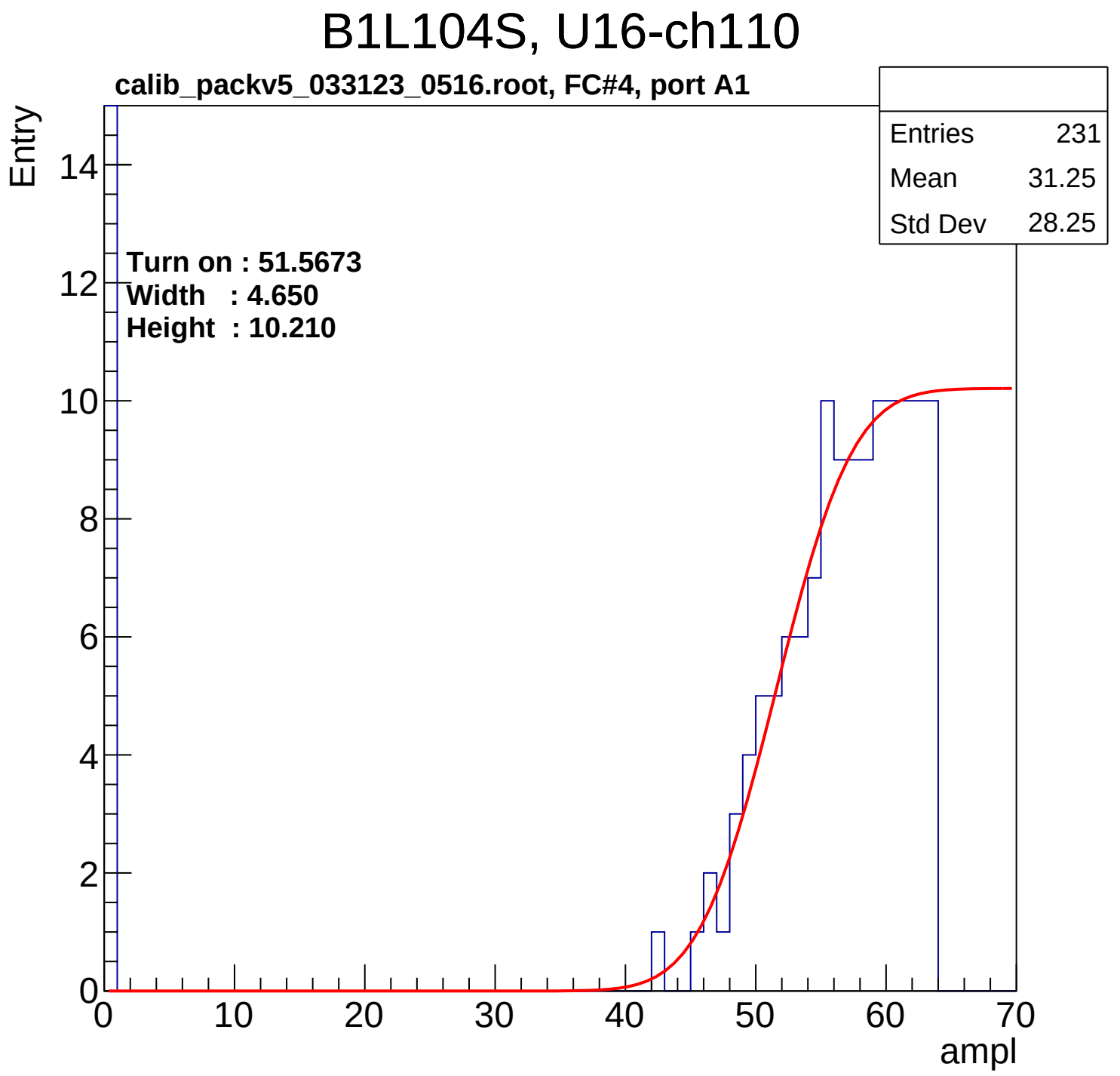
B1L104S, U16-ch110
calib_packv5_033123_0516.root, FC#4, port A1
Entries 231
Mean 31.25
Std Dev 28.25
Turn on : 51.5673
Width : 4.650
Height : 10.210
Entry
ampl
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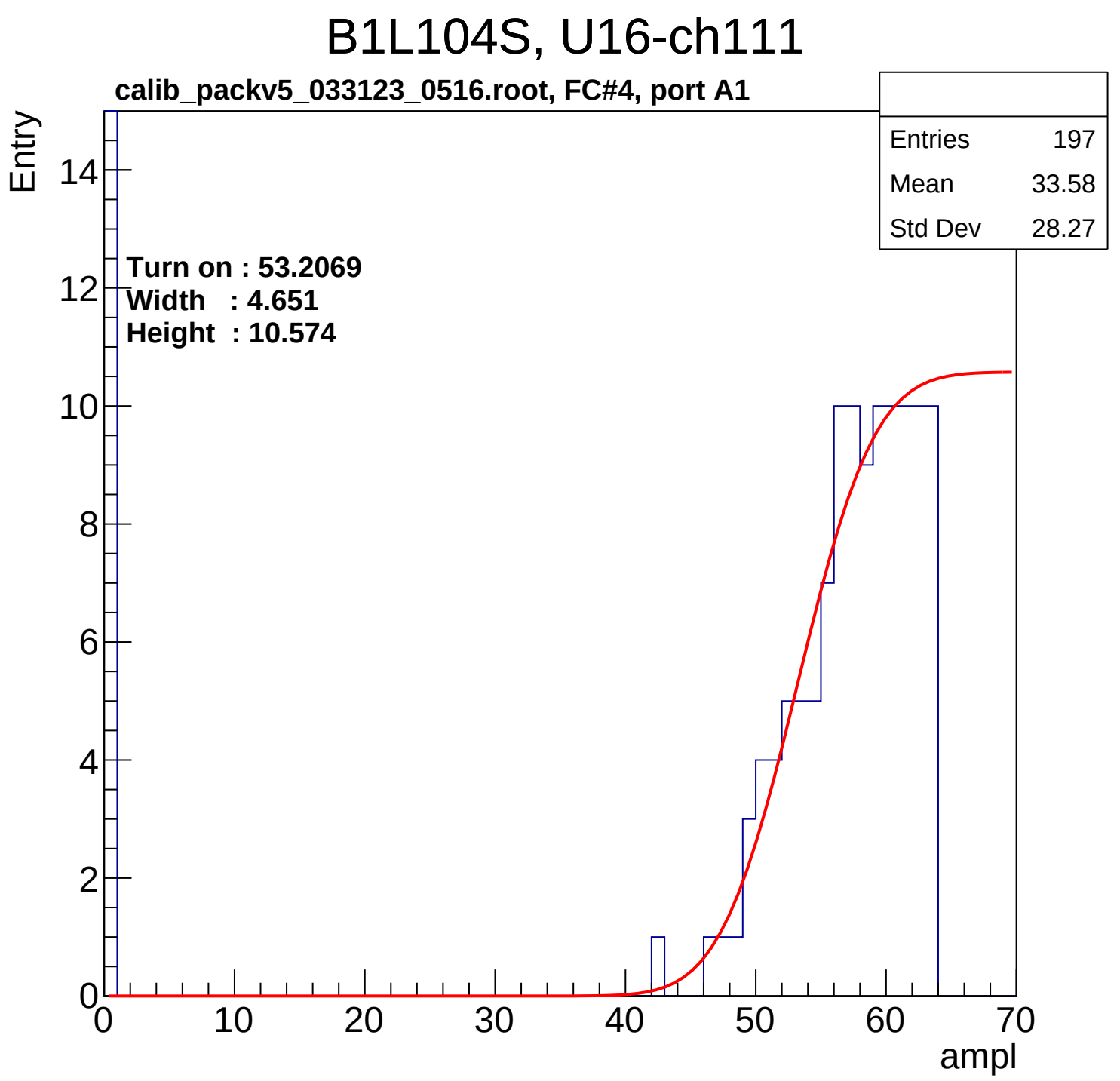
B1L104S, U16-ch111
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 33.58
Std Dev 28.27
Turn on : 53.2069
Width : 4.651
Height : 10.574
Entry
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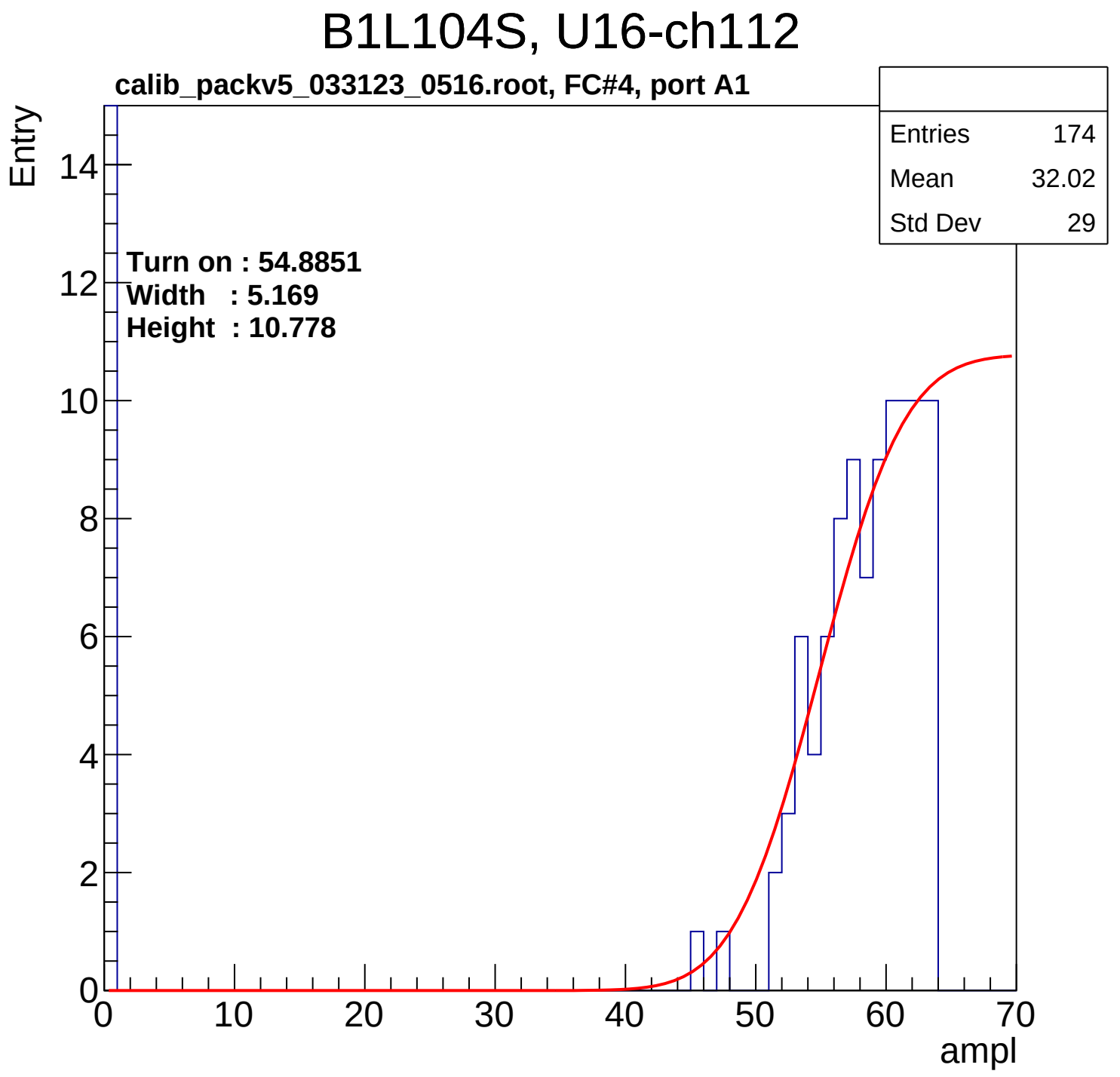
B1L104S, U16-ch112
calib_packv5_033123_0516.root, FC#4, port A1
Entries 174
Mean 32.02
Std Dev 29
Turn on : 54.8851
Width : 5.169
Height : 10.778
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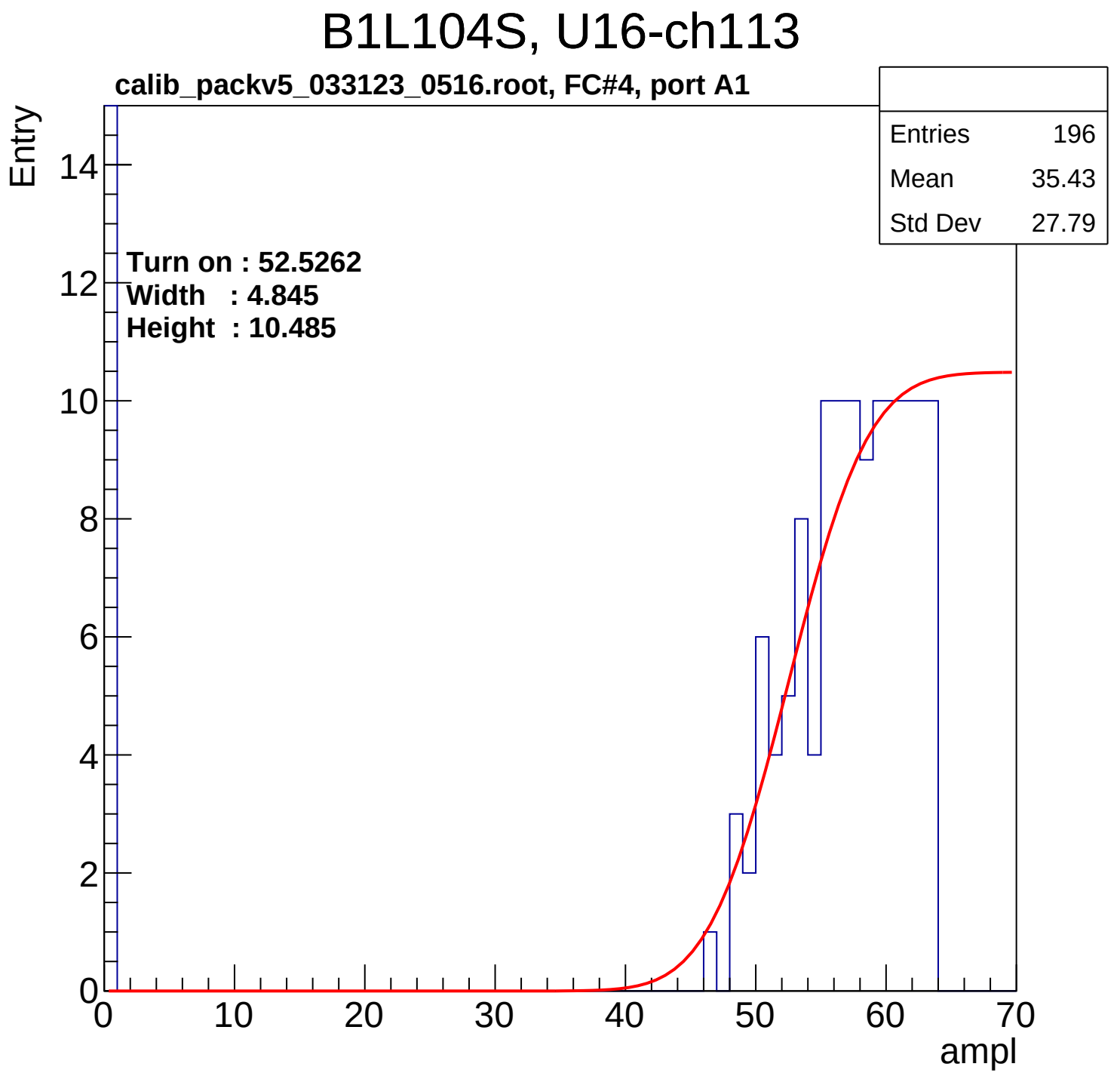
B1L104S, U16-ch113
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 35.43
Std Dev 27.79
Turn on : 52.5262
Width : 4.845
Height : 10.485
Entry
ampl
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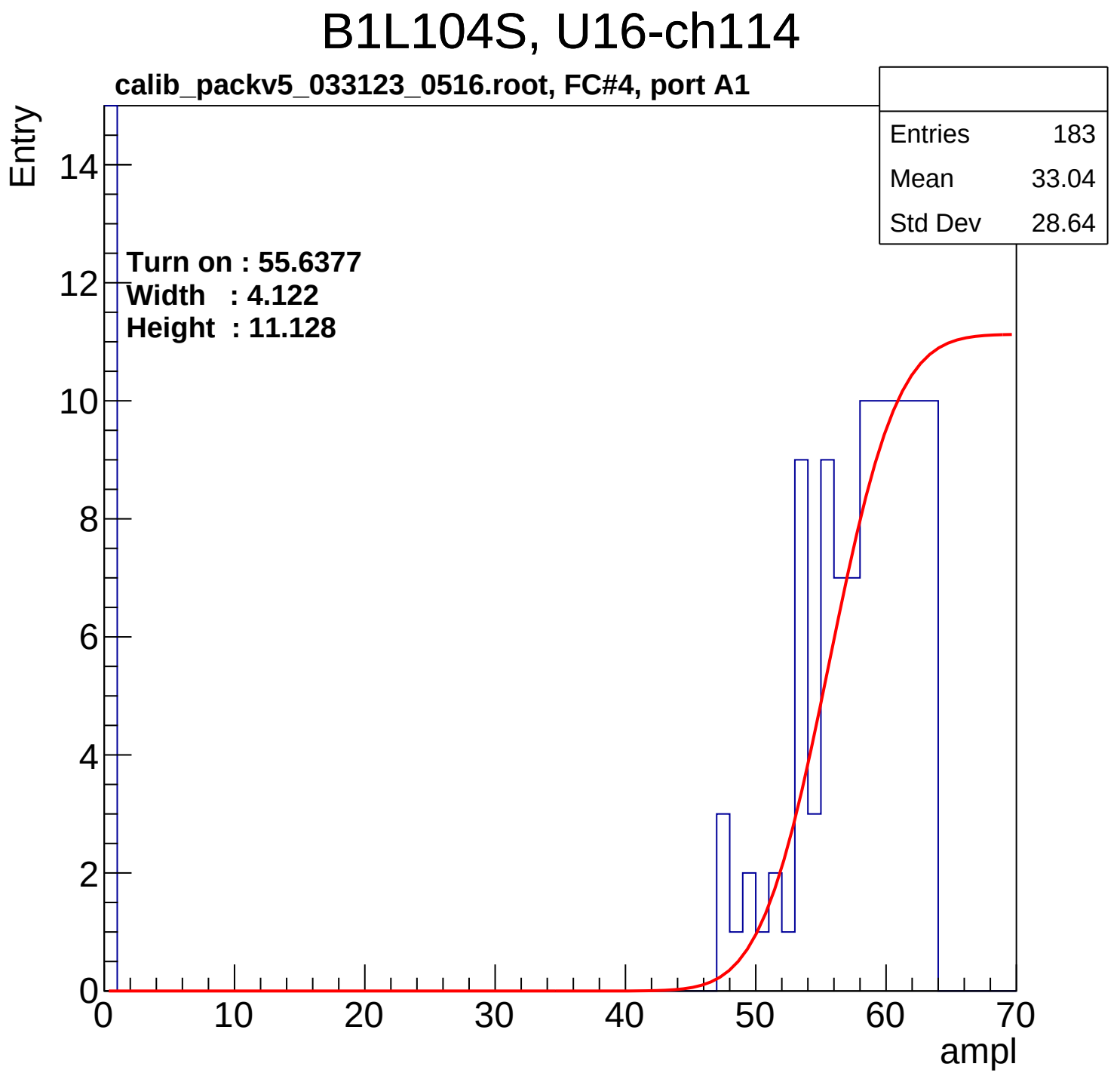
B1L104S, U16-ch114
calib_packv5_033123_0516.root, FC#4, port A1
Entries 183
Mean 33.04
Std Dev 28.64
Turn on : 55.6377
Width : 4.122
Height : 11.128
Entry
ampl
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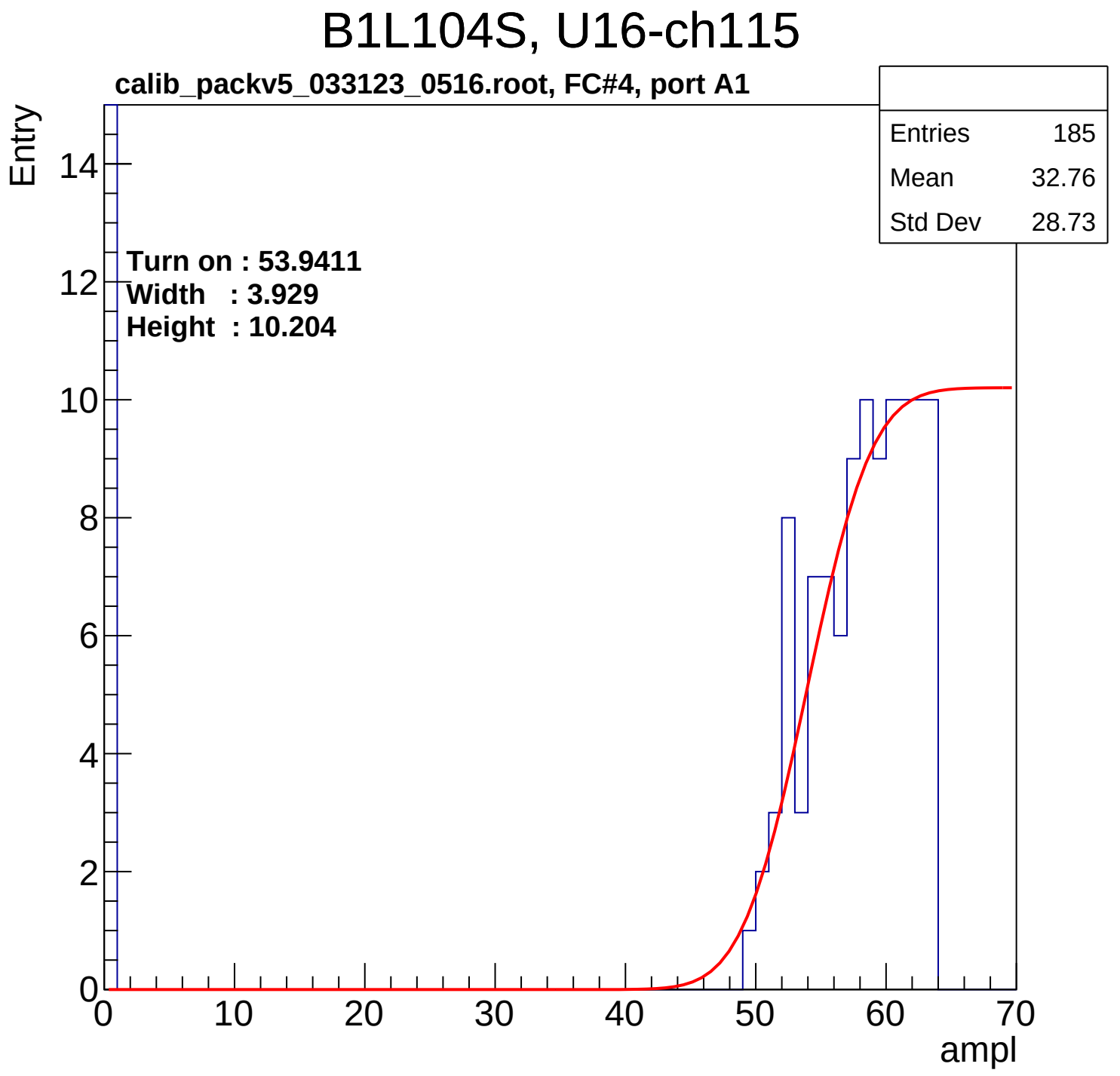
B1L104S, U16-ch115
calib_packv5_033123_0516.root, FC#4, port A1
Entries 185
Mean 32.76
Std Dev 28.73
Turn on : 53.9411
Width : 3.929
Height : 10.204
Entry
ampl
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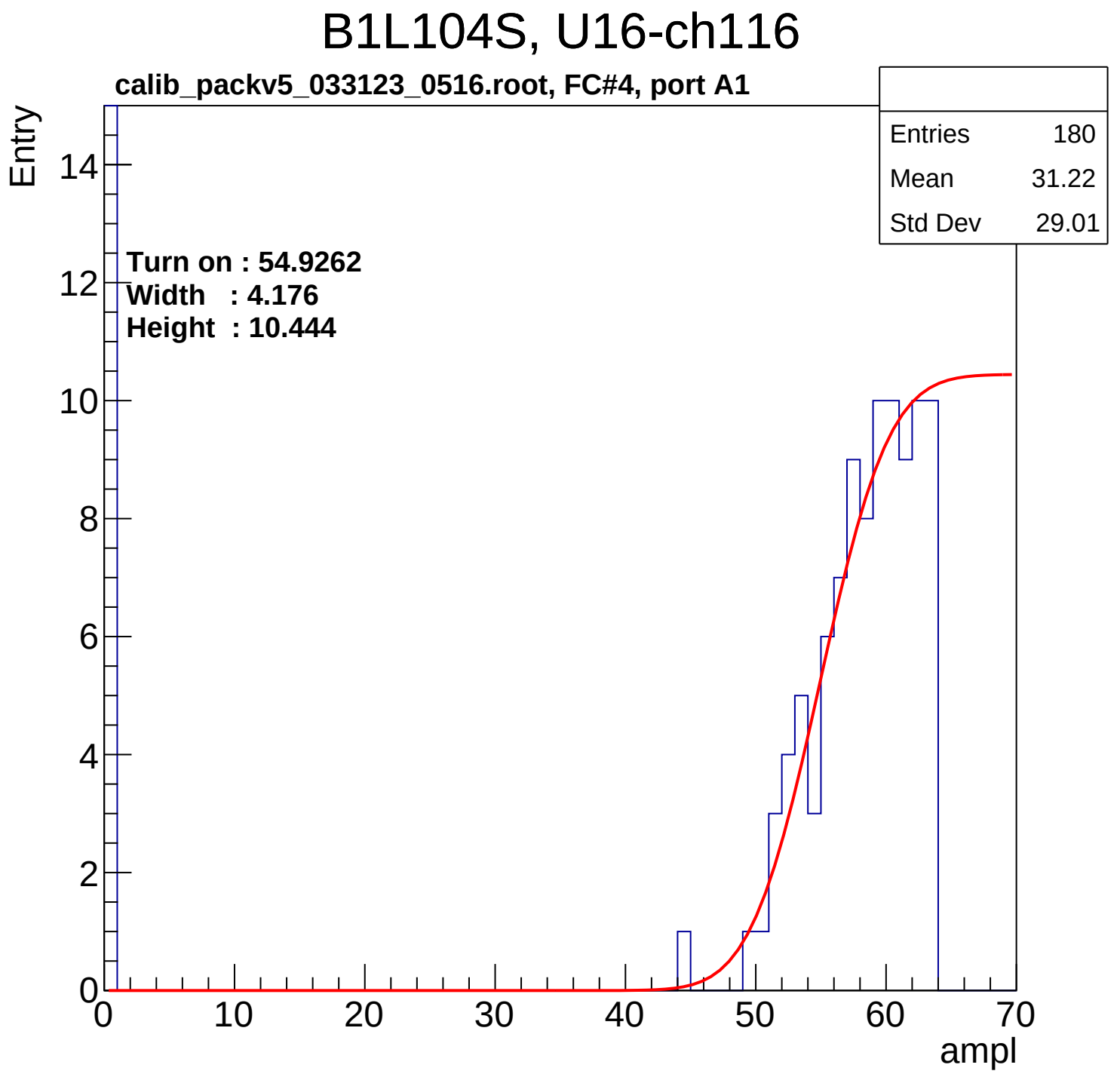
B1L104S, U16-ch116
calib_packv5_033123_0516.root, FC#4, port A1
Entries 180
Mean 31.22
Std Dev 29.01
Turn on : 54.9262
Width : 4.176
Height : 10.444
Entry
ampl
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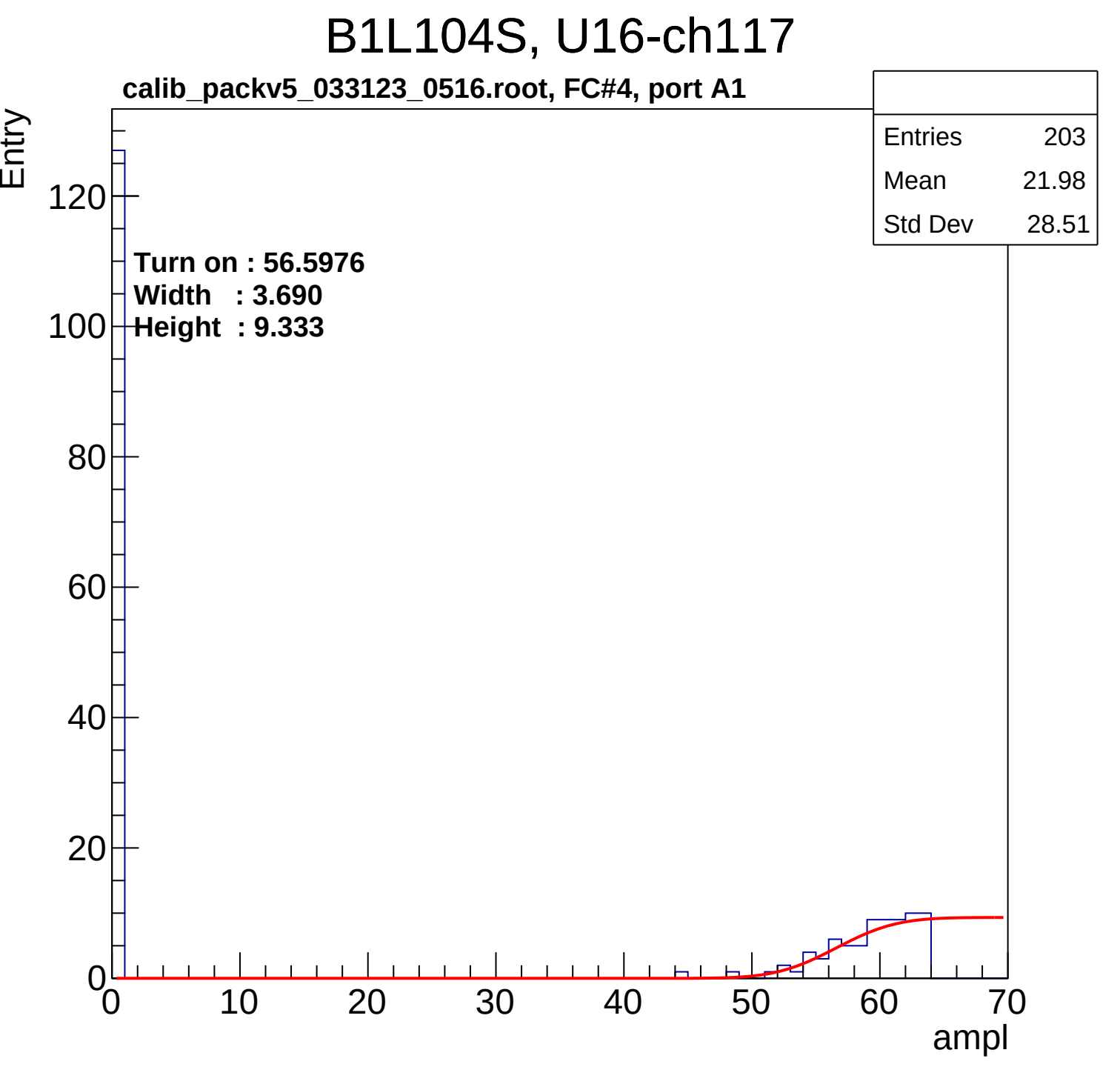
B1L104S, U16-ch117
calib_packv5_033123_0516.root, FC#4, port A1
Entries 203
Mean 21.98
Std Dev 28.51
Turn on : 56.5976
Width : 3.690
Height : 9.333
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ampl
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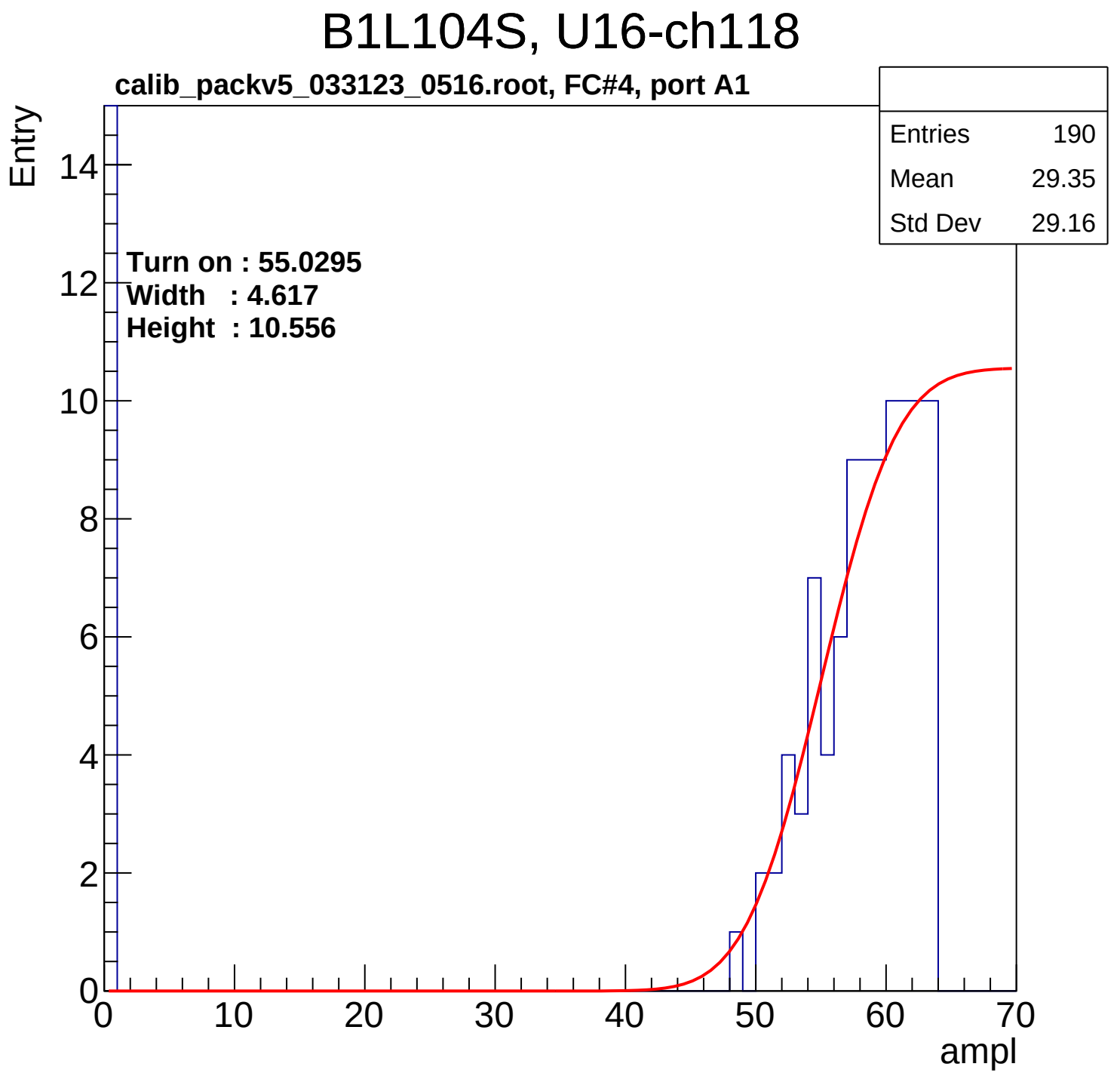
B1L104S, U16-ch118
calib_packv5_033123_0516.root, FC#4, port A1
Entries 190
Mean 29.35
Std Dev 29.16
Turn on : 55.0295
Width : 4.617
Height : 10.556
Entry
ampl
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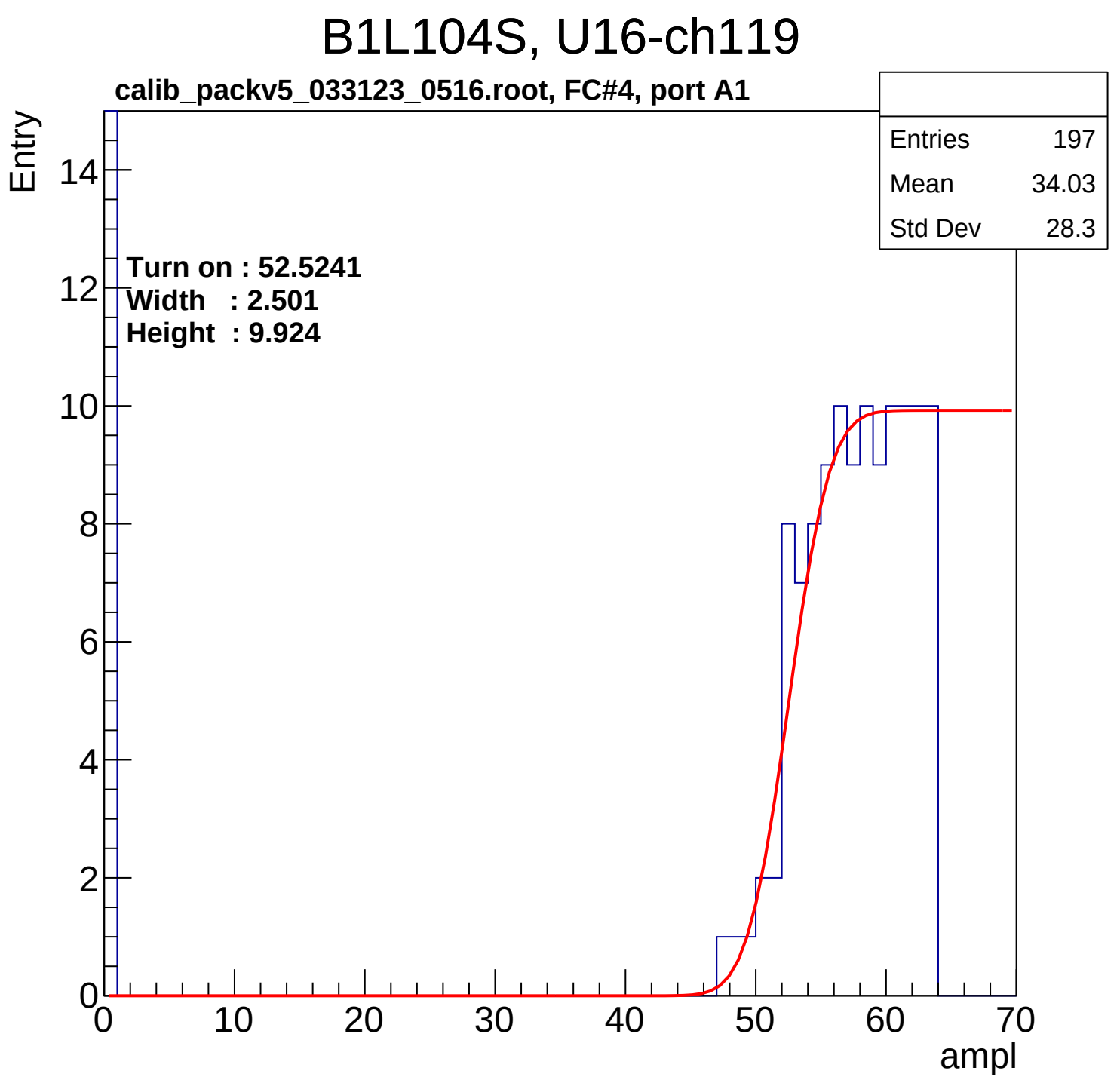
B1L104S, U16-ch119
calib_packv5_033123_0516.root, FC#4, port A1
Entries 197
Mean 34.03
Std Dev 28.3
Turn on : 52.5241
Width : 2.501
Height : 9.924
Entry
ampl
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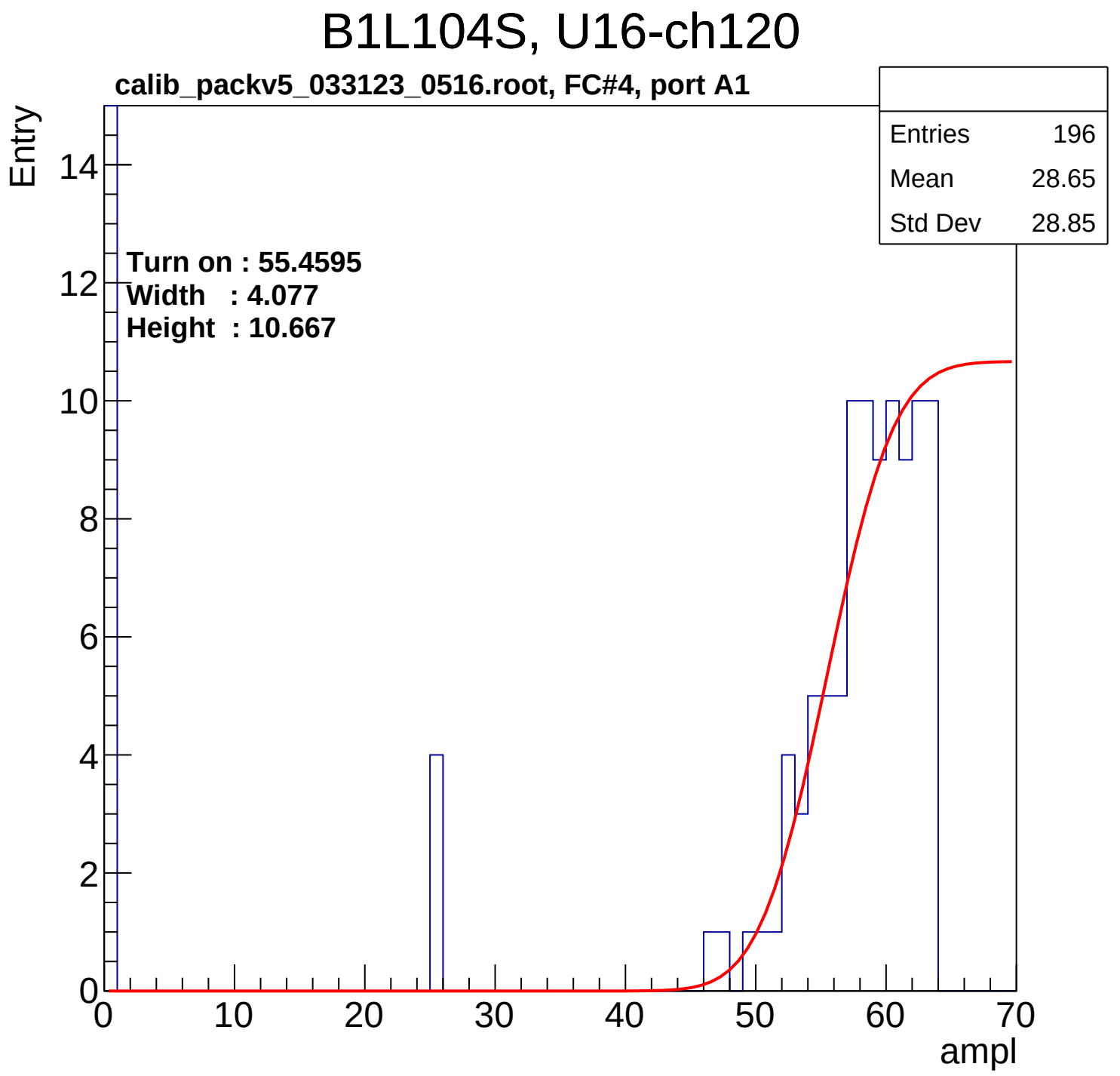
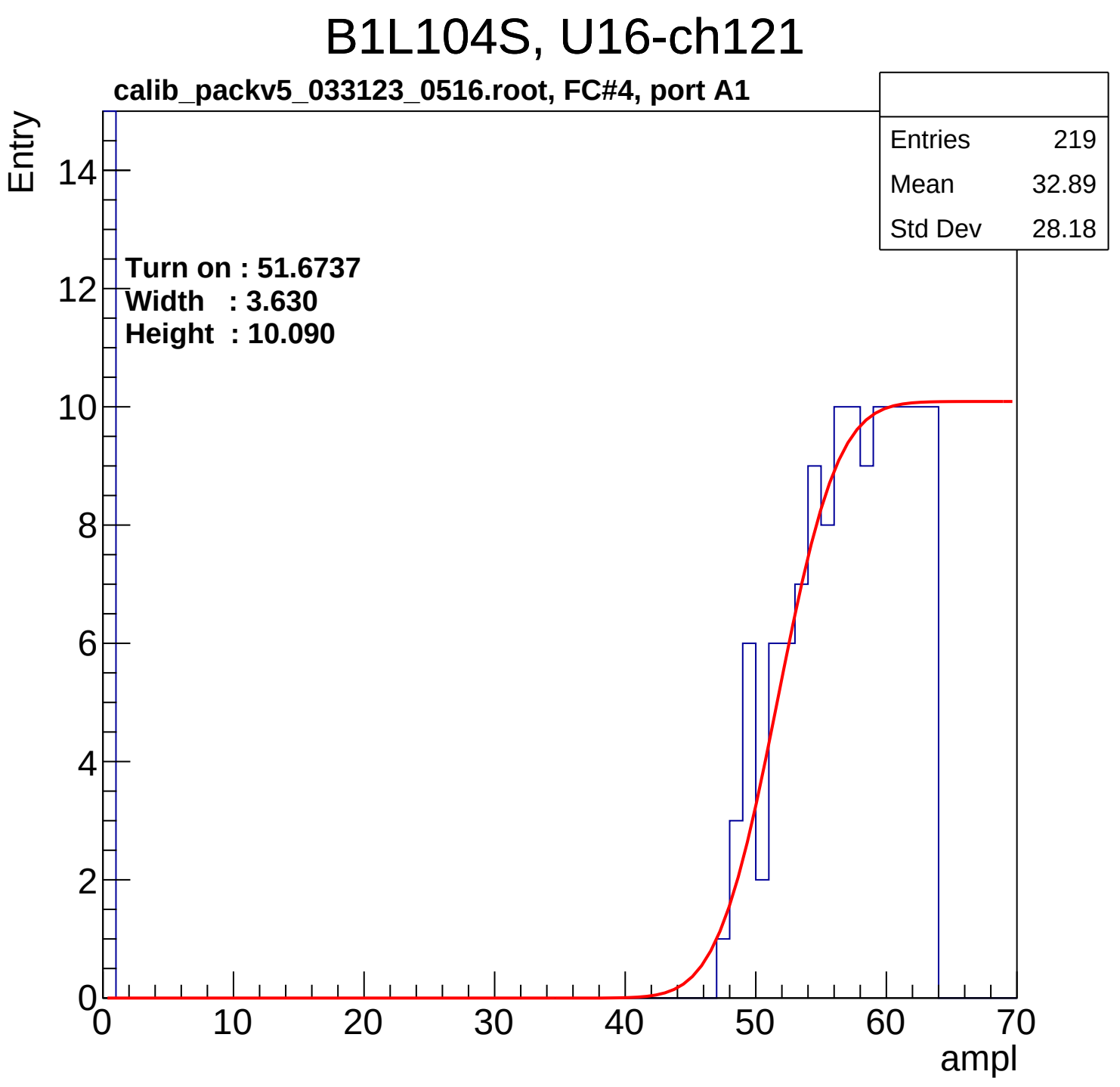
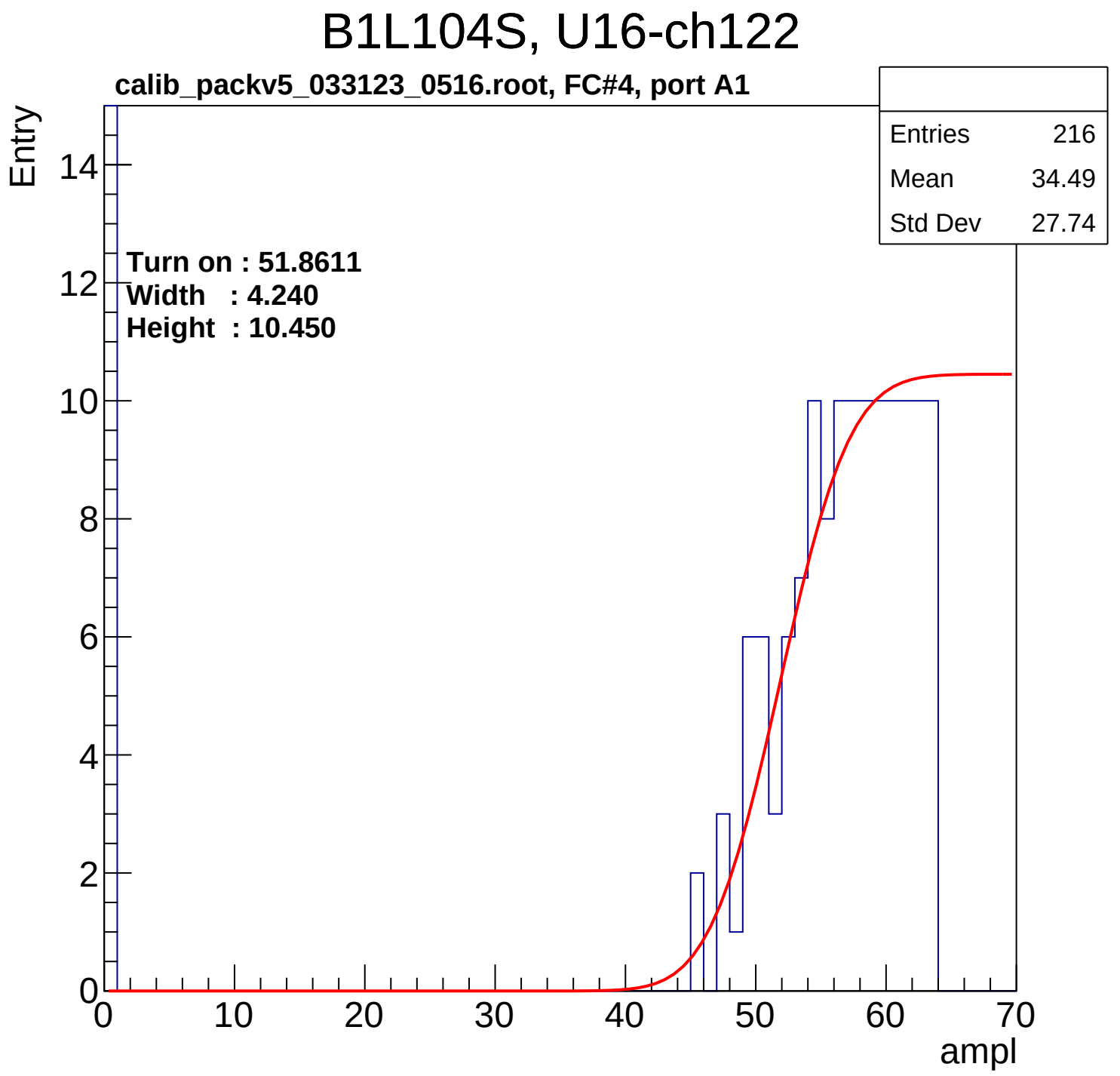
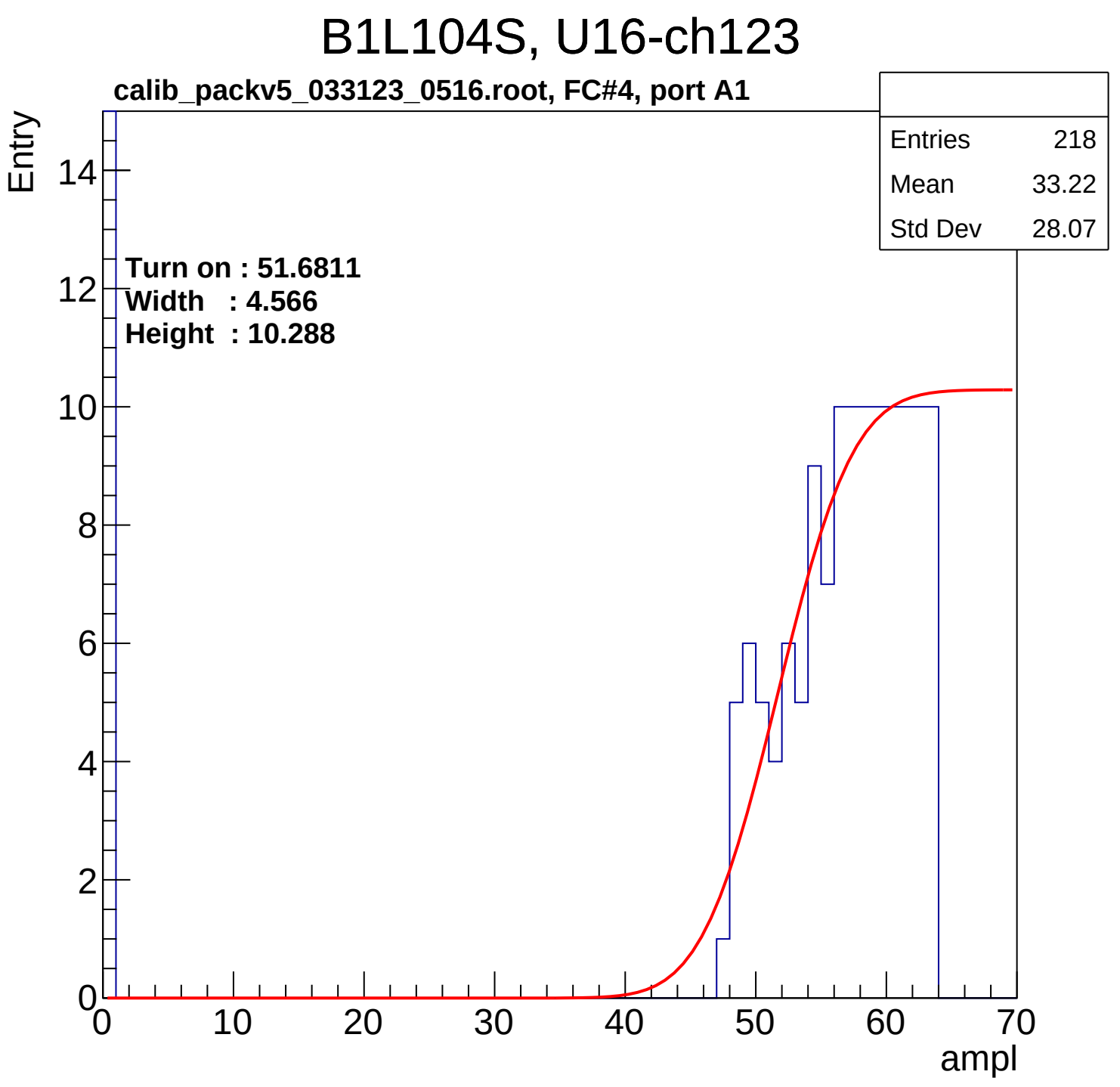
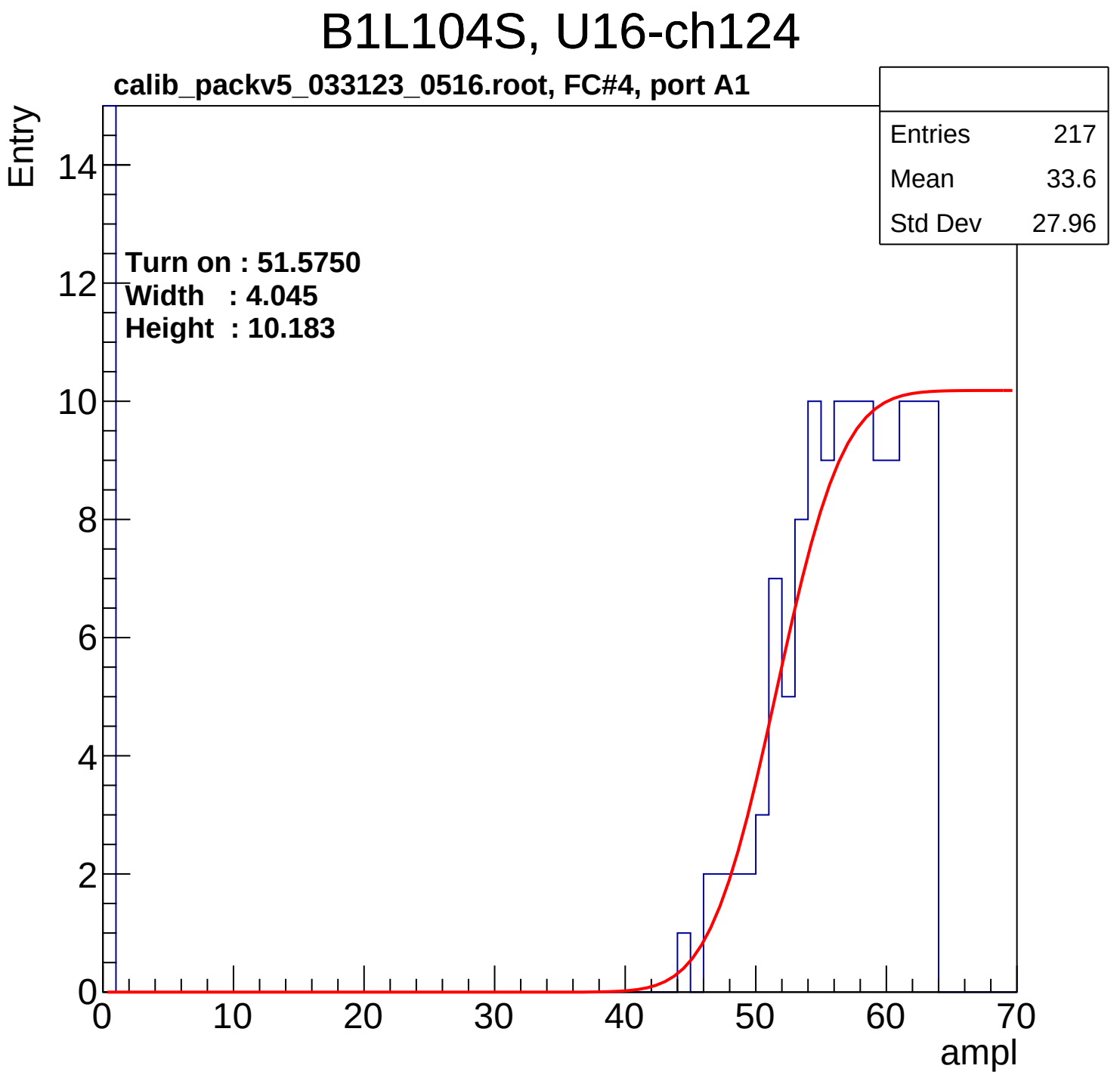
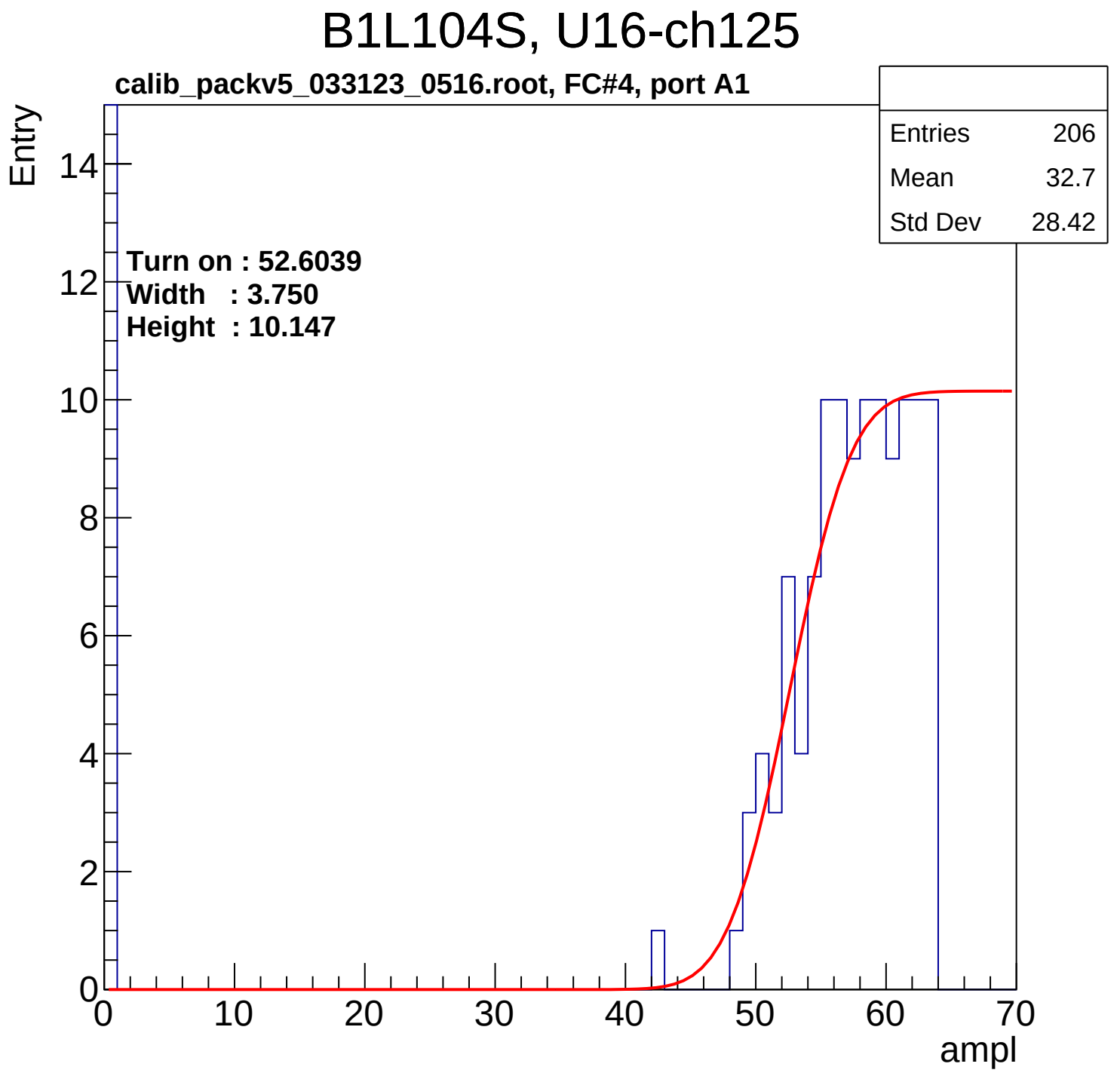

B1L104S, U16-ch120
calib_packv5_033123_0516.root, FC#4, port A1
Entries 196
Mean 28.65
Std Dev 28.85
Turn on : 55.4595
Width : 4.077
Height : 10.667
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

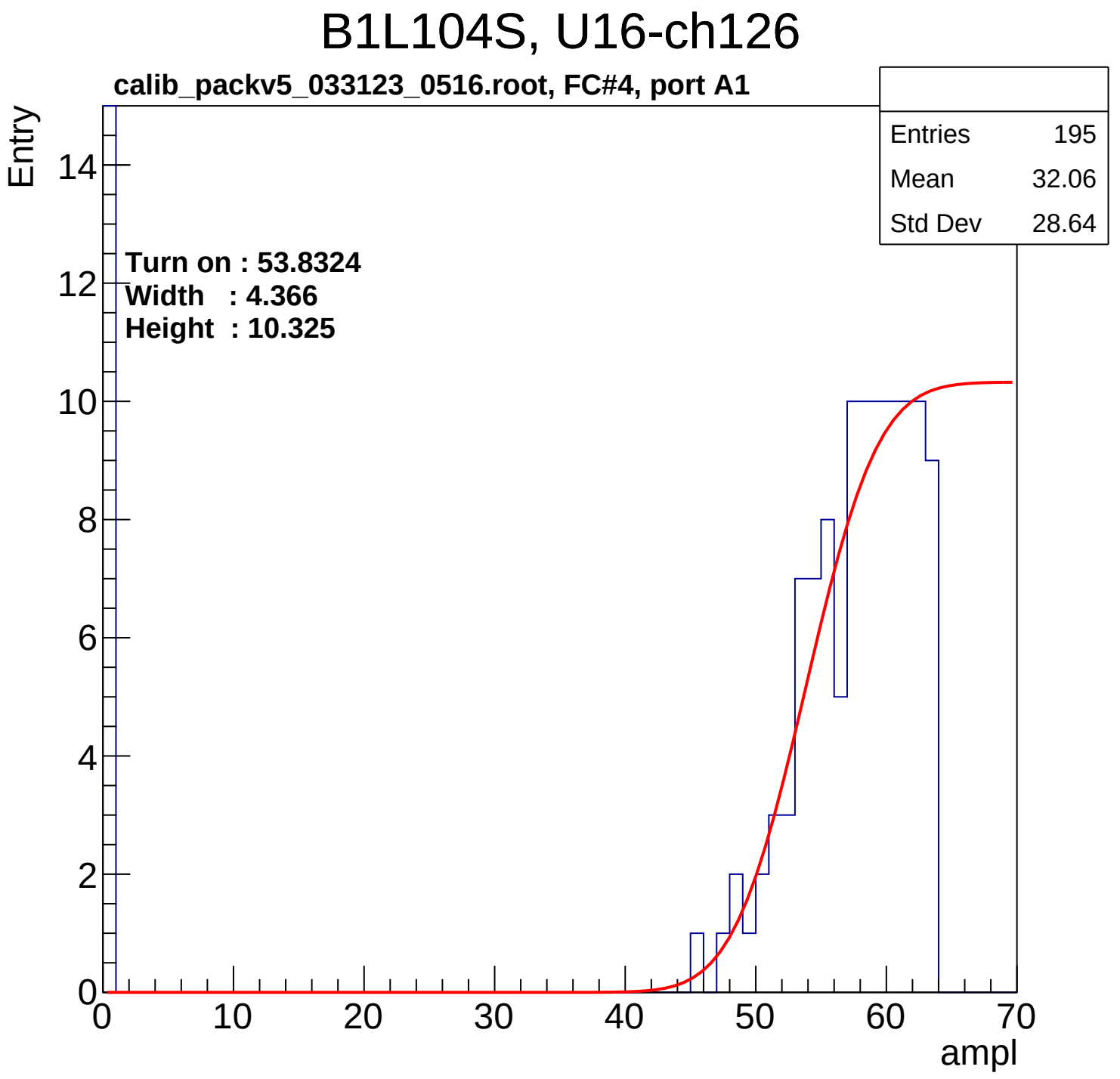

B1L104S, U16-ch121
calib_packv5_033123_0516.root, FC#4, port A1
Entries 219
Mean 32.89
Std Dev 28.18
Turn on : 51.6737
Width : 3.630
Height : 10.090
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

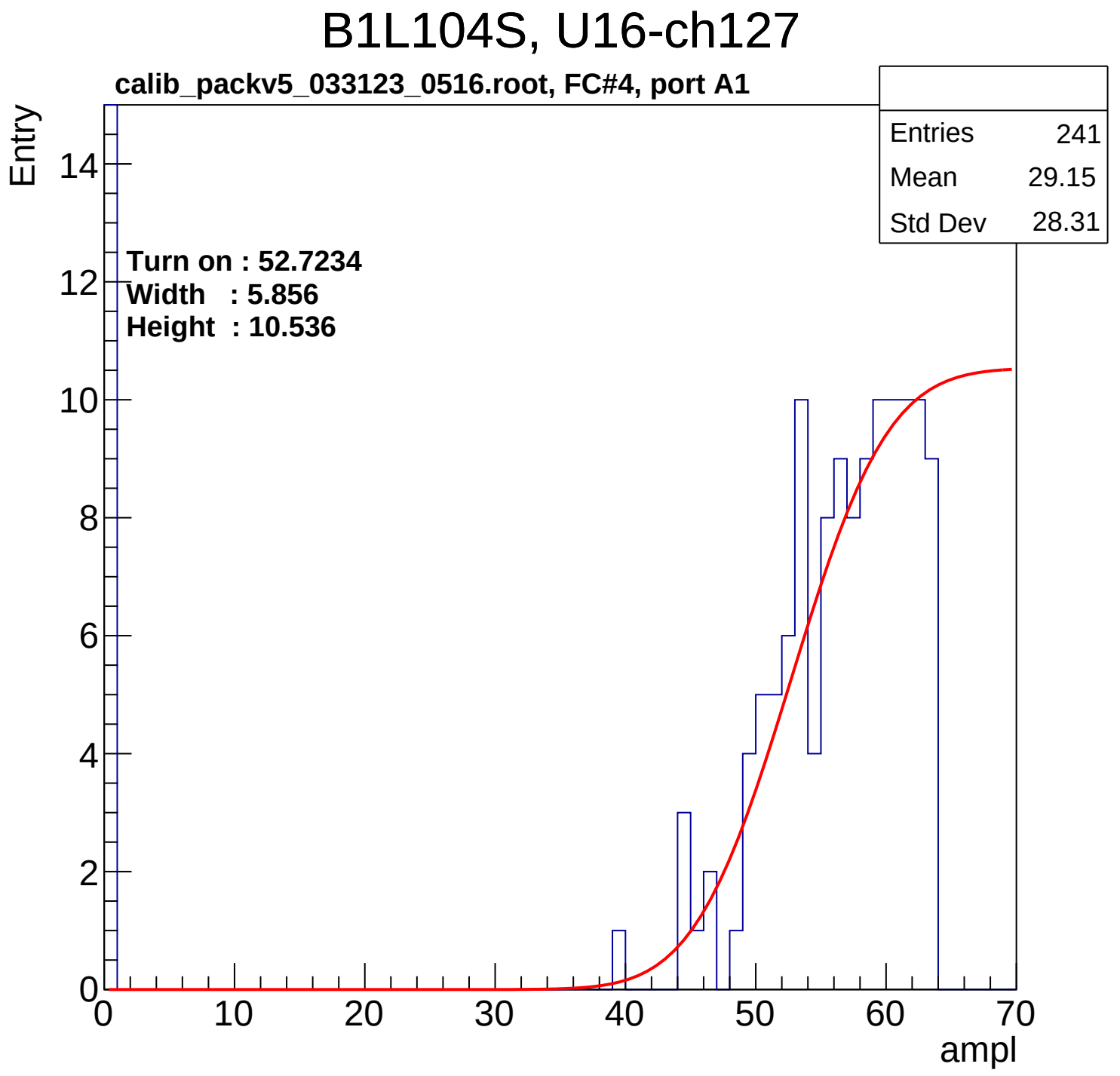

B1L104S, U16-ch122
calib_packv5_033123_0516.root, FC#4, port A1
Entries 216
Mean 34.49
Std Dev 27.74
Turn on : 51.8611
Width : 4.240
Height : 10.450
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

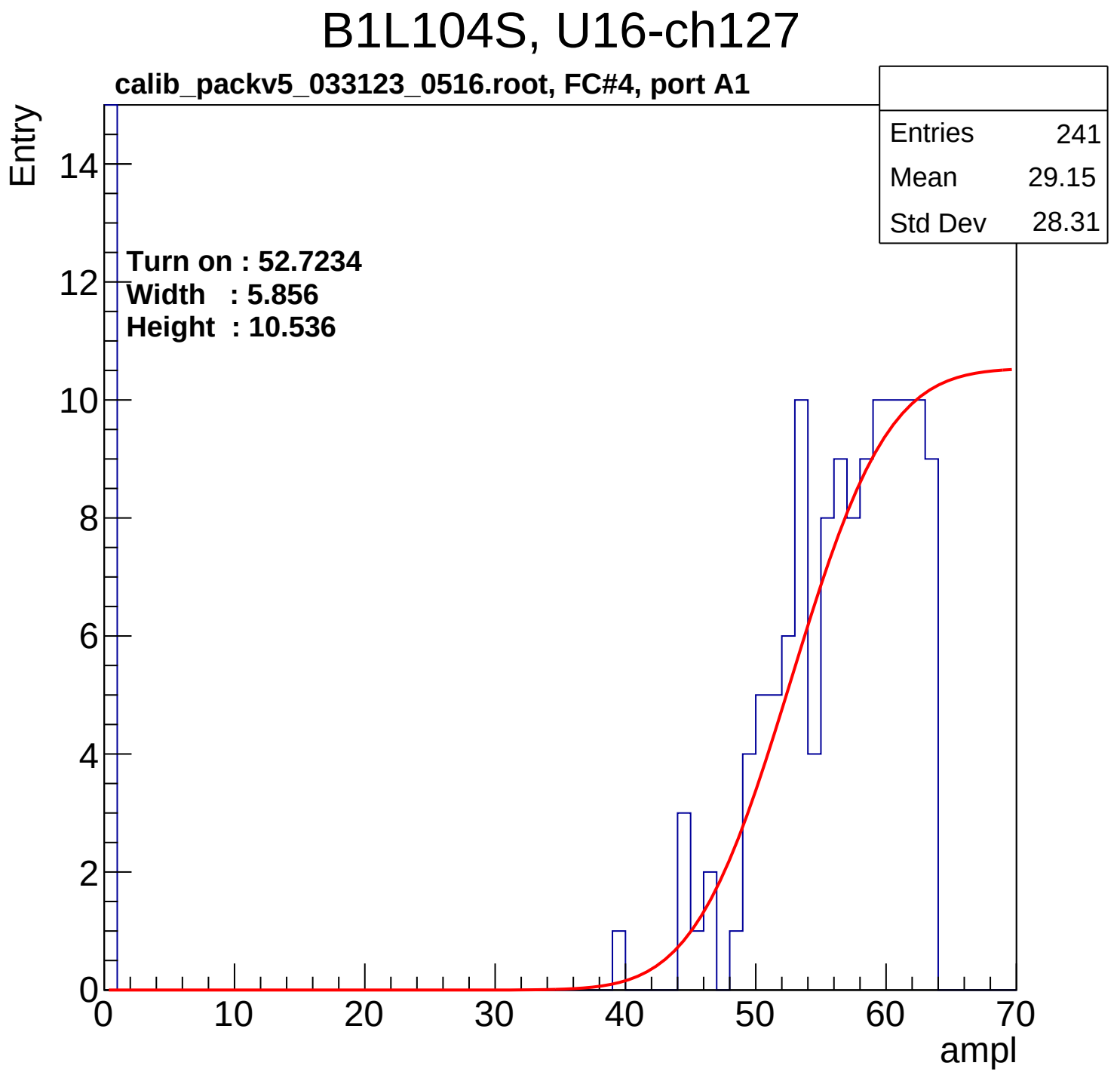

B1L104S, U16-ch123
calib_packv5_033123_0516.root, FC#4, port A1
Entries 218
Mean 33.22
Std Dev 28.07
Turn on : 51.6811
Width : 4.566
Height : 10.288
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U16-ch124
calib_packv5_033123_0516.root, FC#4, port A1
Entries 217
Mean 33.6
Std Dev 27.96
Turn on : 51.5750
Width : 4.045
Height : 10.183
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U16-ch125
calib_packv5_033123_0516.root, FC#4, port A1
Entries 206
Mean 32.7
Std Dev 28.42
Turn on : 52.6039
Width : 3.750
Height : 10.147
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U16-ch126
calib_packv5_033123_0516.root, FC#4, port A1
Entries 195
Mean 32.06
Std Dev 28.64
Turn on : 53.8324
Width : 4.366
Height : 10.325
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U16-ch127
calib_packv5_033123_0516.root, FC#4, port A1
Entries 241
Mean 29.15
Std Dev 28.31
Turn on : 52.7234
Width : 5.856
Height : 10.536
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L104S, U16-ch127
calib_packv5_033123_0516.root, FC#4, port A1
Entries 241
Mean 29.15
Std Dev 28.31
Turn on : 52.7234
Width : 5.856
Height : 10.536
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70