



# B1L003S, U22-ch0, adc0

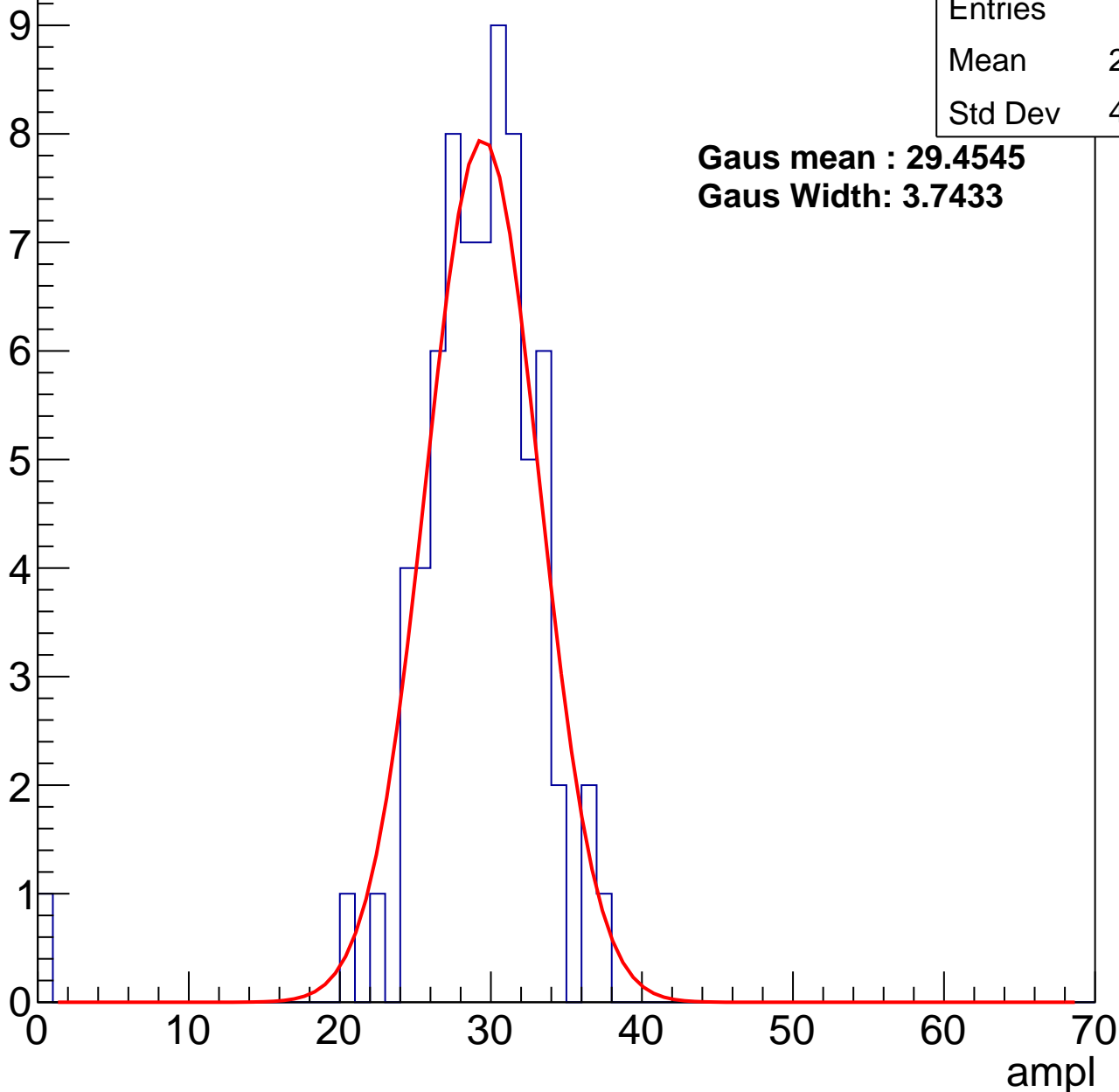
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	28.64
Std Dev	4.733

**Gaus mean : 29.4545**

**Gaus Width: 3.7433**



# B1L003S, U22-ch0, adc1

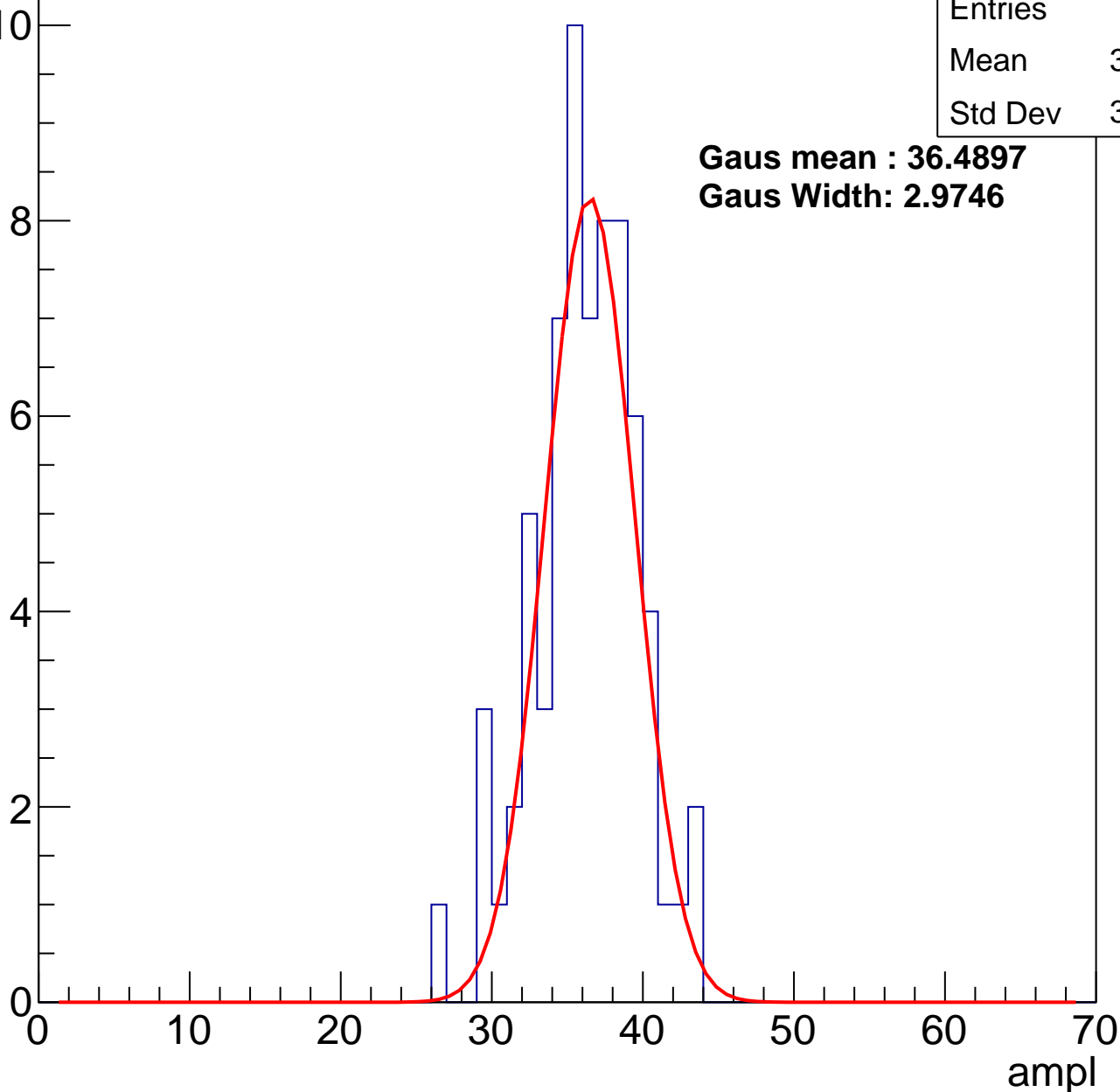
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	35.75
Std Dev	3.385

**Gaus mean : 36.4897**

**Gaus Width: 2.9746**



# B1L003S, U22-ch0, adc2

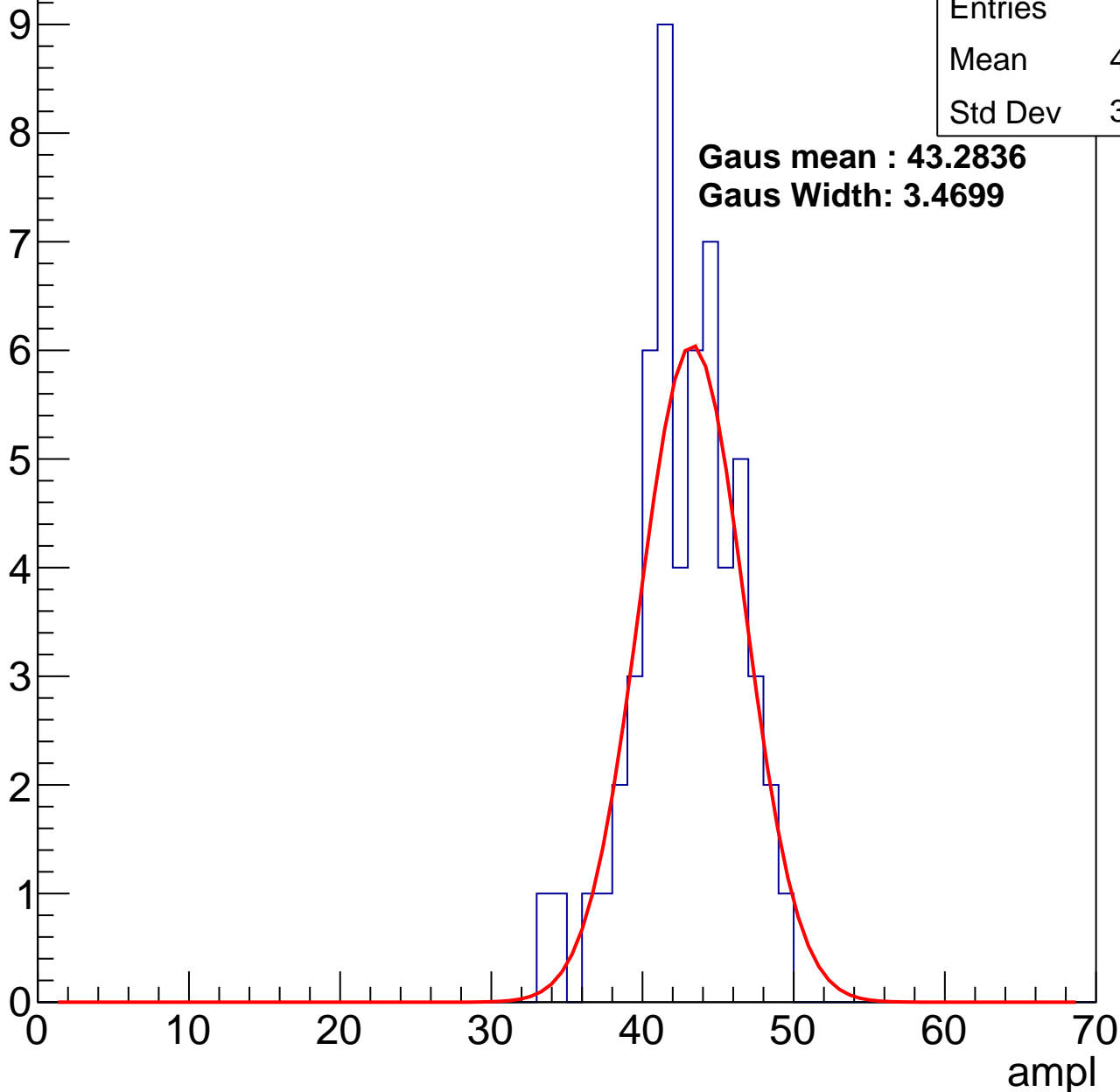
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.36
Std Dev	3.372

**Gaus mean : 43.2836**

**Gaus Width: 3.4699**

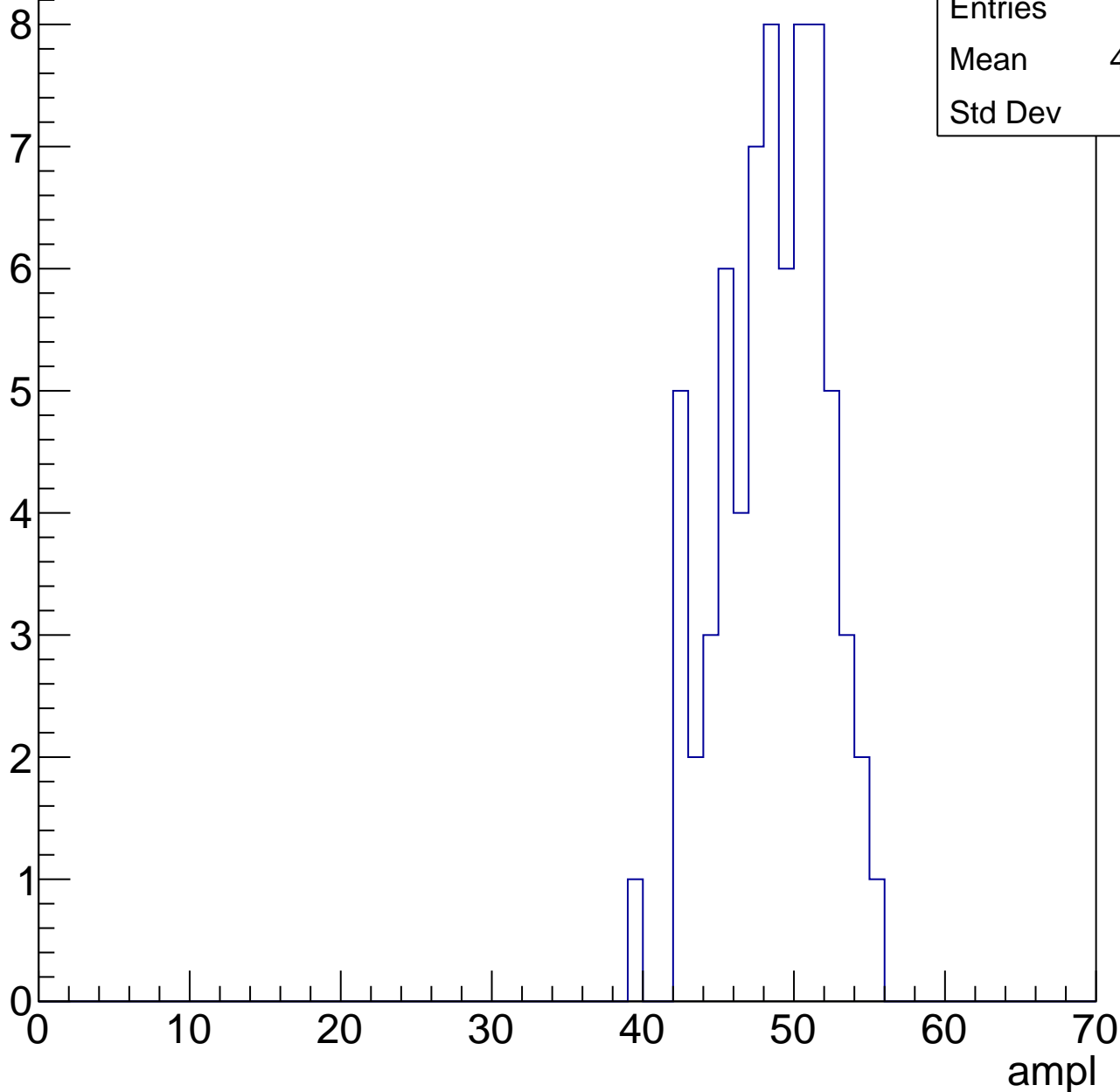


# B1L003S, U22-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	48.09
Std Dev	3.45

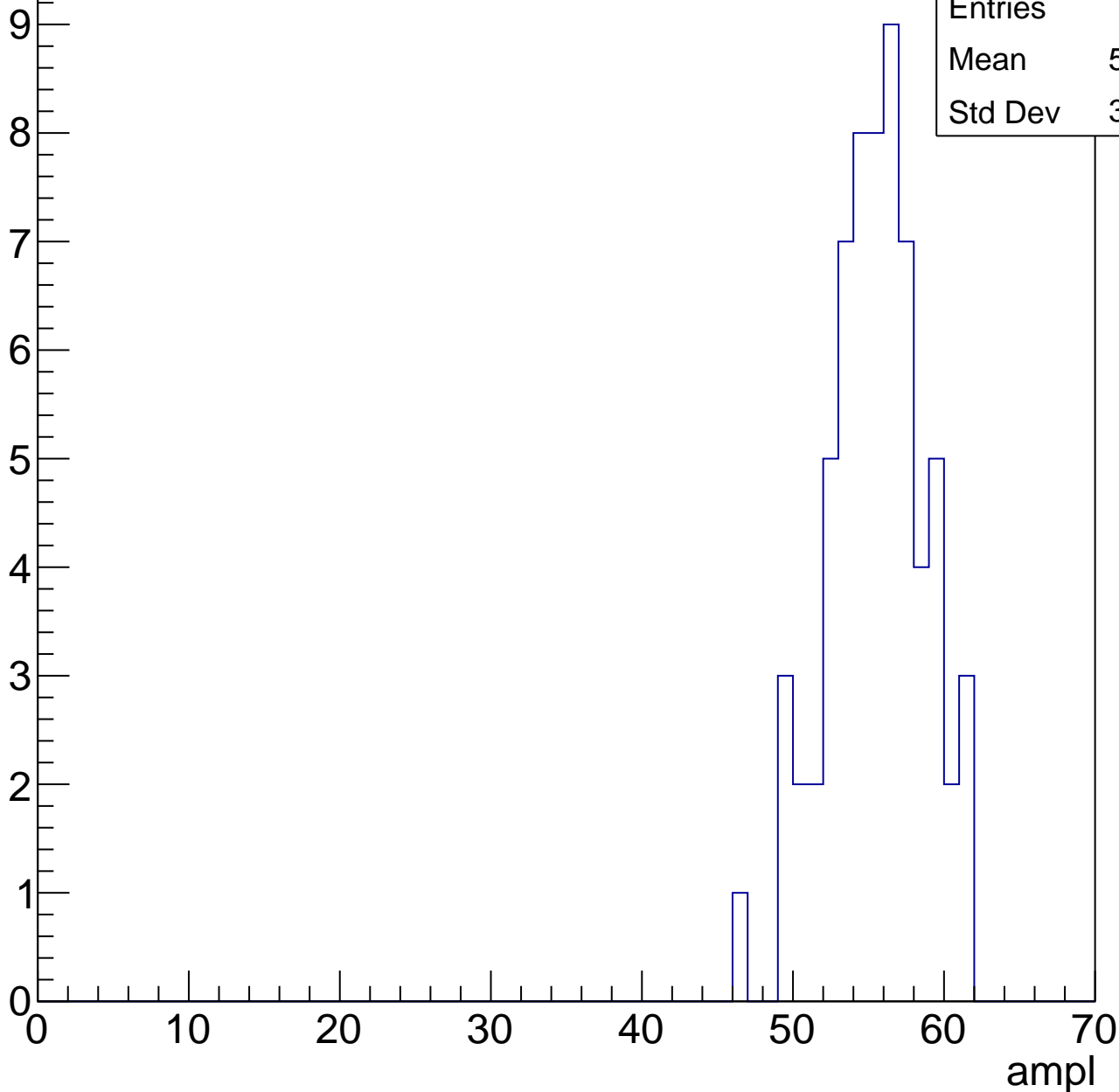


# B1L003S, U22-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	55.02
Std Dev	3.169



# B1L003S, U22-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

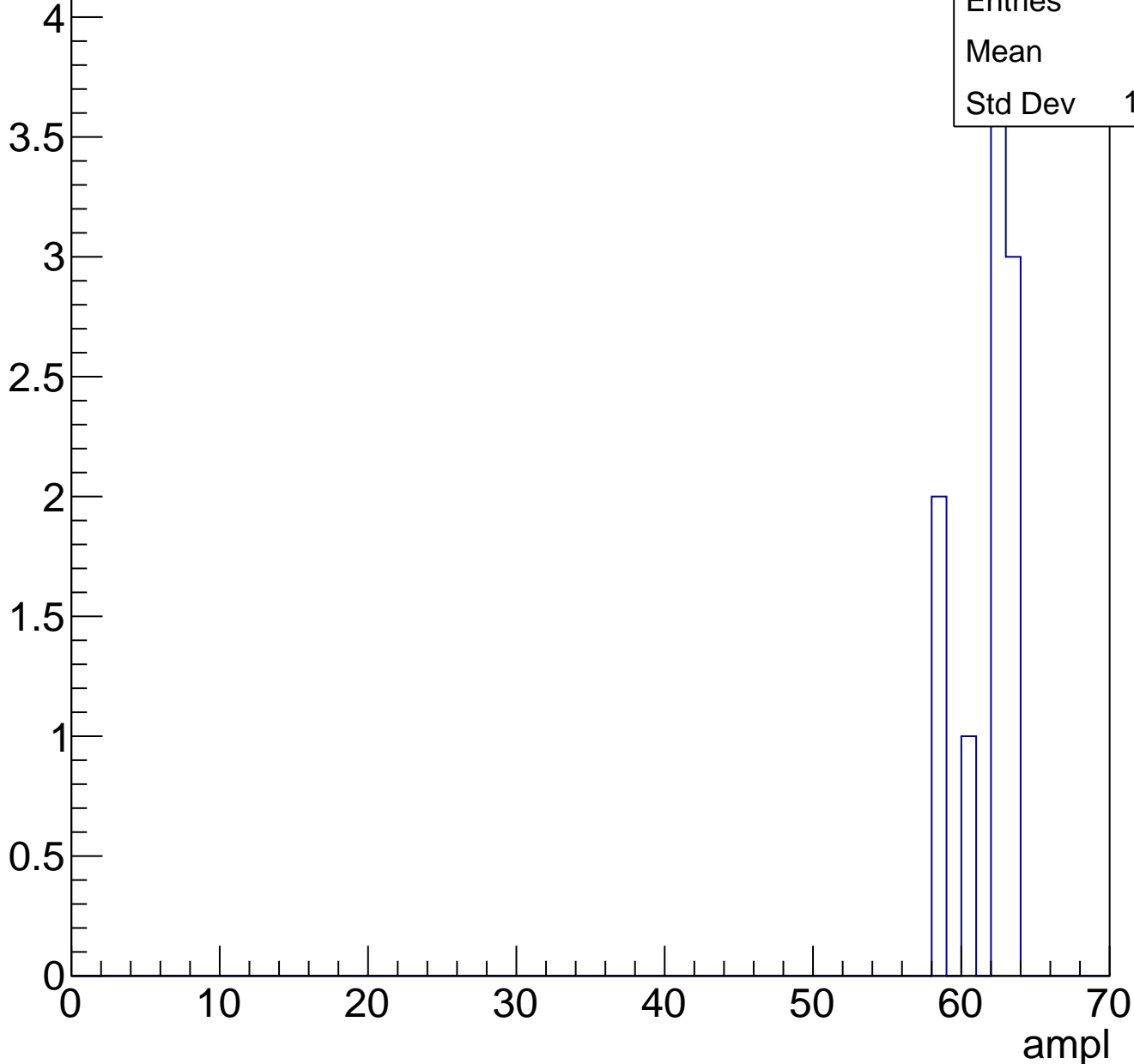
Entries	42
Mean	58.79
Std Dev	9.413

ampl

# B1L003S, U22-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch1, adc0

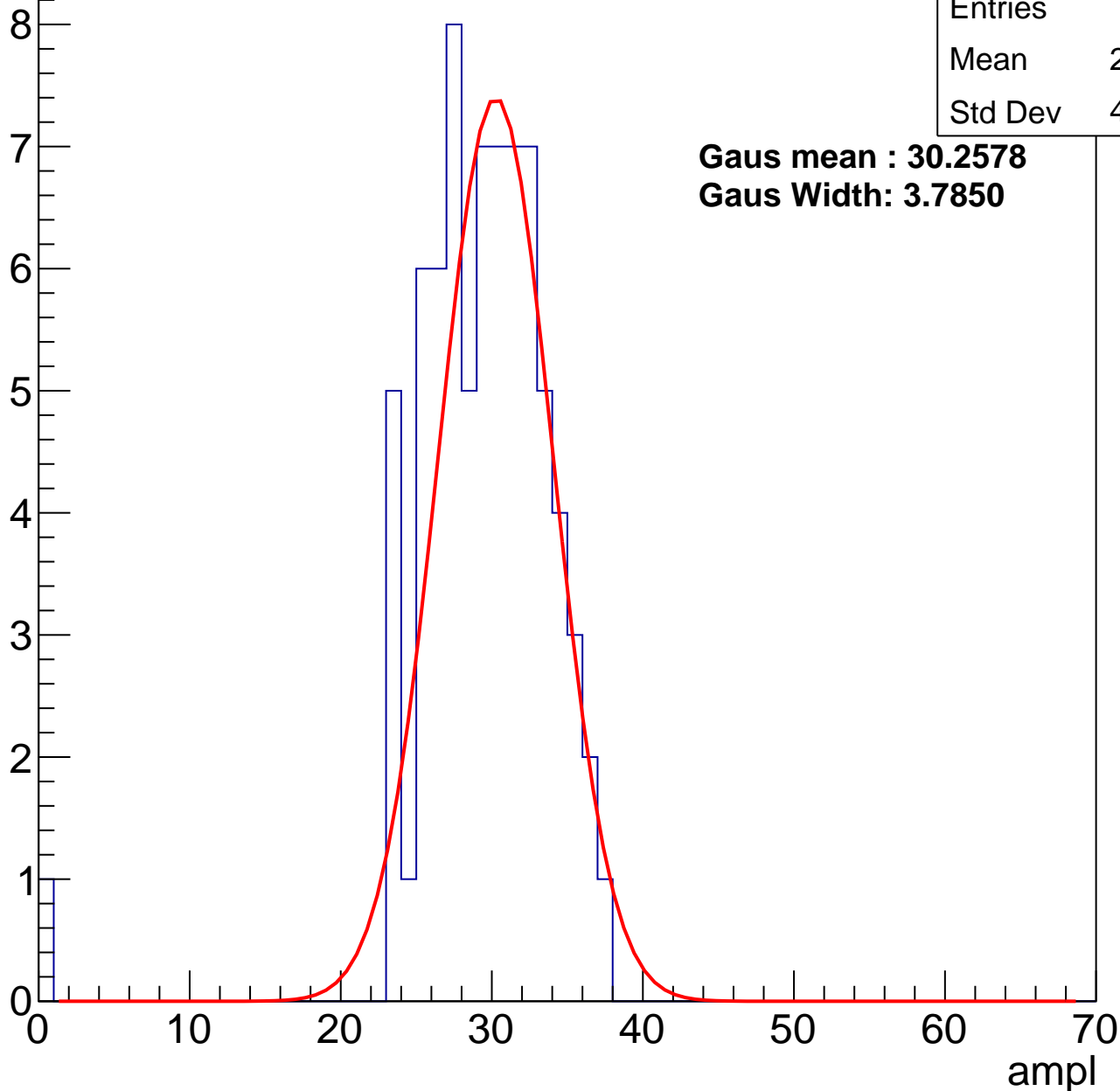
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	28.93
Std Dev	4.873

**Gaus mean : 30.2578**

**Gaus Width: 3.7850**



# B1L003S, U22-ch1, adc1

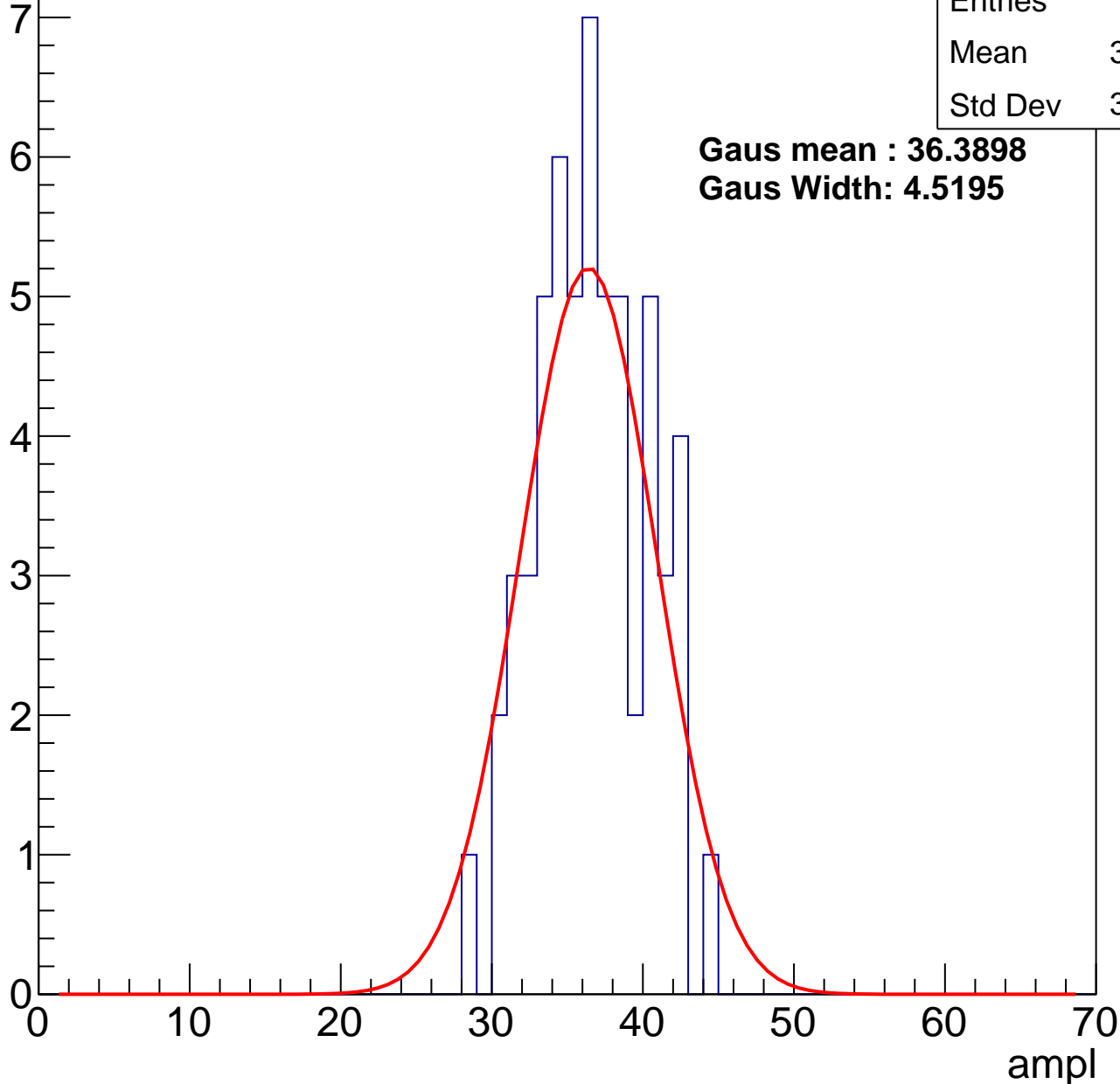
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.16
Std Dev	3.597

**Gaus mean : 36.3898**

**Gaus Width: 4.5195**



# B1L003S, U22-ch1, adc2

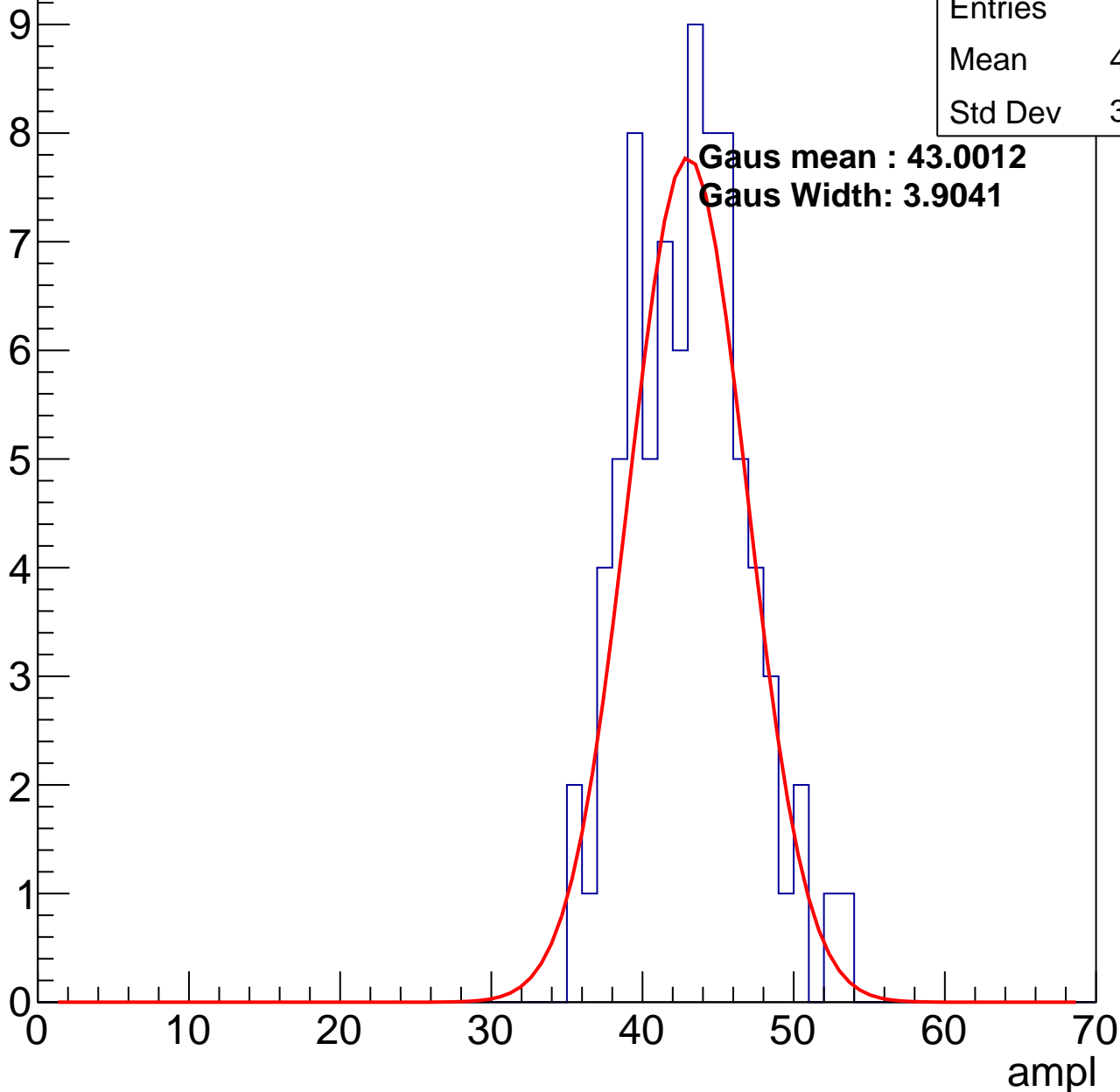
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	42.62
Std Dev	3.842

**Gaus mean : 43.0012**

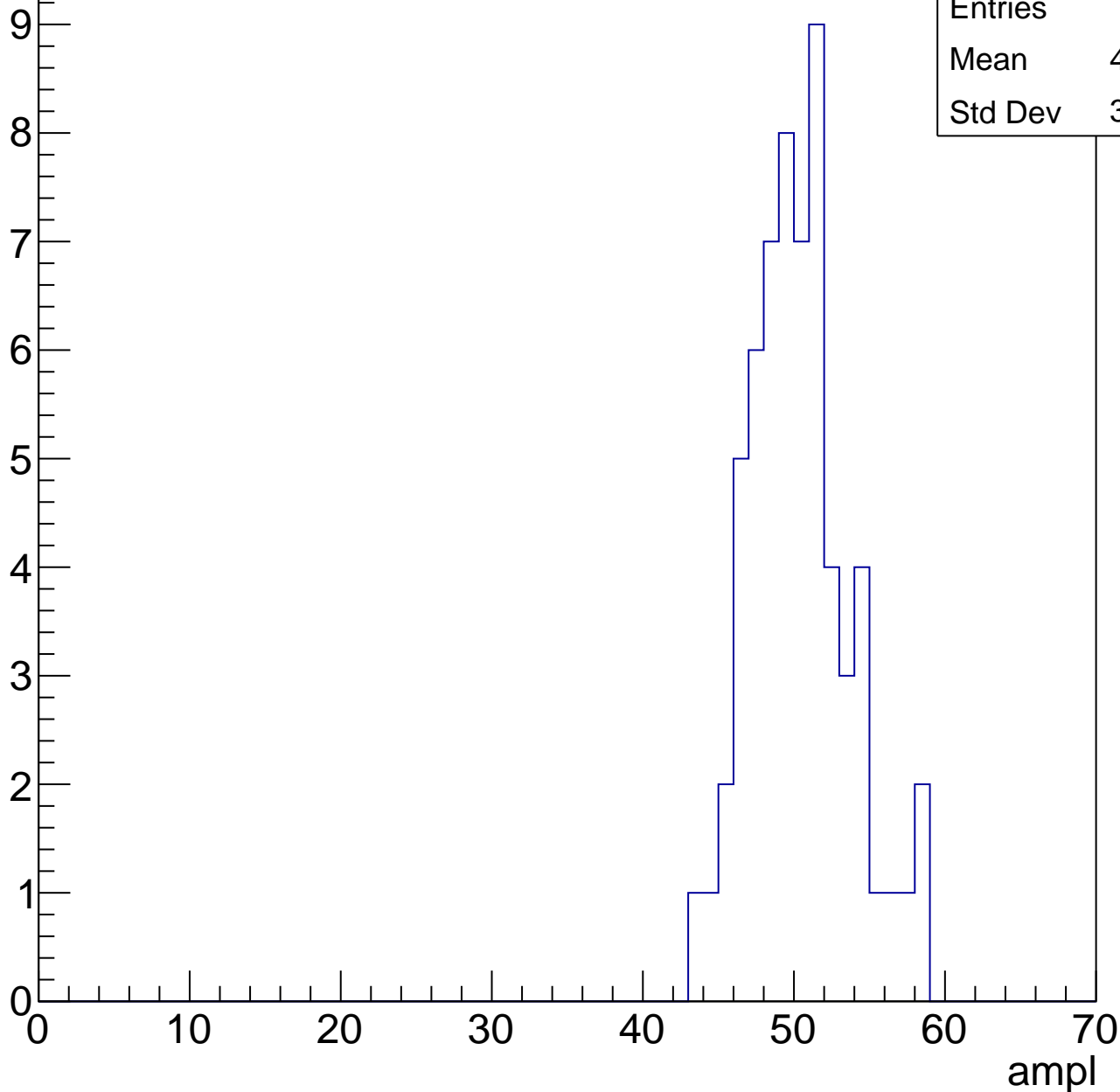
**Gaus Width: 3.9041**



# B1L003S, U22-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

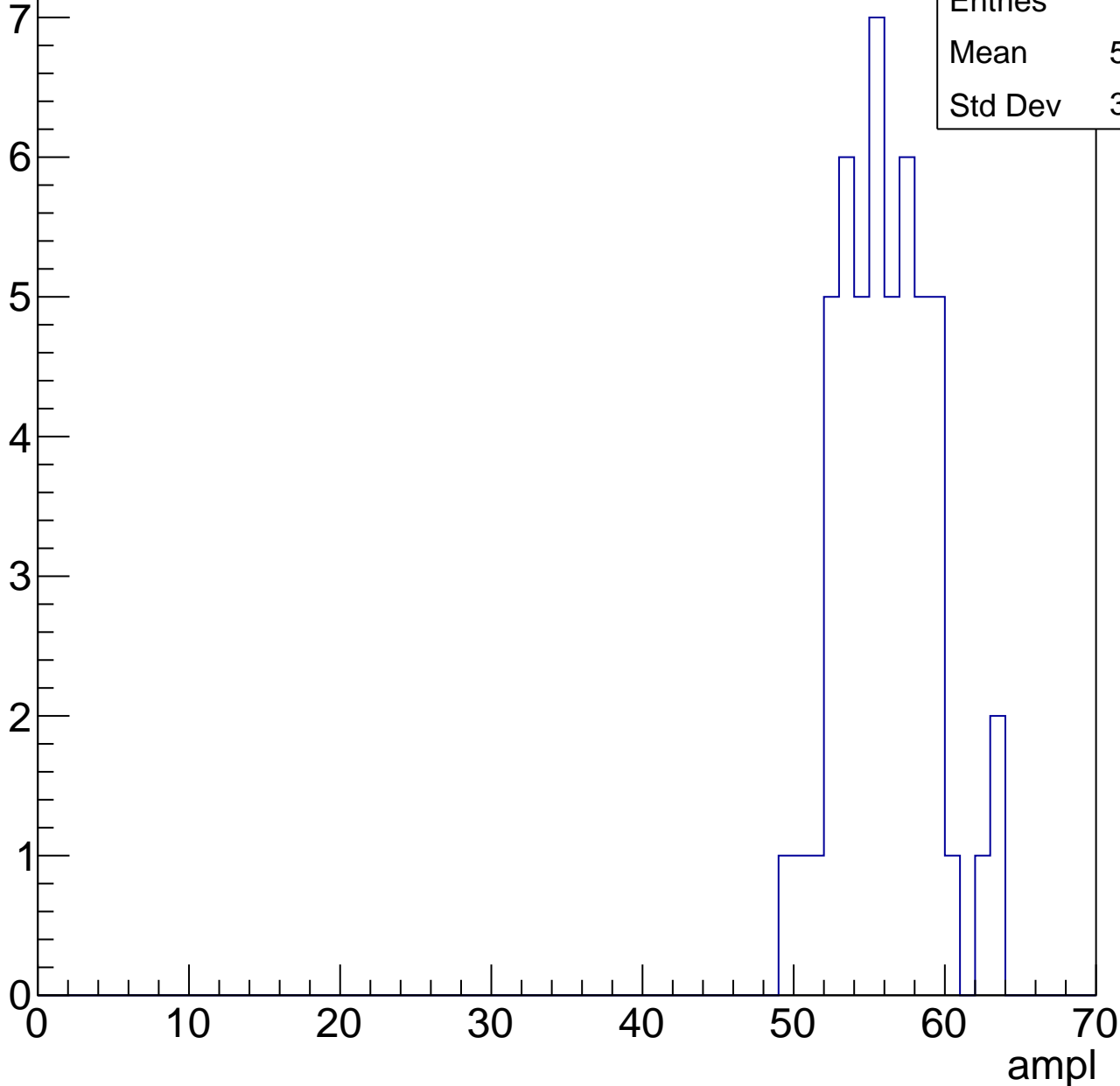


# B1L003S, U22-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	55.65
Std Dev	3.086

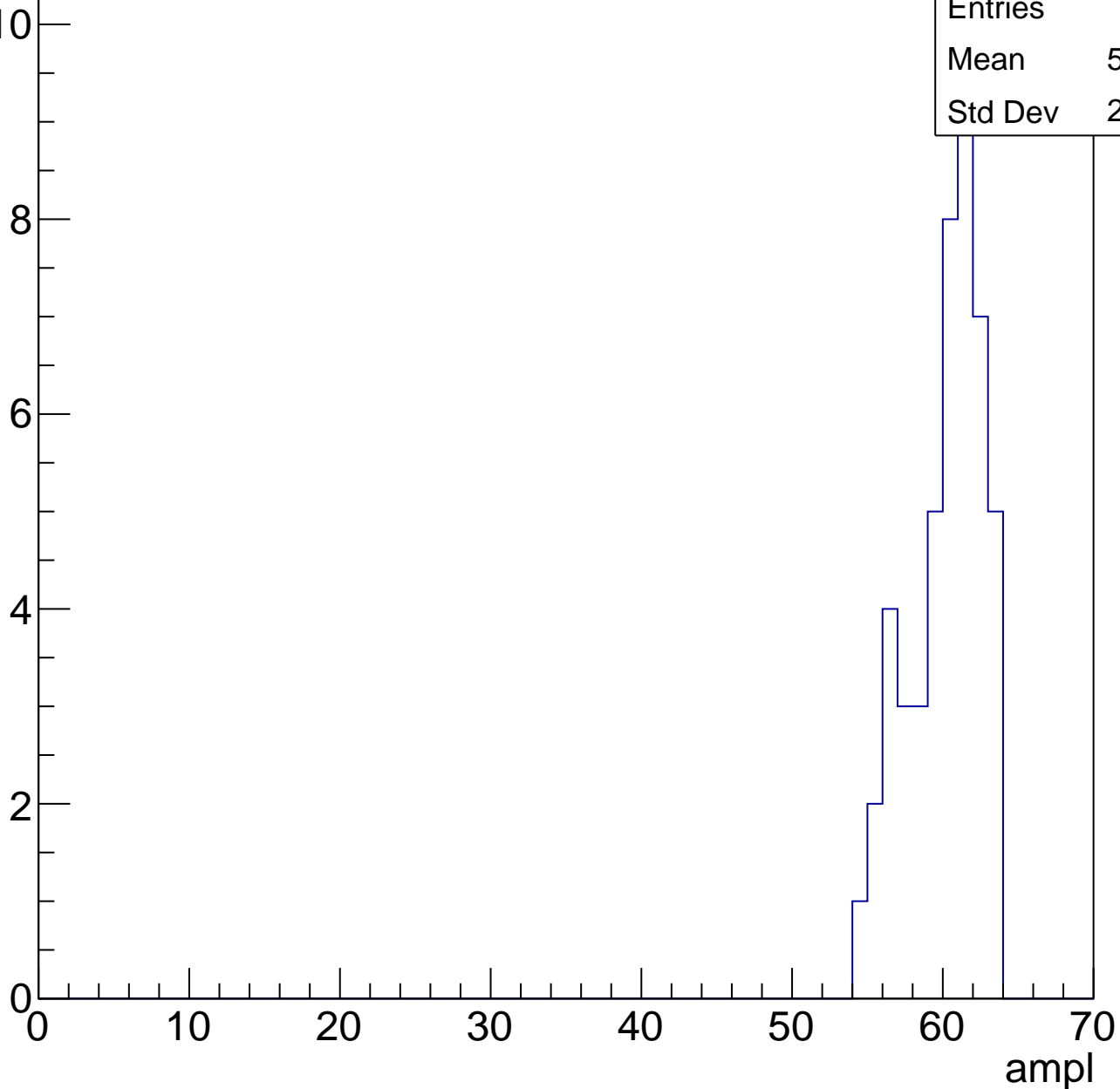


# B1L003S, U22-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

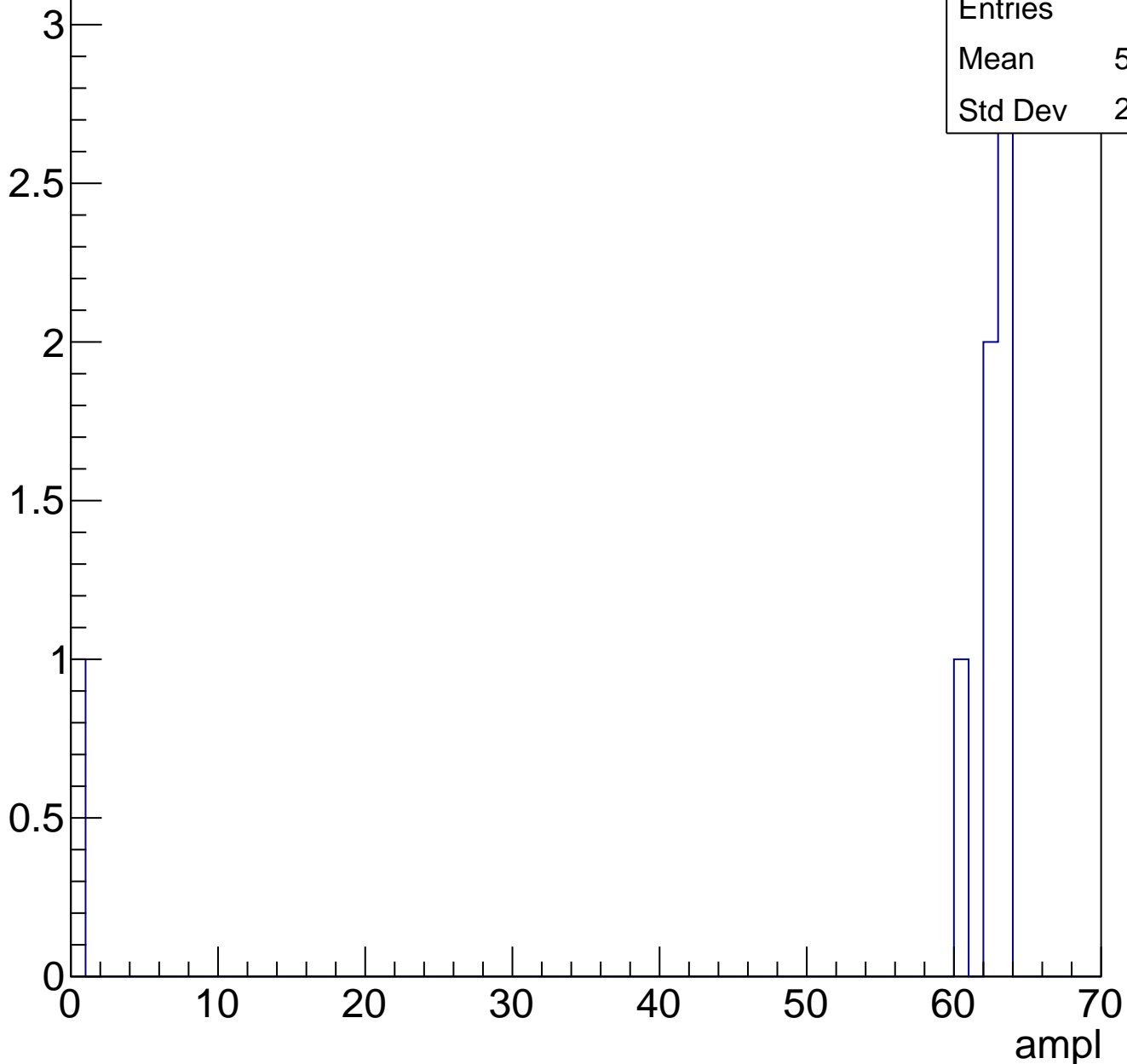
Entries	48
Mean	59.73
Std Dev	2.387



# B1L003S, U22-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch2, adc0

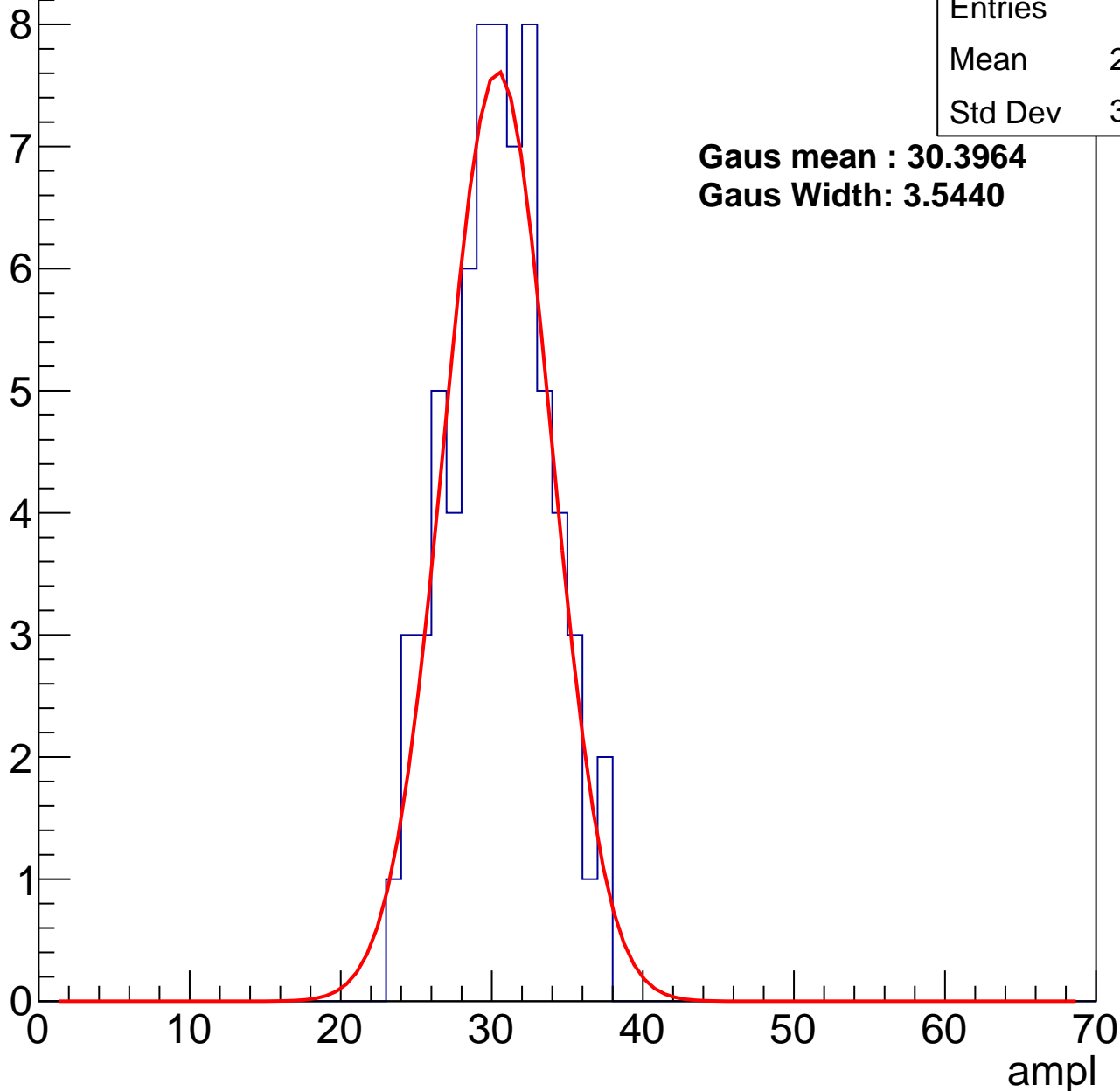
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.96
Std Dev	3.292

**Gaus mean : 30.3964**

**Gaus Width: 3.5440**



# B1L003S, U22-ch2, adc1

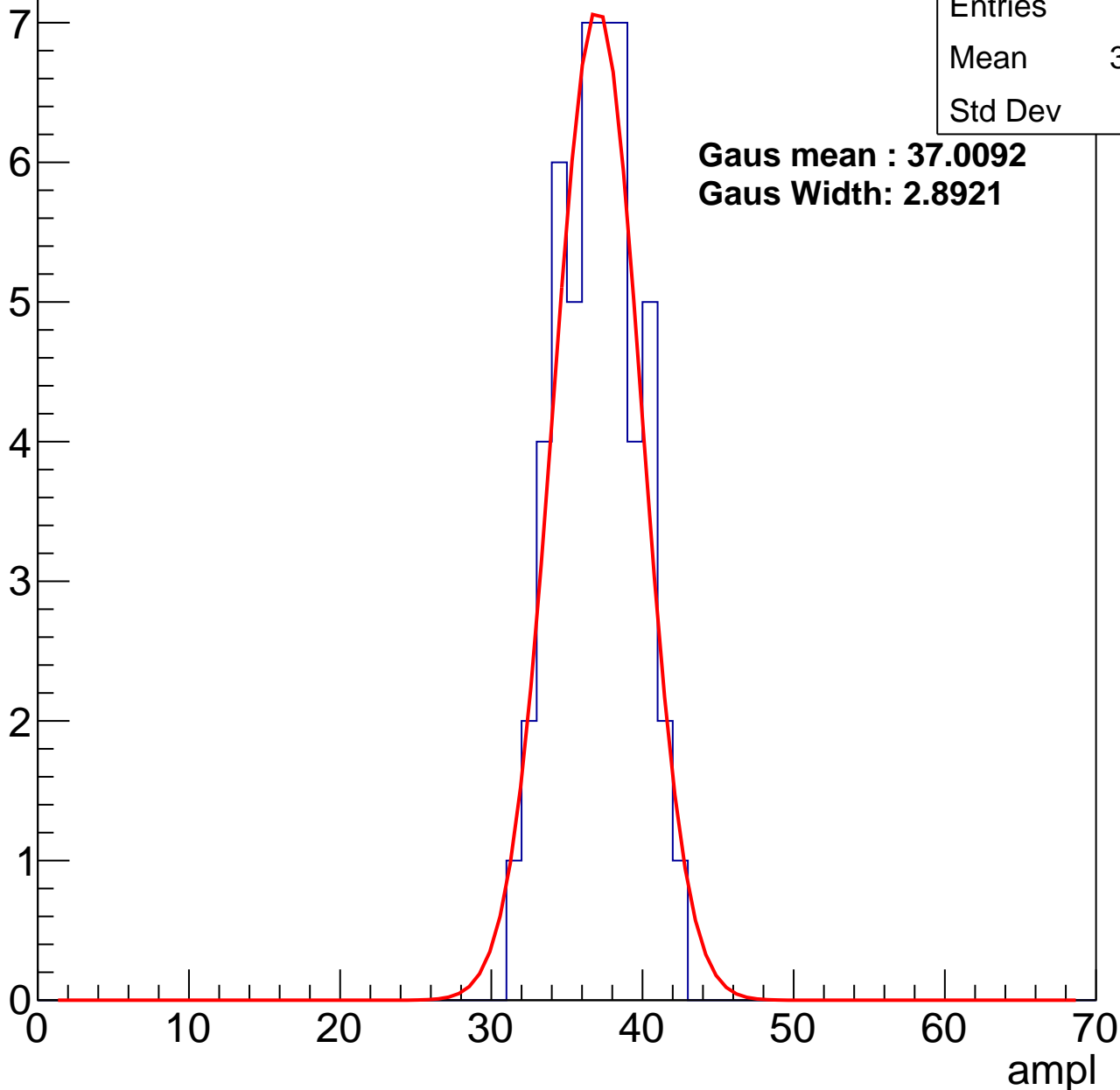
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.53
Std Dev	2.6

**Gaus mean : 37.0092**

**Gaus Width: 2.8921**



# B1L003S, U22-ch2, adc2

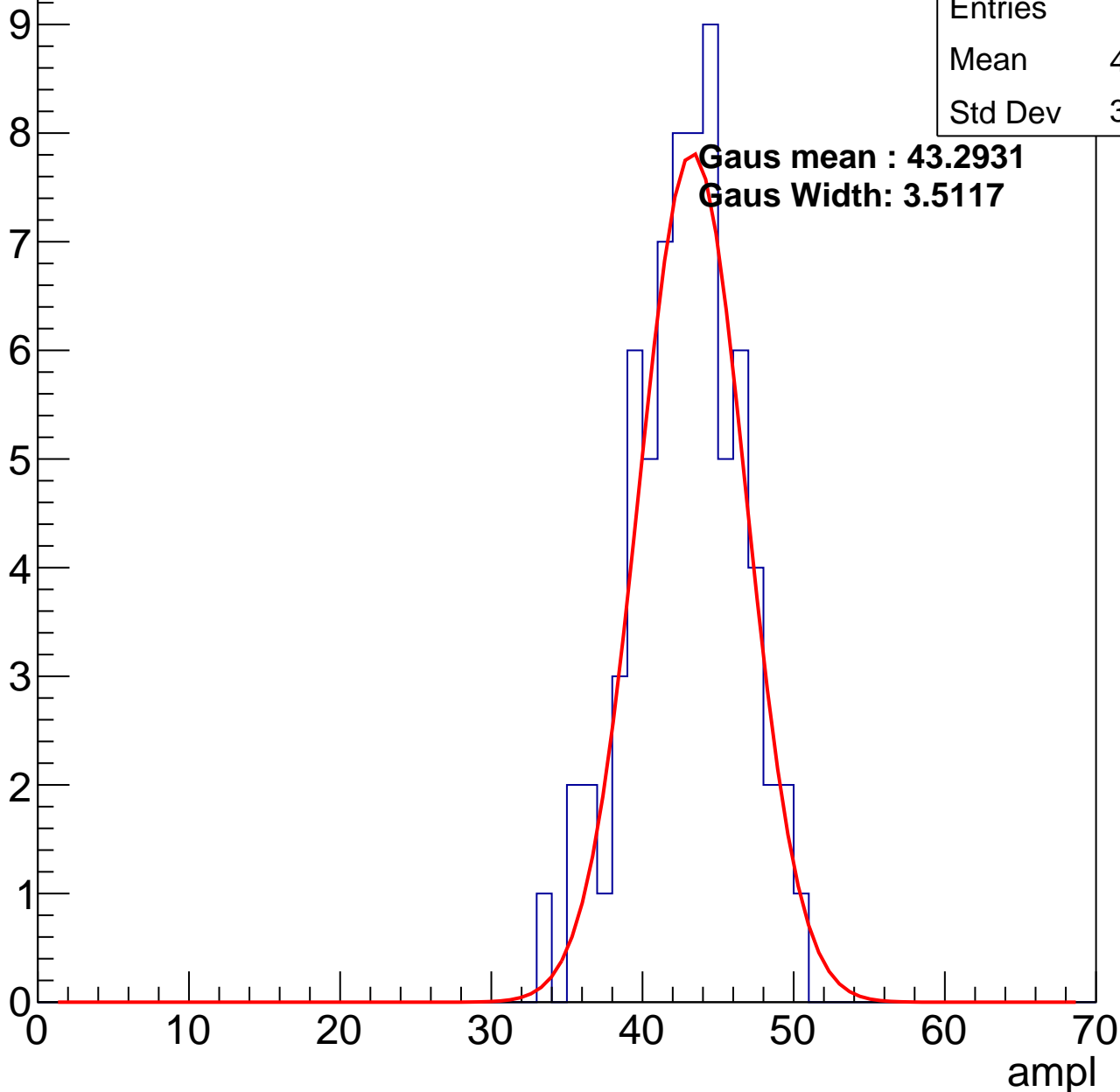
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.44
Std Dev	3.555

**Gaus mean : 43.2931**

**Gaus Width: 3.5117**

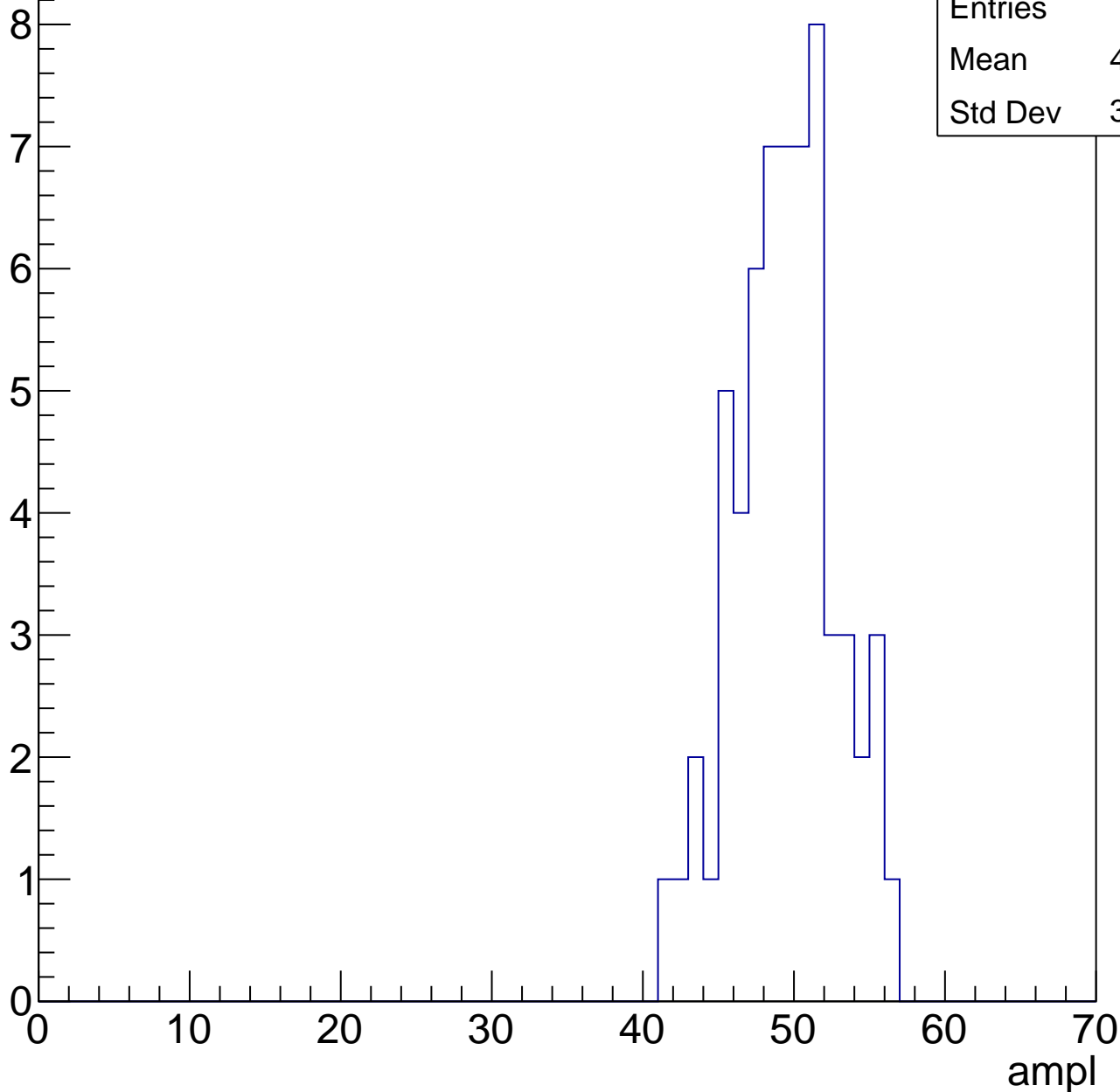


# B1L003S, U22-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.93
Std Dev	3.333

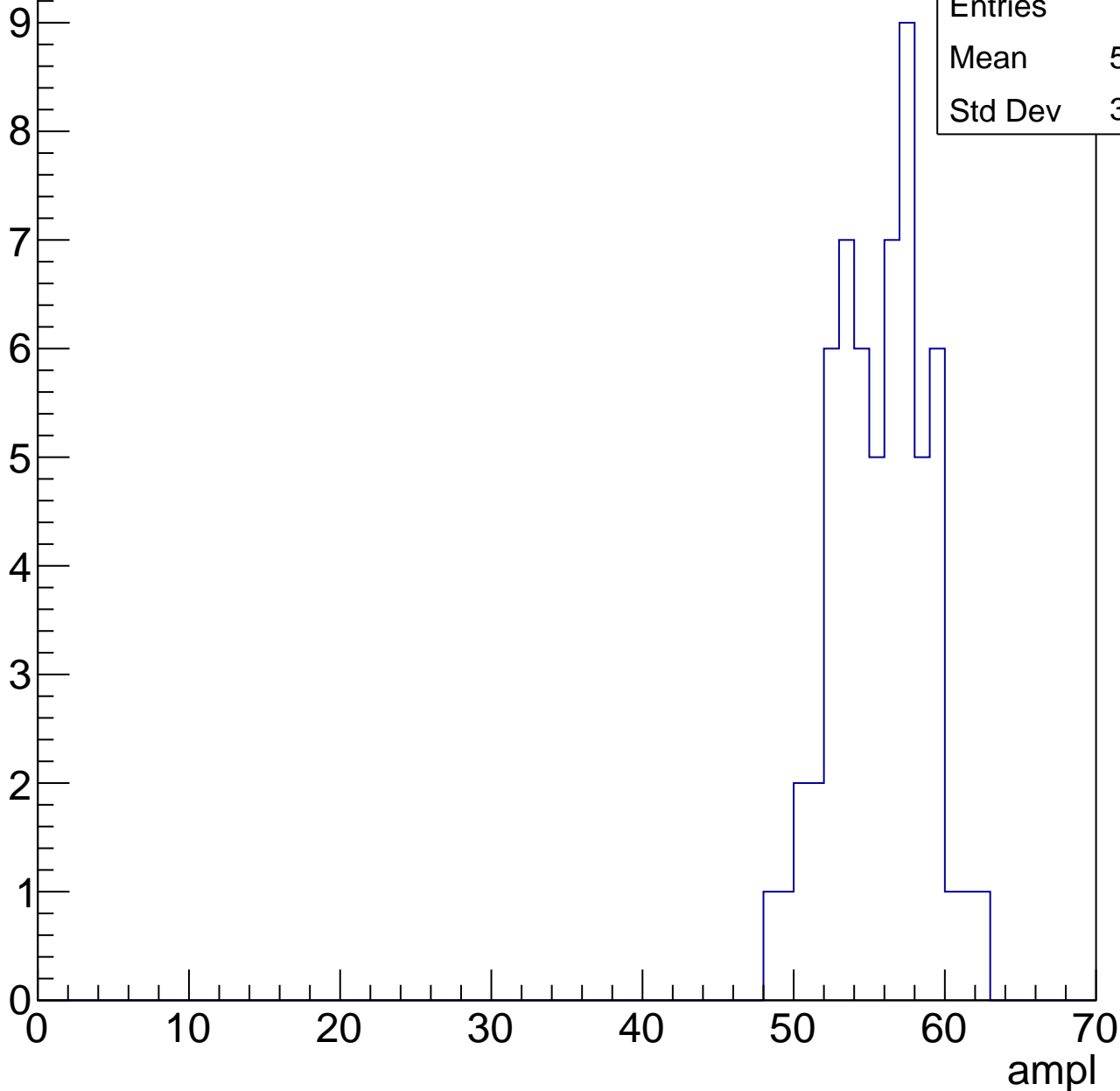


# B1L003S, U22-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.22
Std Dev	3.017

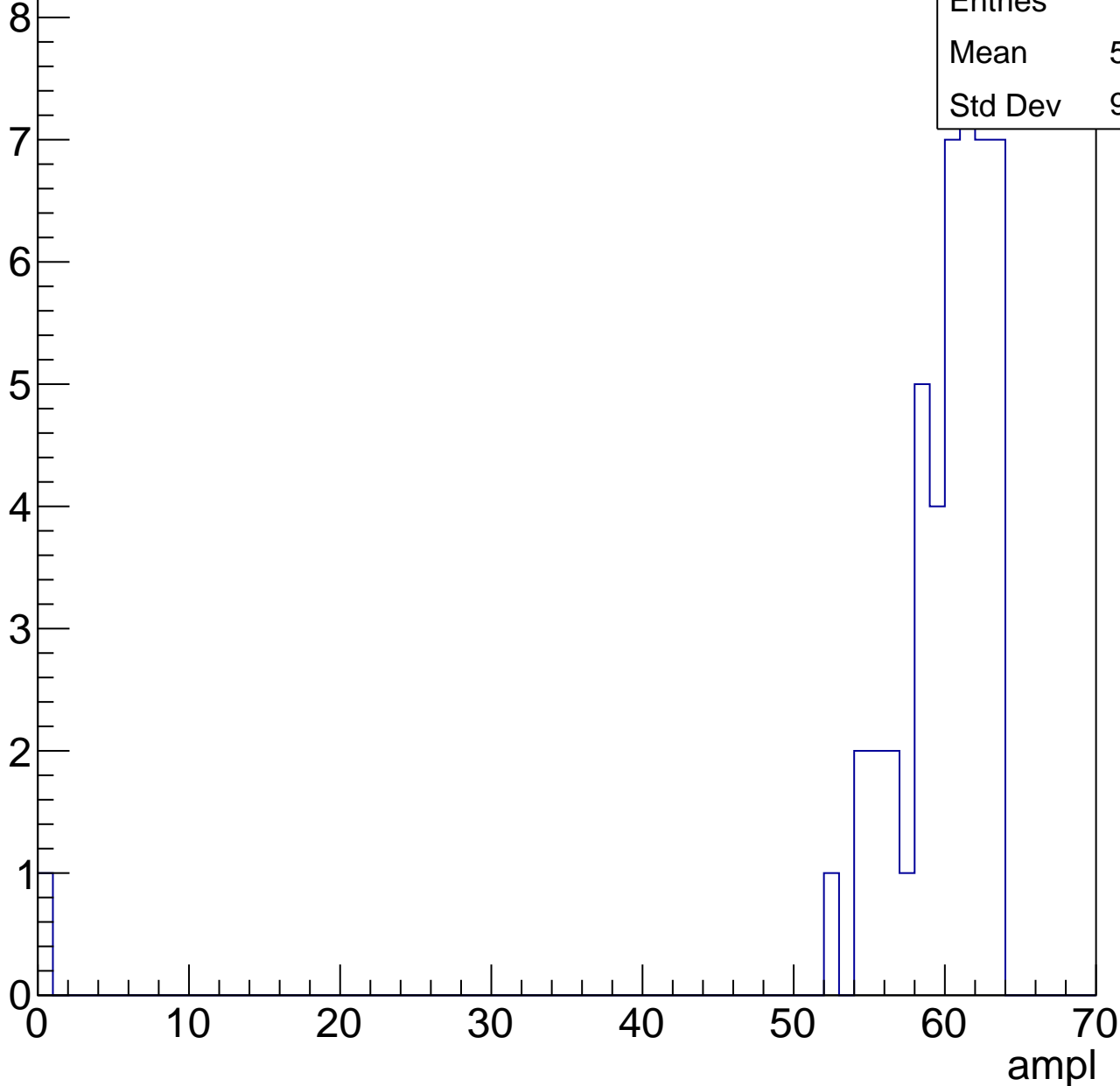


# B1L003S, U22-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

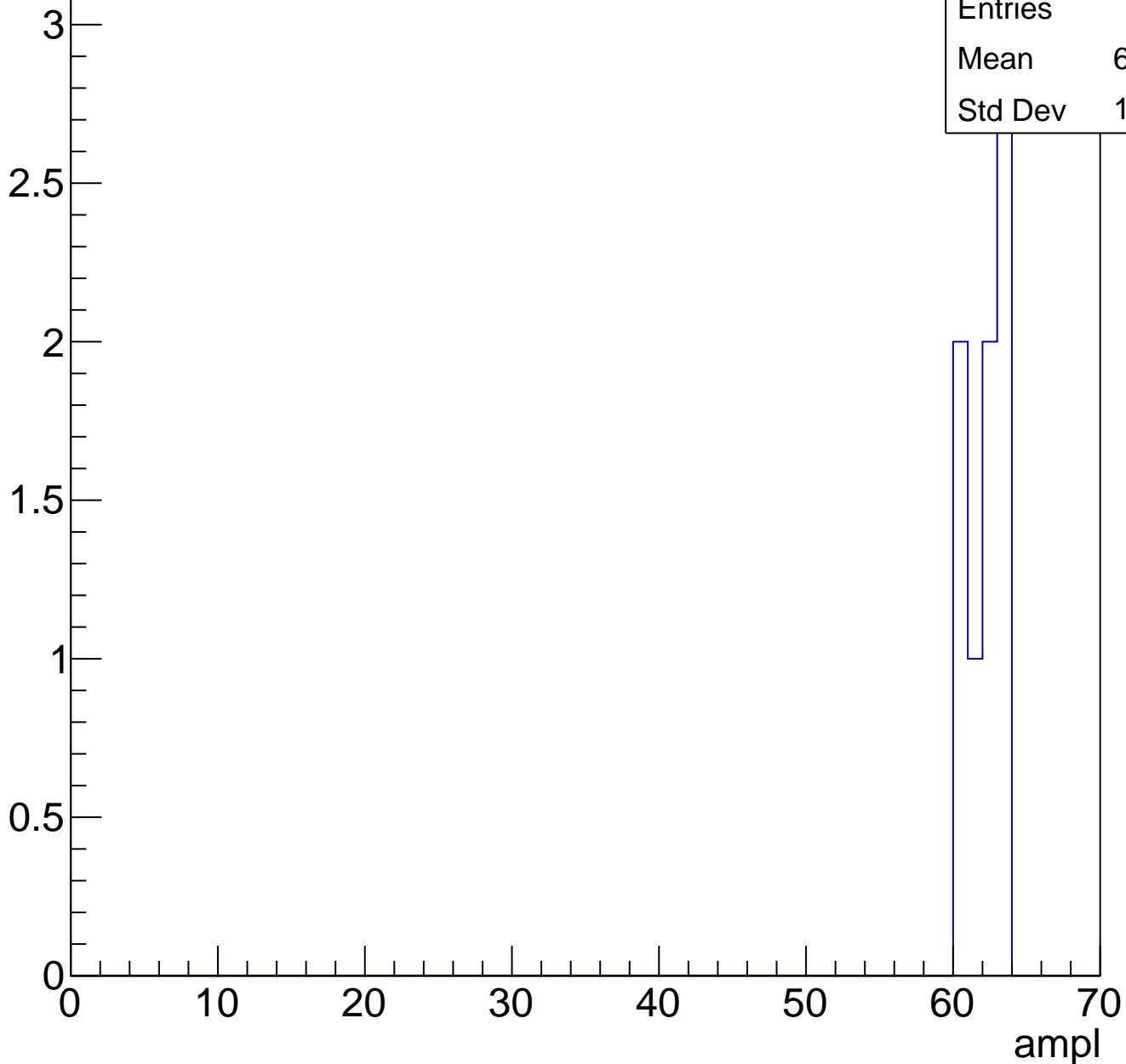
Entries	47
Mean	58.47
Std Dev	9.039



# B1L003S, U22-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch3, adc0

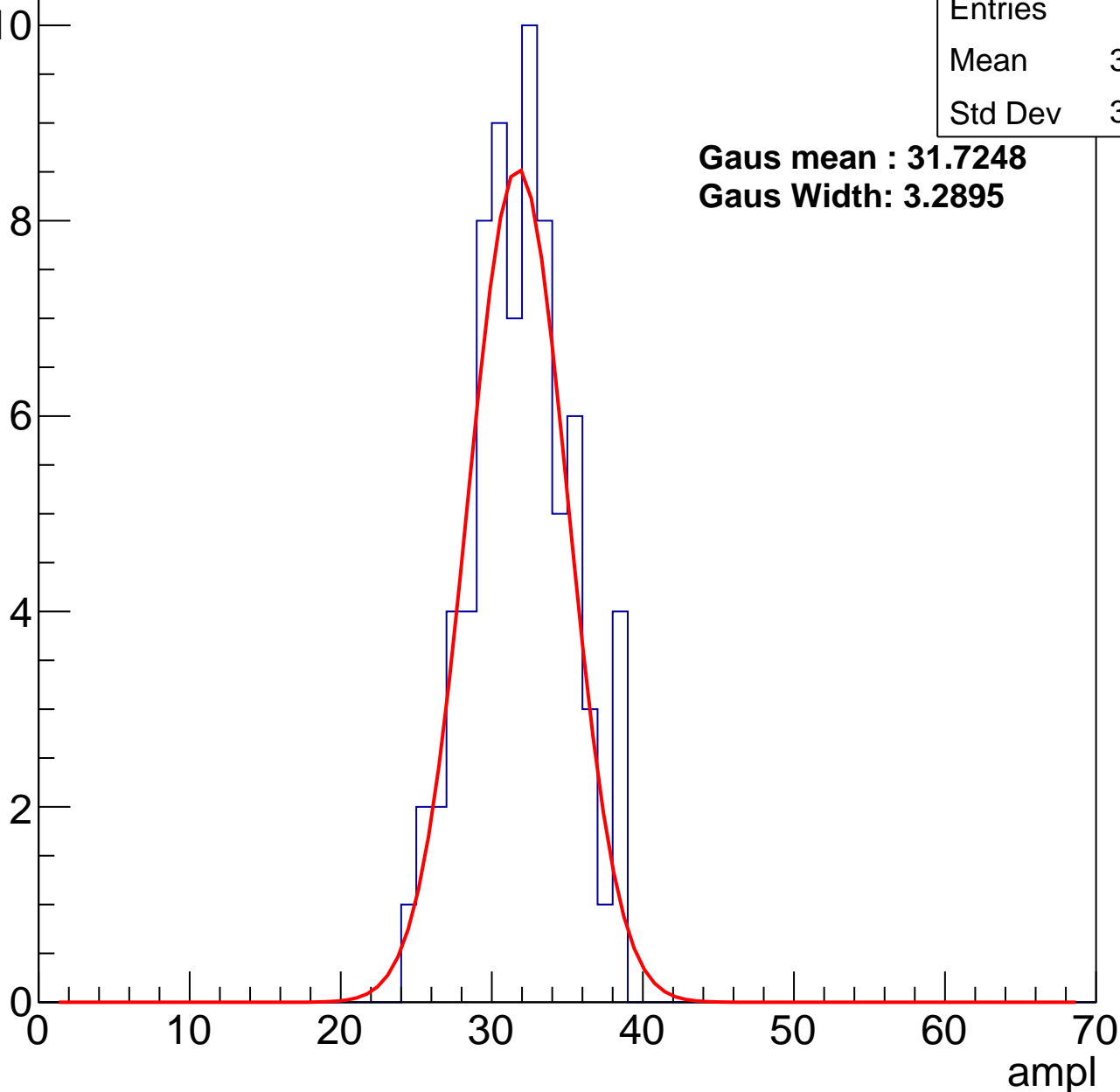
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	31.43
Std Dev	3.264

**Gaus mean : 31.7248**

**Gaus Width: 3.2895**



# B1L003S, U22-ch3, adc1

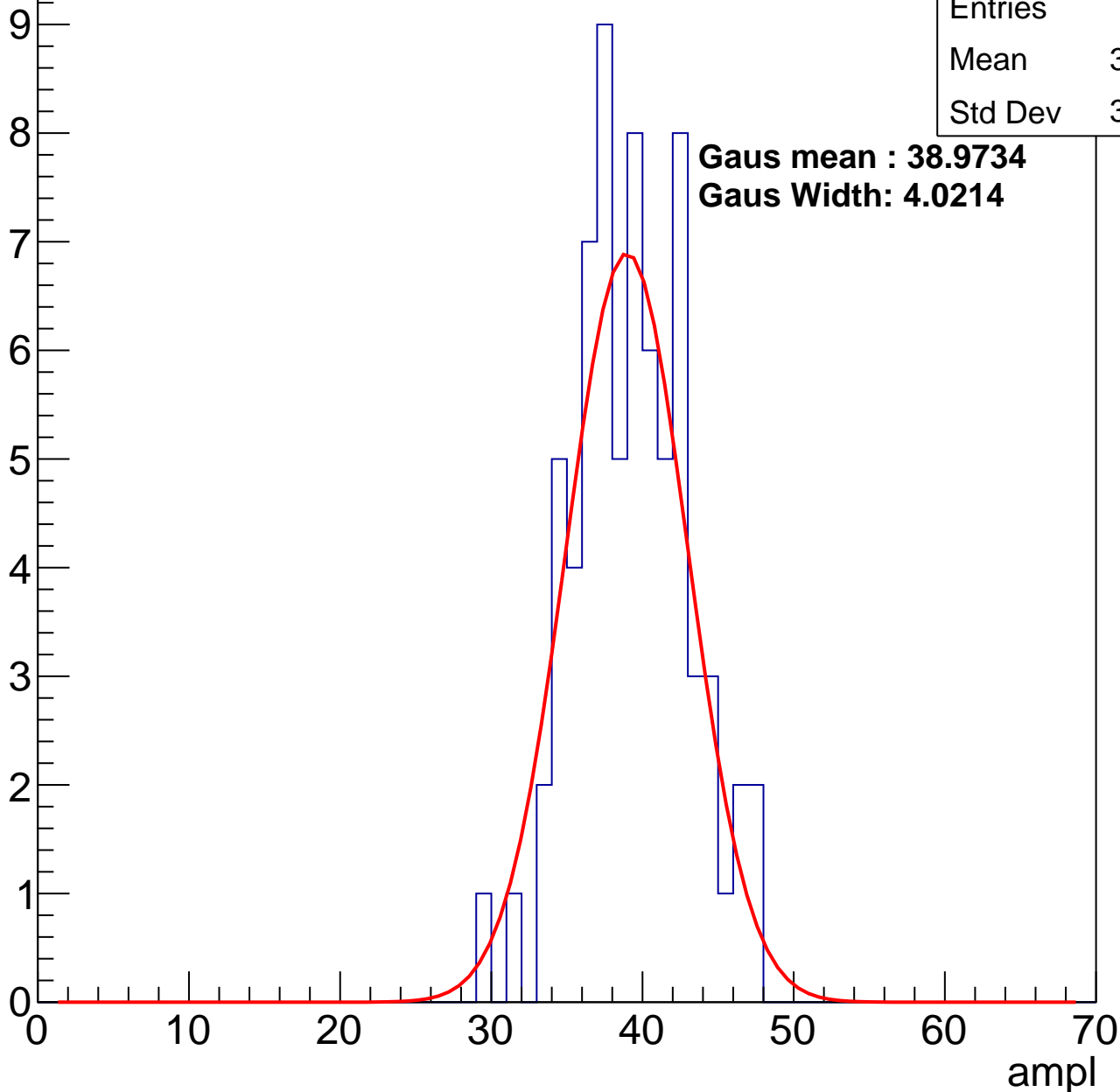
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	38.83
Std Dev	3.745

**Gaus mean : 38.9734**

**Gaus Width: 4.0214**



# B1L003S, U22-ch3, adc2

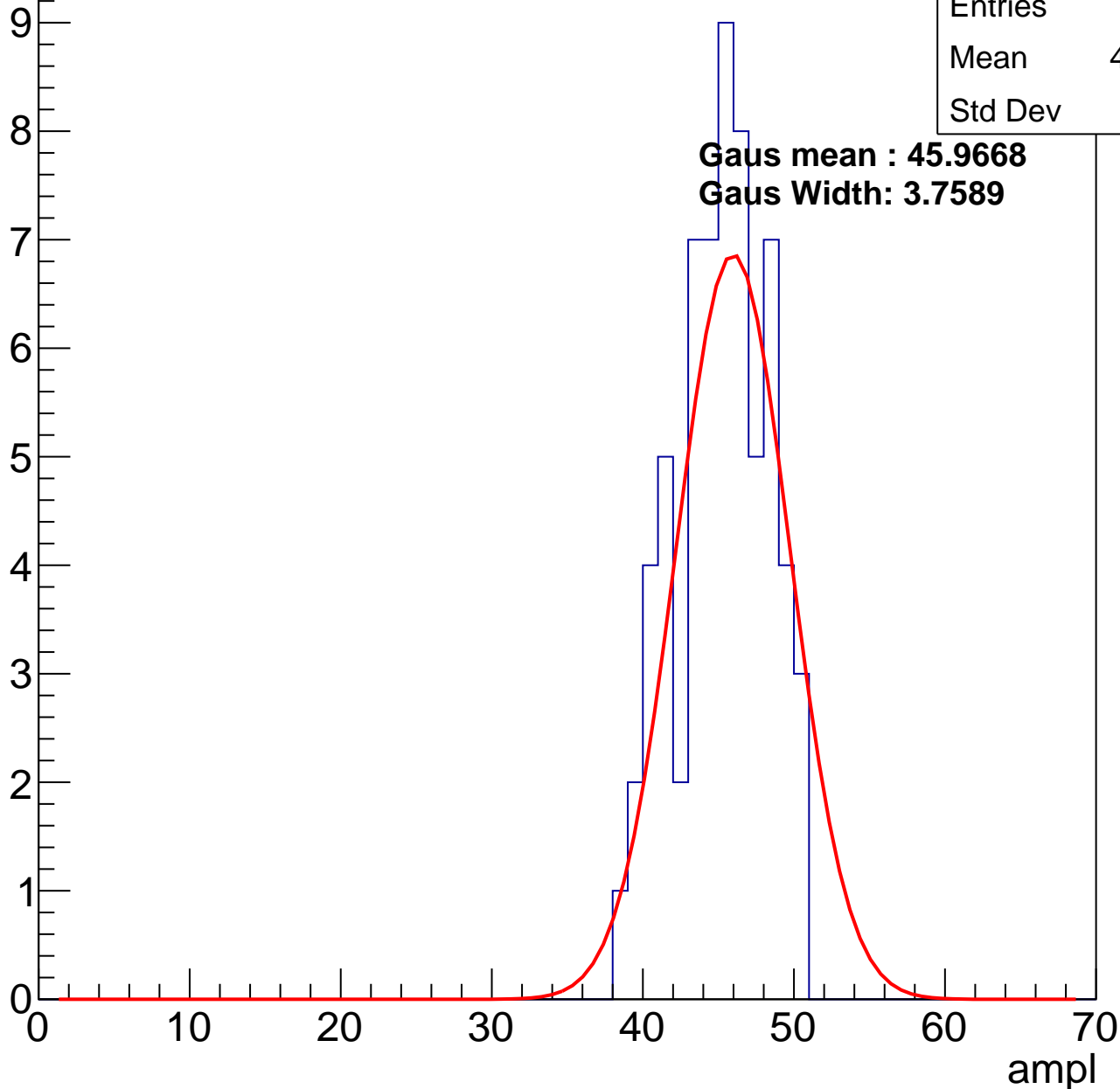
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	44.75
Std Dev	3.01

**Gaus mean : 45.9668**

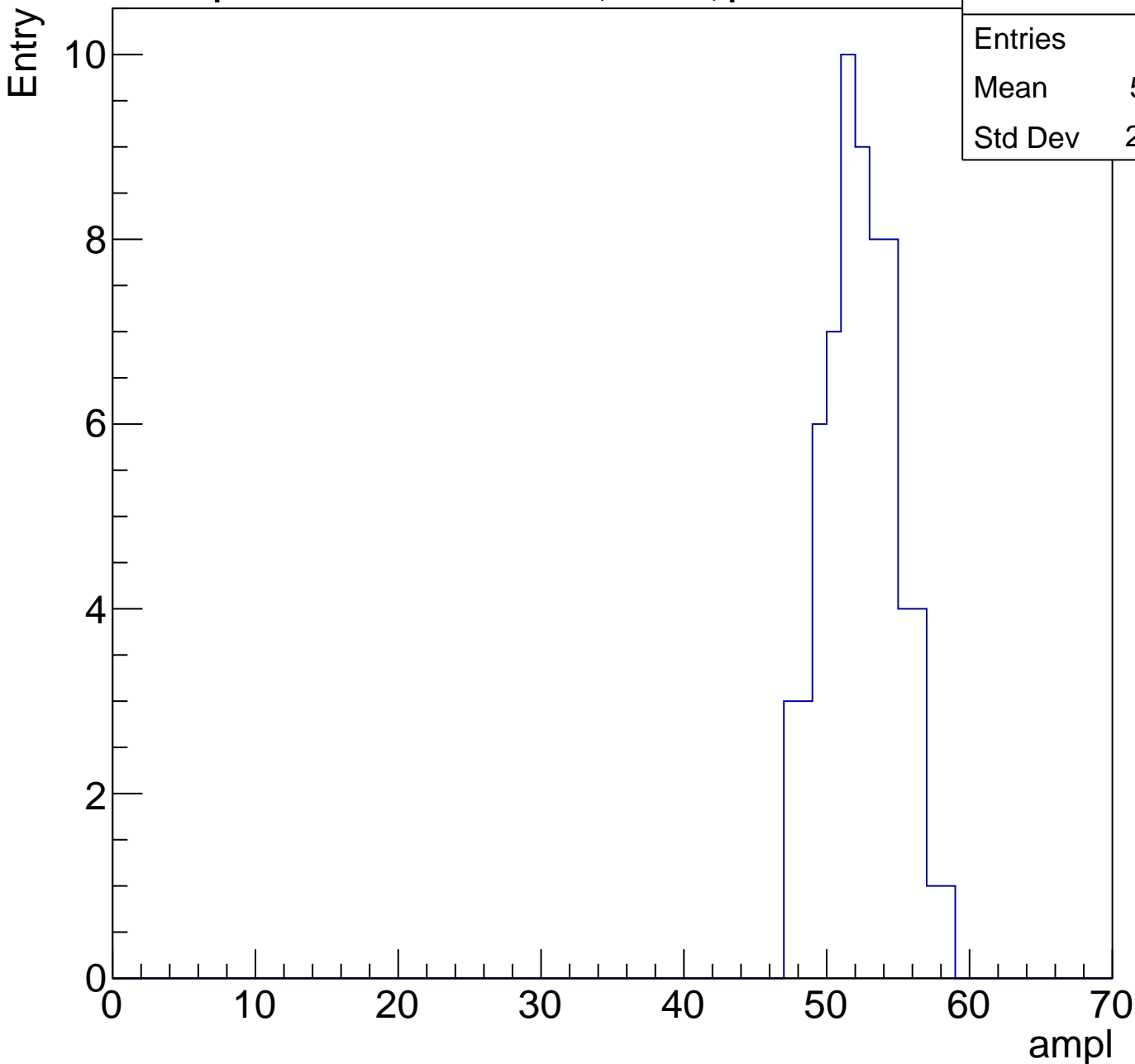
**Gaus Width: 3.7589**



# B1L003S, U22-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	64
Mean	51.91
Std Dev	2.548

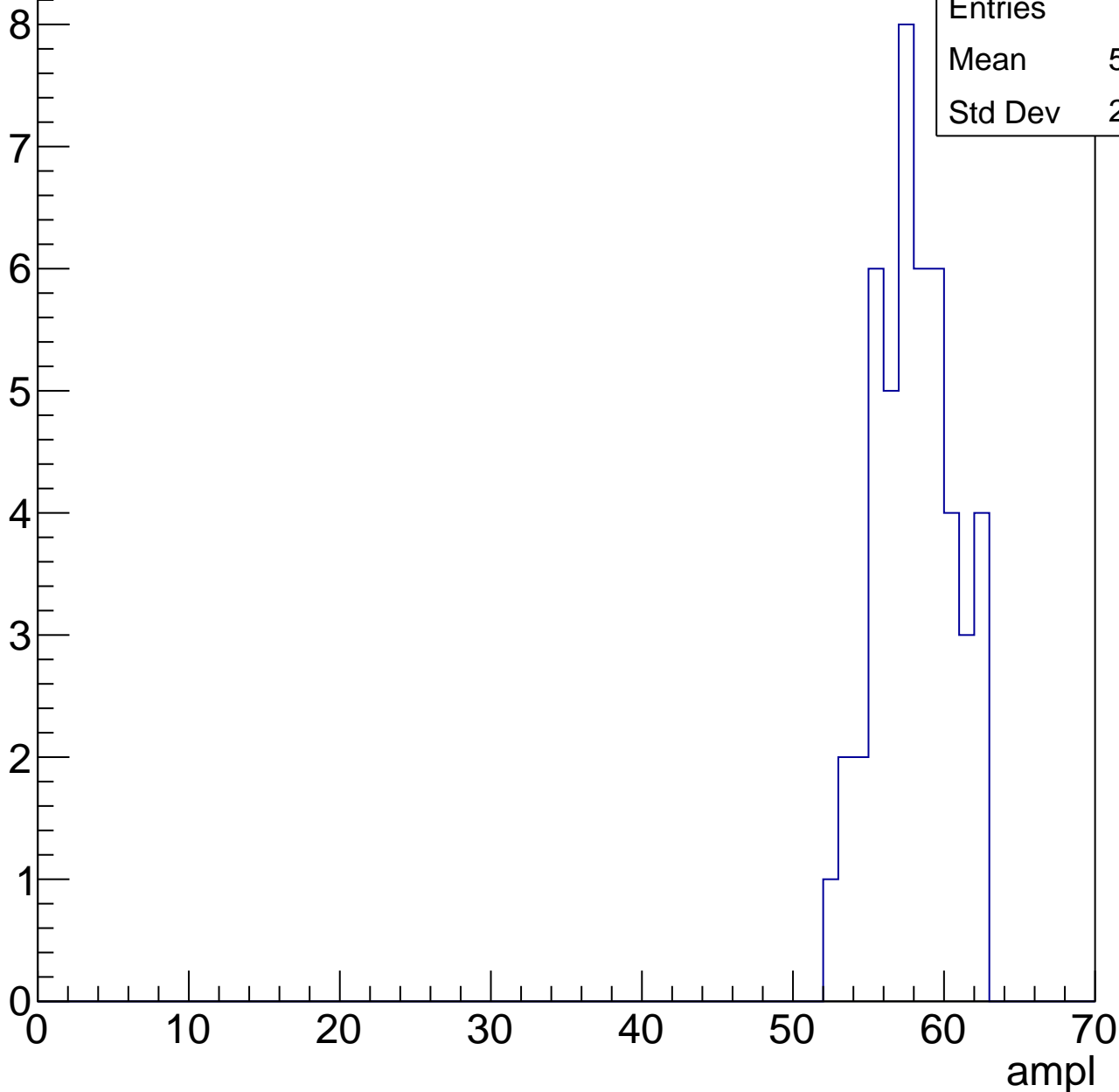


# B1L003S, U22-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	57.55
Std Dev	2.542



# B1L003S, U22-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

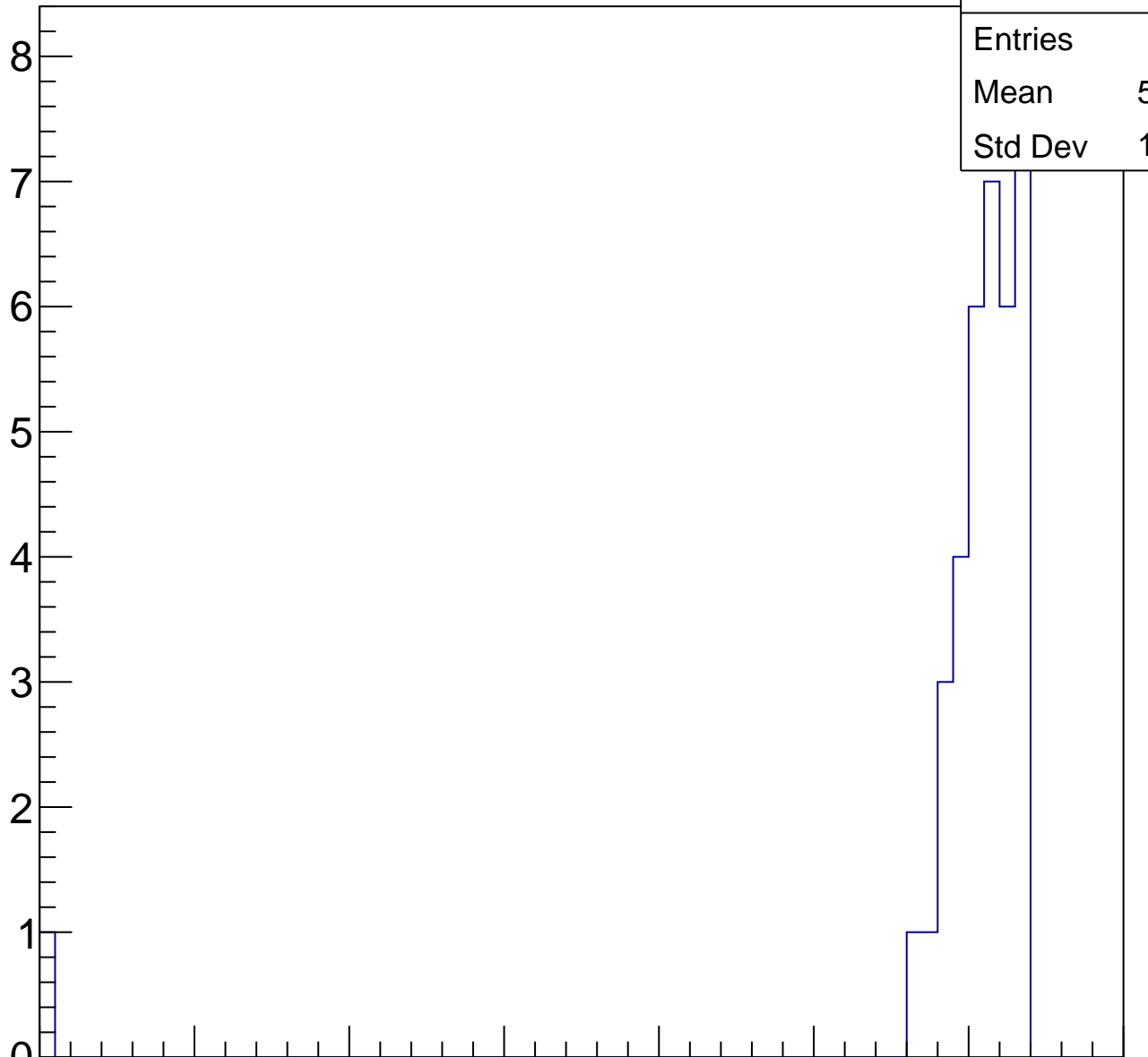
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	37
Mean	59.08
Std Dev	10.02

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch4, adc0

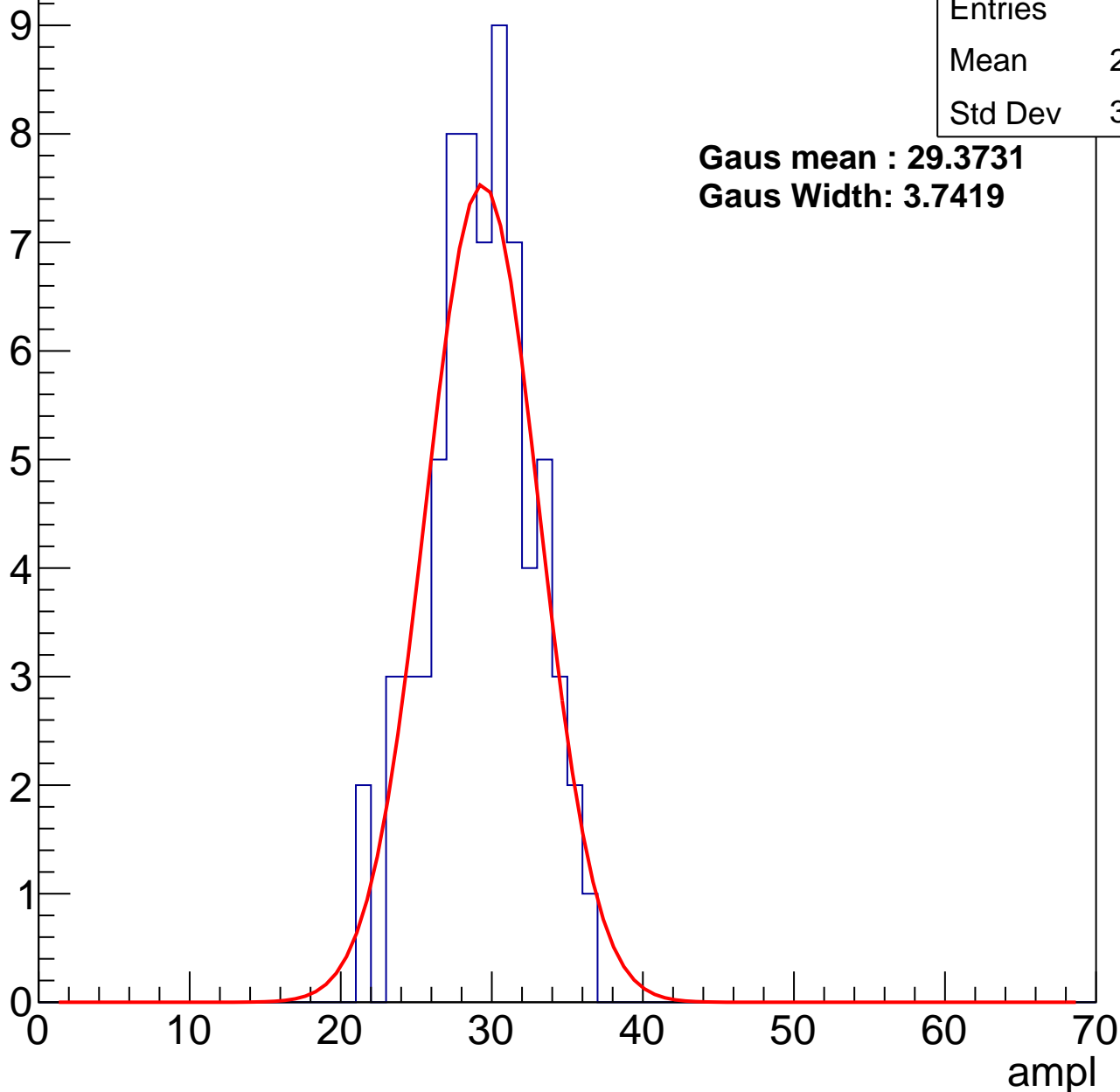
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.84
Std Dev	3.362

**Gaus mean : 29.3731**

**Gaus Width: 3.7419**



# B1L003S, U22-ch4, adc1

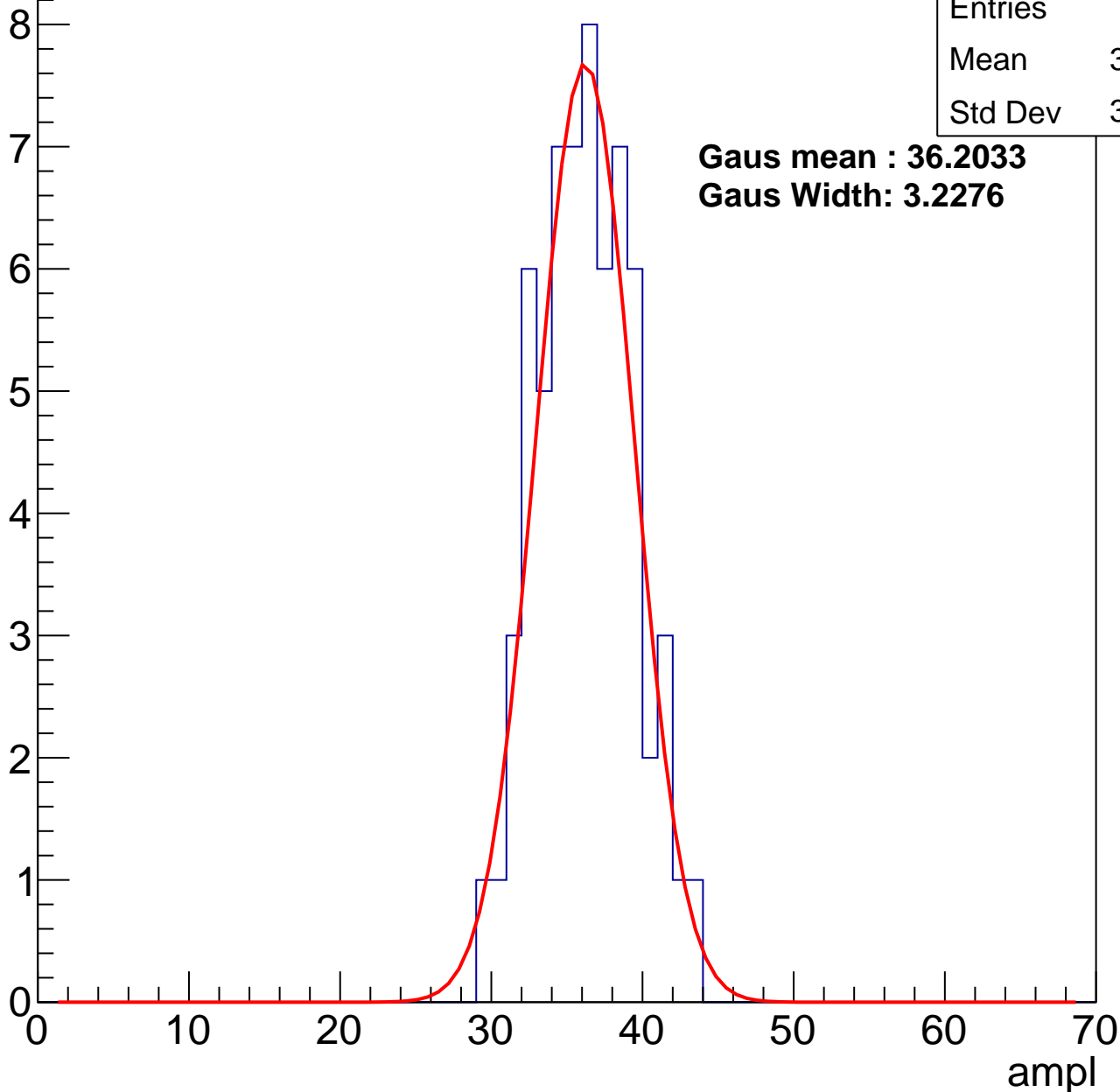
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.78
Std Dev	3.095

**Gaus mean : 36.2033**

**Gaus Width: 3.2276**



# B1L003S, U22-ch4, adc2

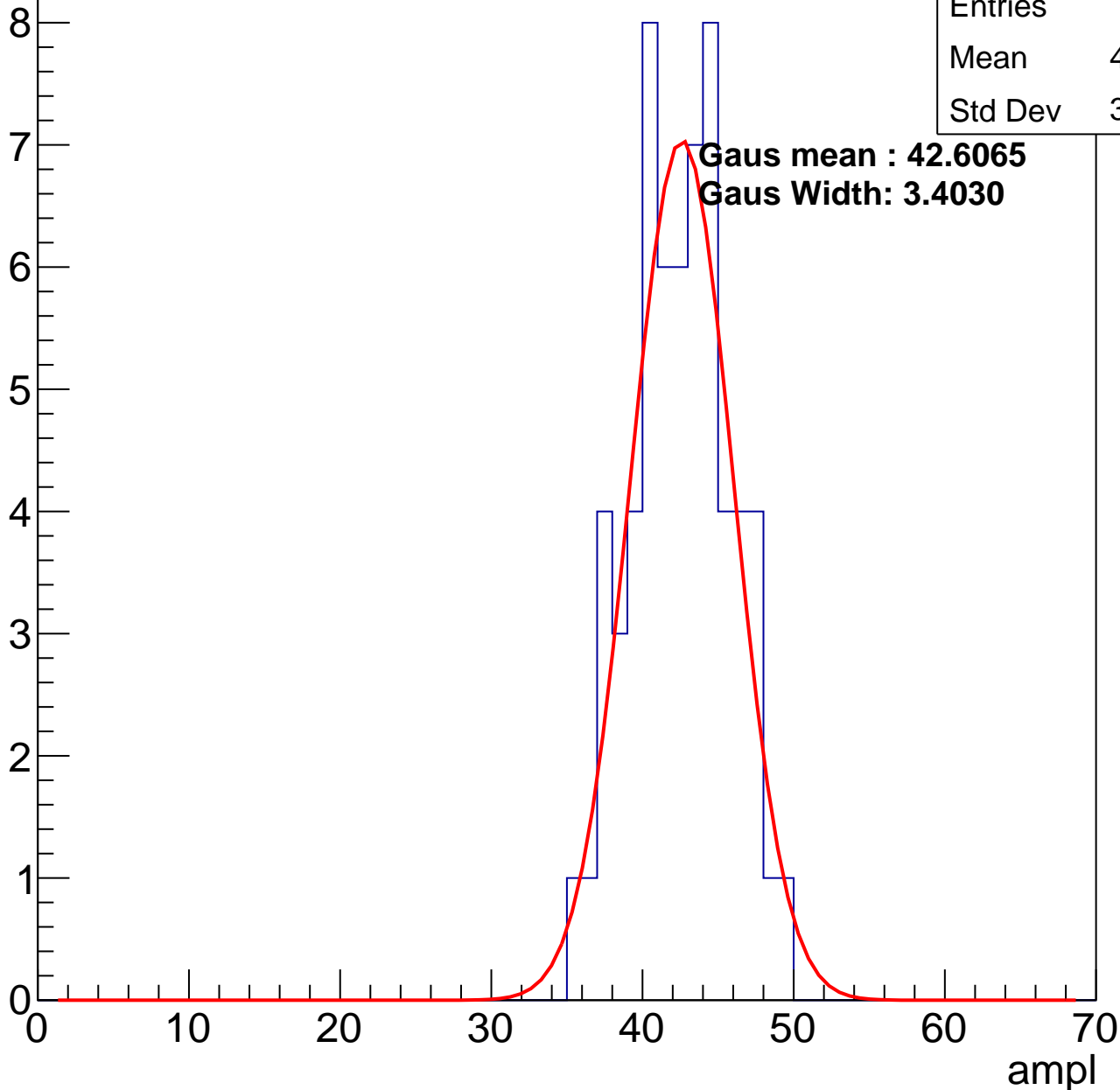
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.08
Std Dev	3.189

**Gaus mean : 42.6065**

**Gaus Width: 3.4030**



# B1L003S, U22-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

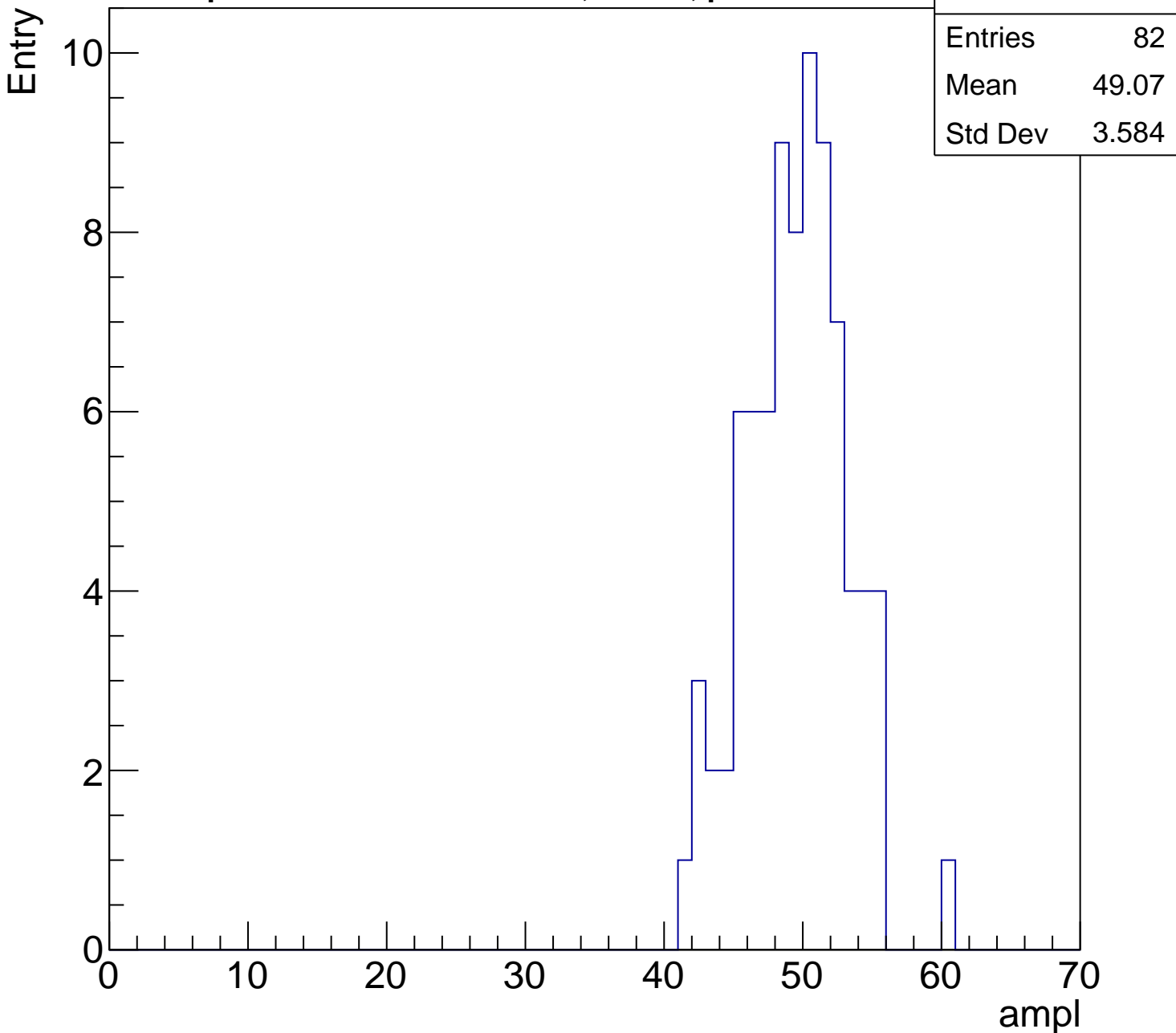
Entries	82
Mean	49.07
Std Dev	3.584

Entry

10  
8  
6  
4  
2  
0

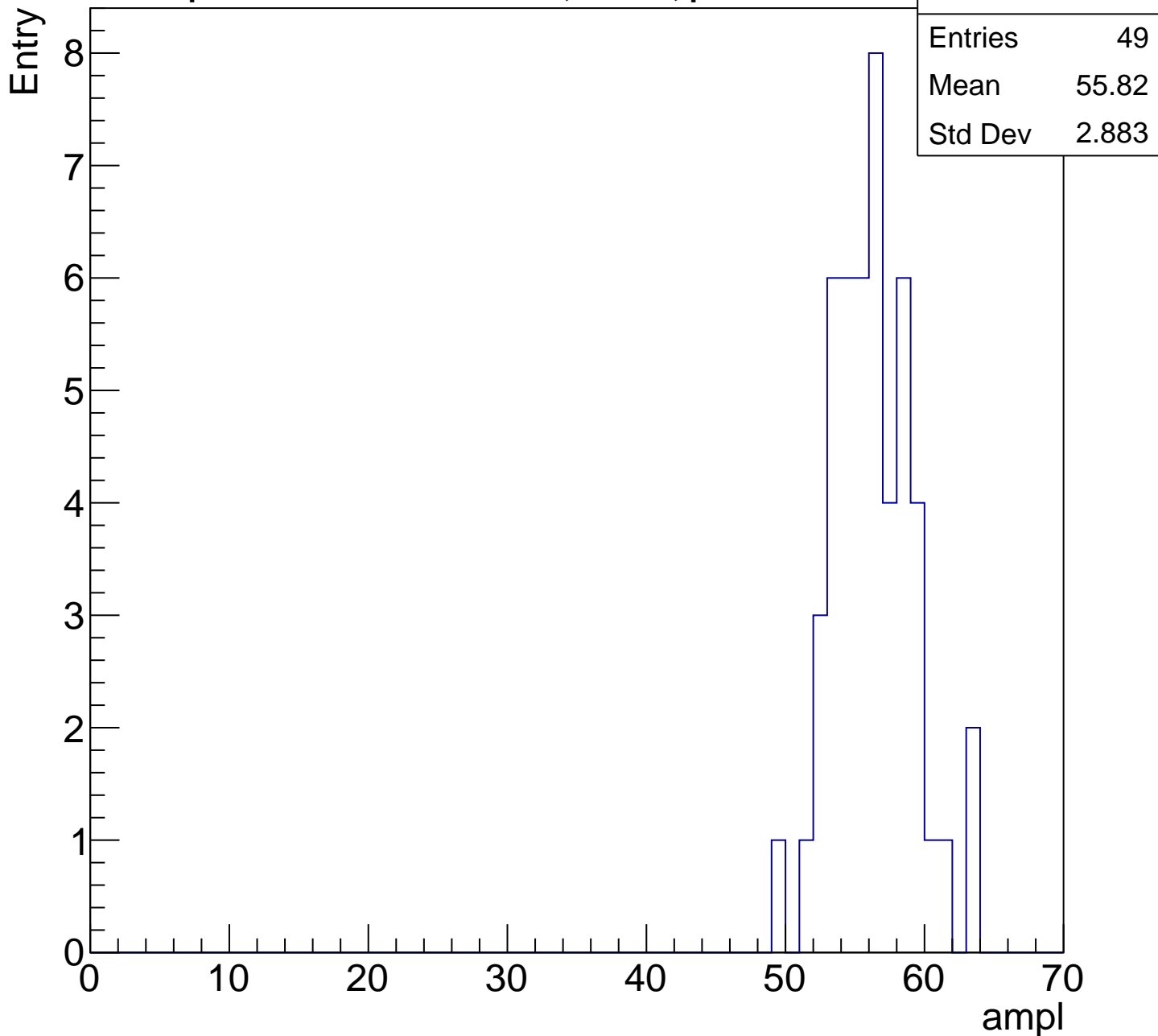
0 10 20 30 40 50 60 70

ampl



# B1L003S, U22-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U22-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

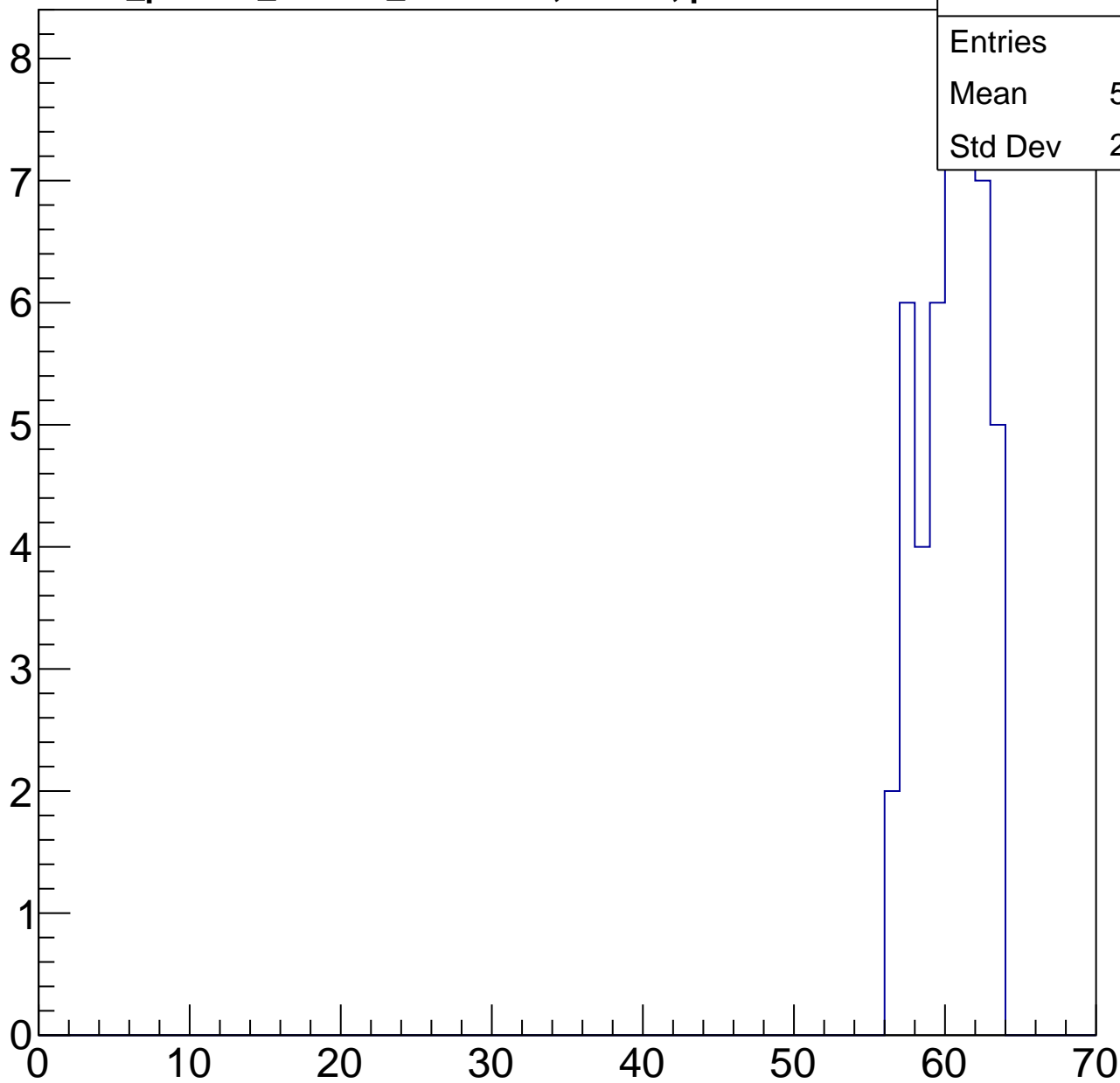
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.93
Std Dev	2.026

ampl

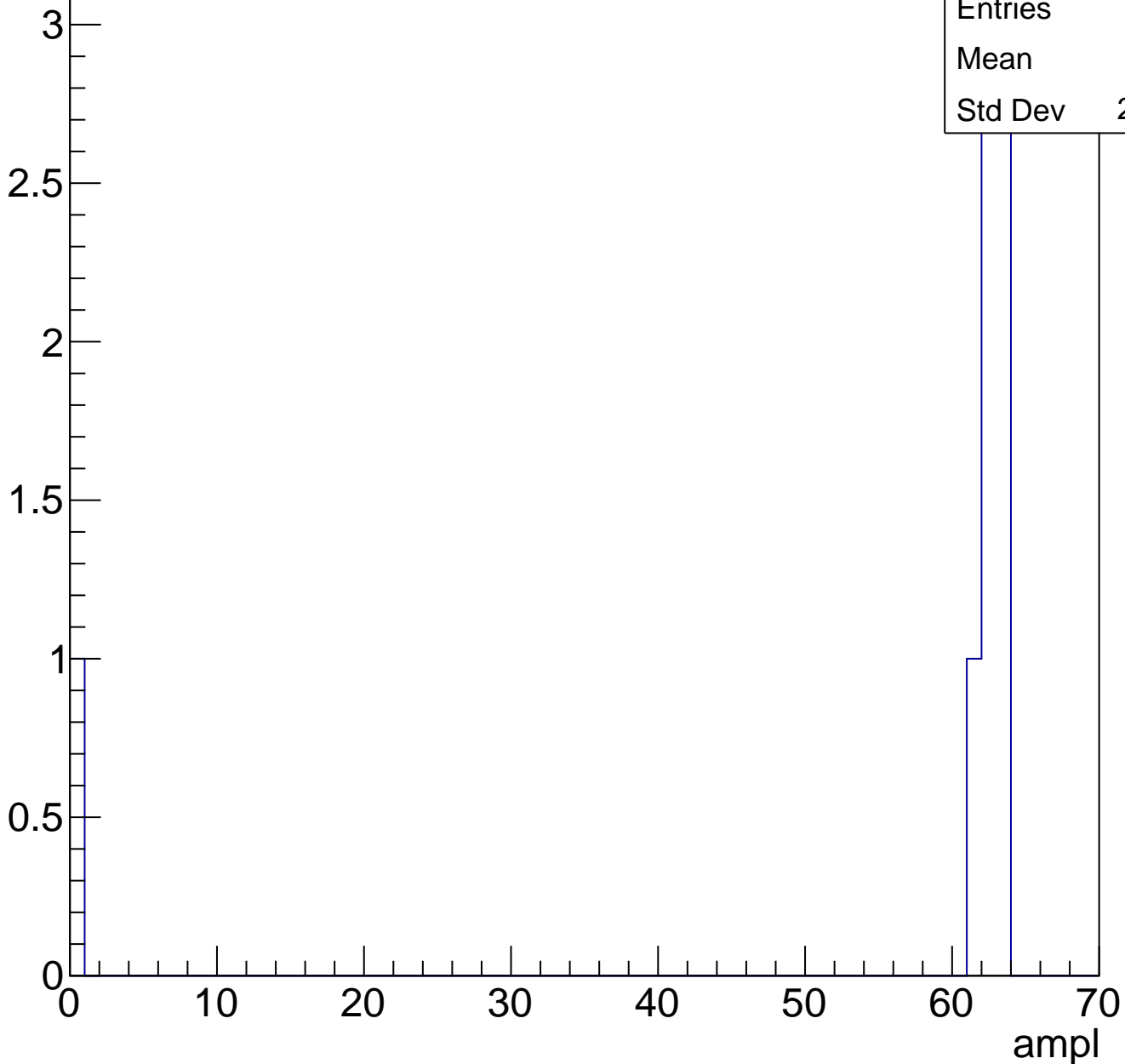
0 10 20 30 40 50 60 70



# B1L003S, U22-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch5, adc0

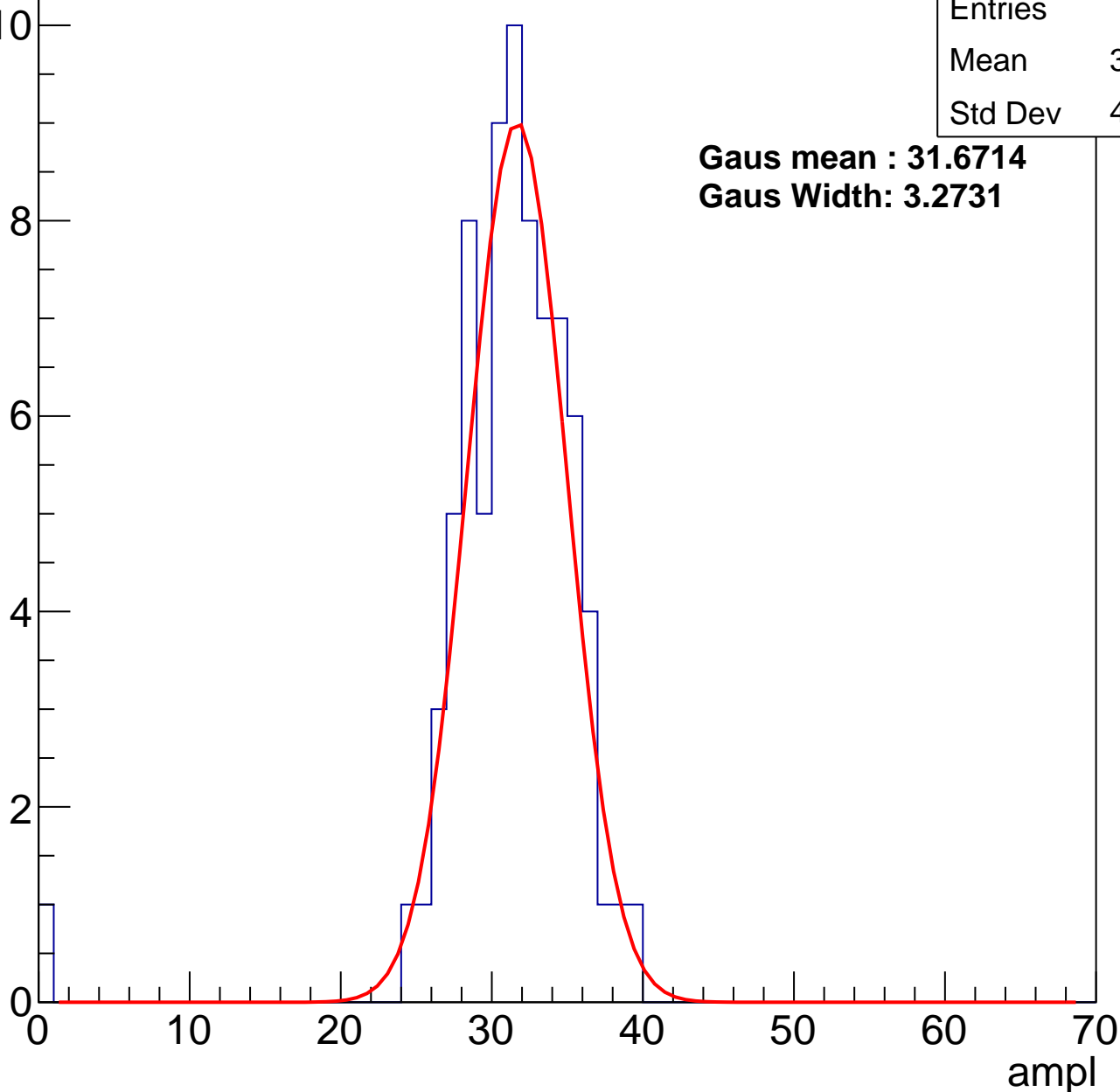
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	30.82
Std Dev	4.728

**Gaus mean : 31.6714**

**Gaus Width: 3.2731**



# B1L003S, U22-ch5, adc1

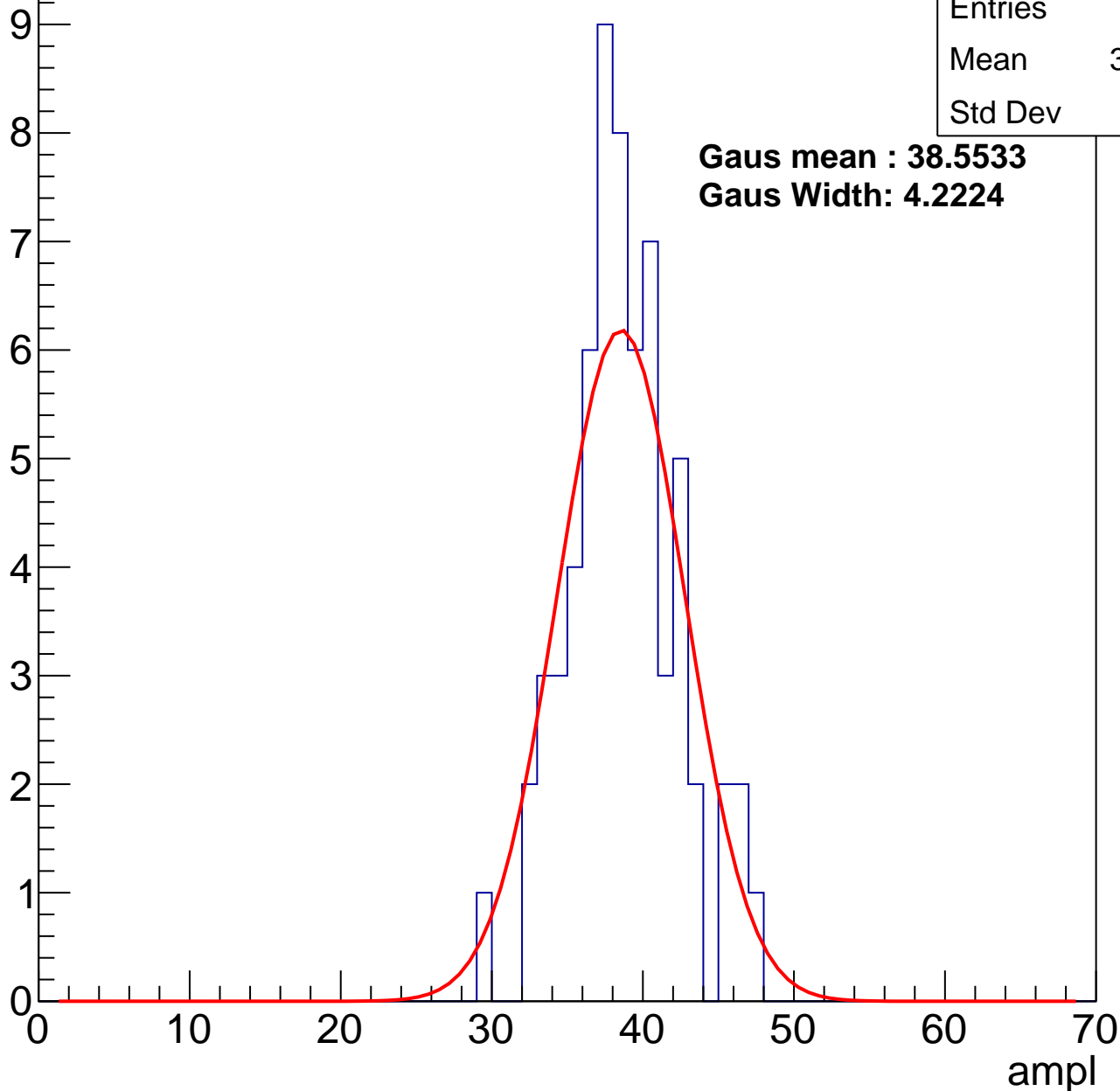
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	38.27
Std Dev	3.62

**Gaus mean : 38.5533**

**Gaus Width: 4.2224**



# B1L003S, U22-ch5, adc2

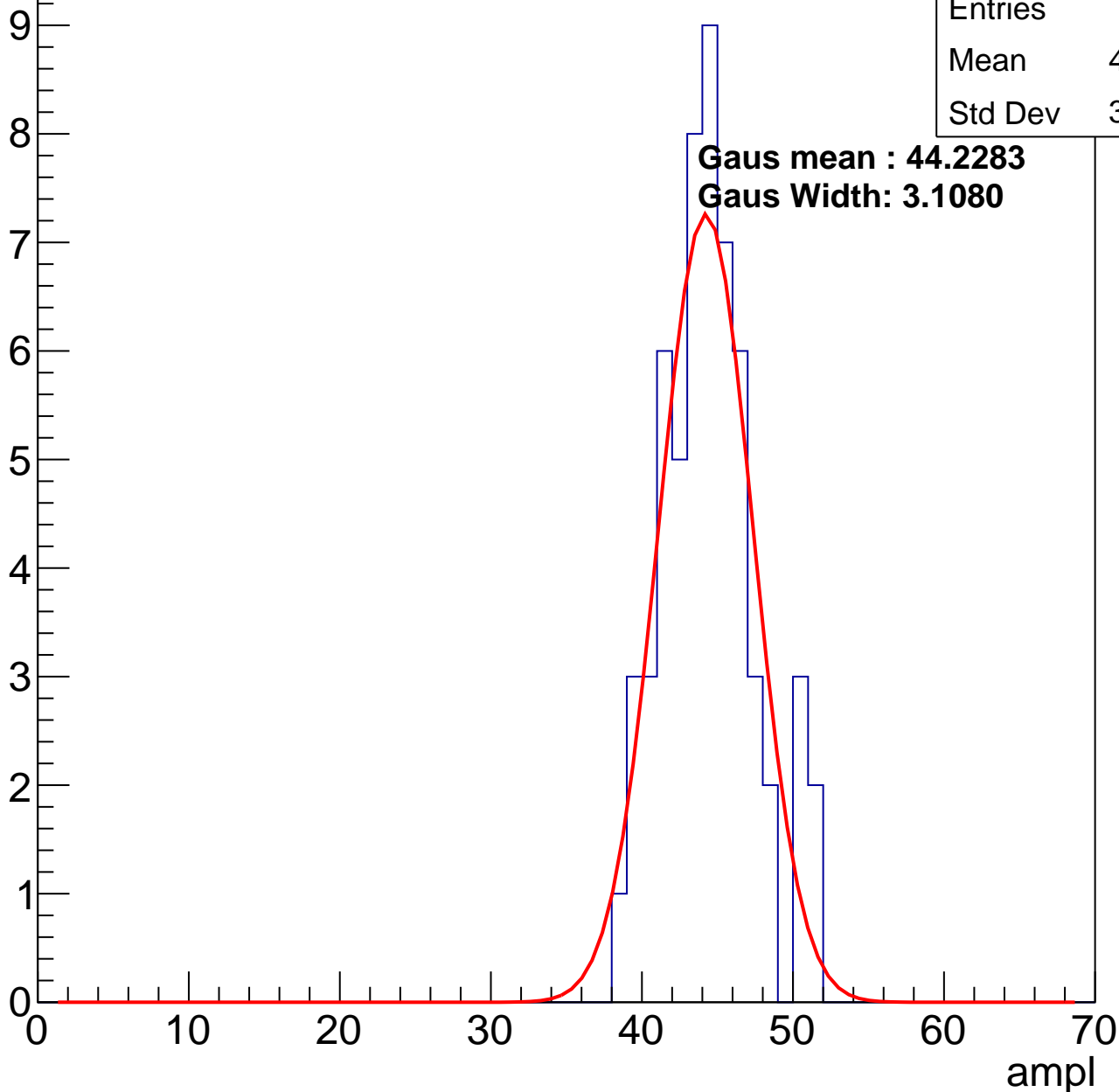
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.98
Std Dev	3.043

**Gaus mean : 44.2283**

**Gaus Width: 3.1080**

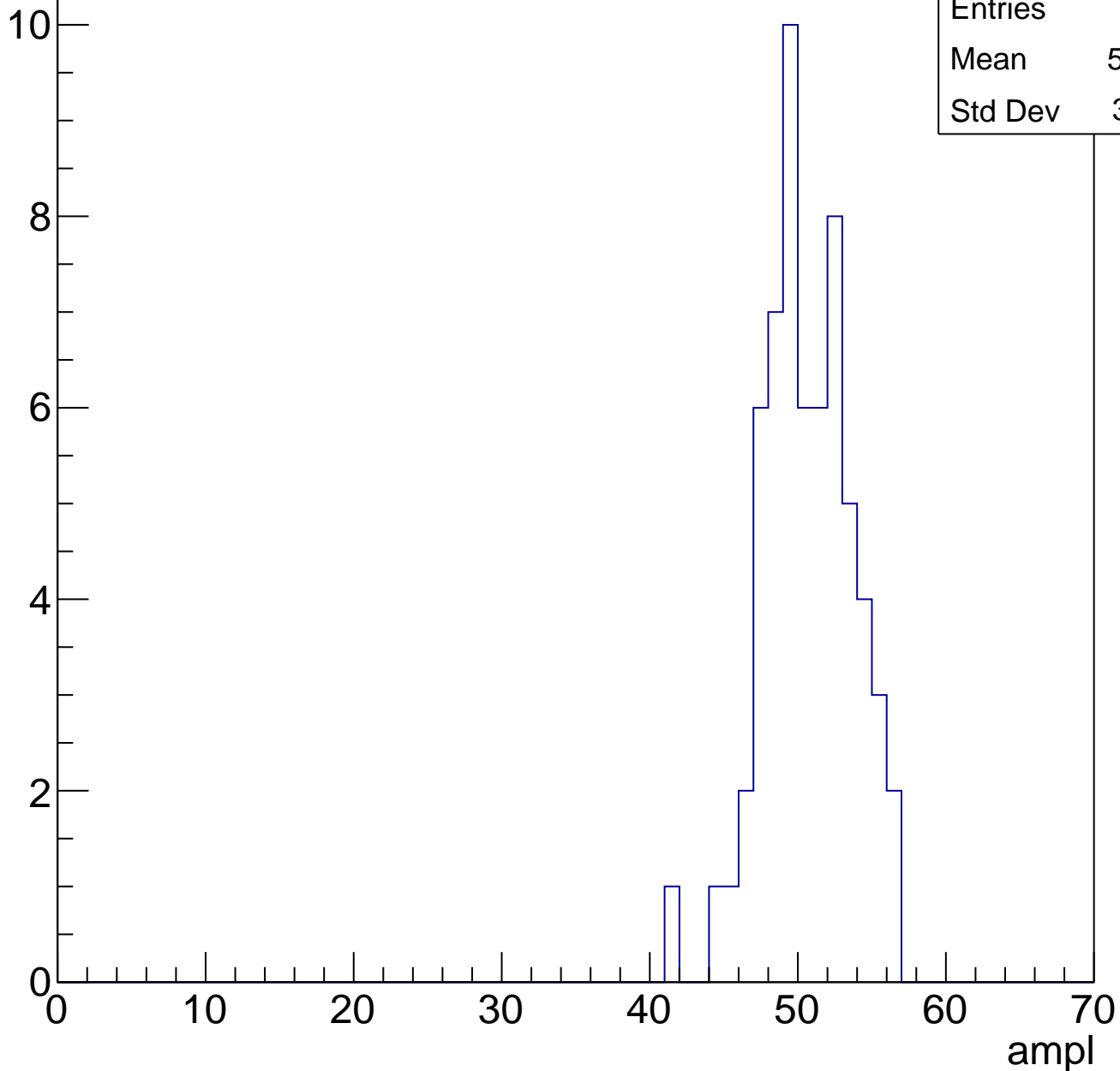


# B1L003S, U22-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	62
Mean	50.16
Std Dev	3.001

Entry

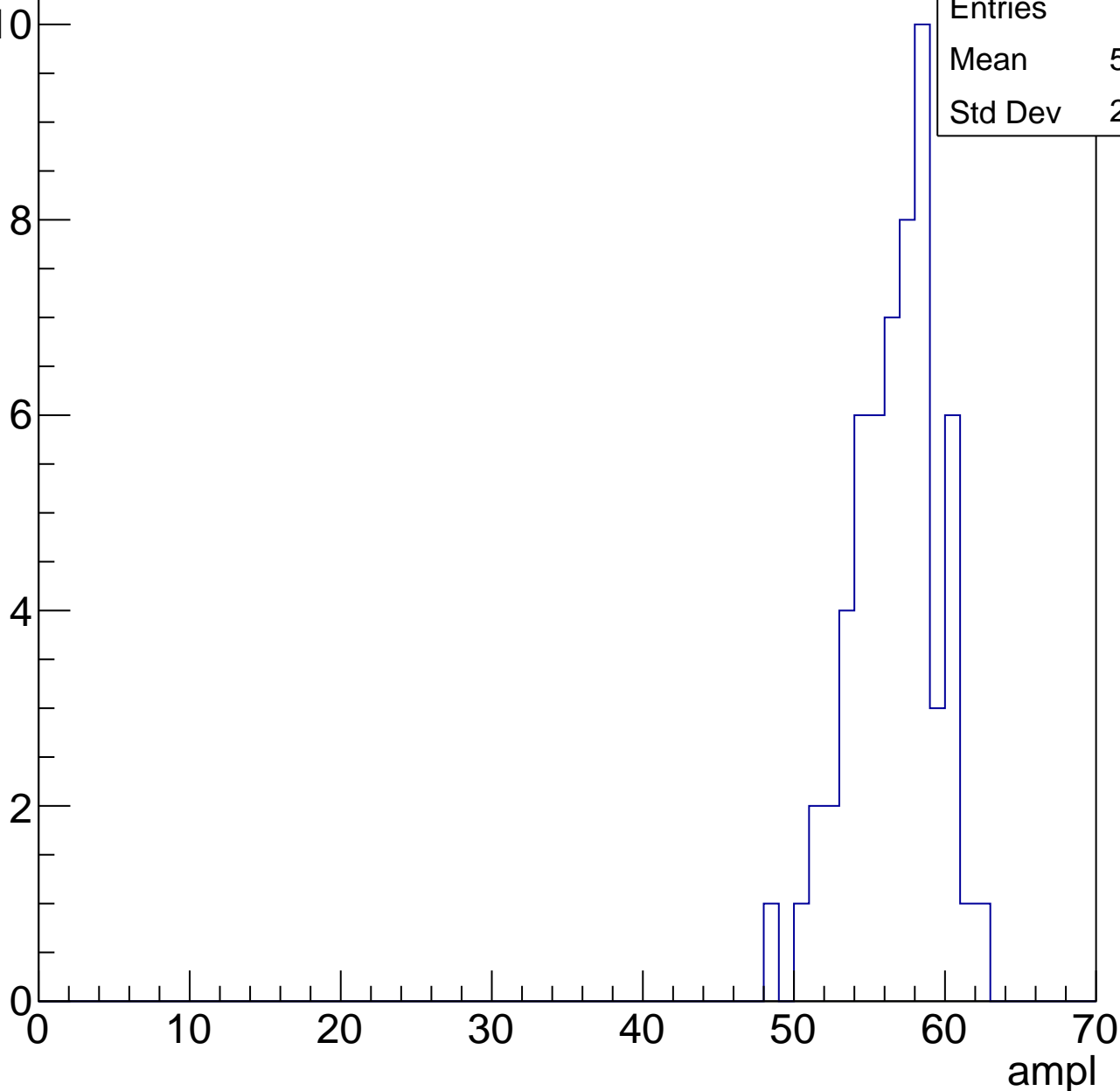


# B1L003S, U22-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	56.17
Std Dev	2.872

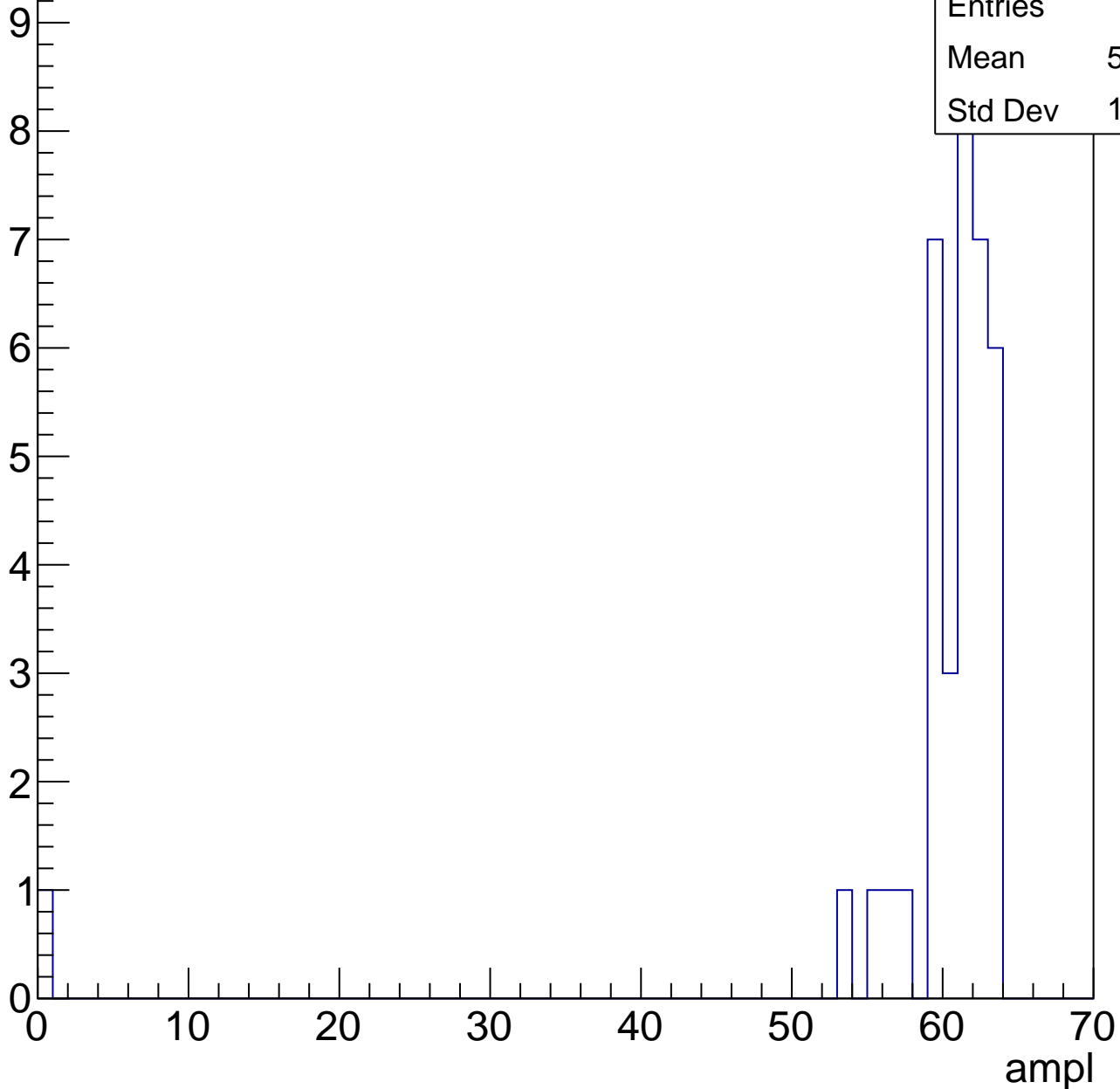


# B1L003S, U22-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

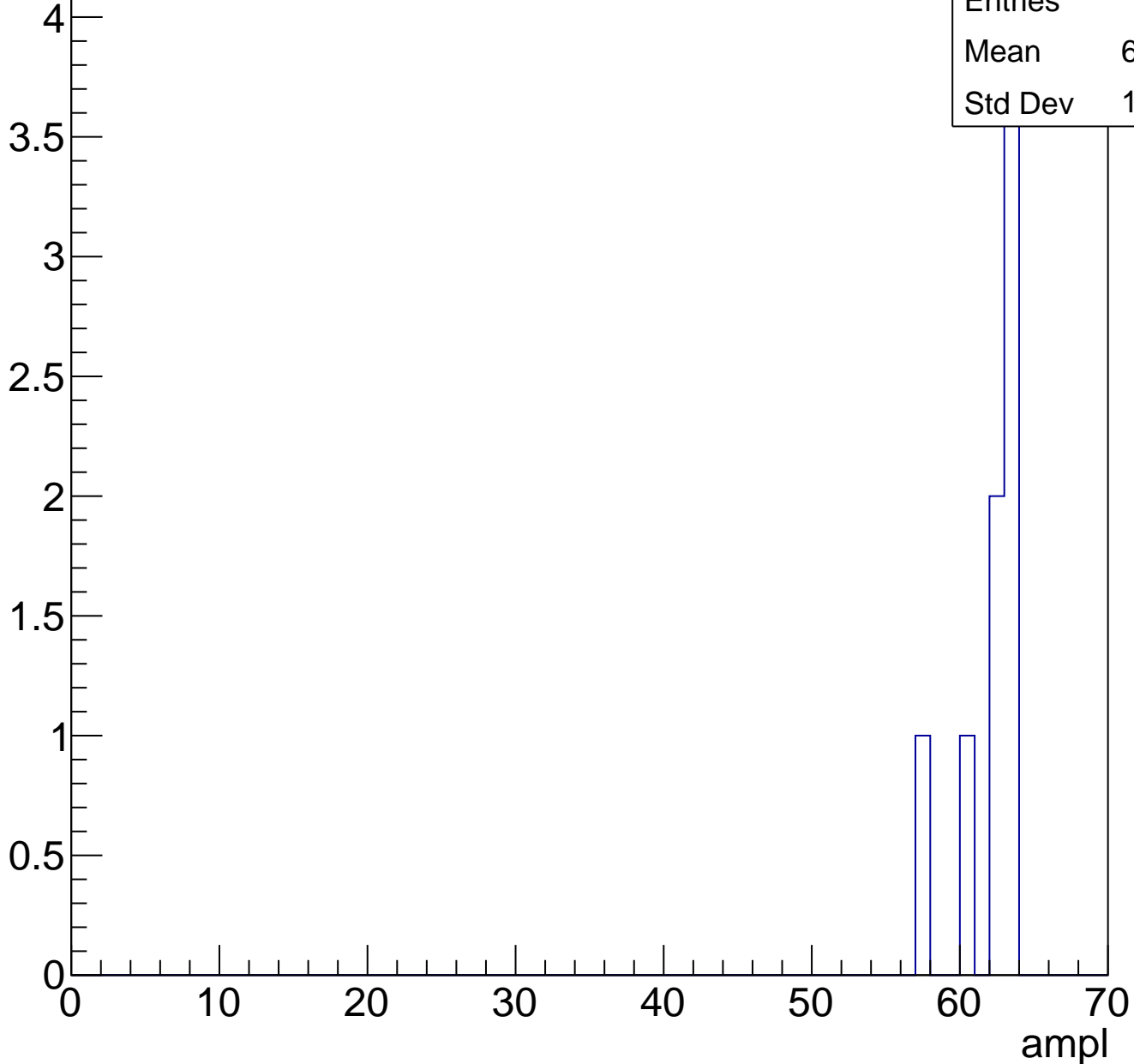
Entries	37
Mean	58.78
Std Dev	10.06



# B1L003S, U22-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	8
Mean	61.62
Std Dev	1.996



# B1L003S, U22-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch6, adc0

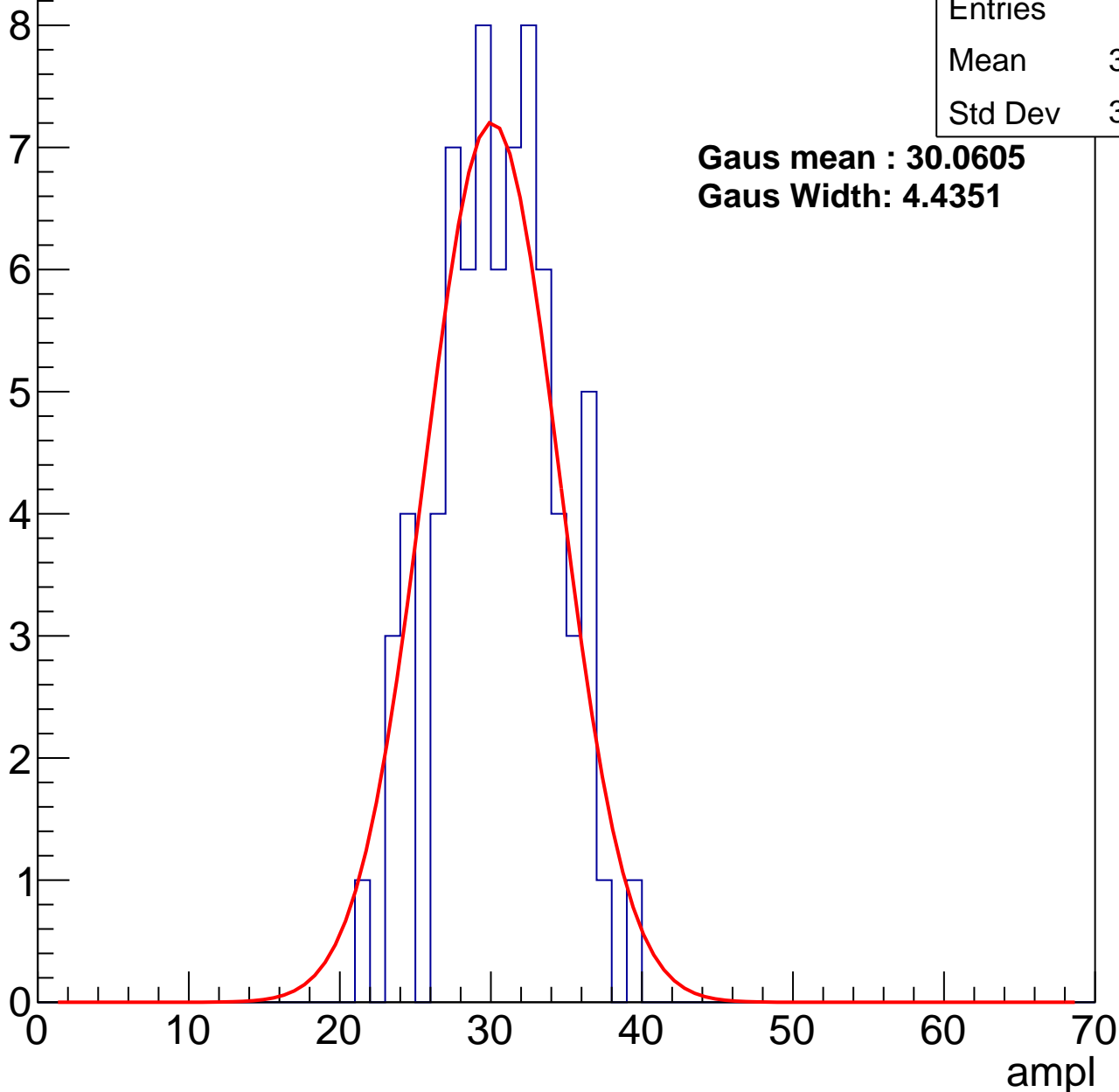
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	30.09
Std Dev	3.807

**Gaus mean : 30.0605**

**Gaus Width: 4.4351**



# B1L003S, U22-ch6, adc1

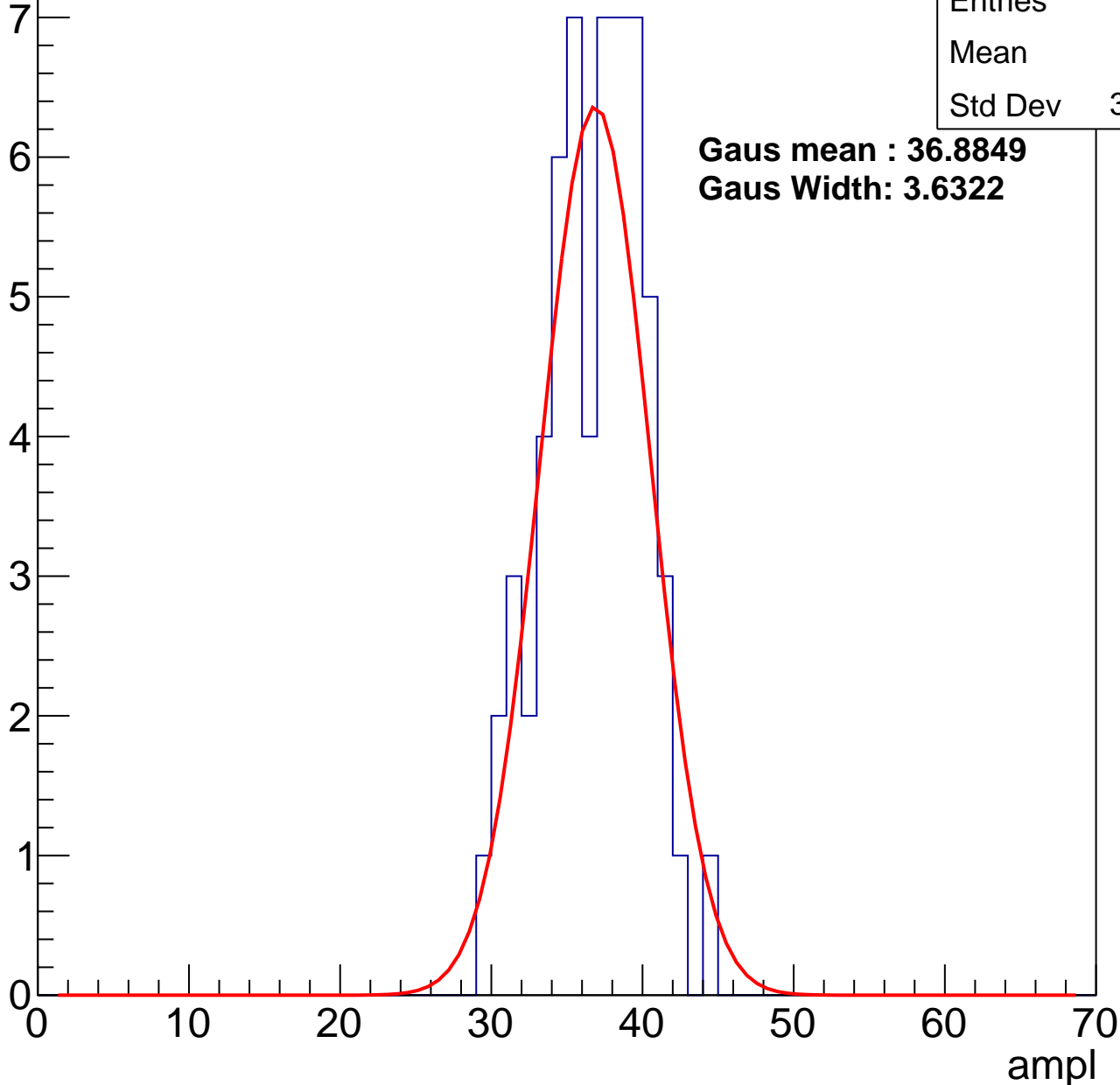
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.3
Std Dev	3.273

**Gaus mean : 36.8849**

**Gaus Width: 3.6322**



# B1L003S, U22-ch6, adc2

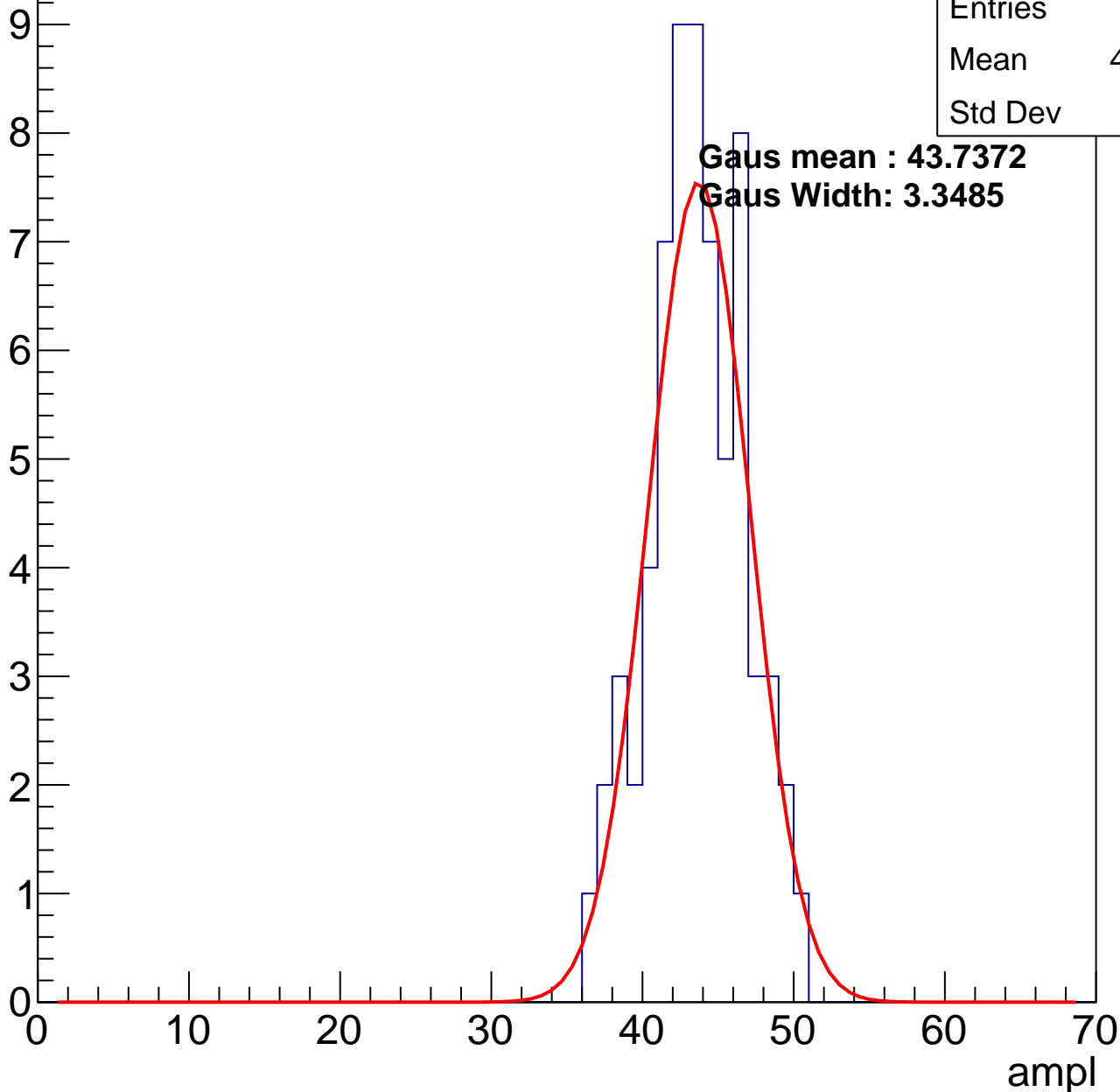
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	43.15
Std Dev	3.12

**Gaus mean : 43.7372**

**Gaus Width: 3.3485**

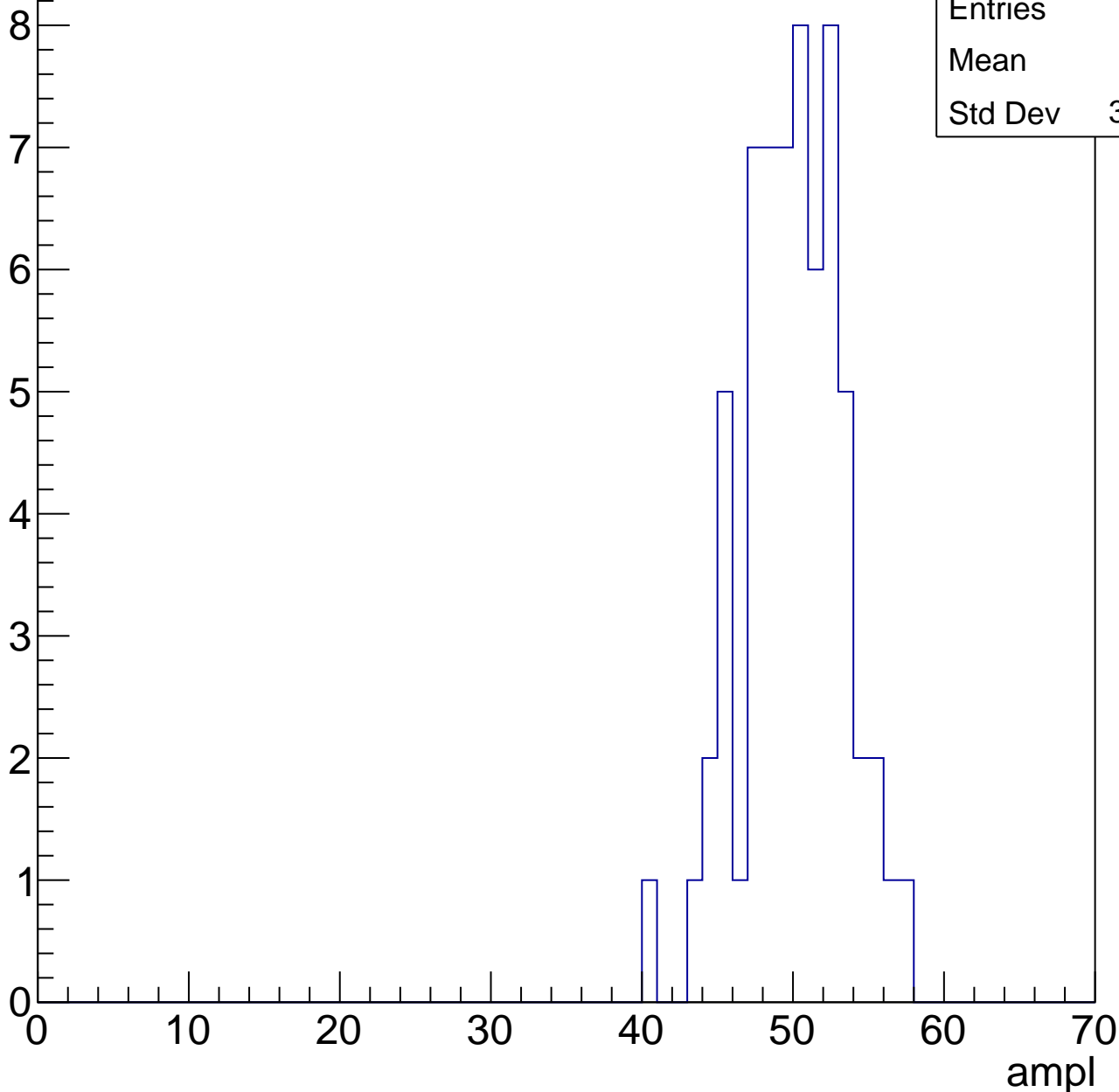


# B1L003S, U22-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.5
Std Dev	3.293

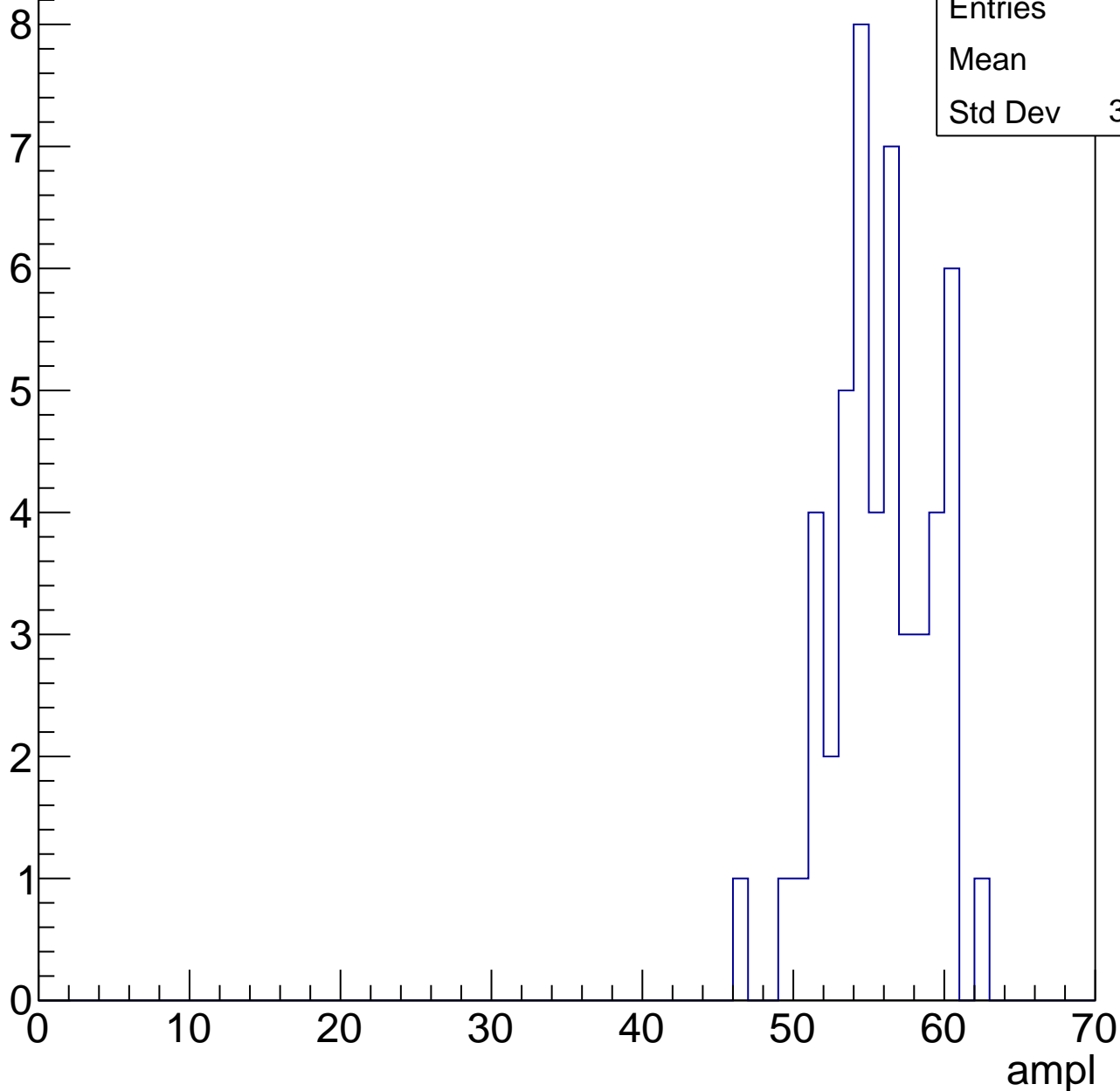


# B1L003S, U22-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.3
Std Dev	3.336

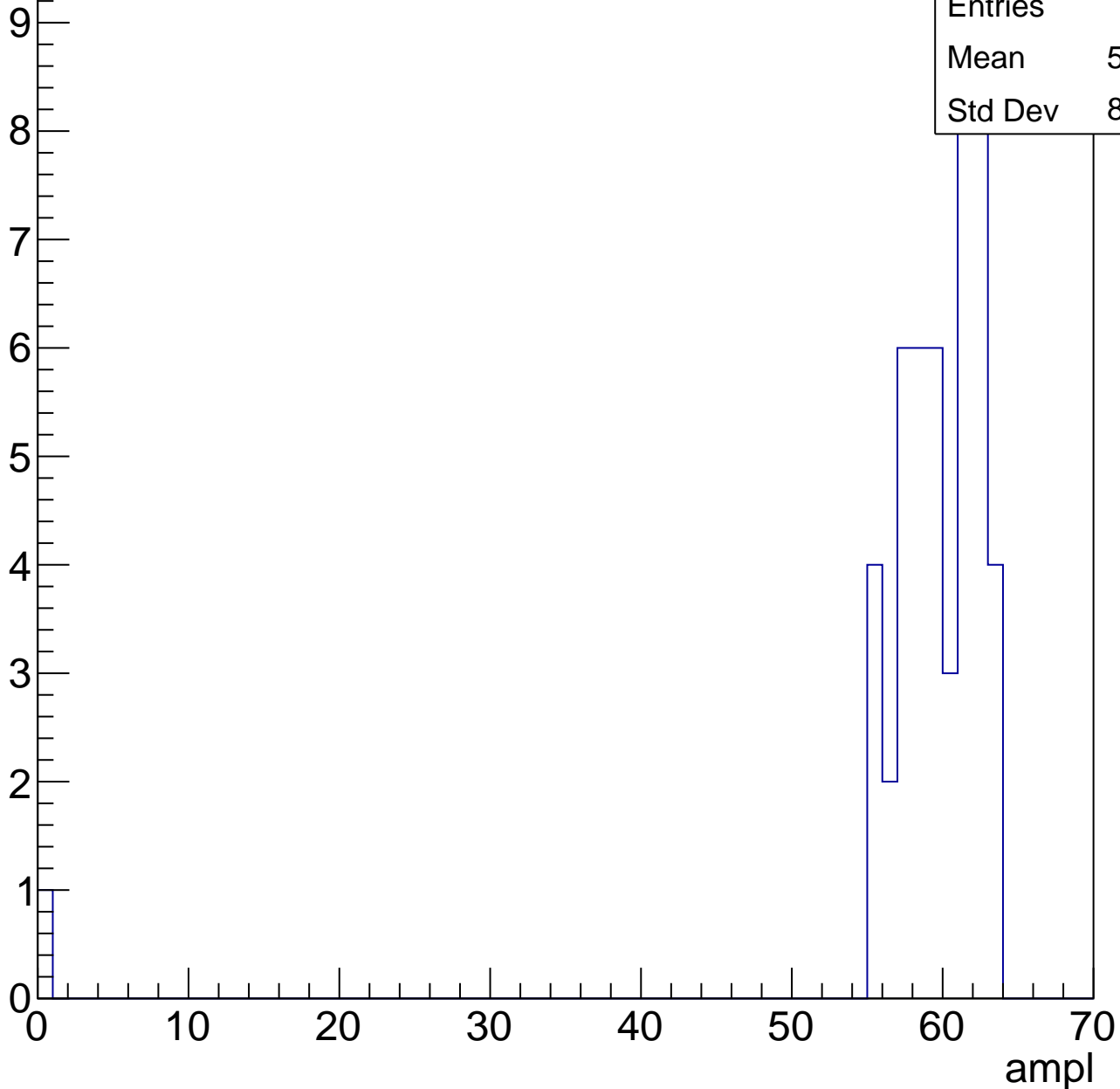


# B1L003S, U22-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.22
Std Dev	8.735

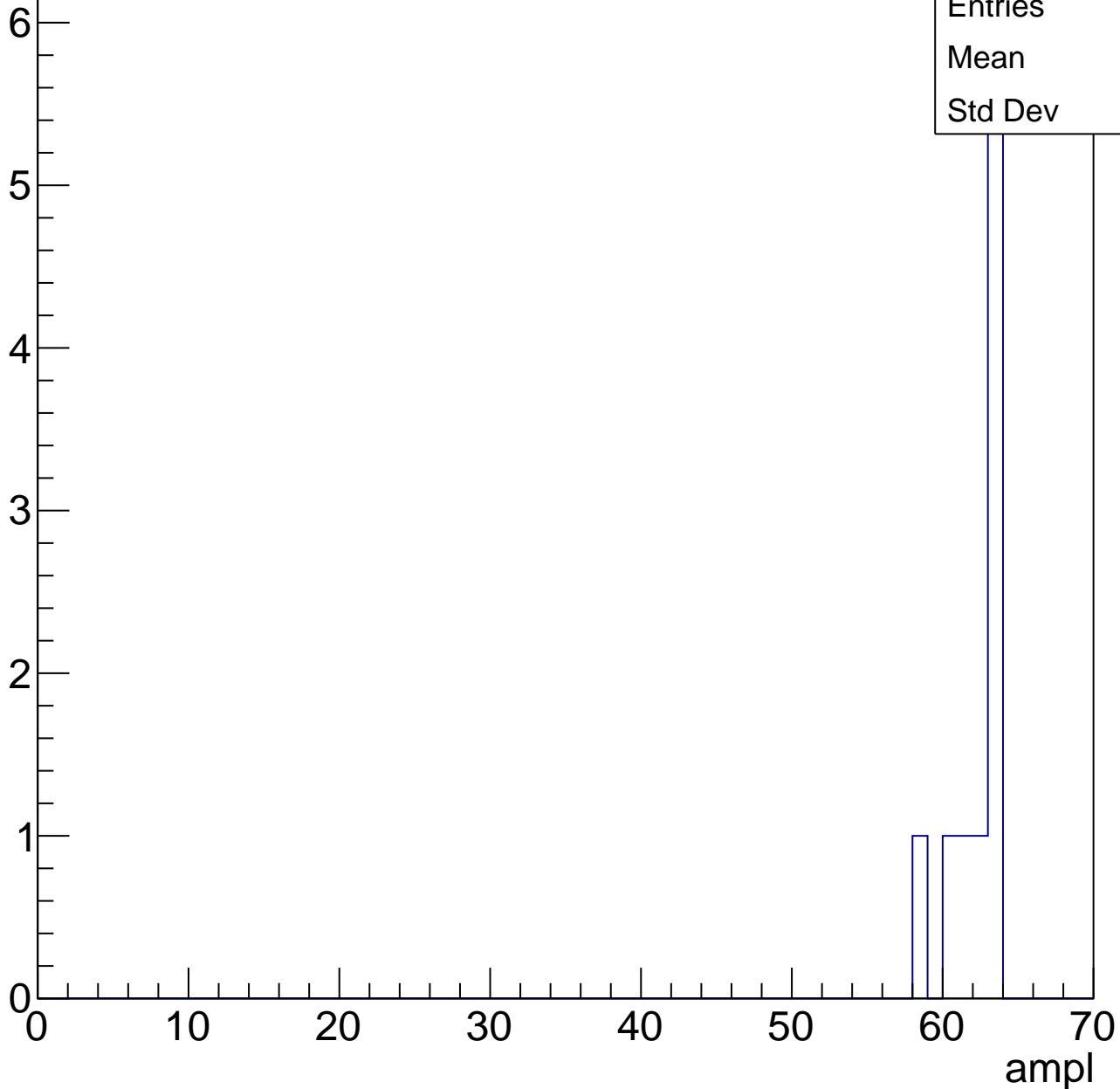


# B1L003S, U22-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.9
Std Dev	1.64





# B1L003S, U22-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch7, adc0

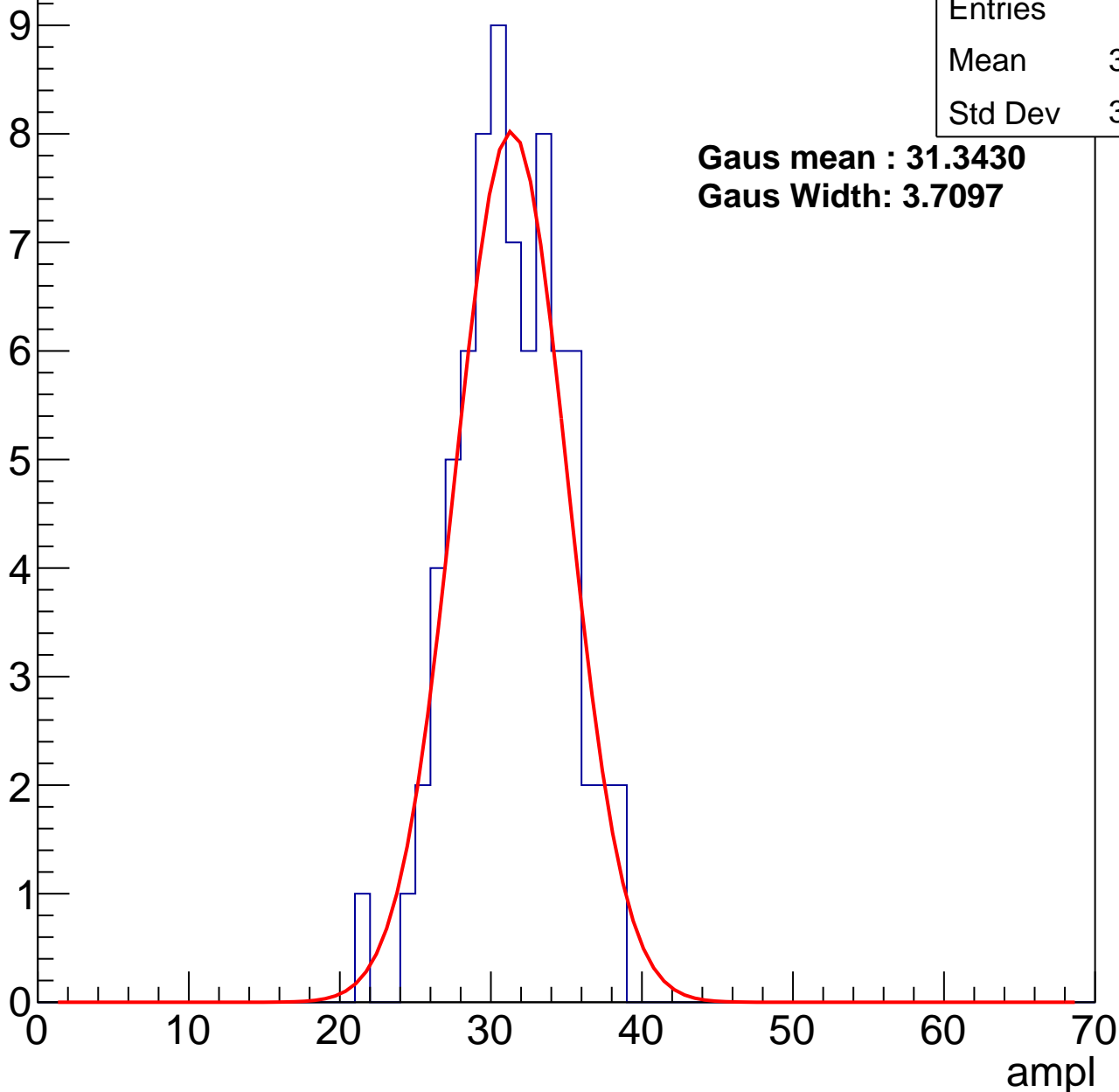
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30.84
Std Dev	3.468

**Gaus mean : 31.3430**

**Gaus Width: 3.7097**



# B1L003S, U22-ch7, adc1

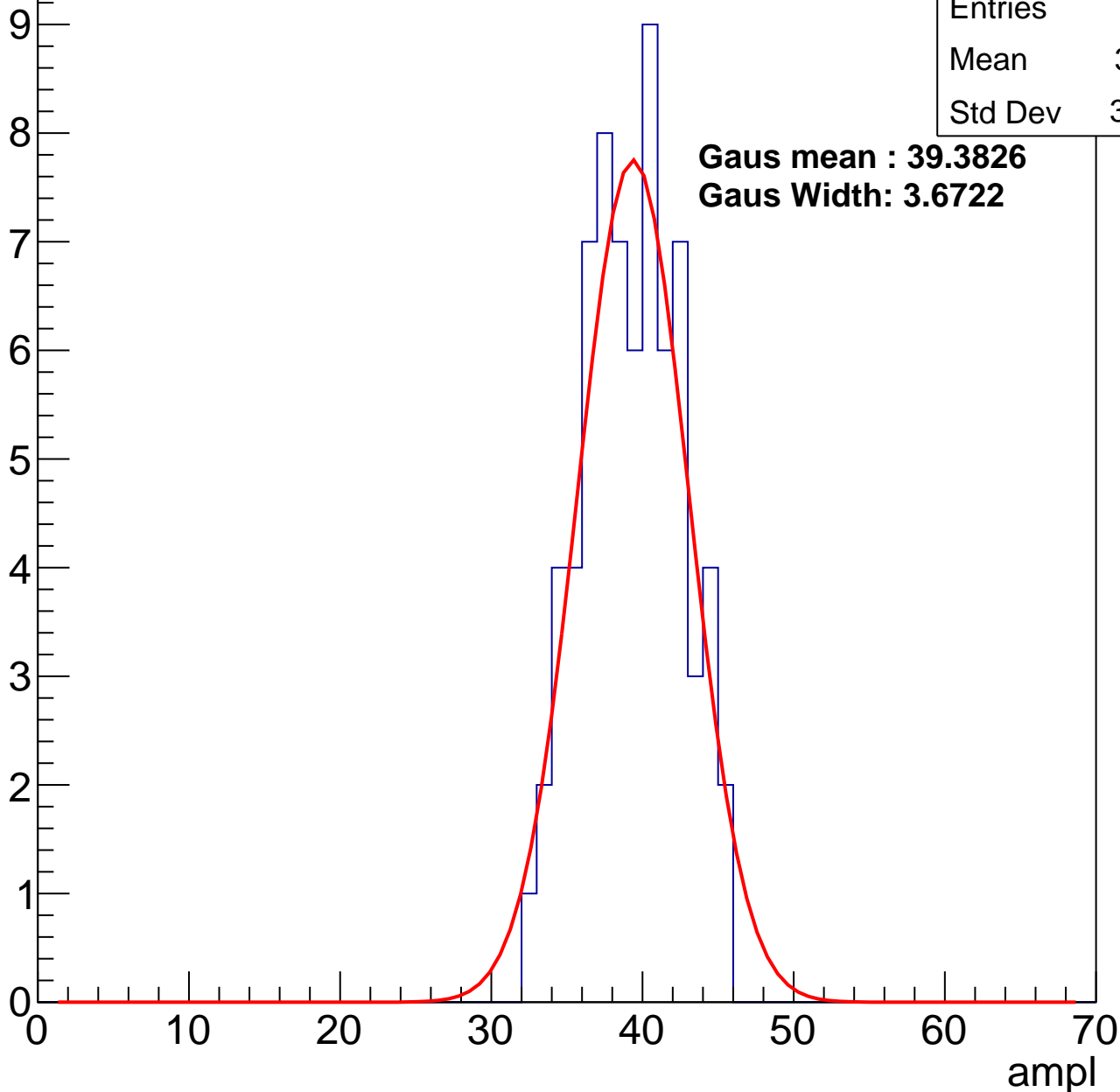
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	38.81
Std Dev	3.164

**Gaus mean : 39.3826**

**Gaus Width: 3.6722**



# B1L003S, U22-ch7, adc2

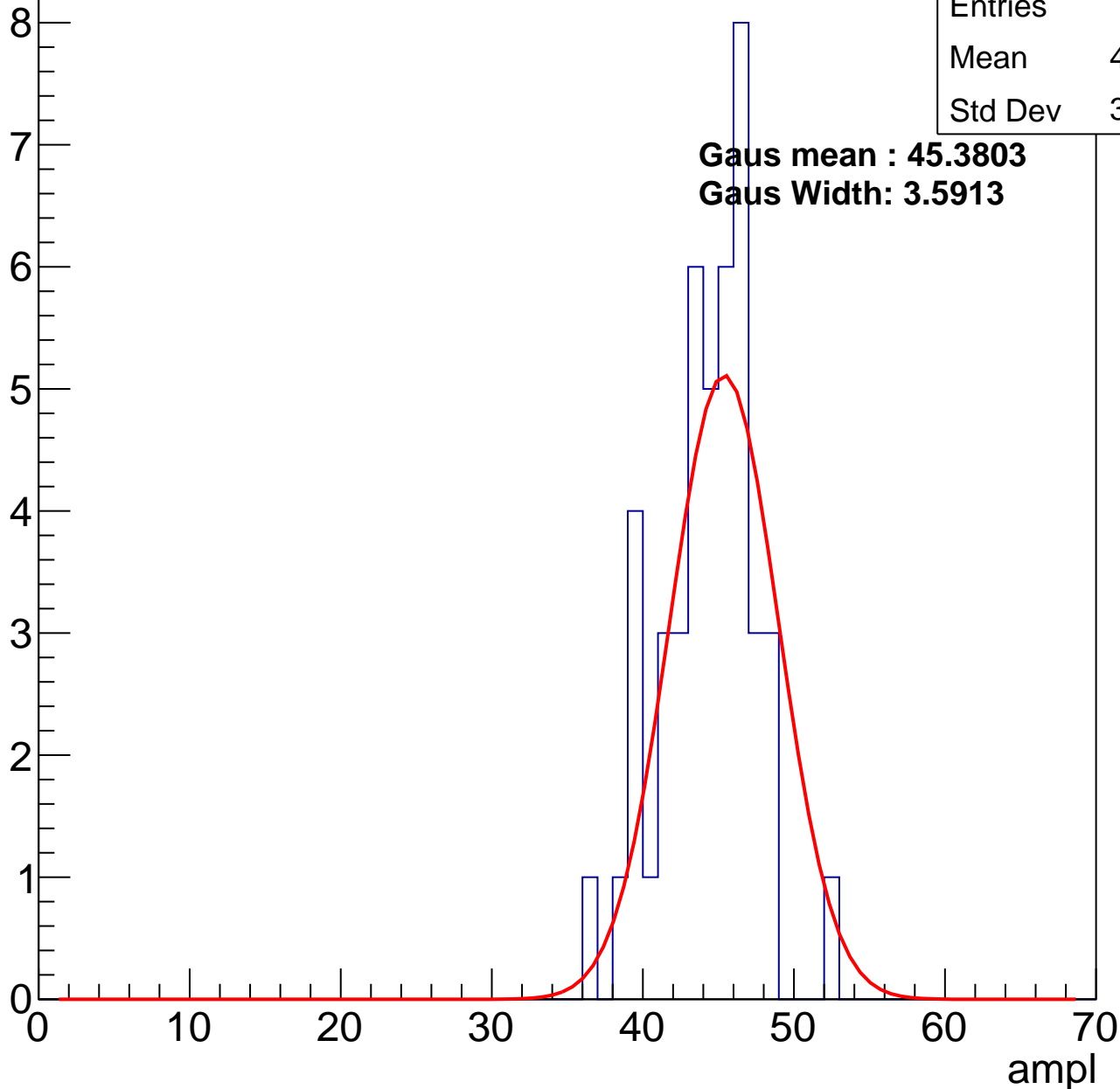
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	43.82
Std Dev	3.115

**Gaus mean : 45.3803**

**Gaus Width: 3.5913**

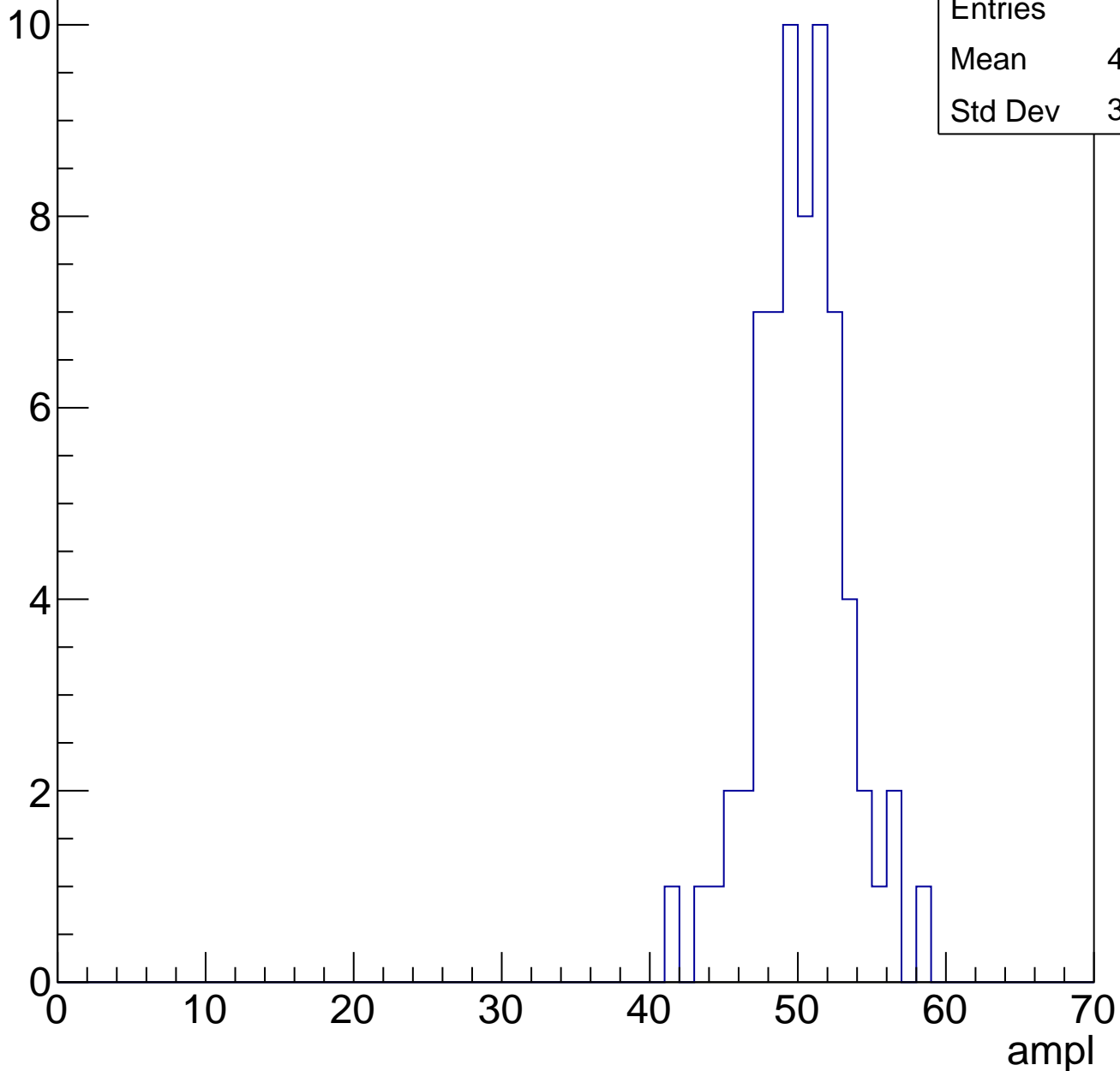


# B1L003S, U22-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	49.76
Std Dev	3.045

Entry

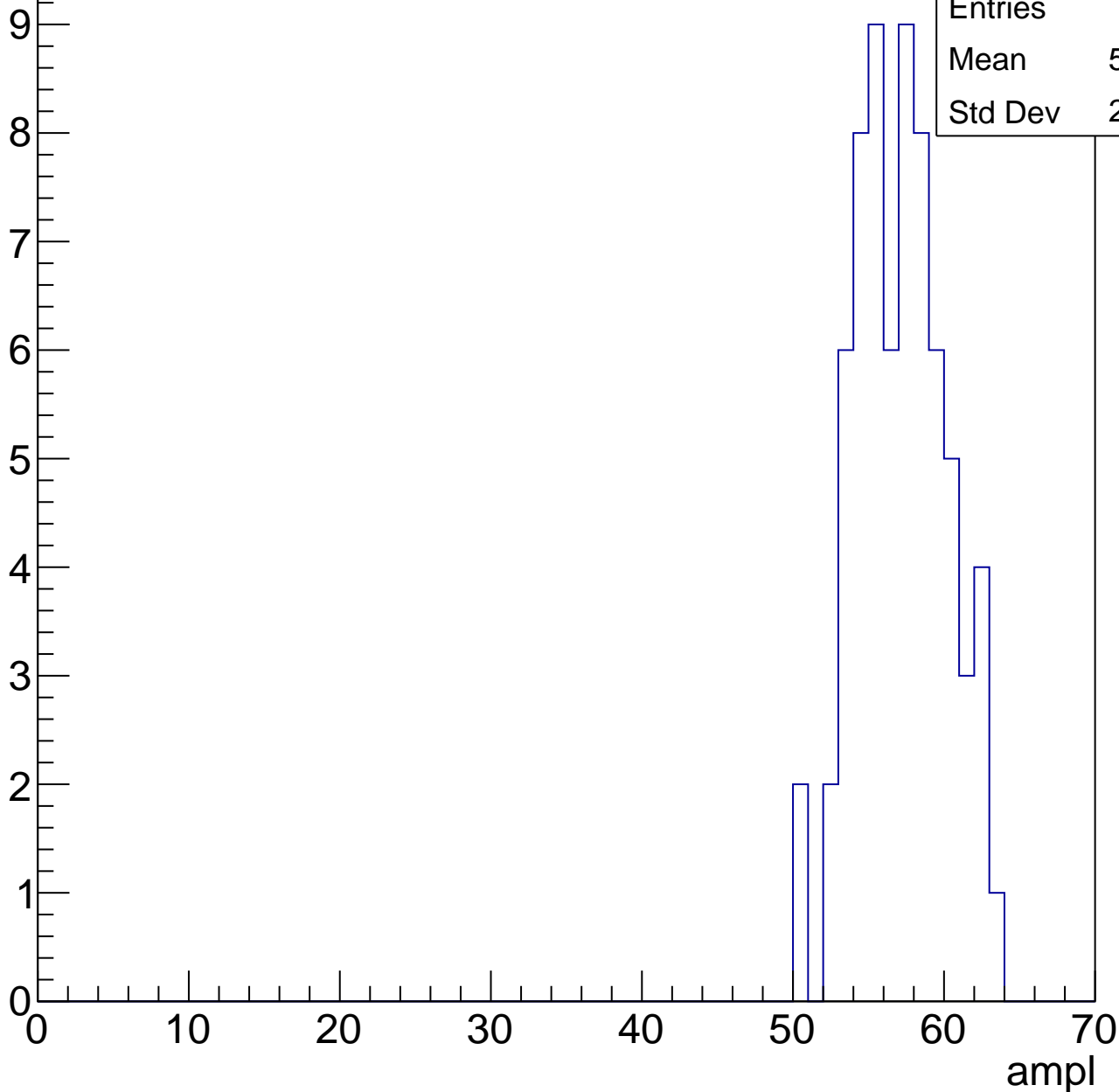


# B1L003S, U22-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	56.67
Std Dev	2.977

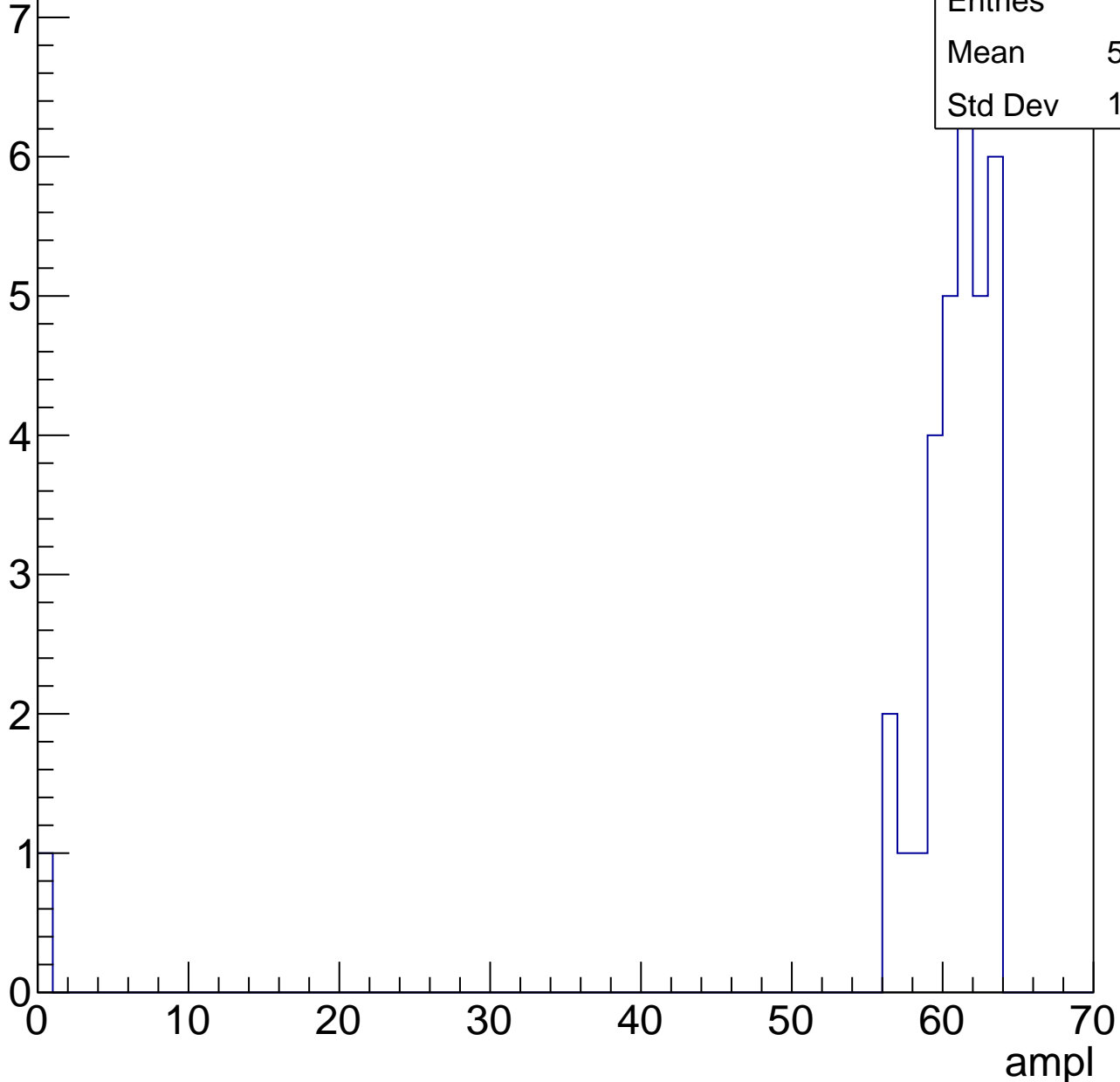


# B1L003S, U22-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

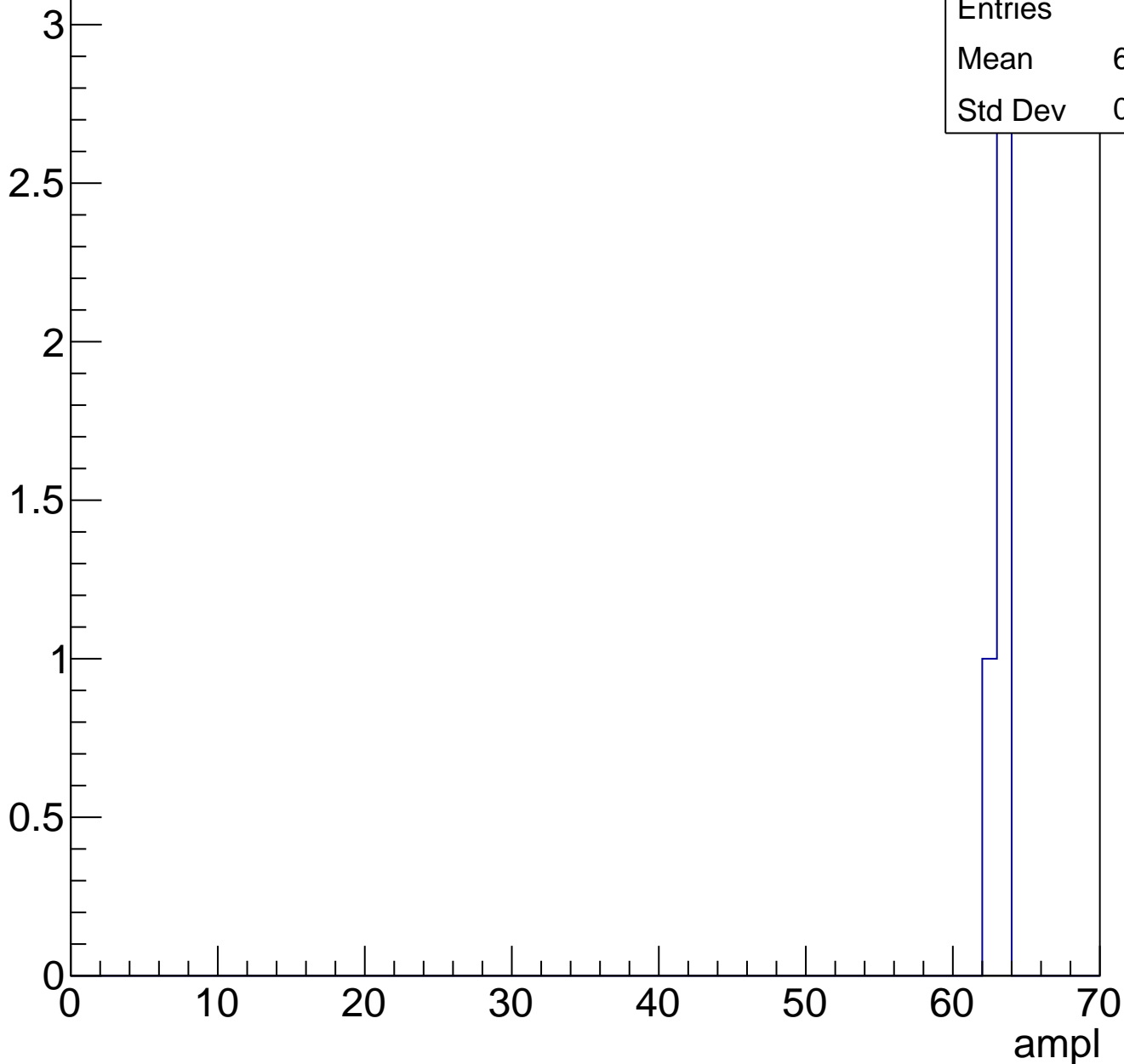
Entries	32
Mean	58.69
Std Dev	10.72



# B1L003S, U22-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch8, adc0

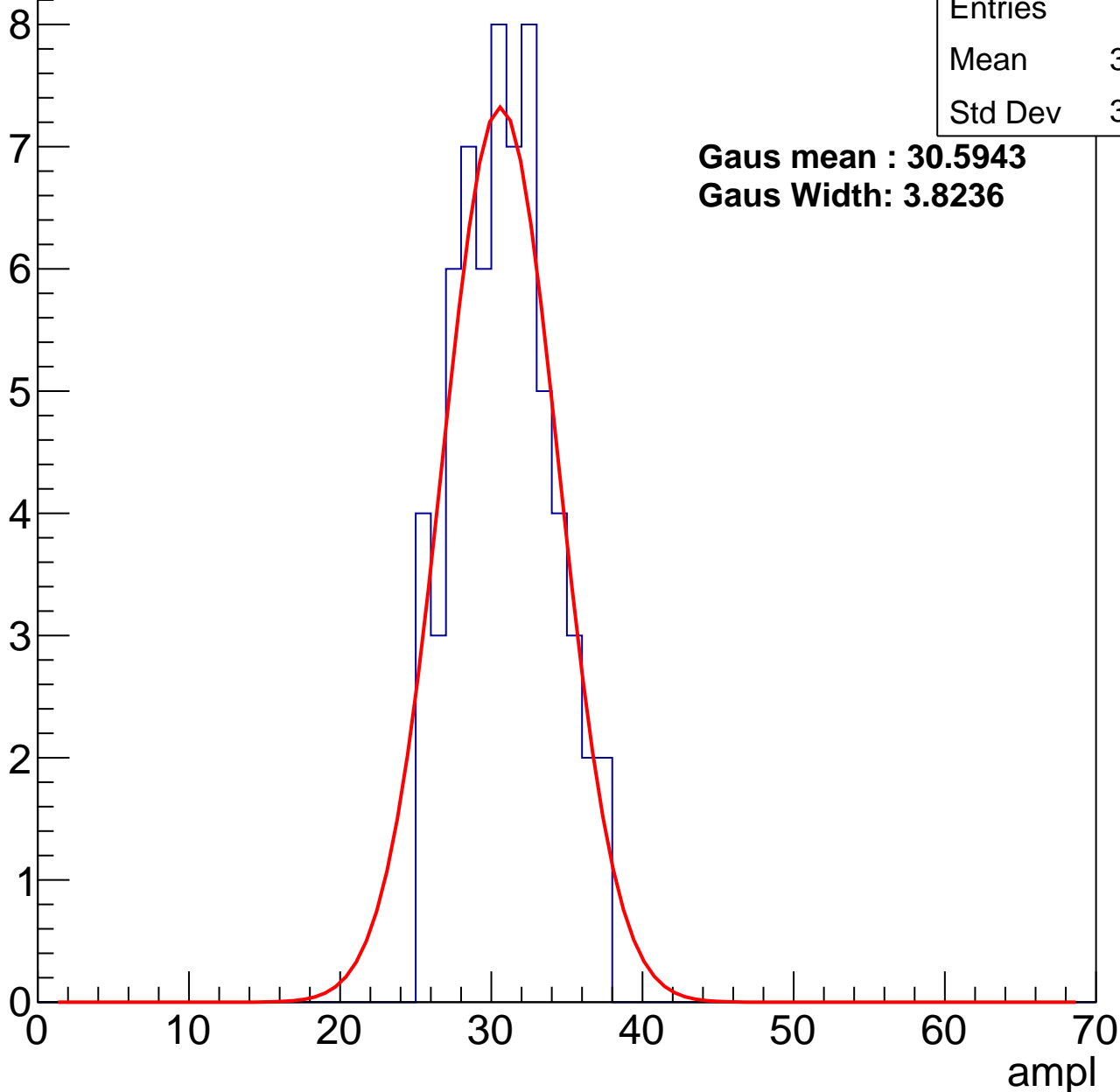
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.38
Std Dev	3.087

**Gaus mean : 30.5943**

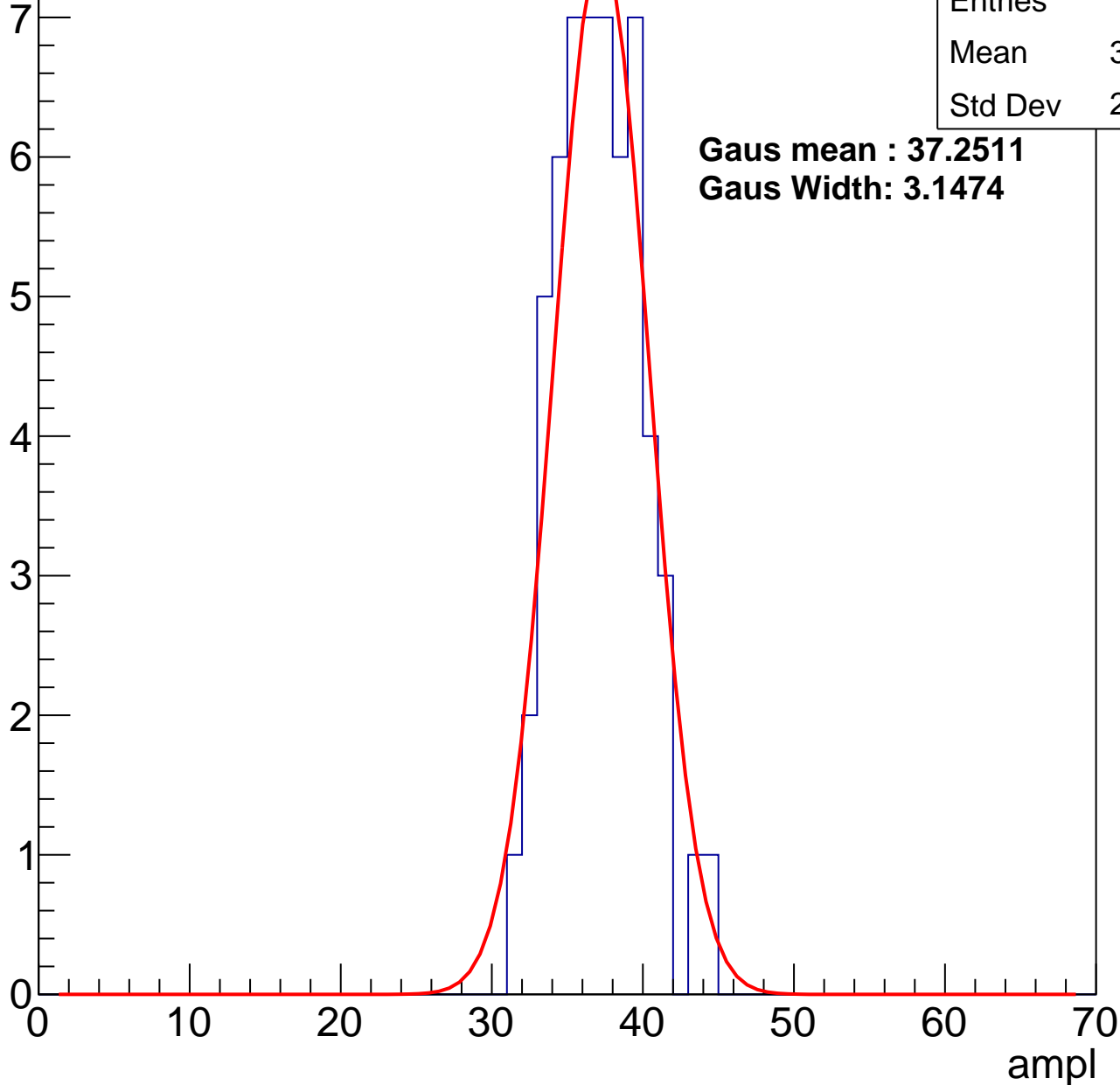
**Gaus Width: 3.8236**



# B1L003S, U22-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch8, adc2

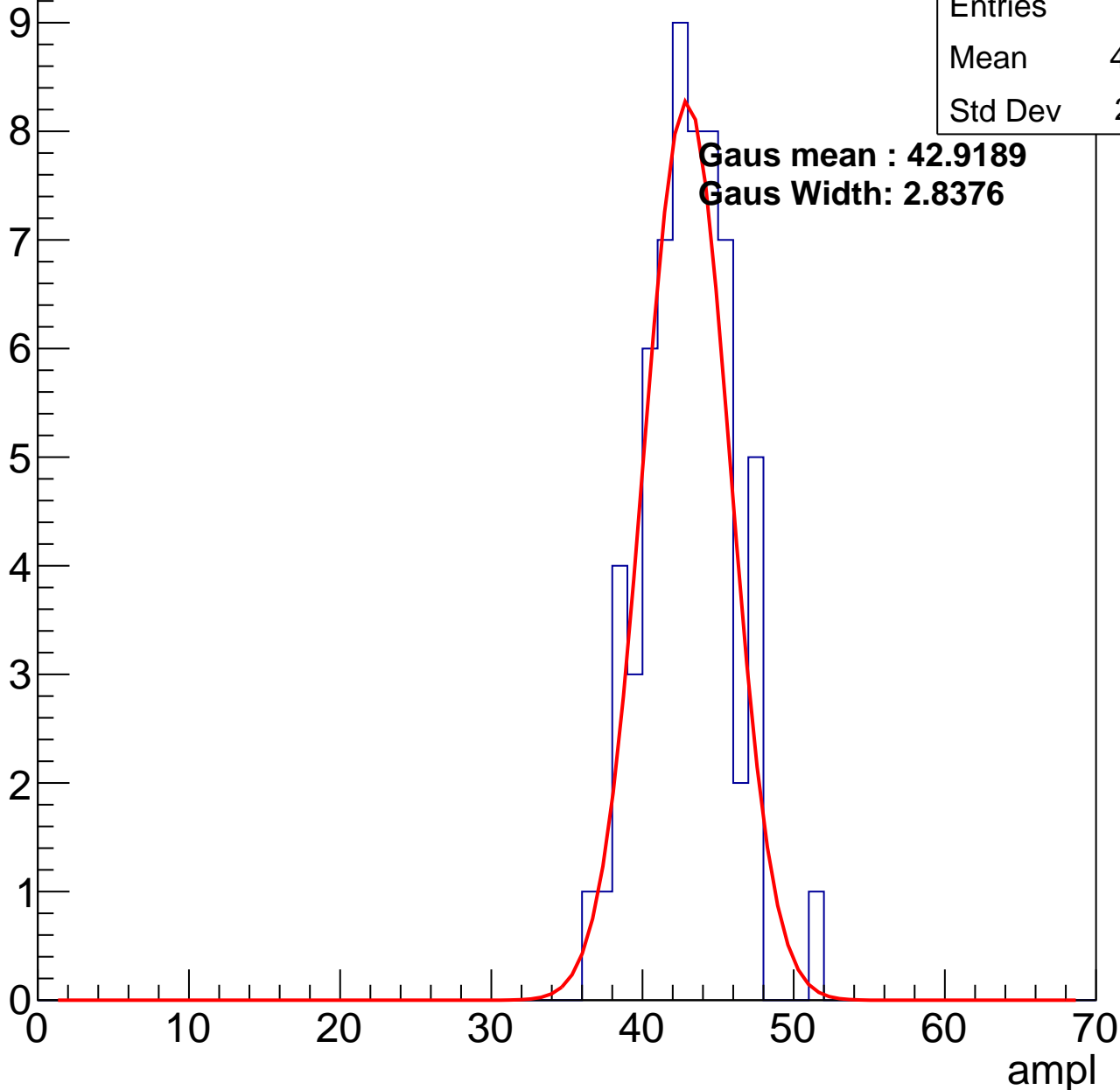
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.52
Std Dev	2.861

**Gaus mean : 42.9189**

**Gaus Width: 2.8376**



# B1L003S, U22-ch8, adc3

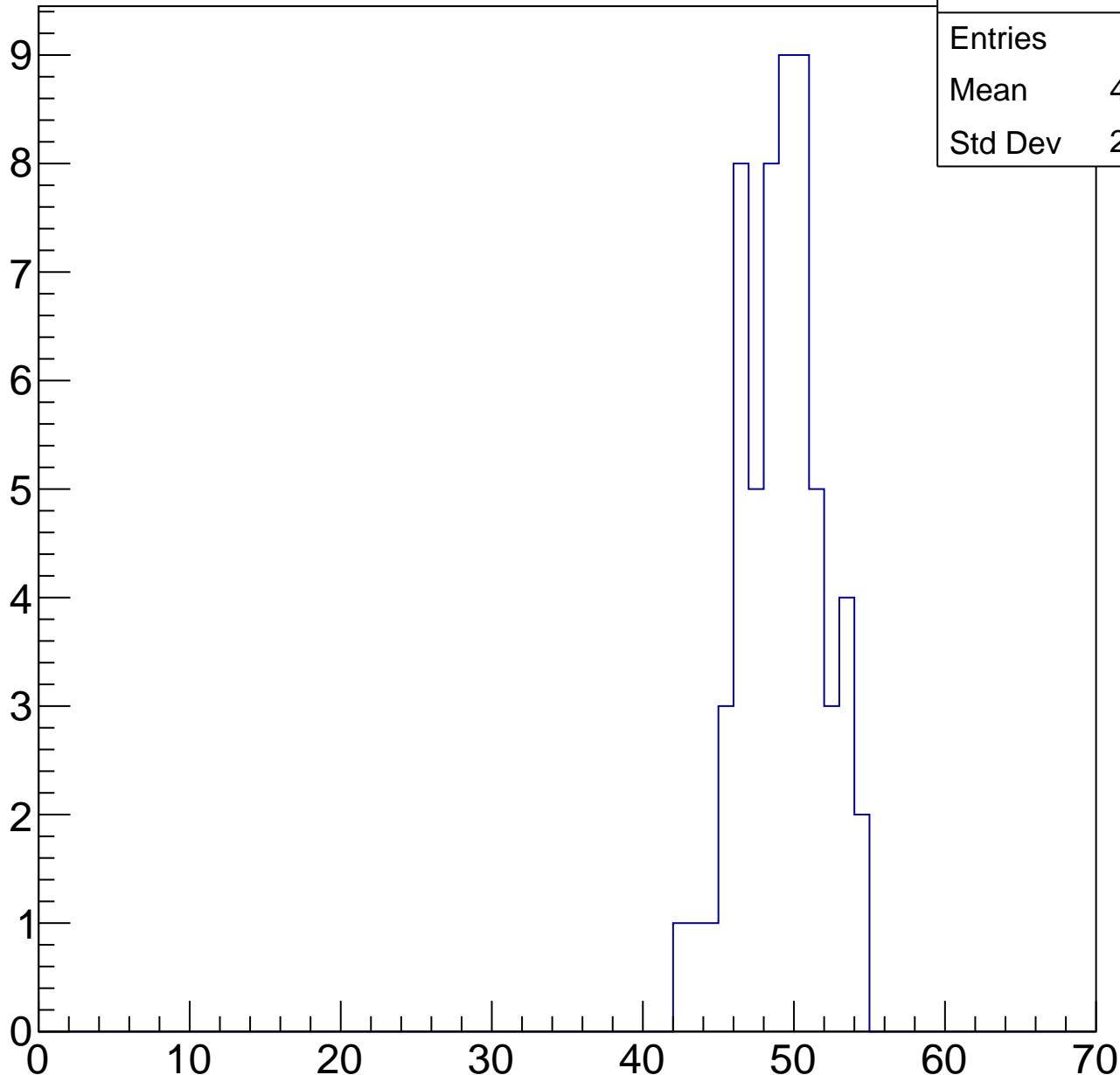
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	48.69
Std Dev	2.676

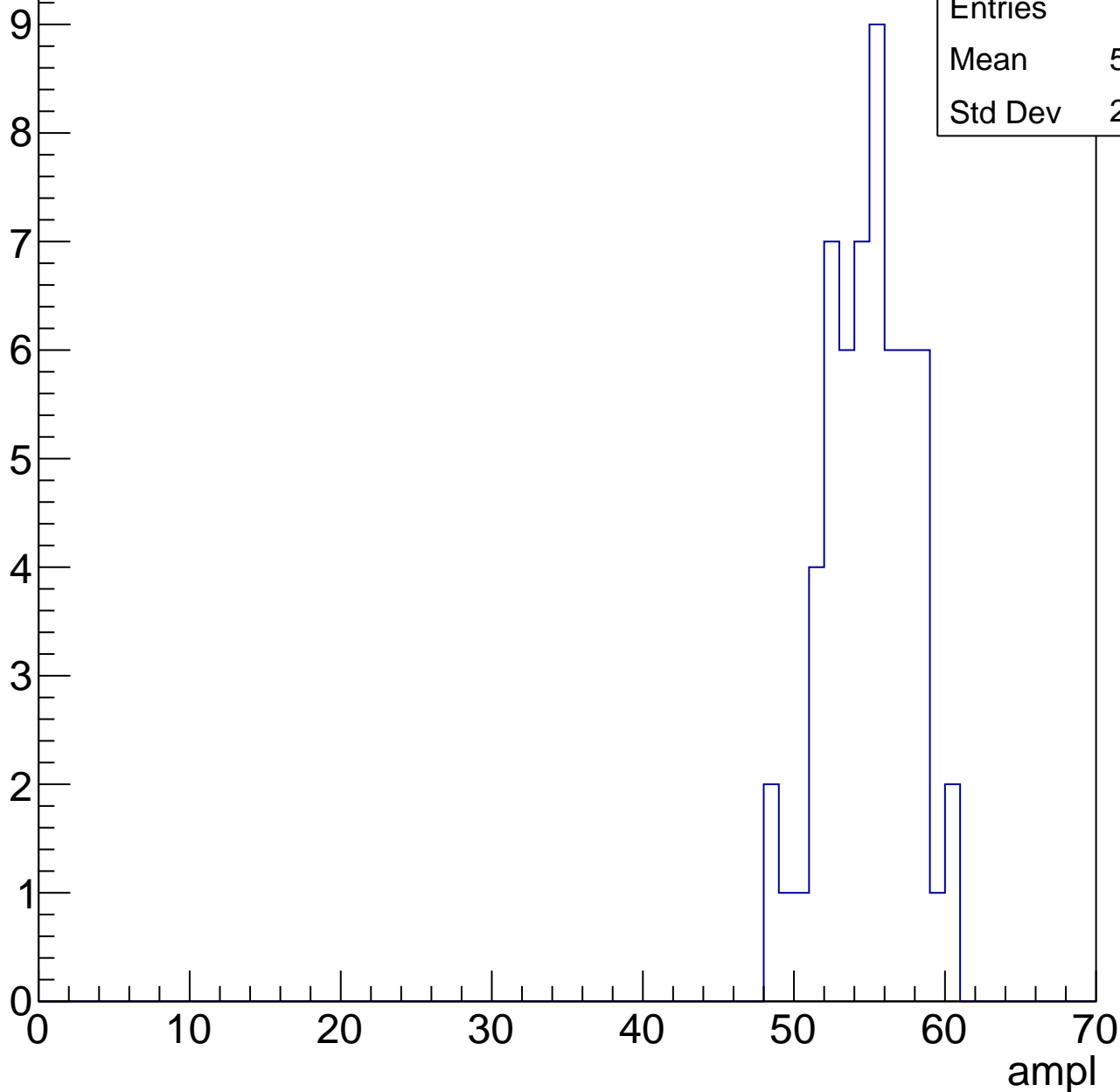
ampl



# B1L003S, U22-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	58
Mean	54.47
Std Dev	2.787

# B1L003S, U22-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.52
Std Dev	2.319

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

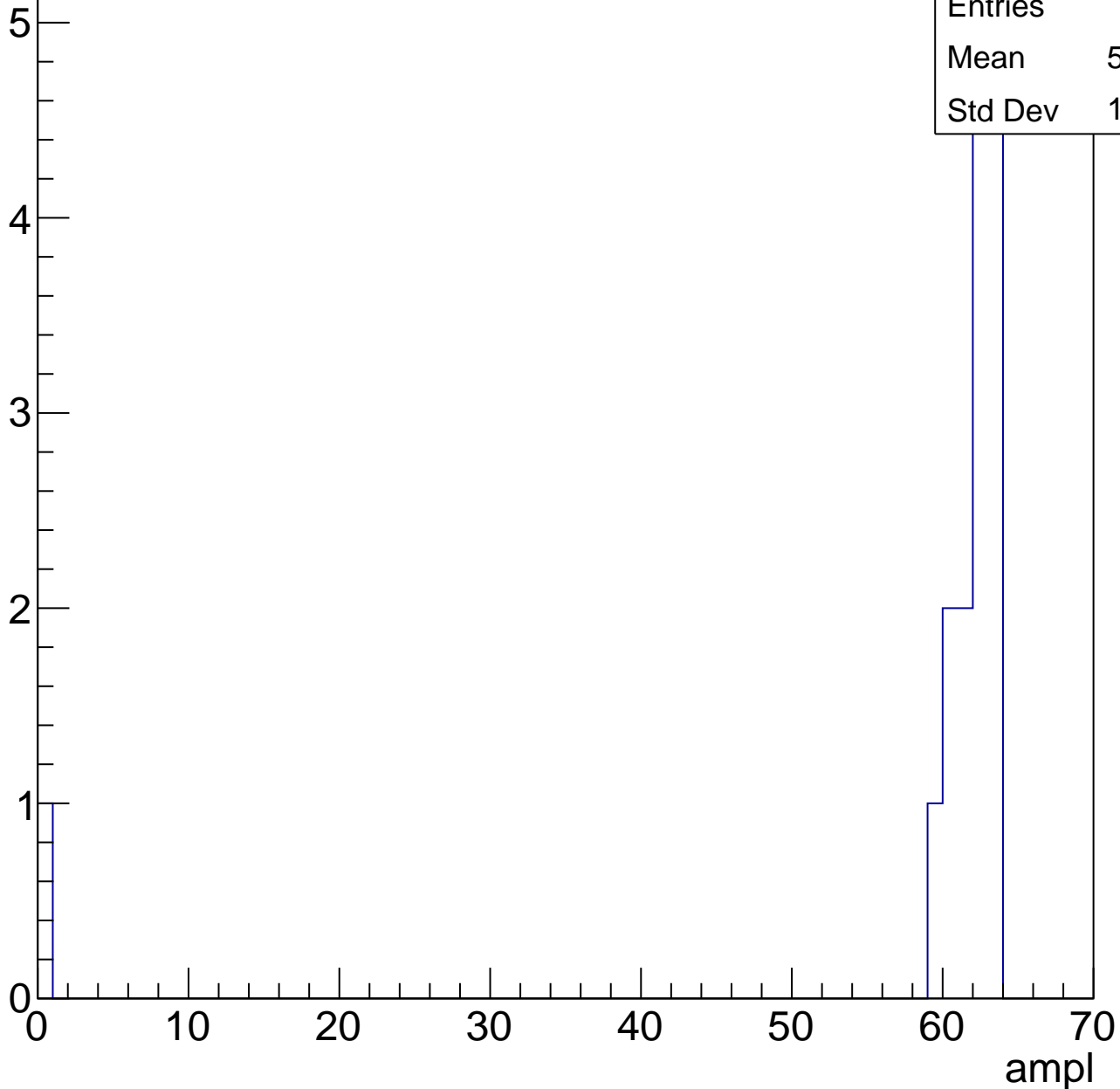
8

# B1L003S, U22-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.88
Std Dev	14.99





# B1L003S, U22-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch9, adc0

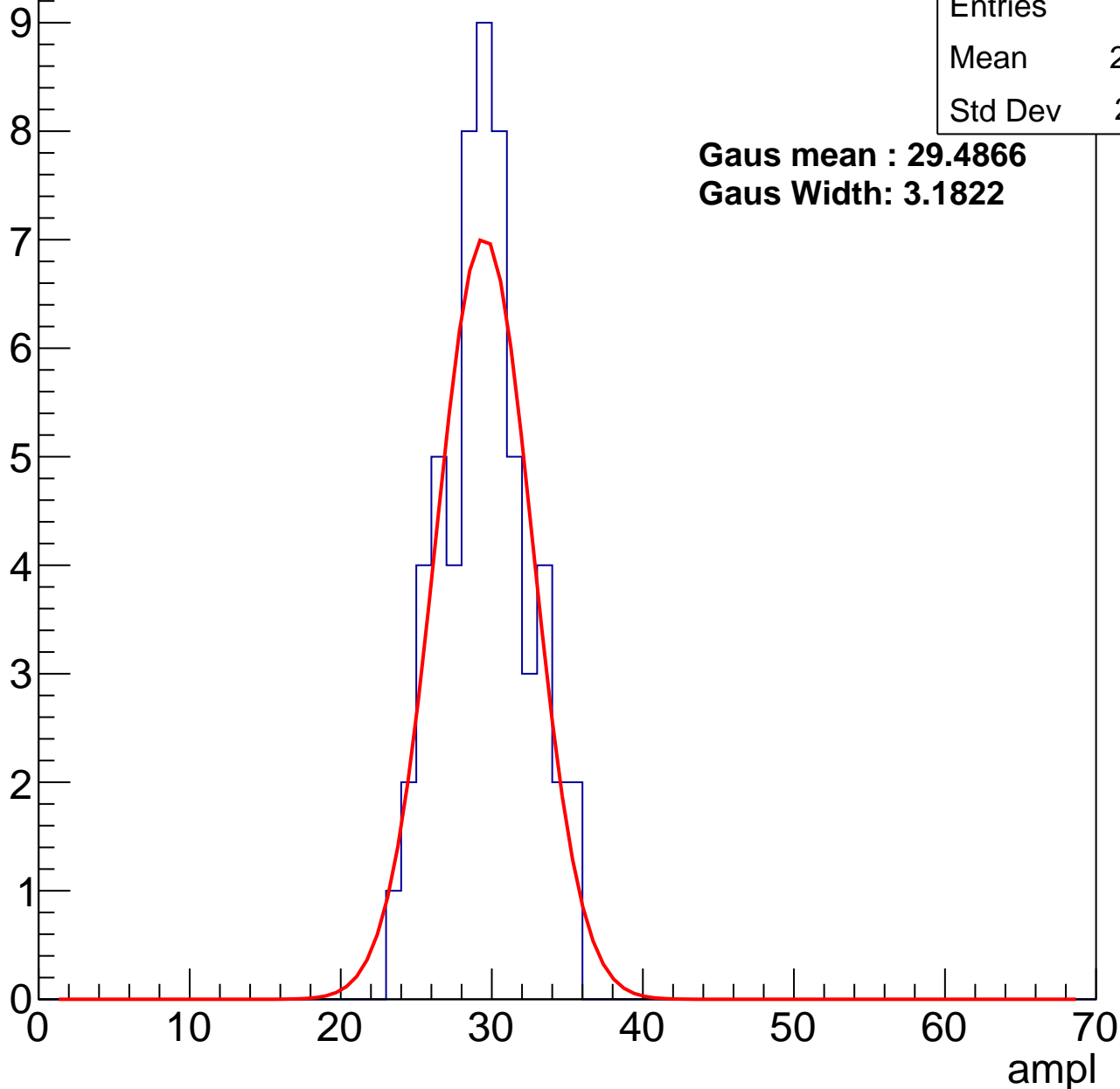
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.04
Std Dev	2.841

**Gaus mean : 29.4866**

**Gaus Width: 3.1822**



# B1L003S, U22-ch9, adc1

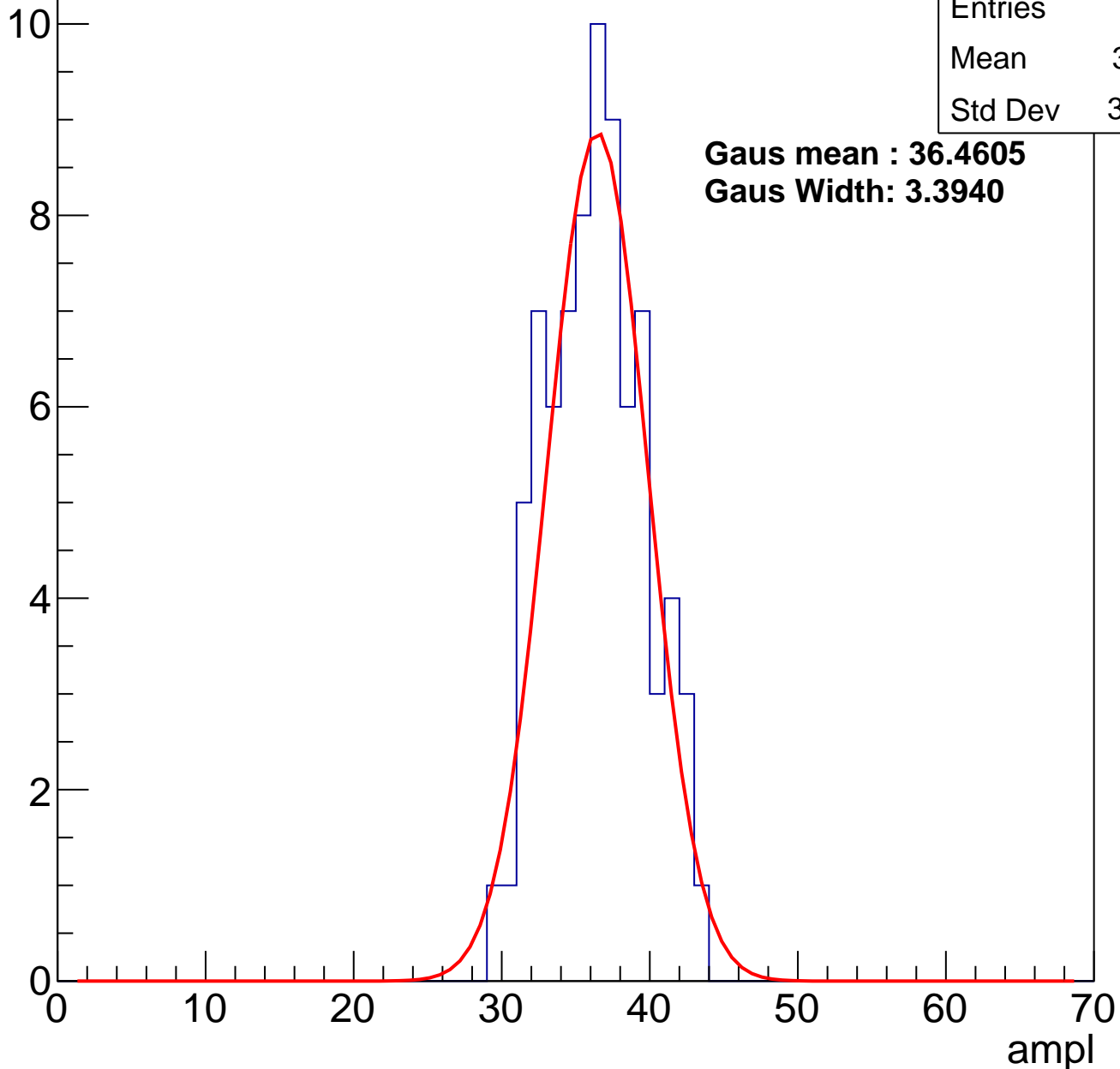
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	35.91
Std Dev	3.227

**Gaus mean : 36.4605**

**Gaus Width: 3.3940**

Entry



# B1L003S, U22-ch9, adc2

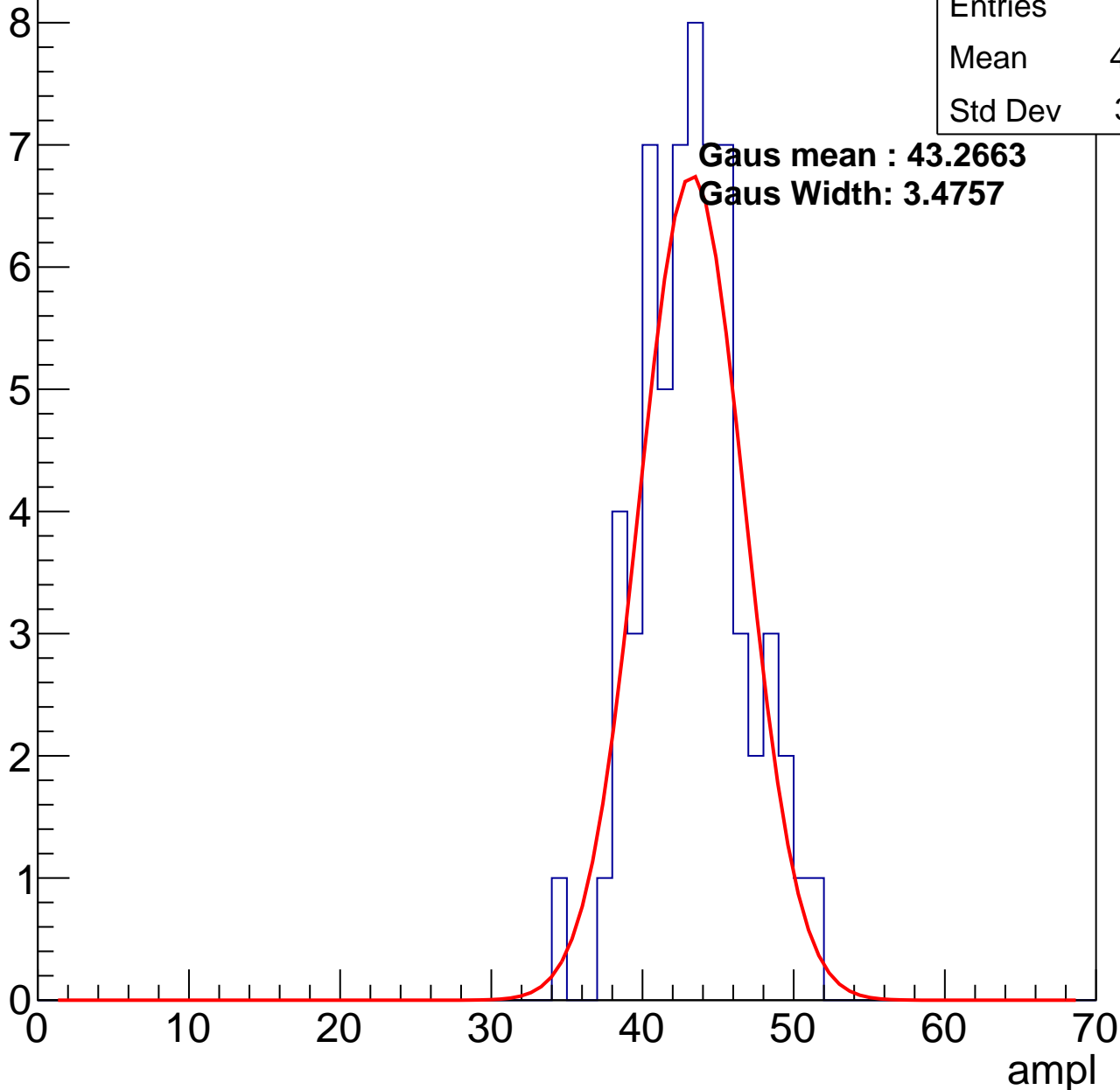
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.92
Std Dev	3.381

**Gaus mean : 43.2663**

**Gaus Width: 3.4757**

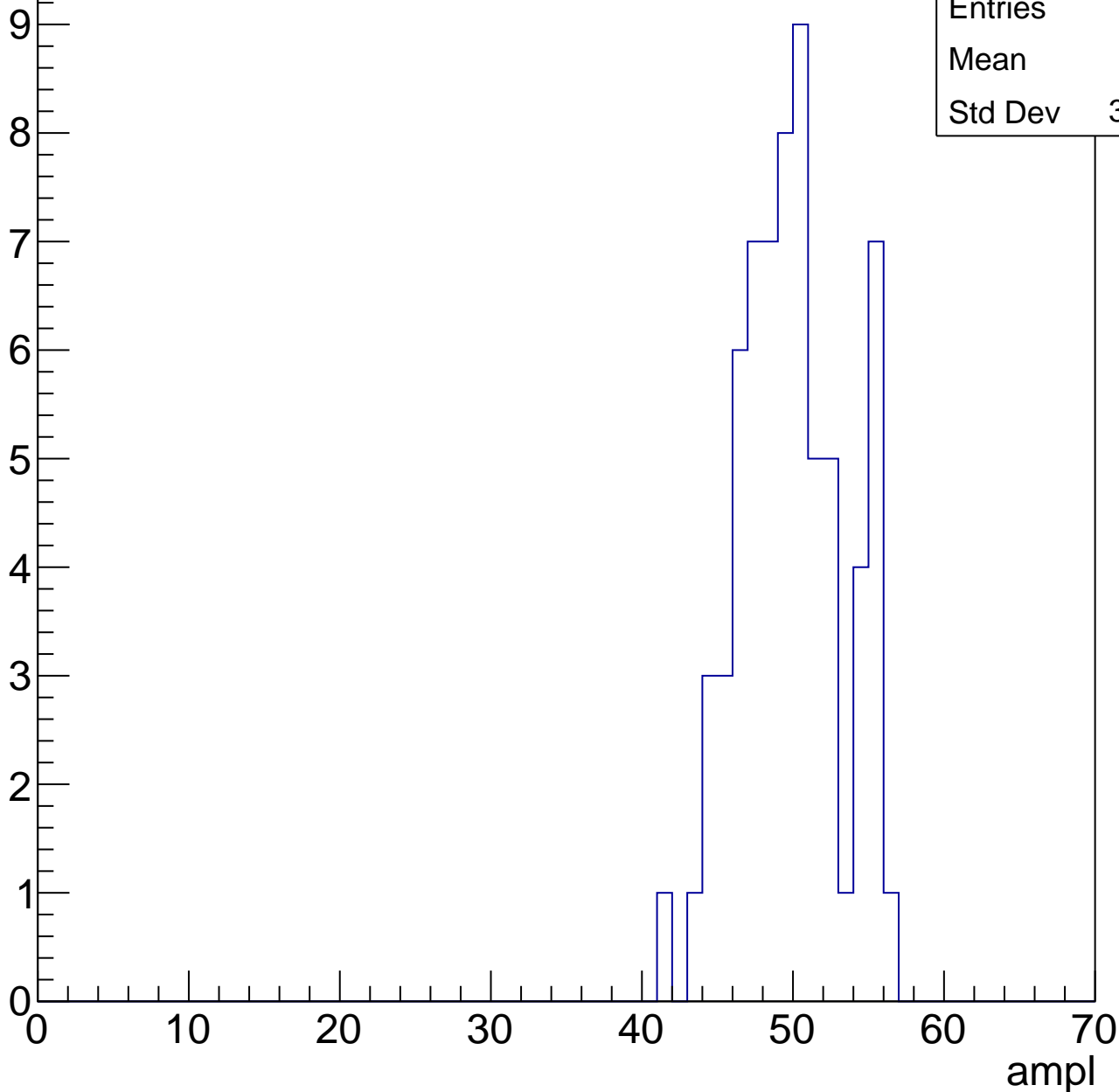


# B1L003S, U22-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.4
Std Dev	3.413

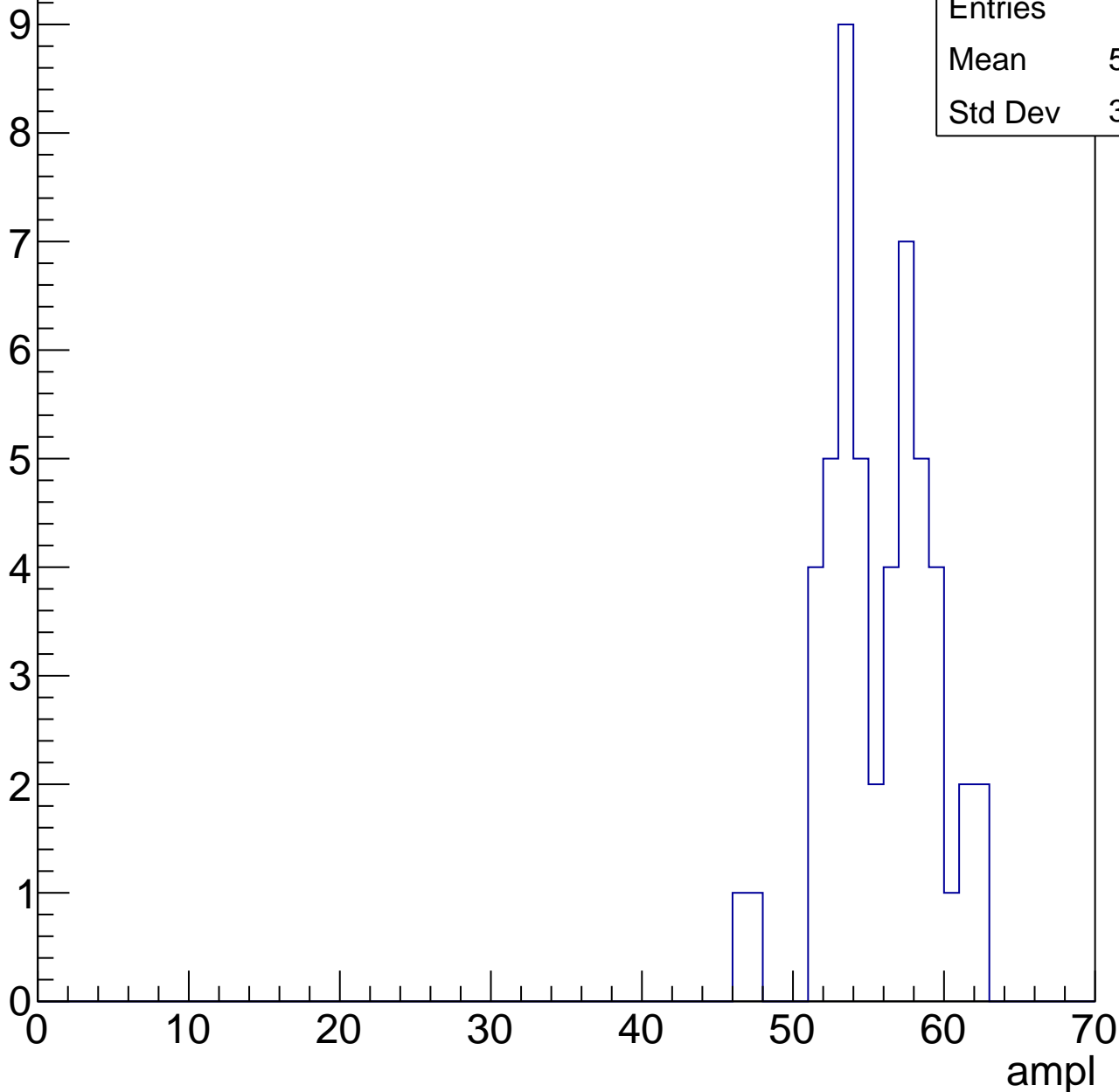


# B1L003S, U22-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.17
Std Dev	3.479

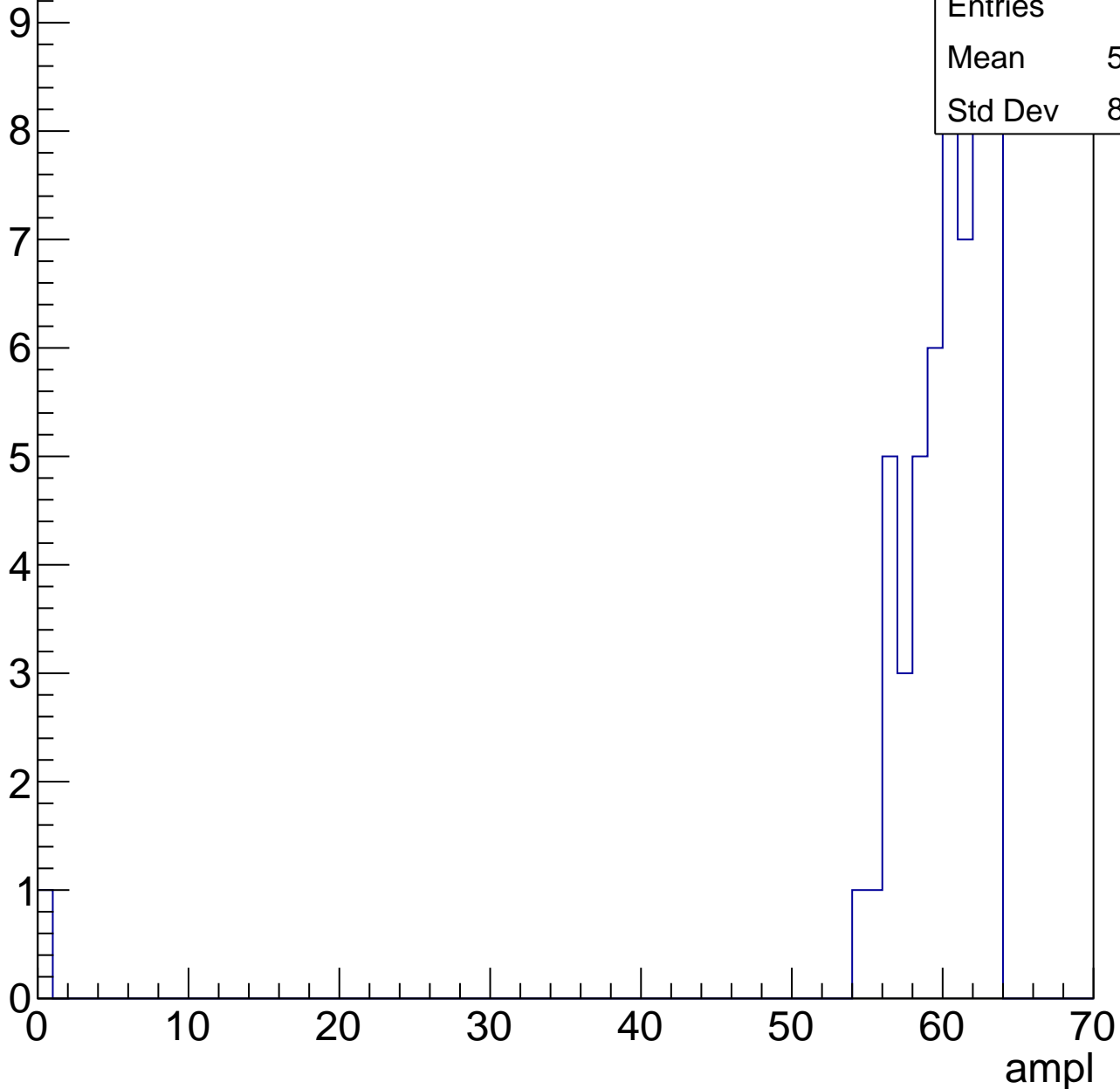


# B1L003S, U22-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.78
Std Dev	8.425



# B1L003S, U22-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch10, adc0

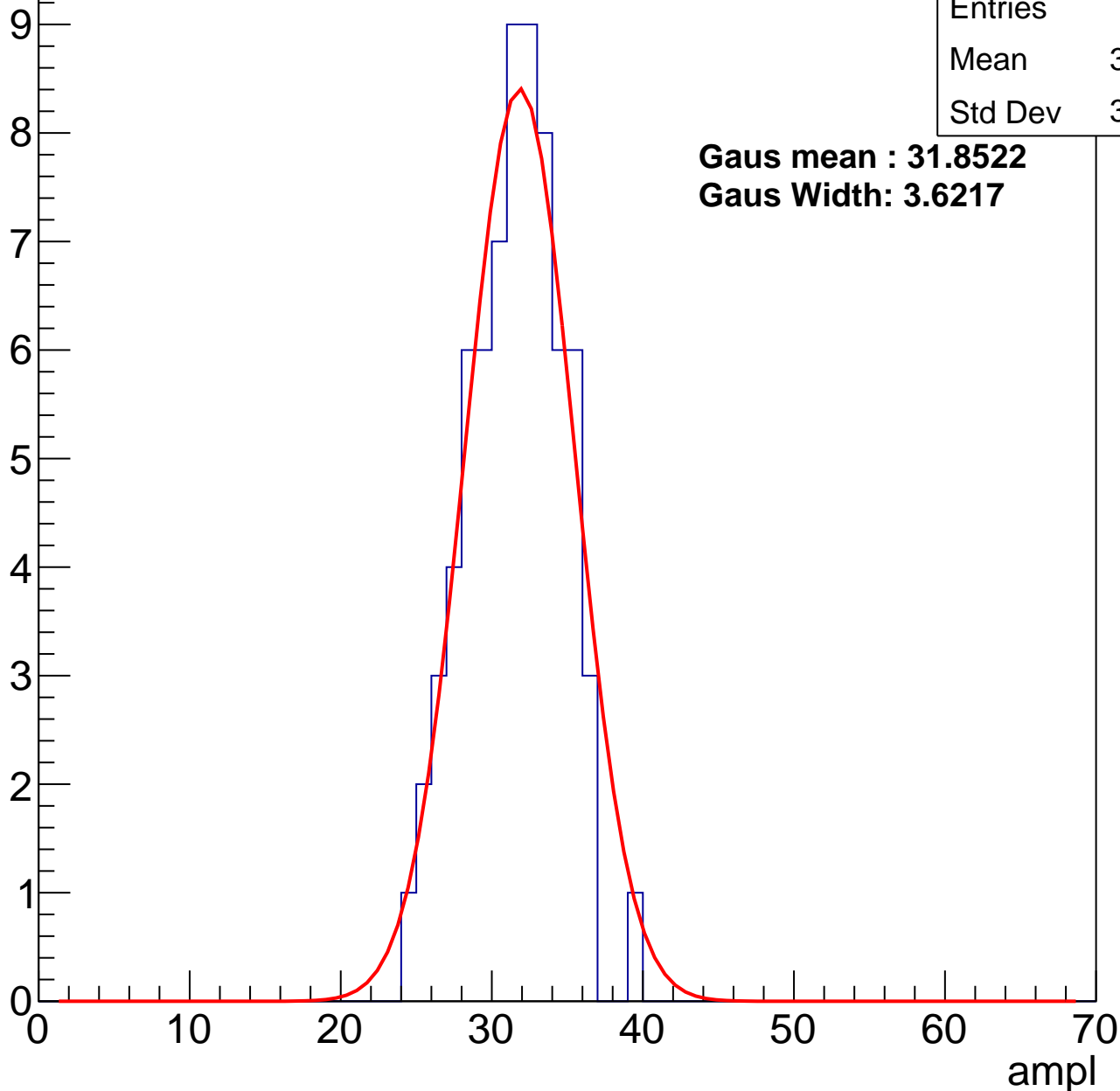
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	31.04
Std Dev	3.083

**Gaus mean : 31.8522**

**Gaus Width: 3.6217**



# B1L003S, U22-ch10, adc1

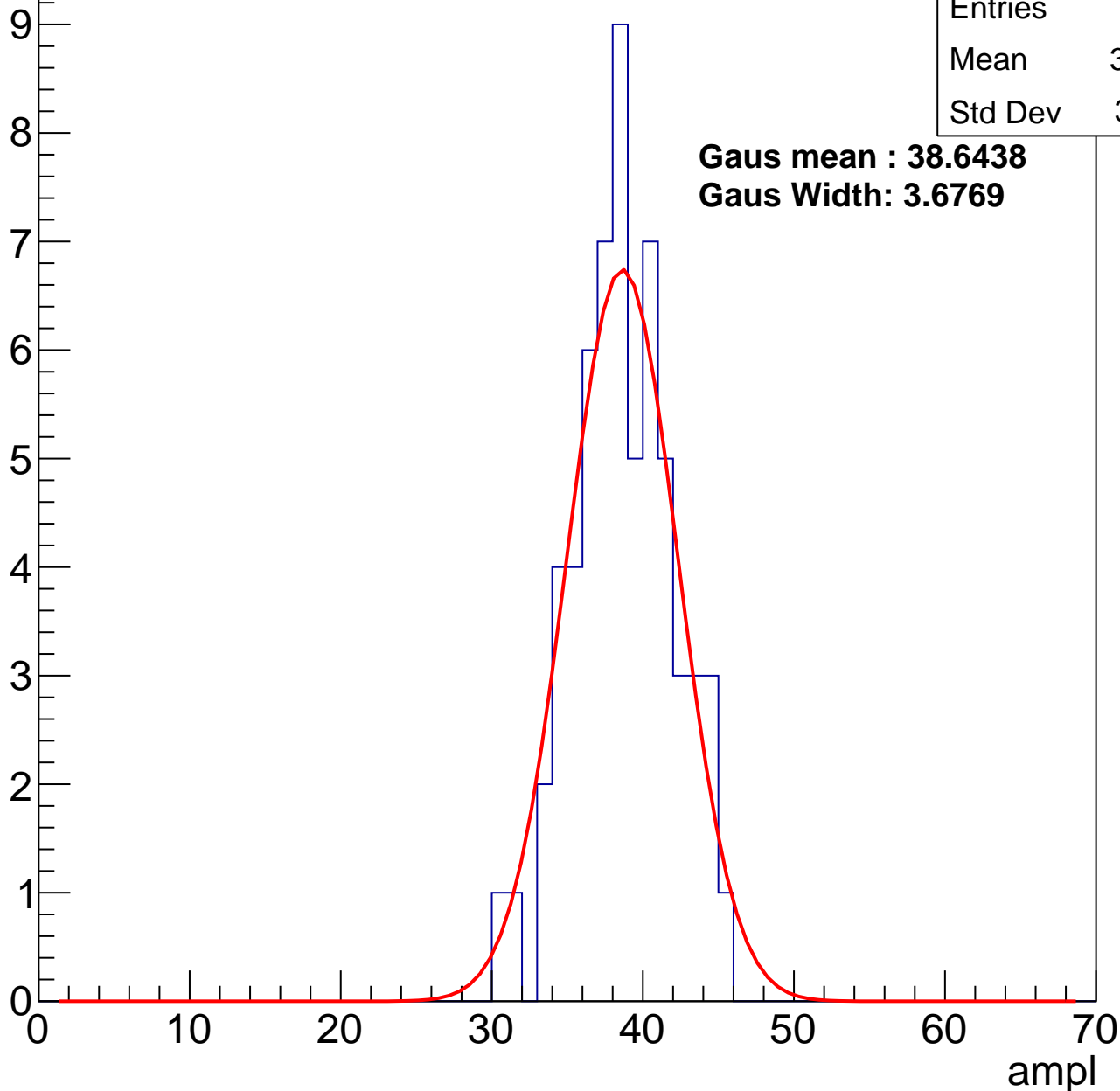
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	38.23
Std Dev	3.261

**Gaus mean : 38.6438**

**Gaus Width: 3.6769**



# B1L003S, U22-ch10, adc2

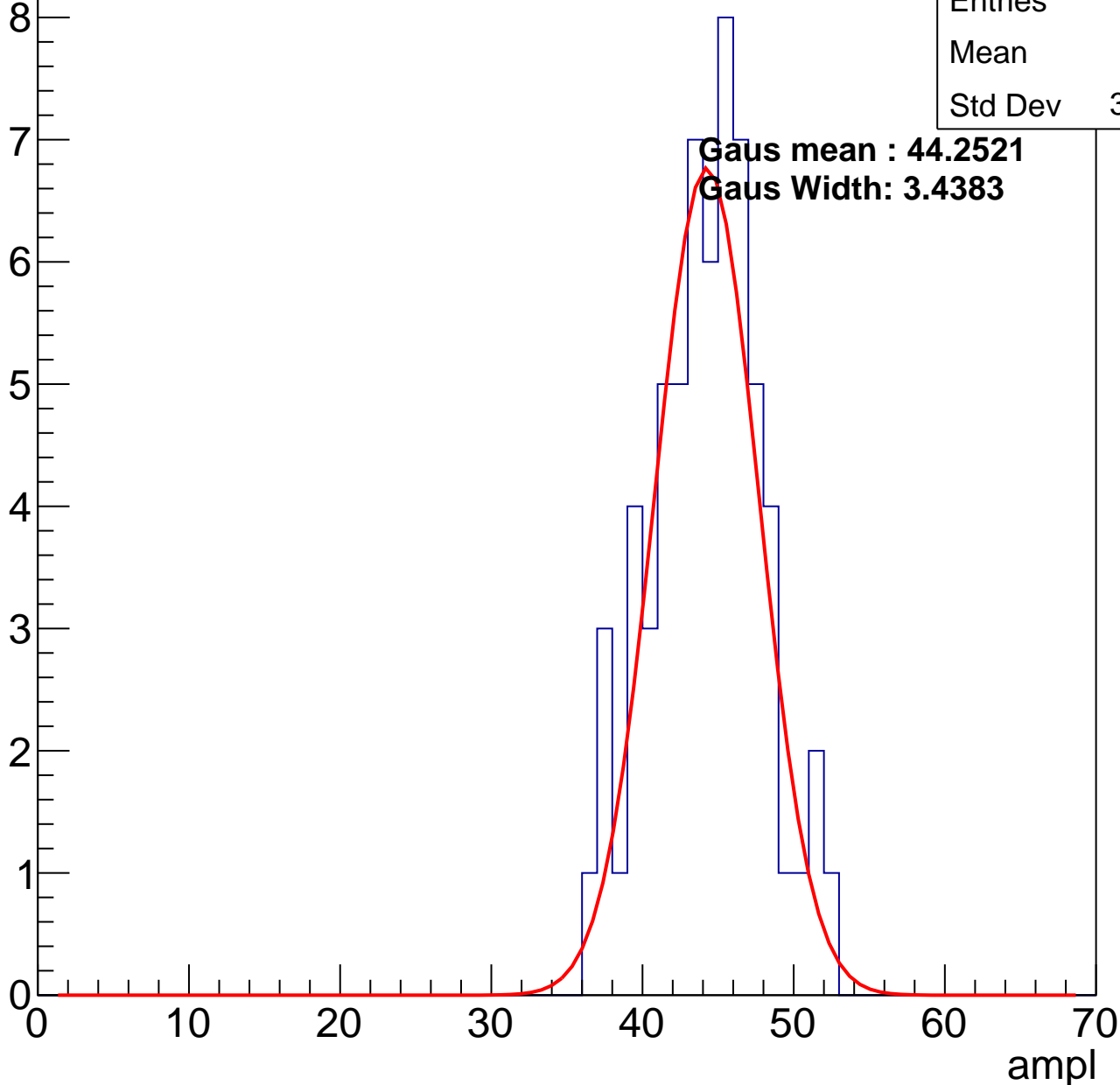
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.8
Std Dev	3.606

**Gaus mean : 44.2521**

**Gaus Width: 3.4383**

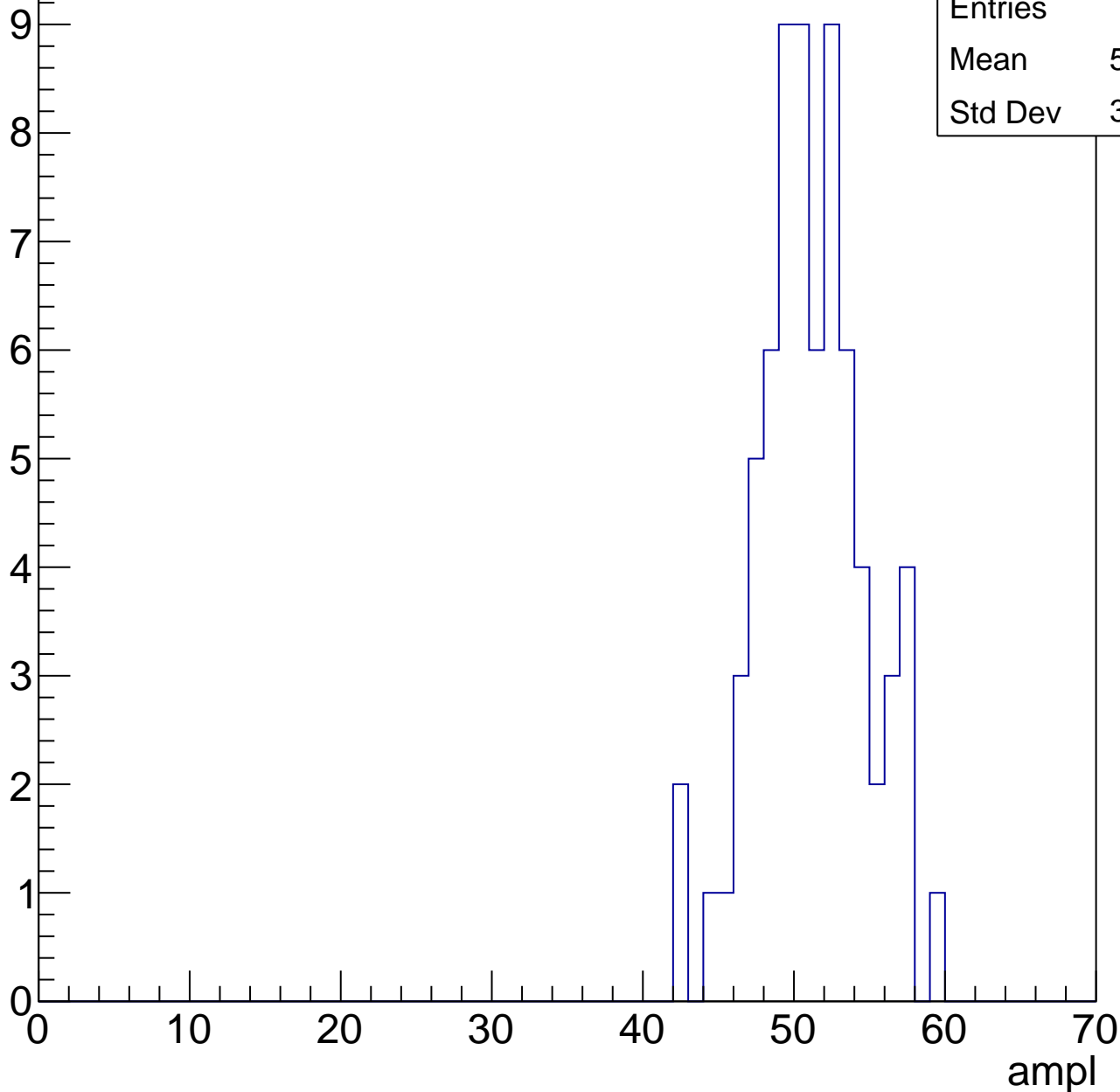


# B1L003S, U22-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	50.68
Std Dev	3.516

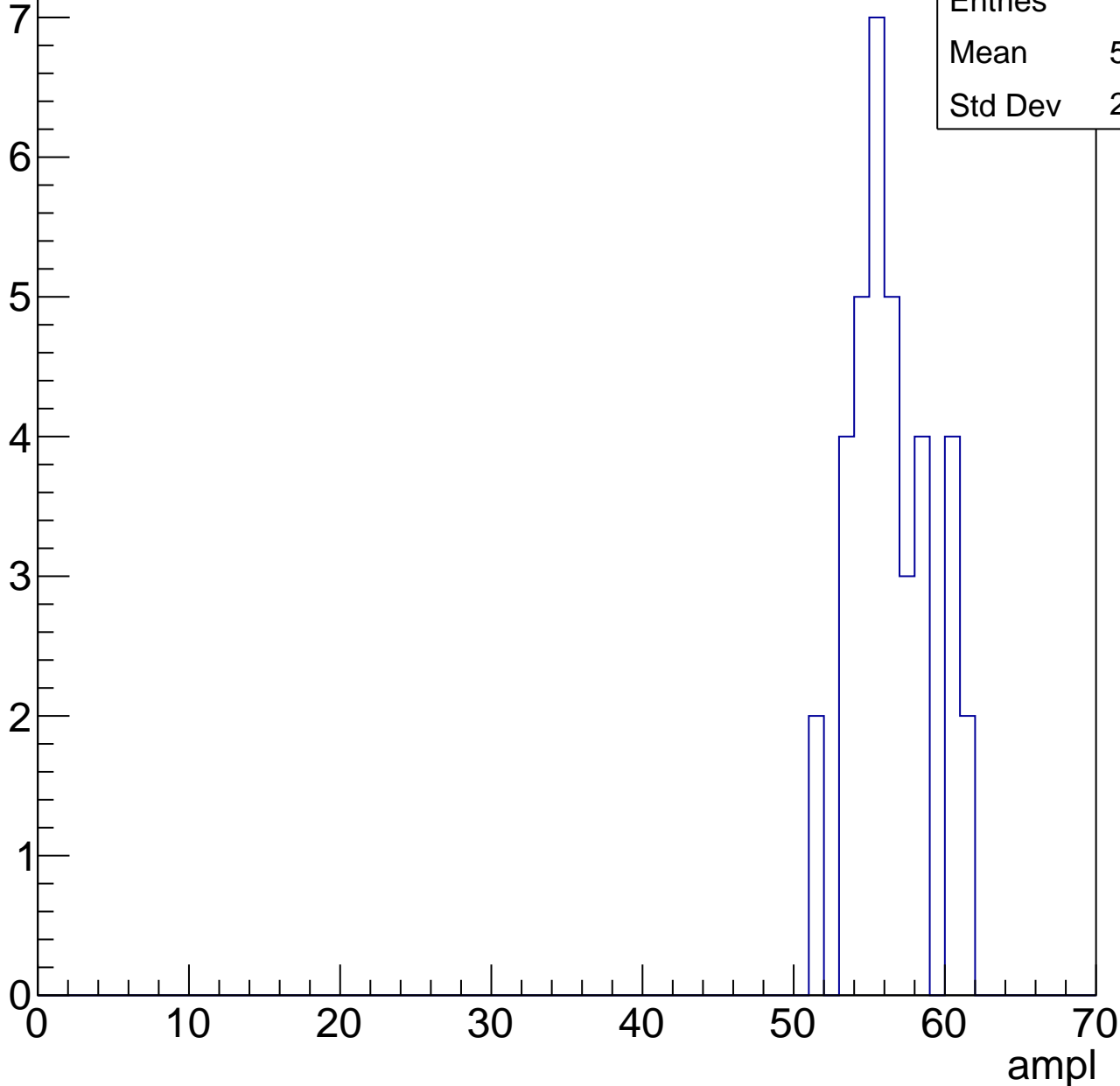


# B1L003S, U22-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	36
Mean	55.94
Std Dev	2.613

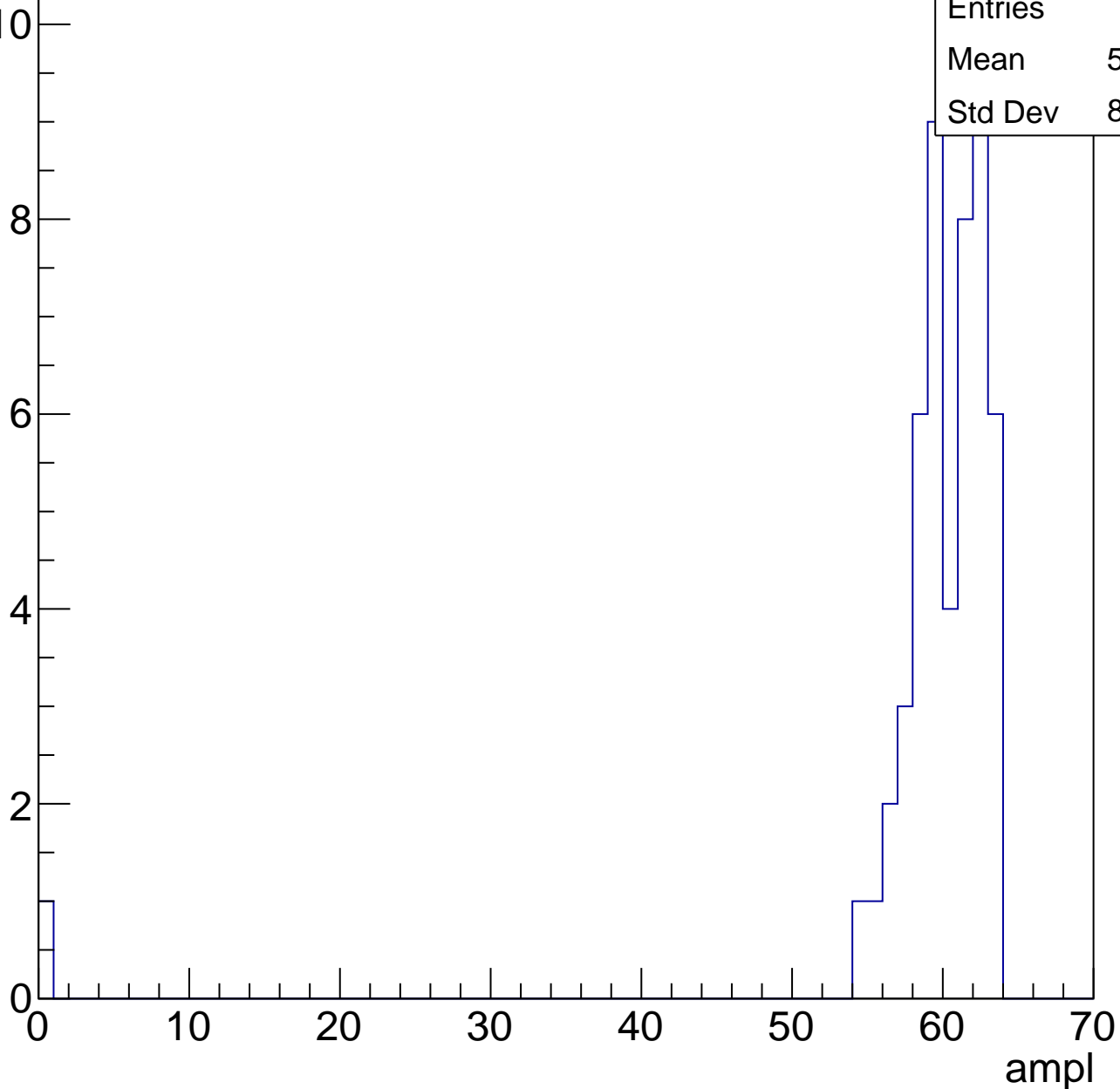


# B1L003S, U22-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

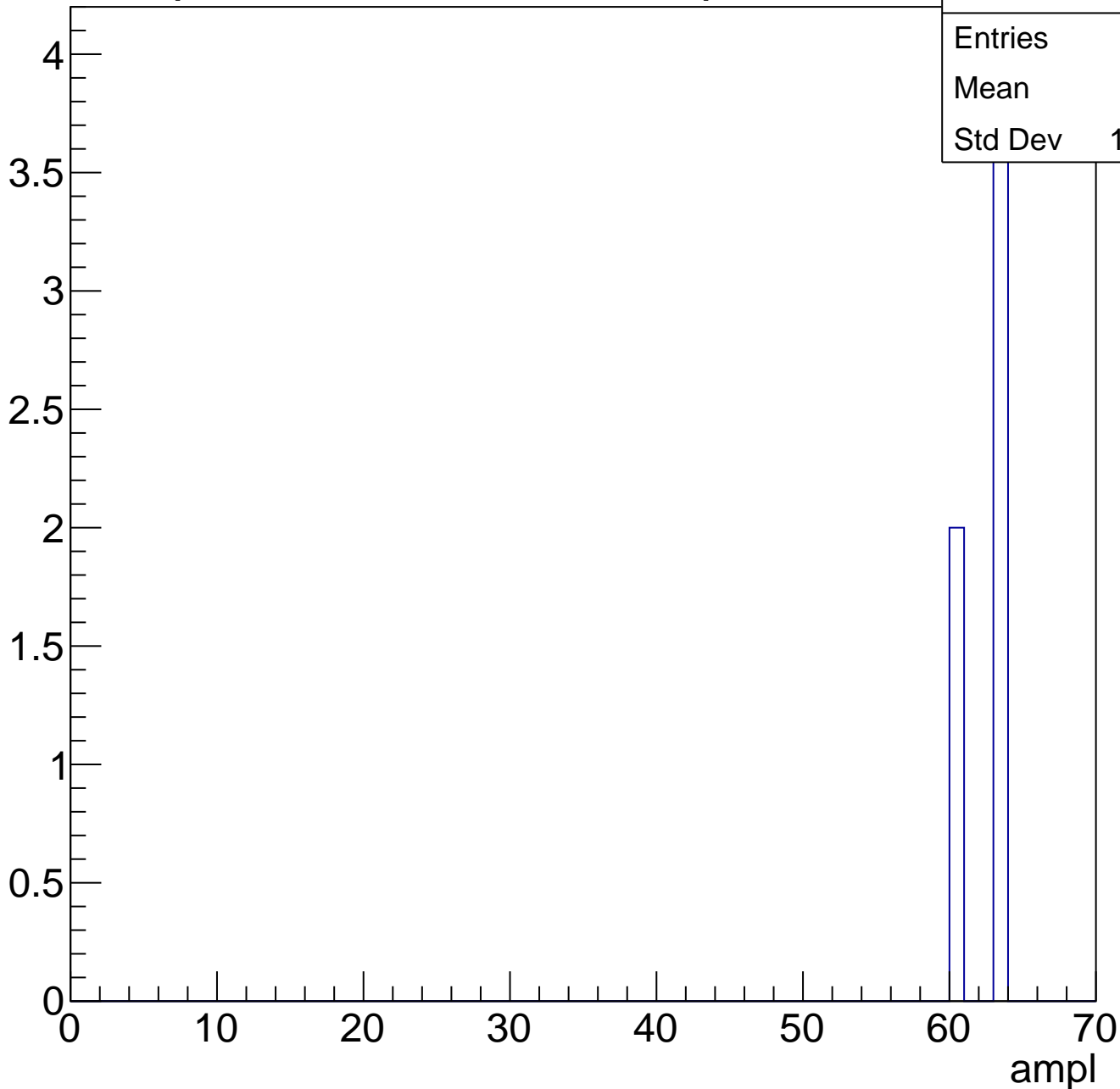
Entries	51
Mean	58.76
Std Dev	8.606



# B1L003S, U22-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	22
Std Dev	0

# B1L003S, U22-ch11, adc0

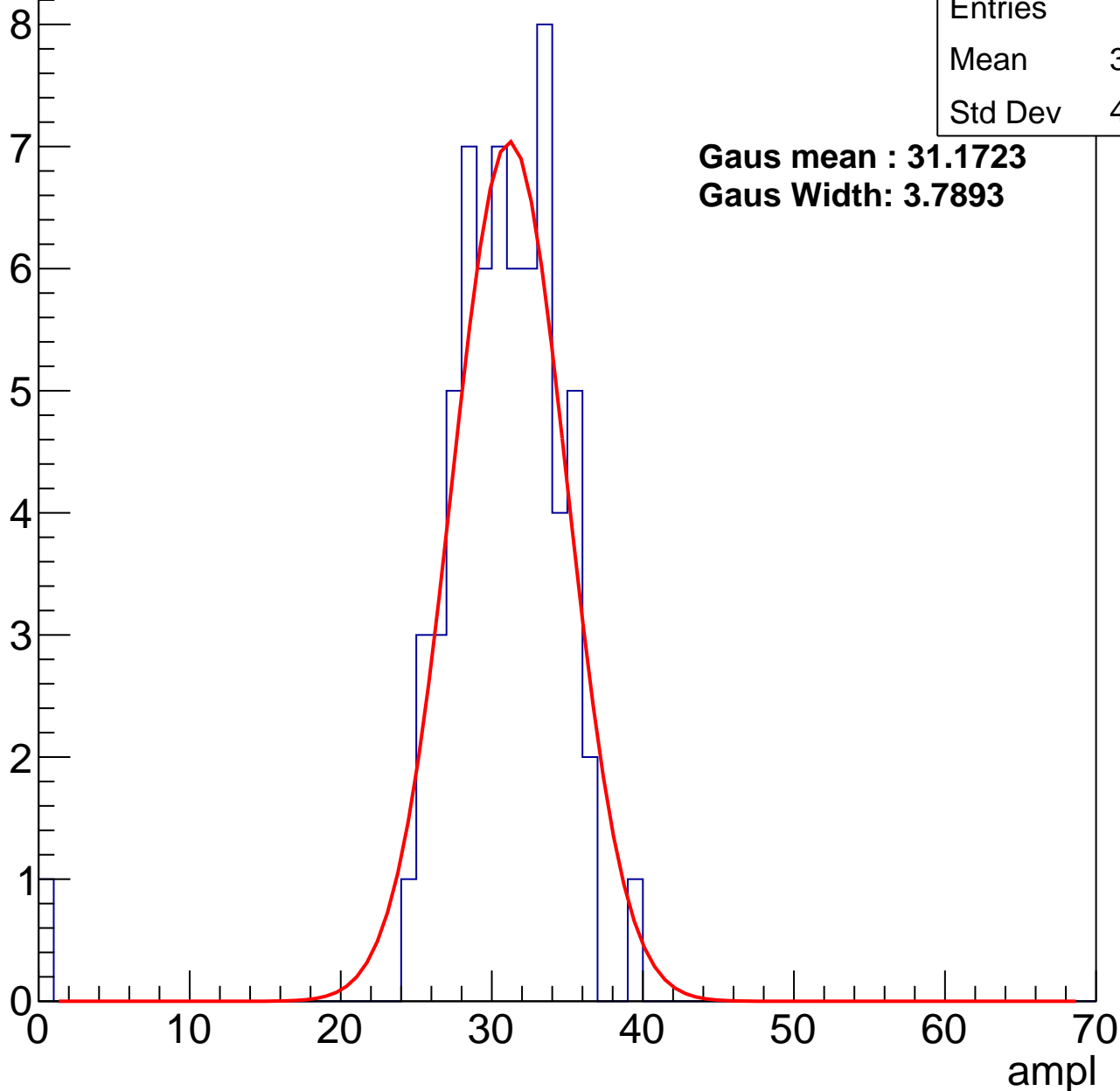
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.09
Std Dev	4.933

**Gaus mean : 31.1723**

**Gaus Width: 3.7893**



# B1L003S, U22-ch11, adc1

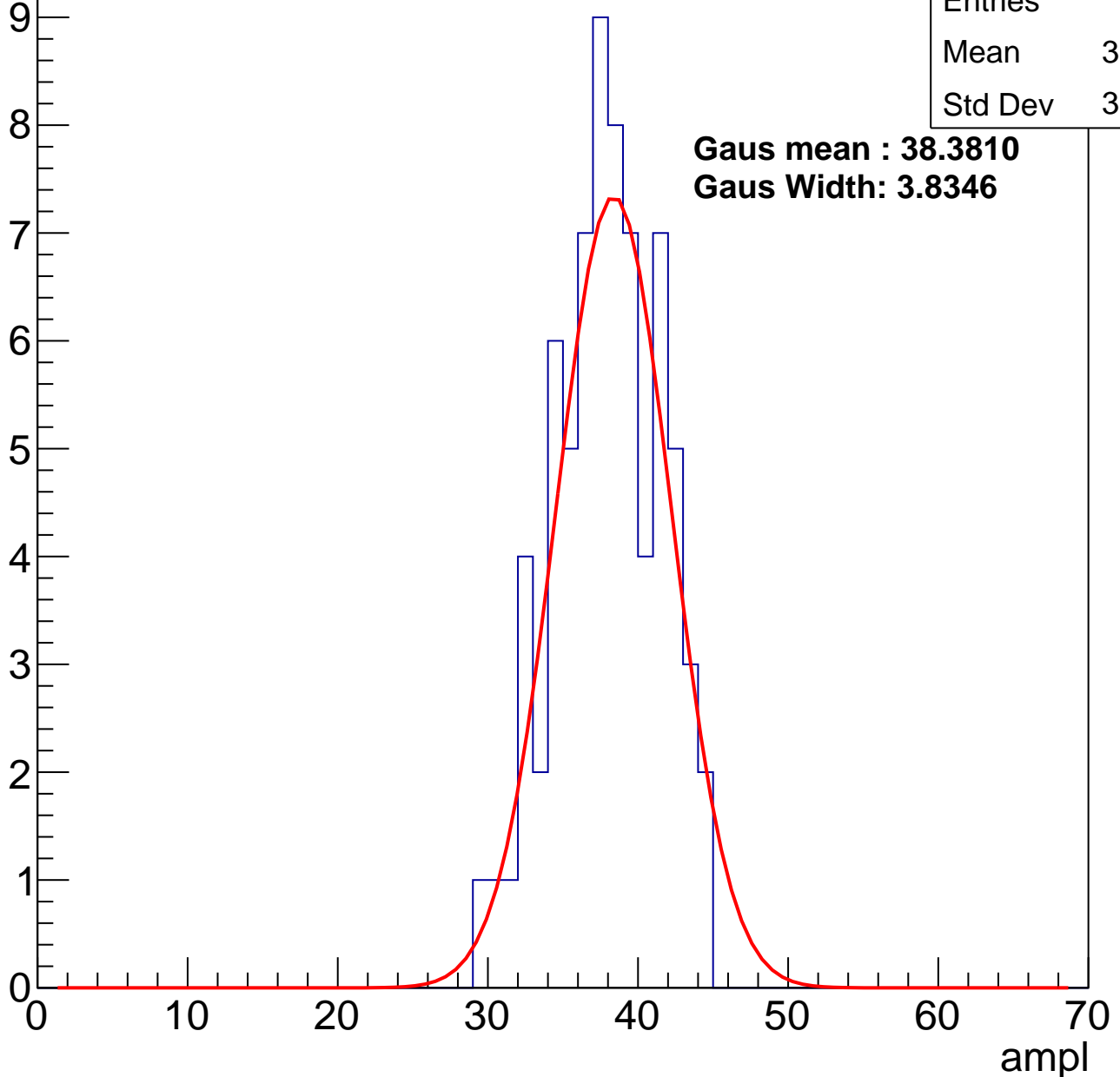
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.49
Std Dev	3.456

**Gaus mean : 38.3810**

**Gaus Width: 3.8346**



# B1L003S, U22-ch11, adc2

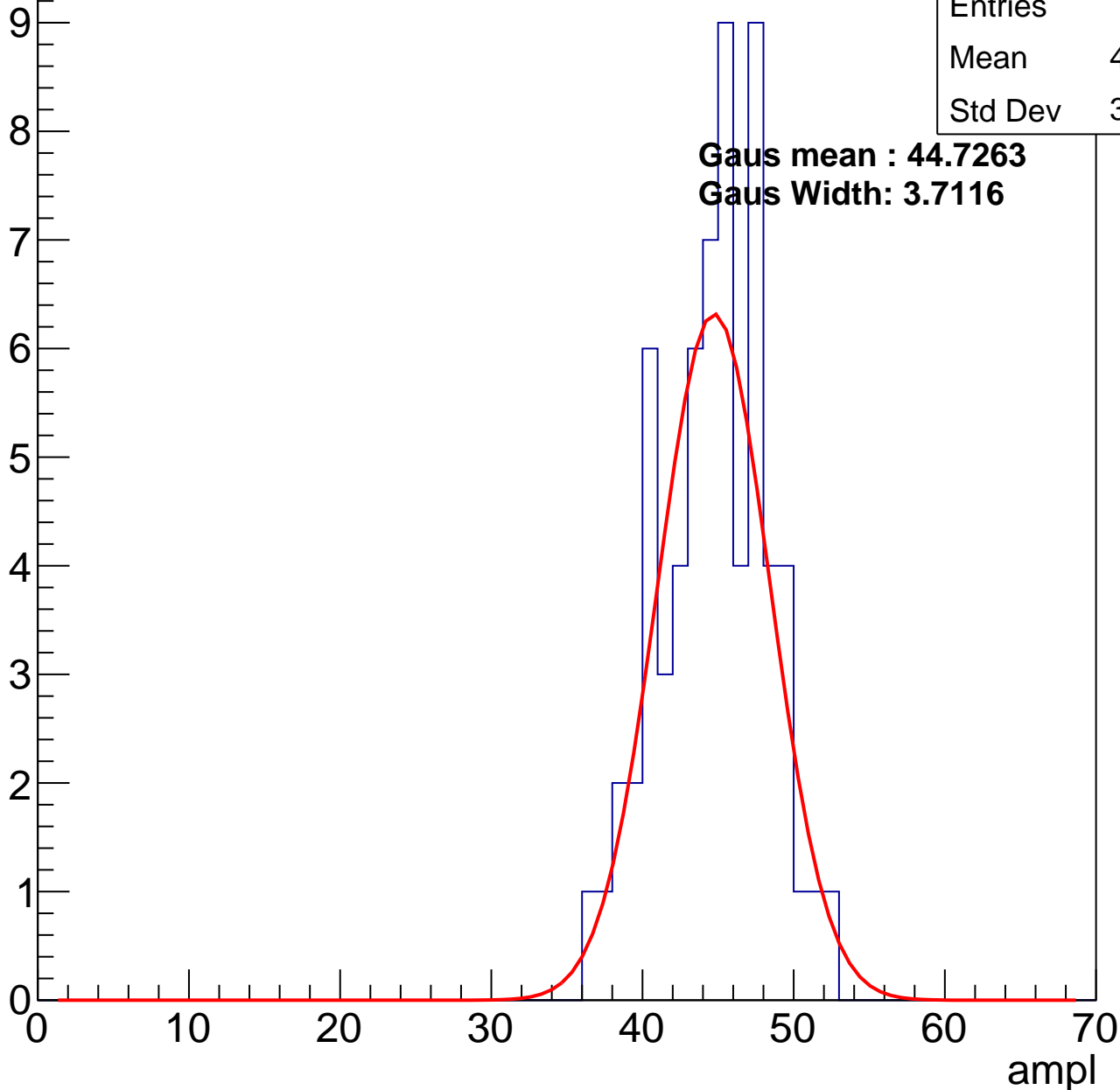
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	44.26
Std Dev	3.496

**Gaus mean : 44.7263**

**Gaus Width: 3.7116**

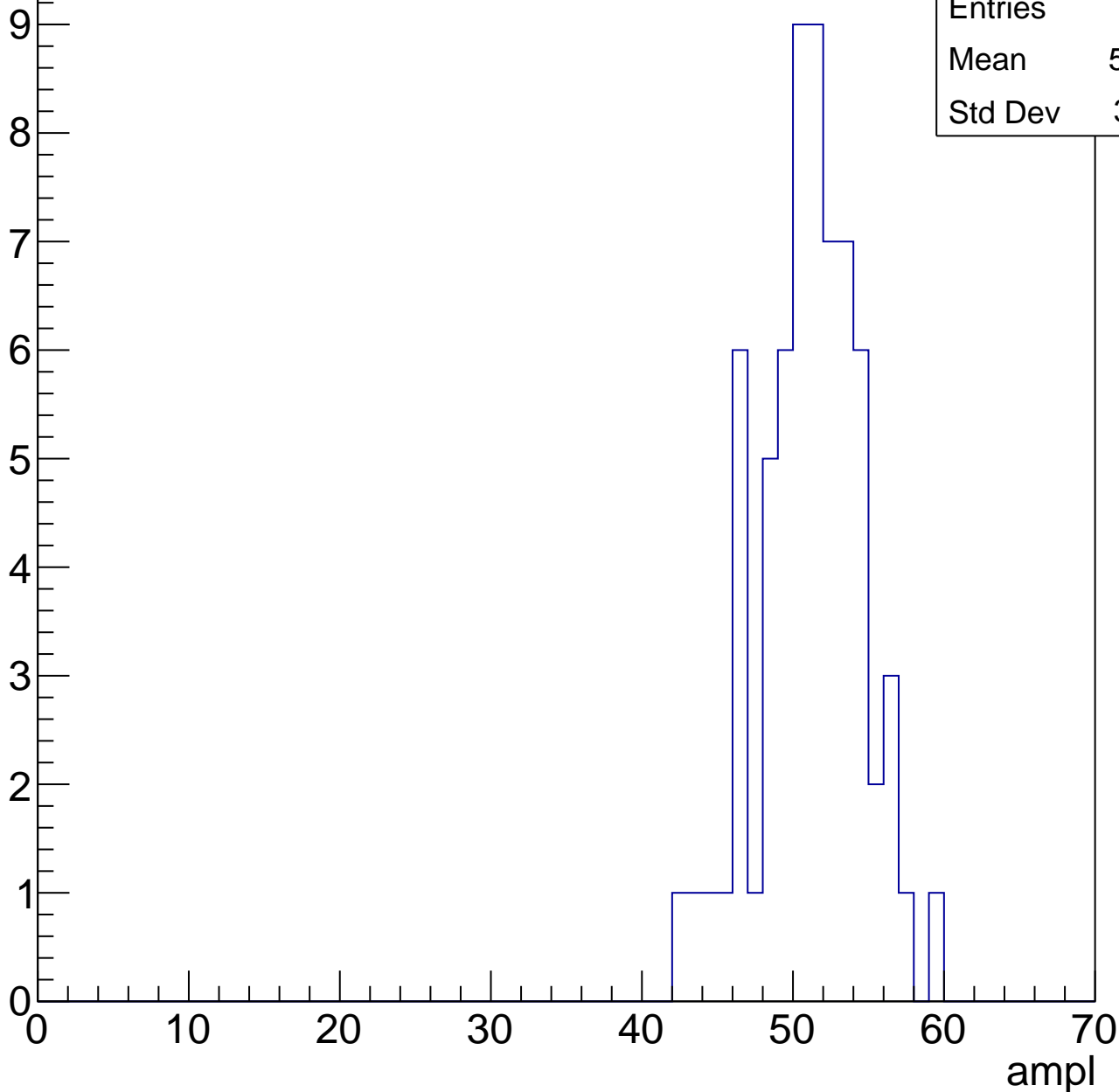


# B1L003S, U22-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	50.64
Std Dev	3.371

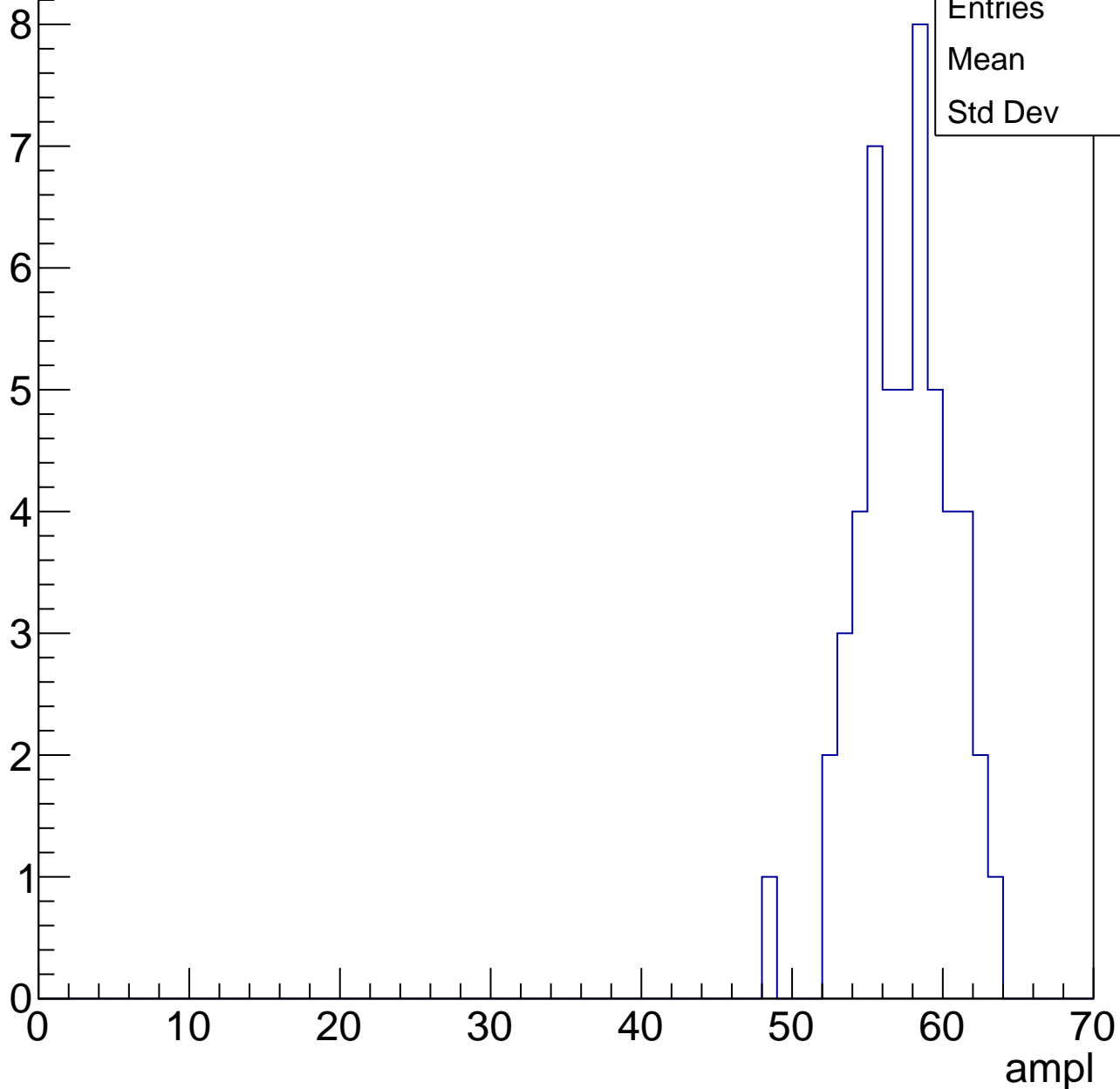


# B1L003S, U22-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	57
Std Dev	3.01

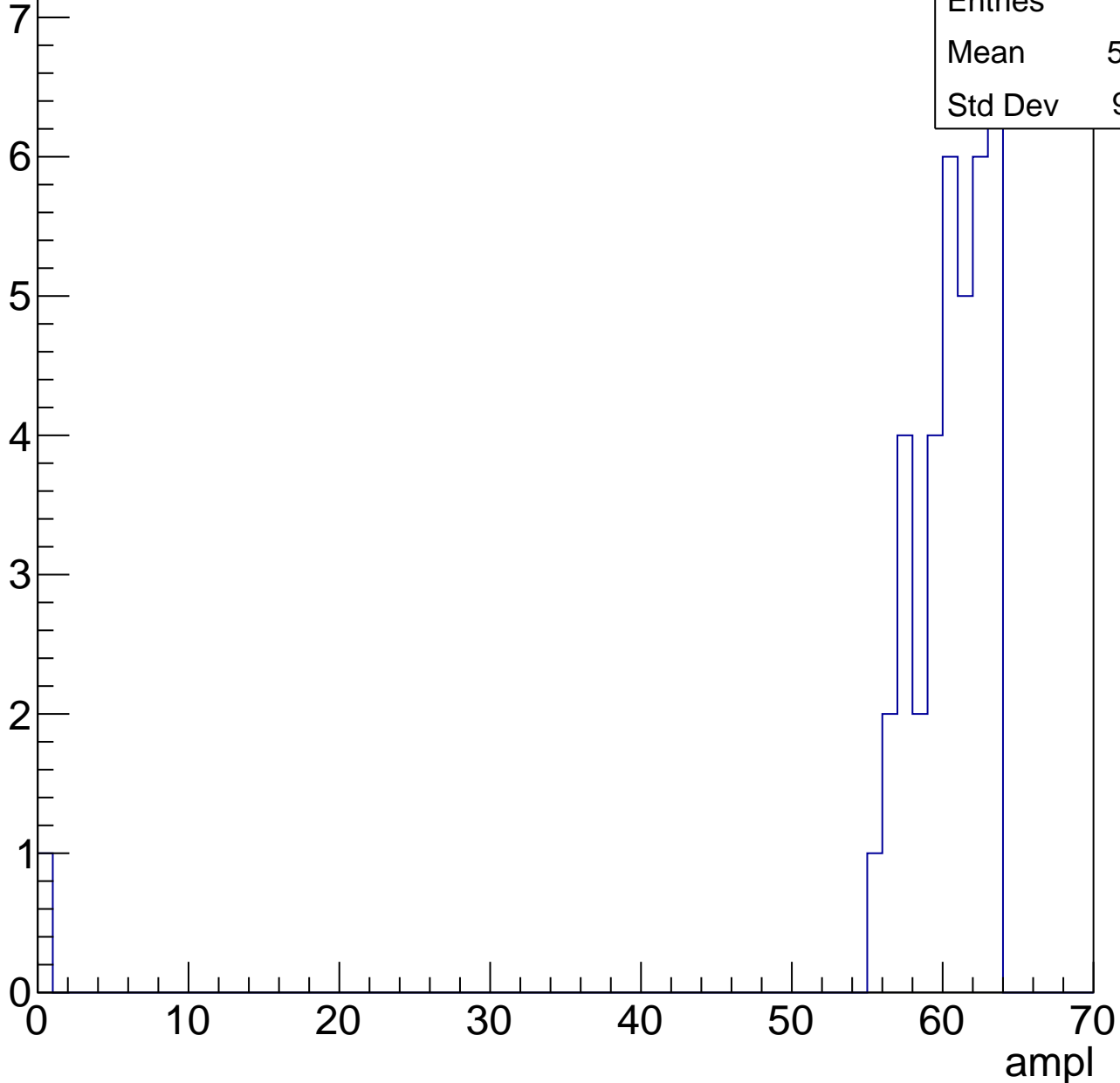


# B1L003S, U22-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	38
Mean	58.55
Std Dev	9.891



# B1L003S, U22-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

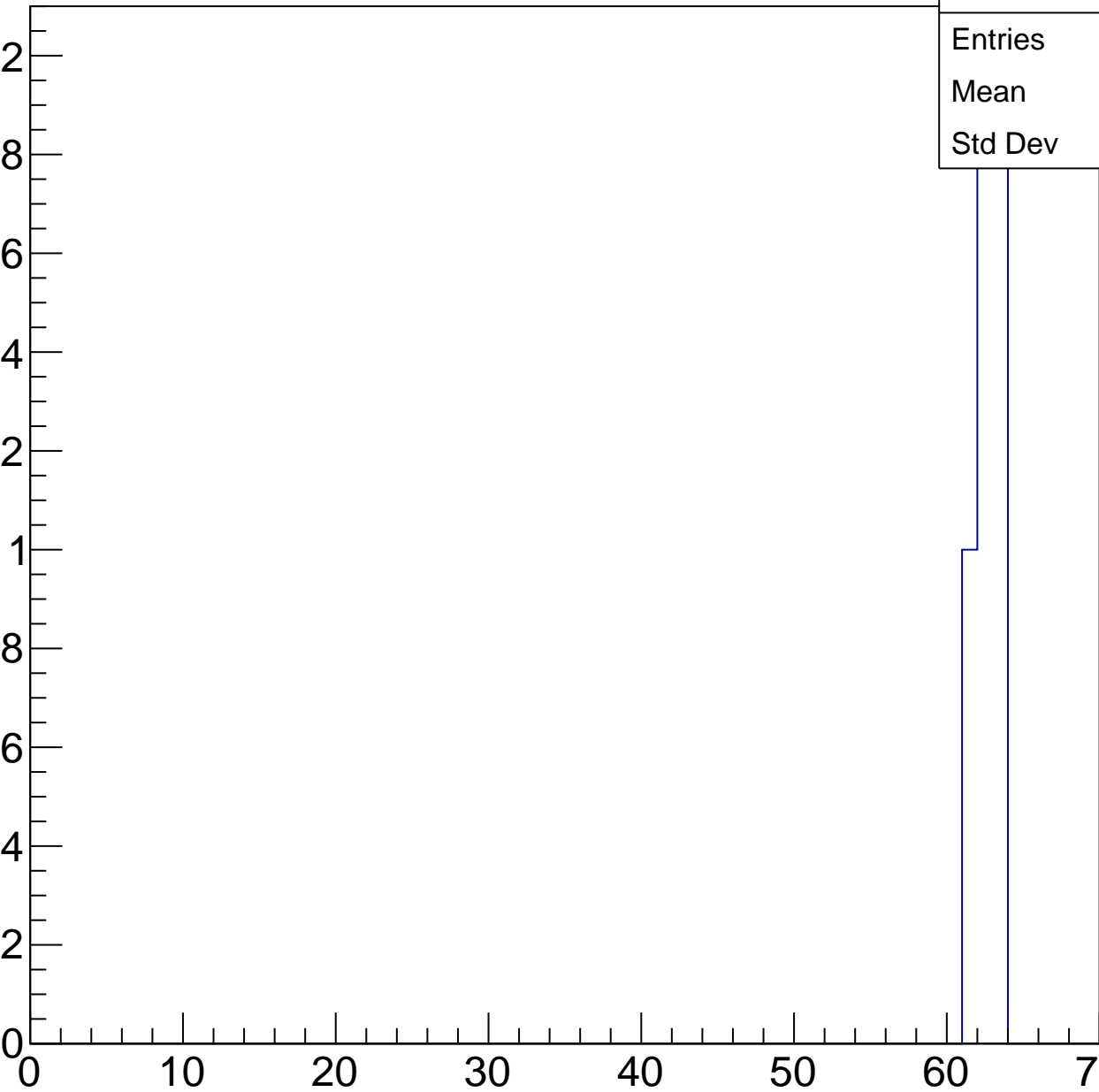
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl





# B1L003S, U22-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch12, adc0

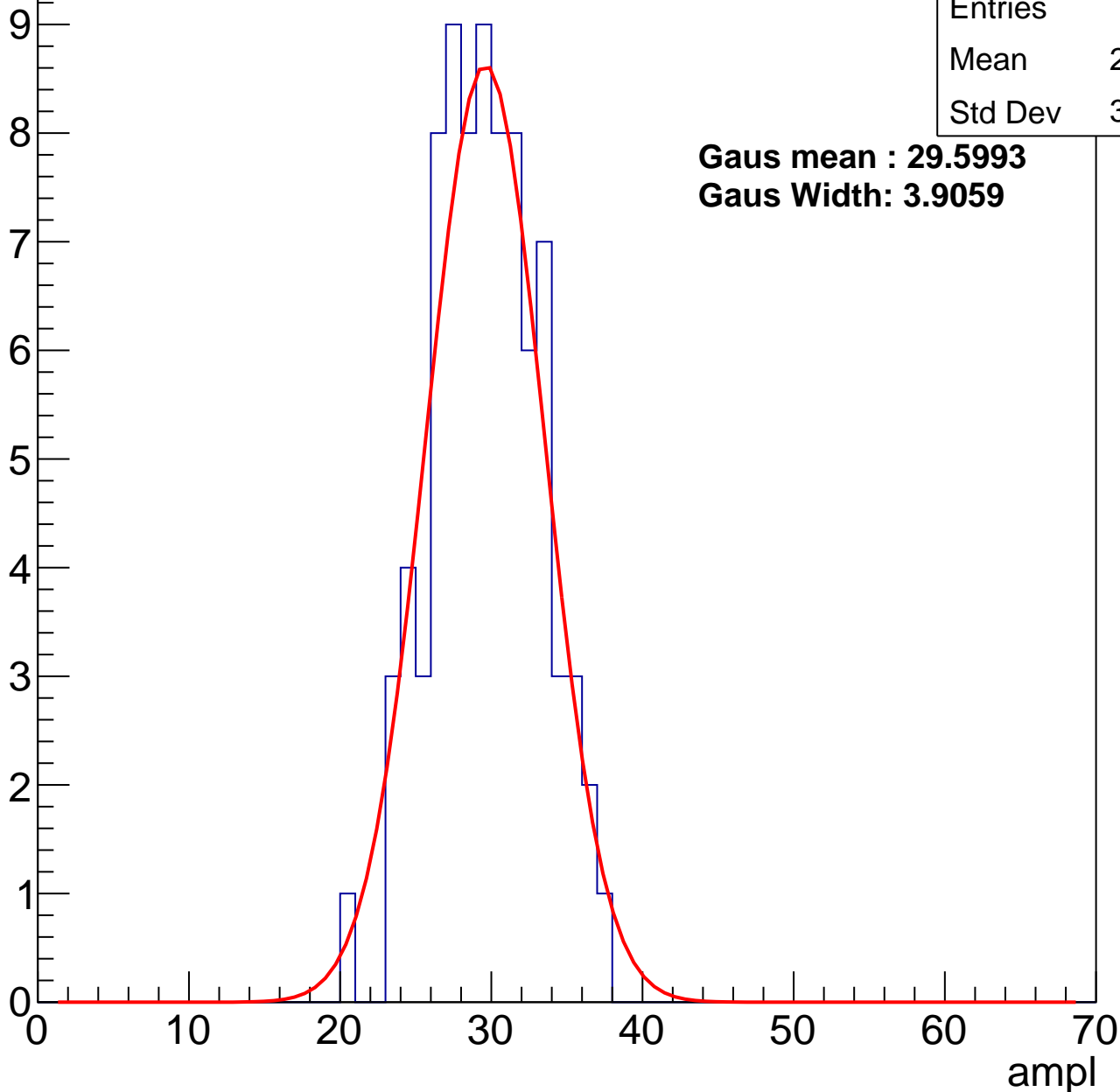
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	29.19
Std Dev	3.473

**Gaus mean : 29.5993**

**Gaus Width: 3.9059**



# B1L003S, U22-ch12, adc1

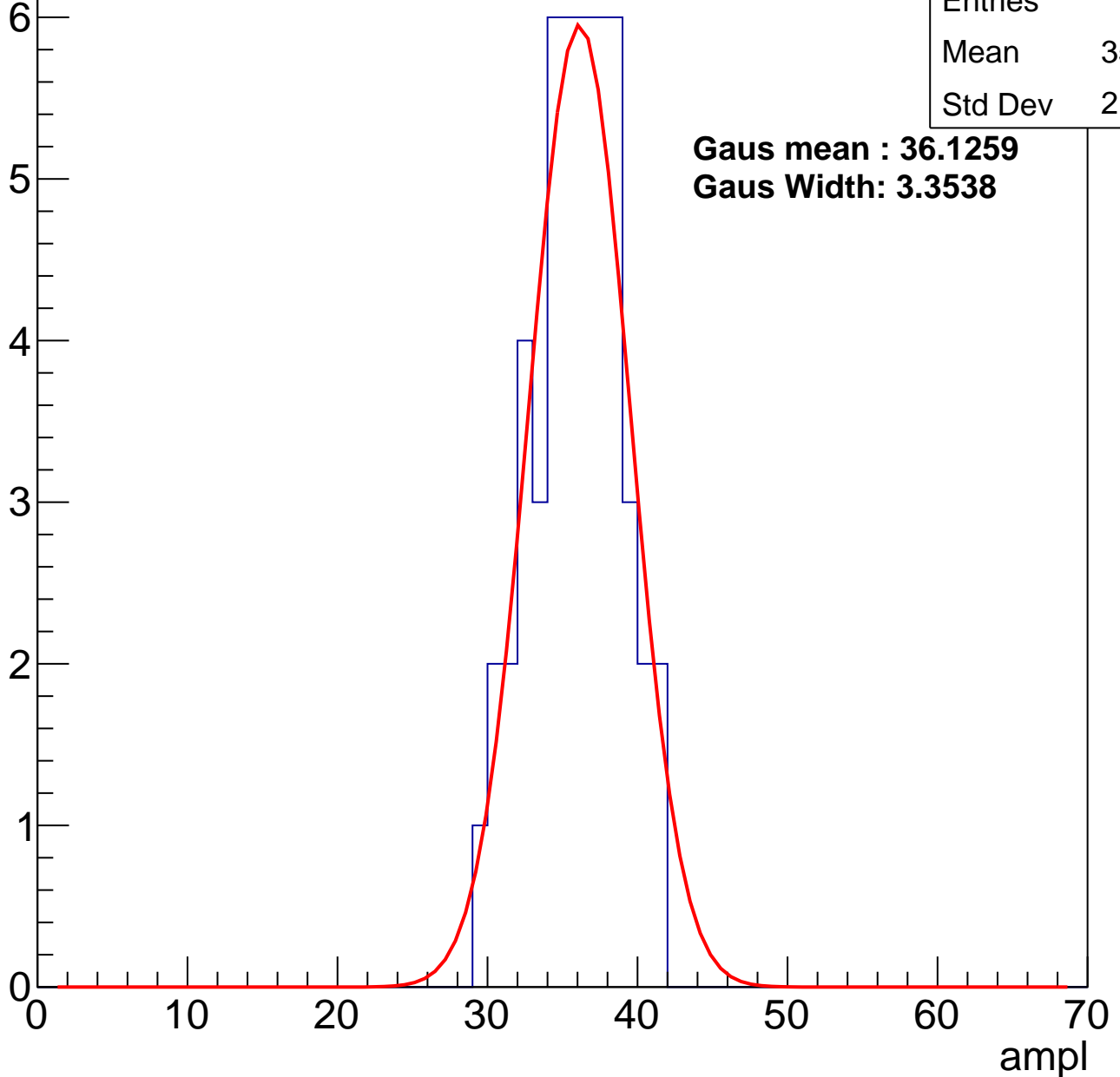
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	35.45
Std Dev	2.914

**Gaus mean : 36.1259**

**Gaus Width: 3.3538**



# B1L003S, U22-ch12, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	41.69
Std Dev	3.046

**Gaus mean : 42.1137**

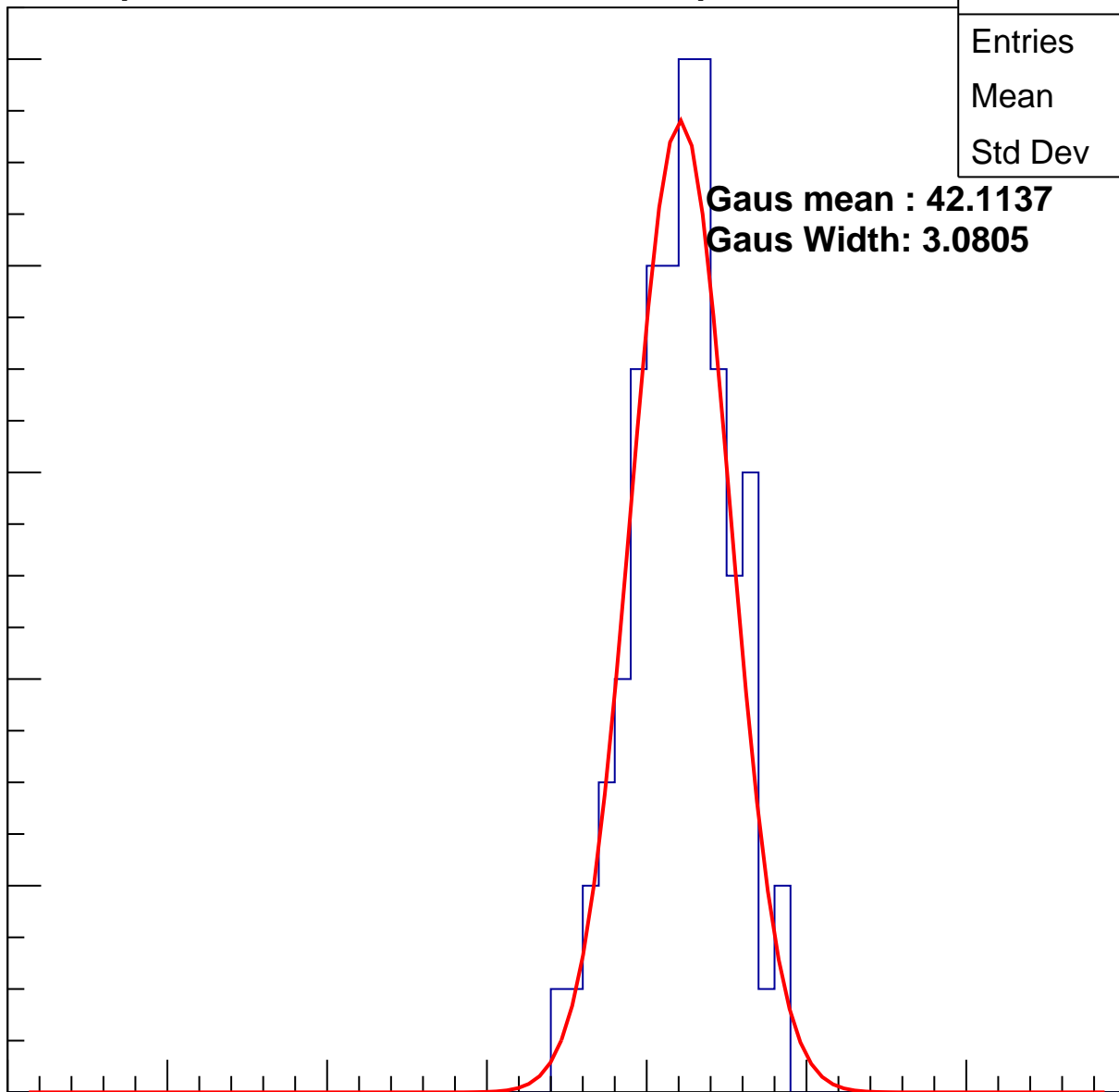
**Gaus Width: 3.0805**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

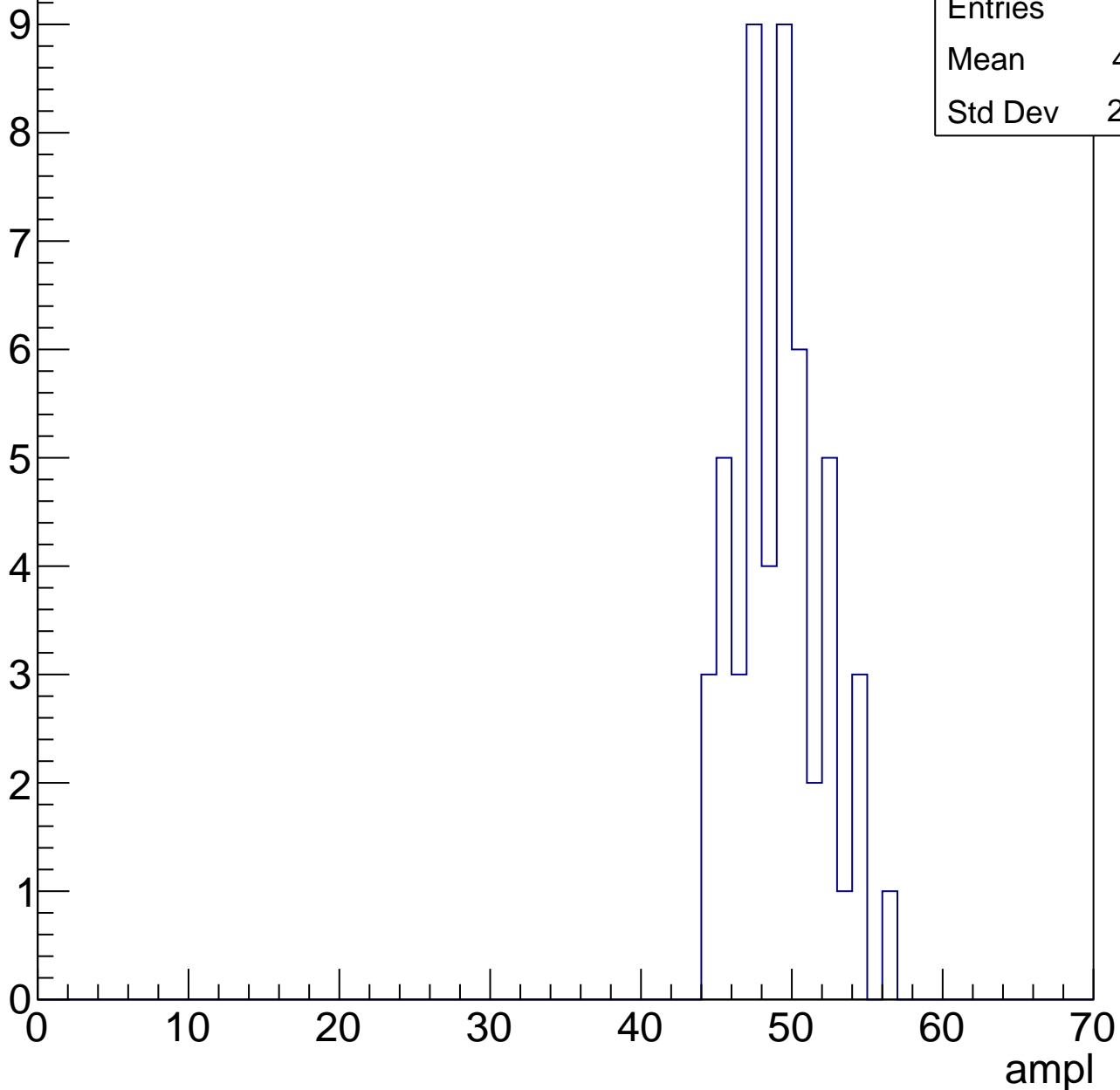


# B1L003S, U22-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	48.71
Std Dev	2.858

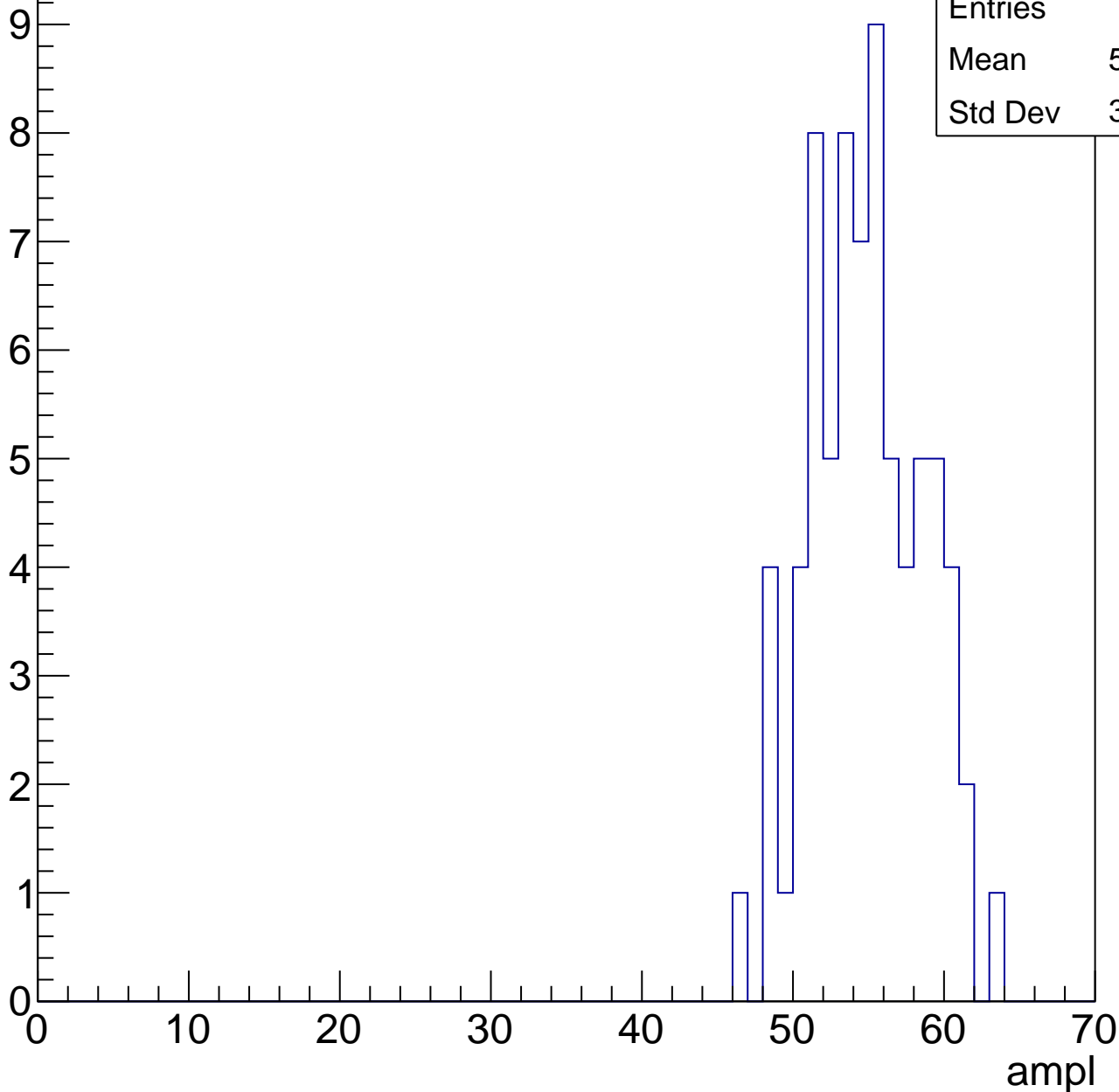


# B1L003S, U22-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	54.38
Std Dev	3.663

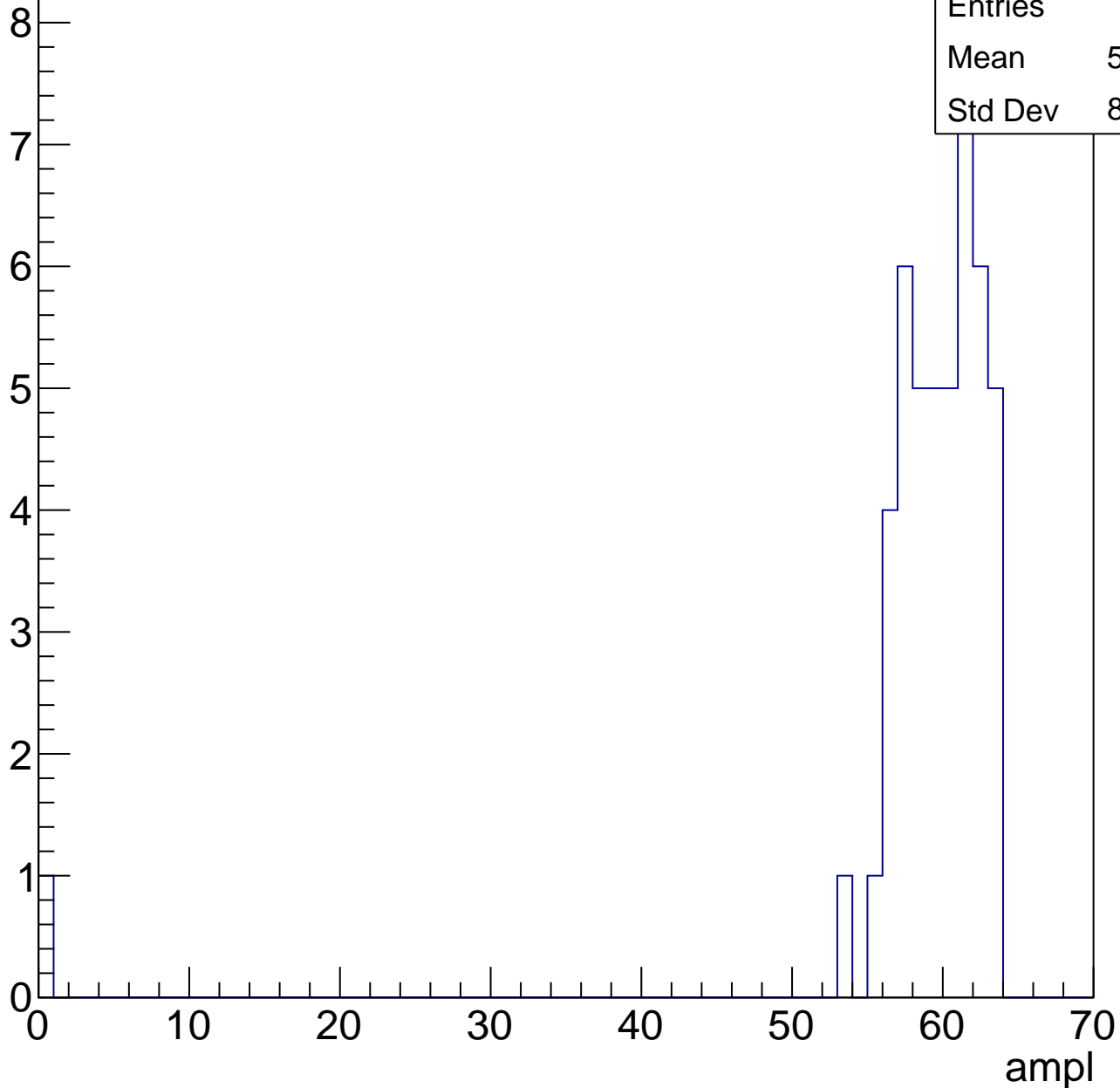


# B1L003S, U22-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

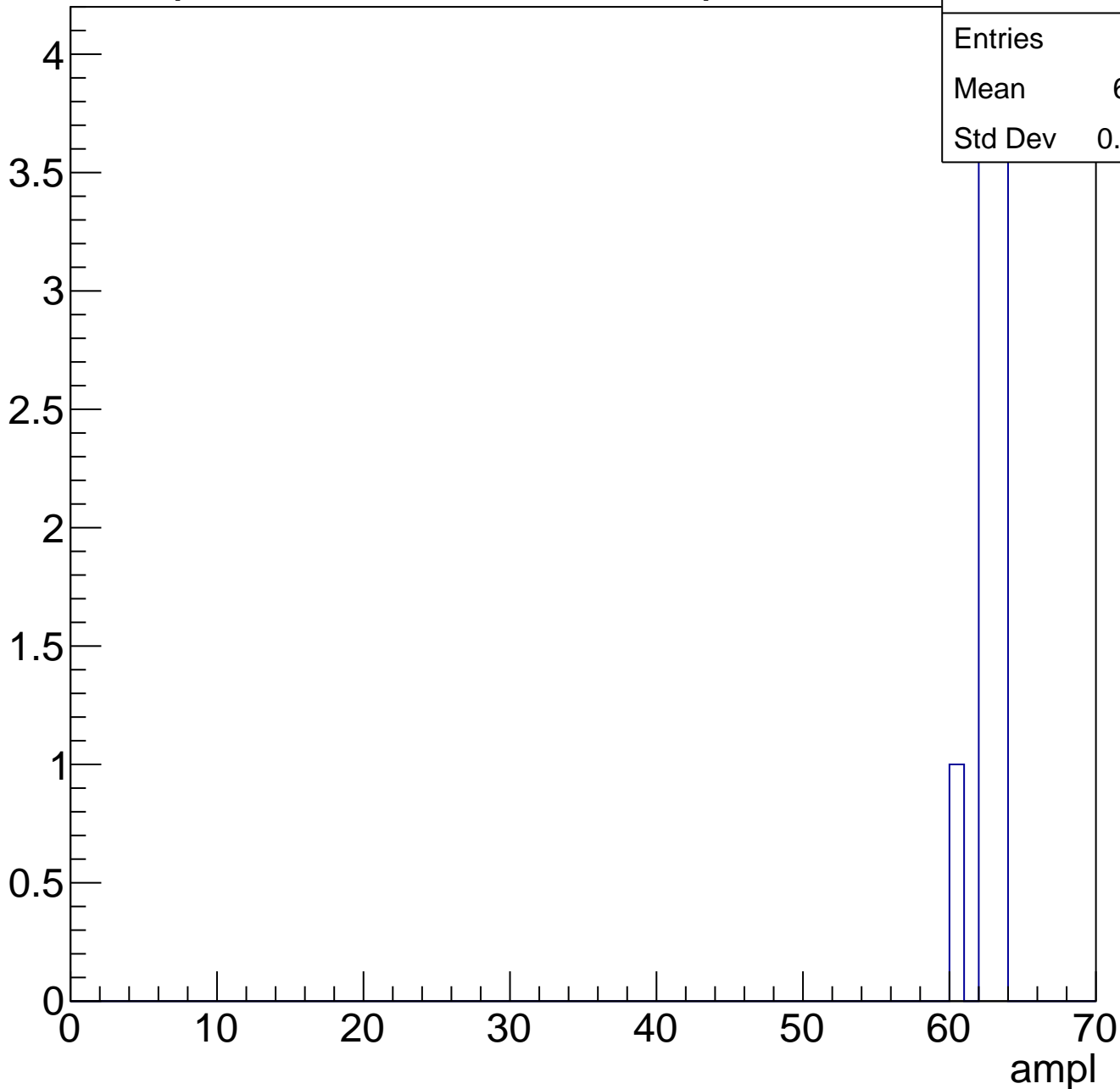
Entries	47
Mean	58.17
Std Dev	8.916



# B1L003S, U22-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch13, adc0

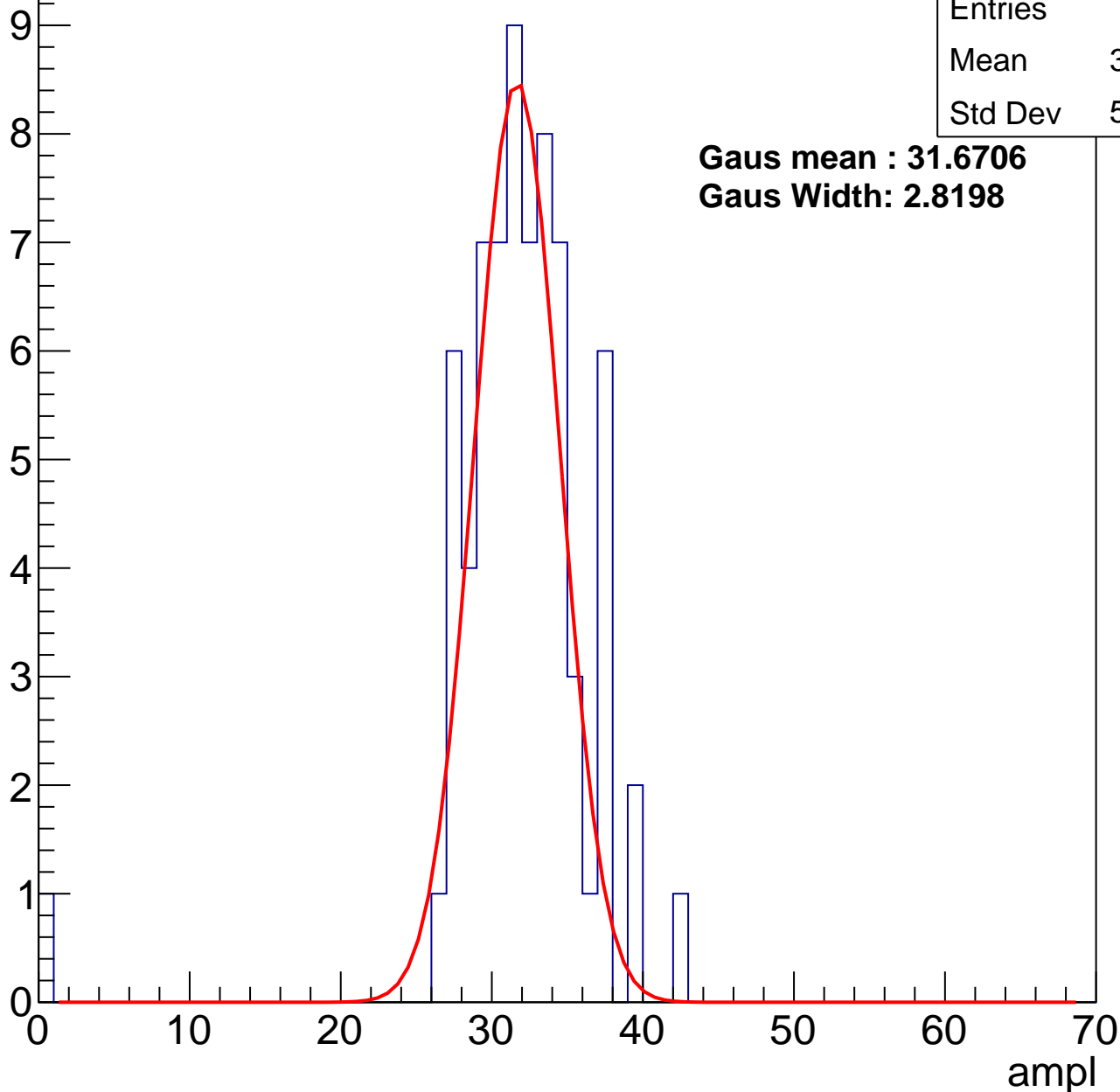
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	31.44
Std Dev	5.042

**Gaus mean : 31.6706**

**Gaus Width: 2.8198**



# B1L003S, U22-ch13, adc1

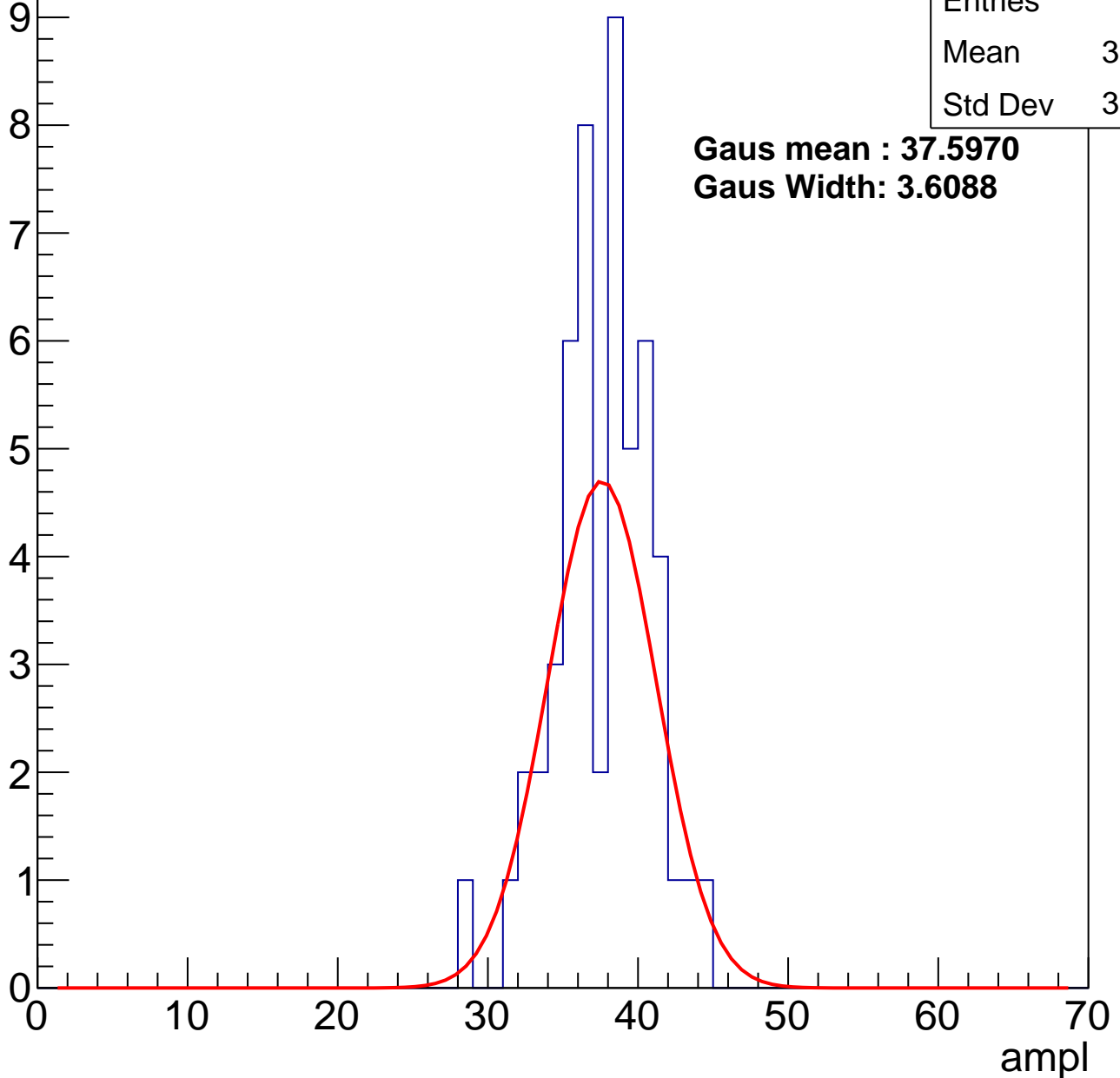
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	37.17
Std Dev	3.142

**Gaus mean : 37.5970**

**Gaus Width: 3.6088**



# B1L003S, U22-ch13, adc2

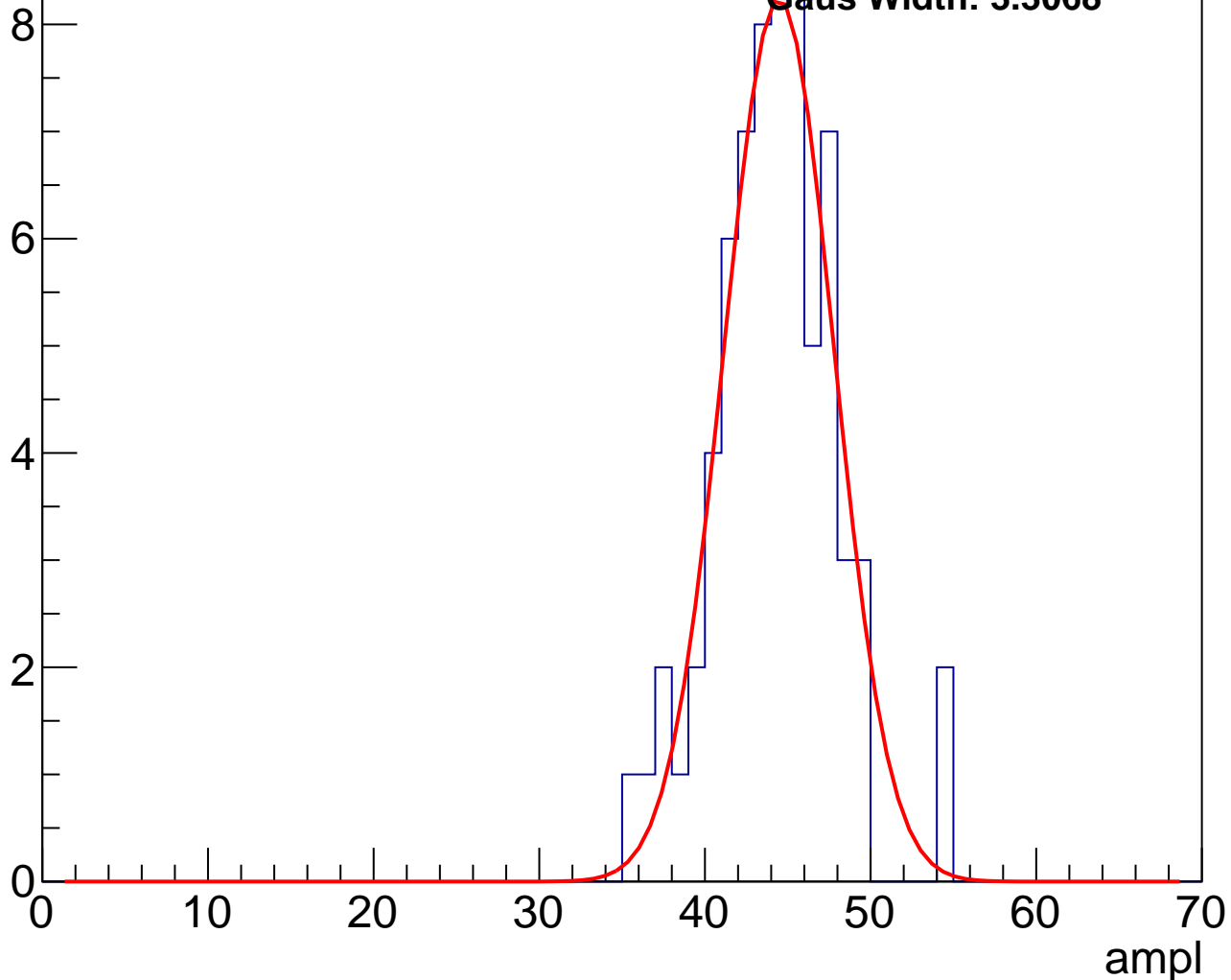
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	43.79
Std Dev	3.552

**Gaus mean : 44.4691**

**Gaus Width: 3.3068**

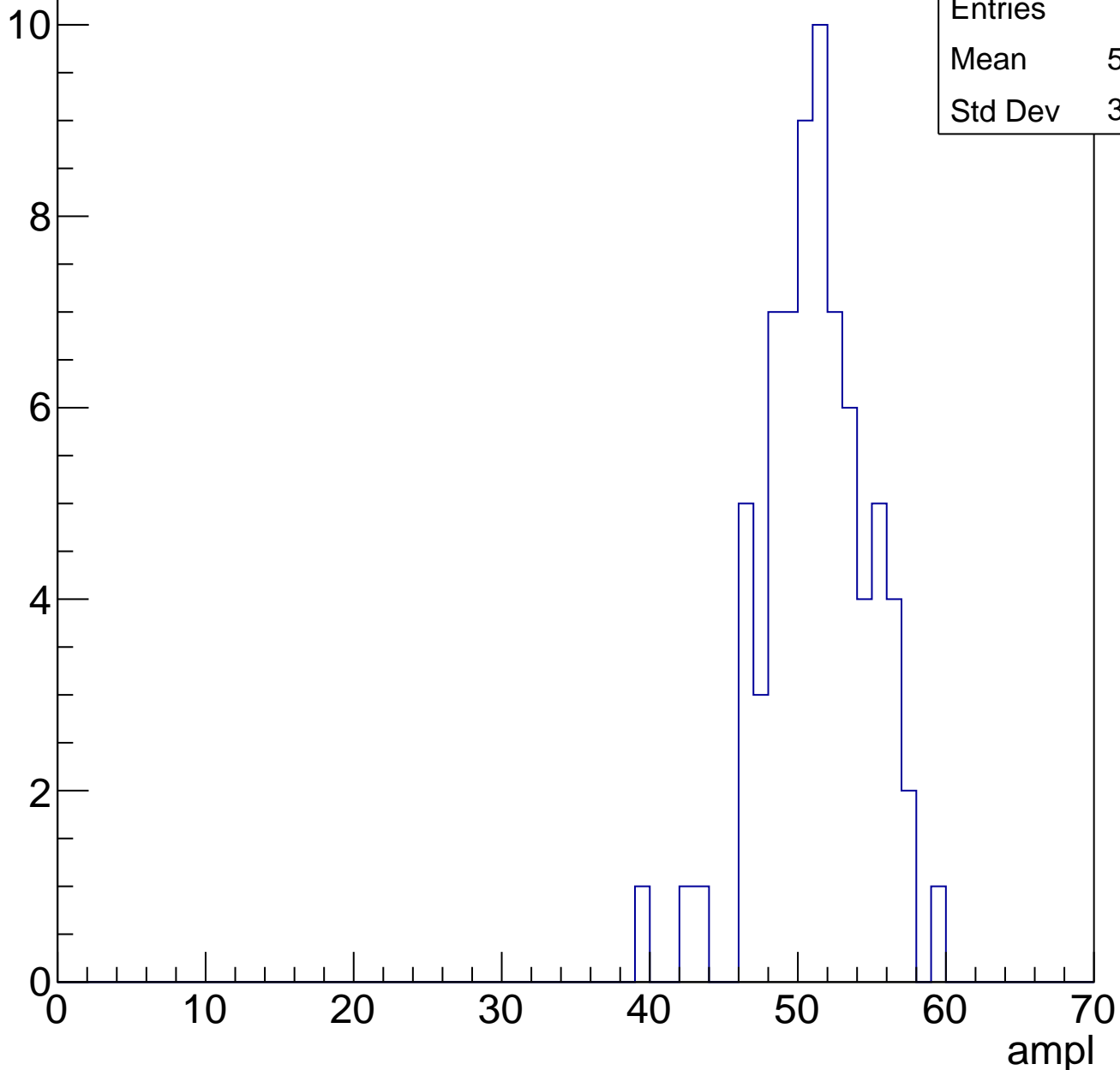


# B1L003S, U22-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	50.74
Std Dev	3.589

Entry

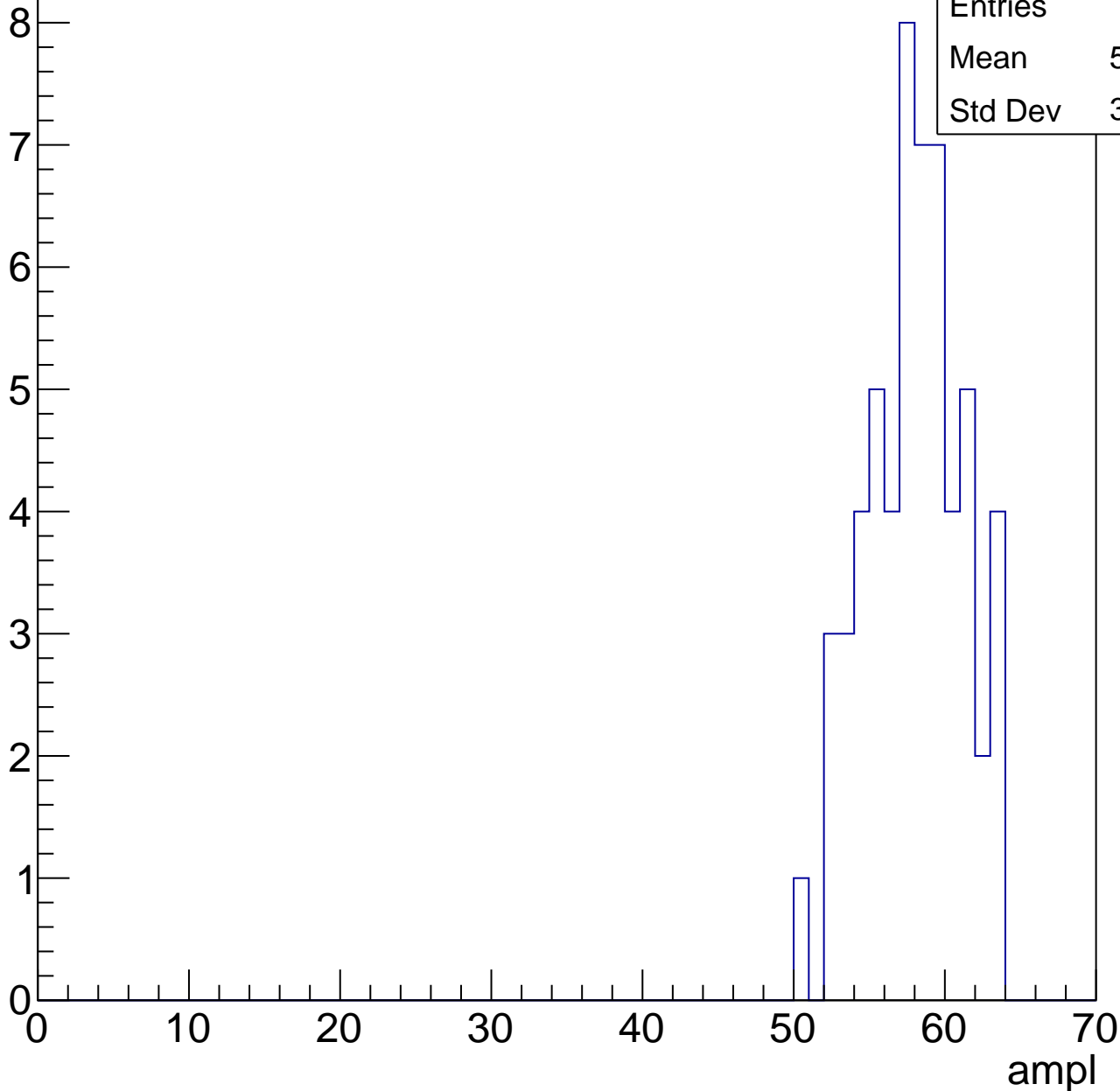


# B1L003S, U22-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57.47
Std Dev	3.146

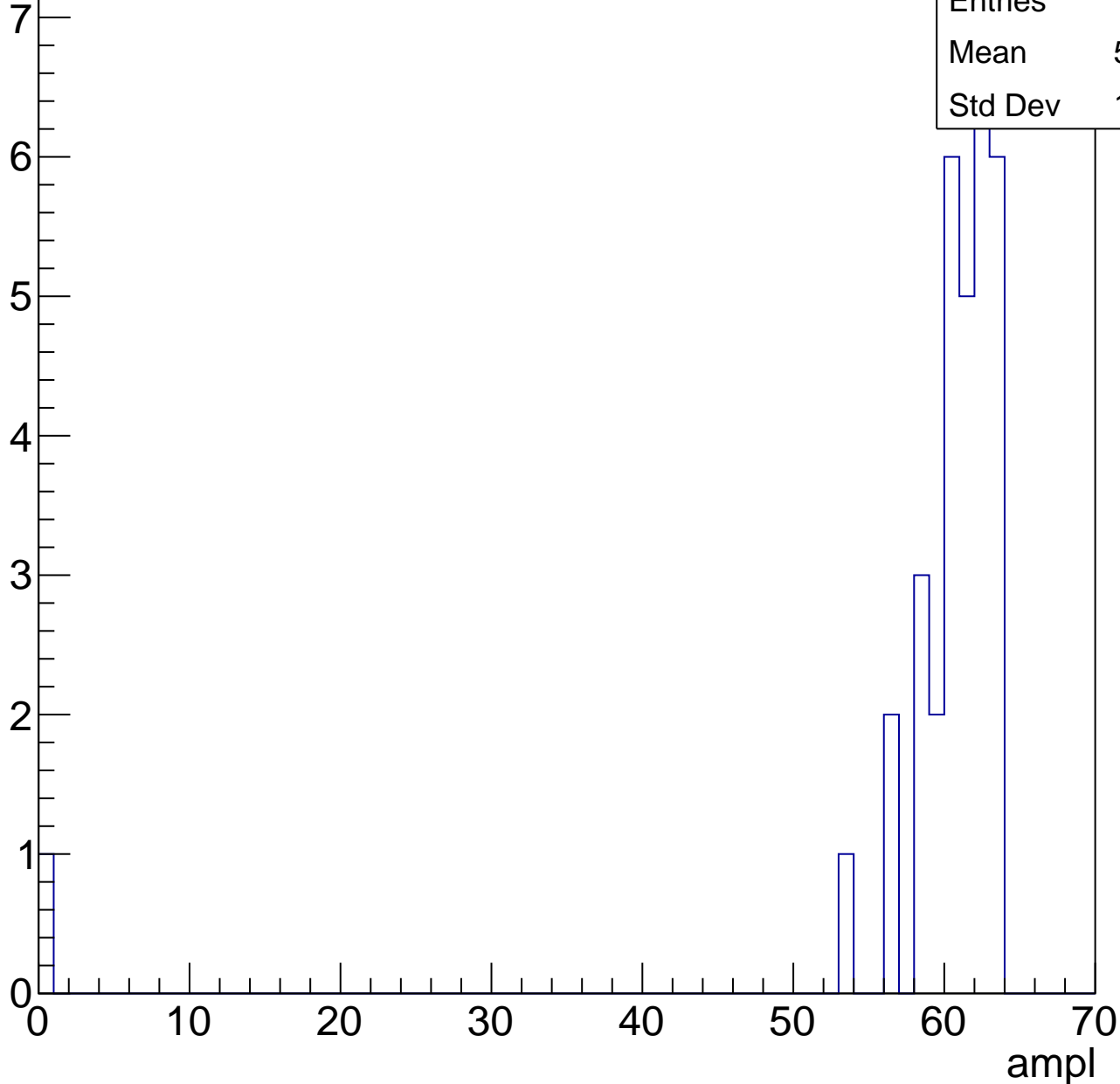


# B1L003S, U22-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.61
Std Dev	10.61



# B1L003S, U22-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

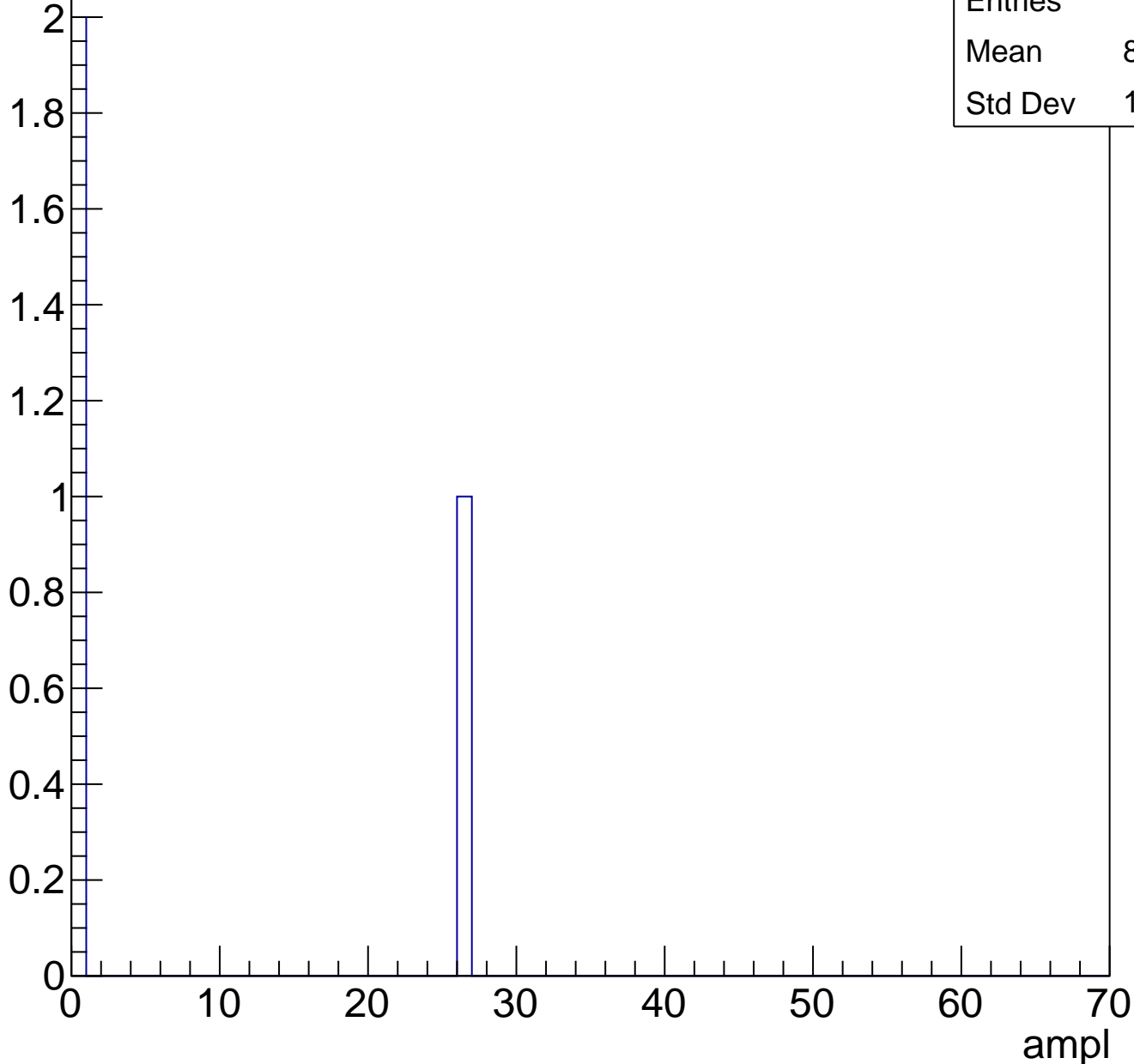




# B1L003S, U22-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch14, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	90
Mean	29.19
Std Dev	5.806

**Gaus mean : 29.7658**

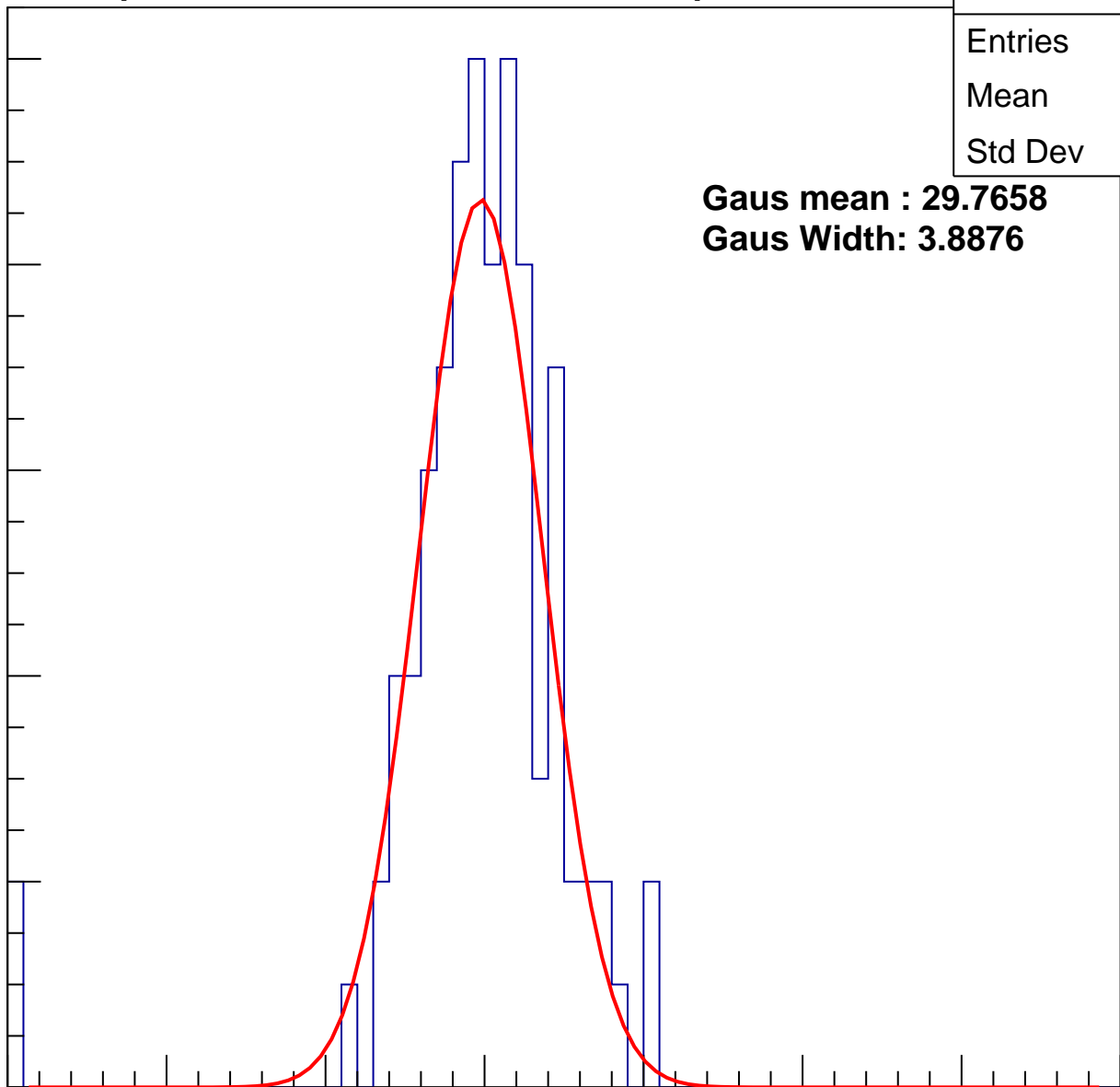
**Gaus Width: 3.8876**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch14, adc1

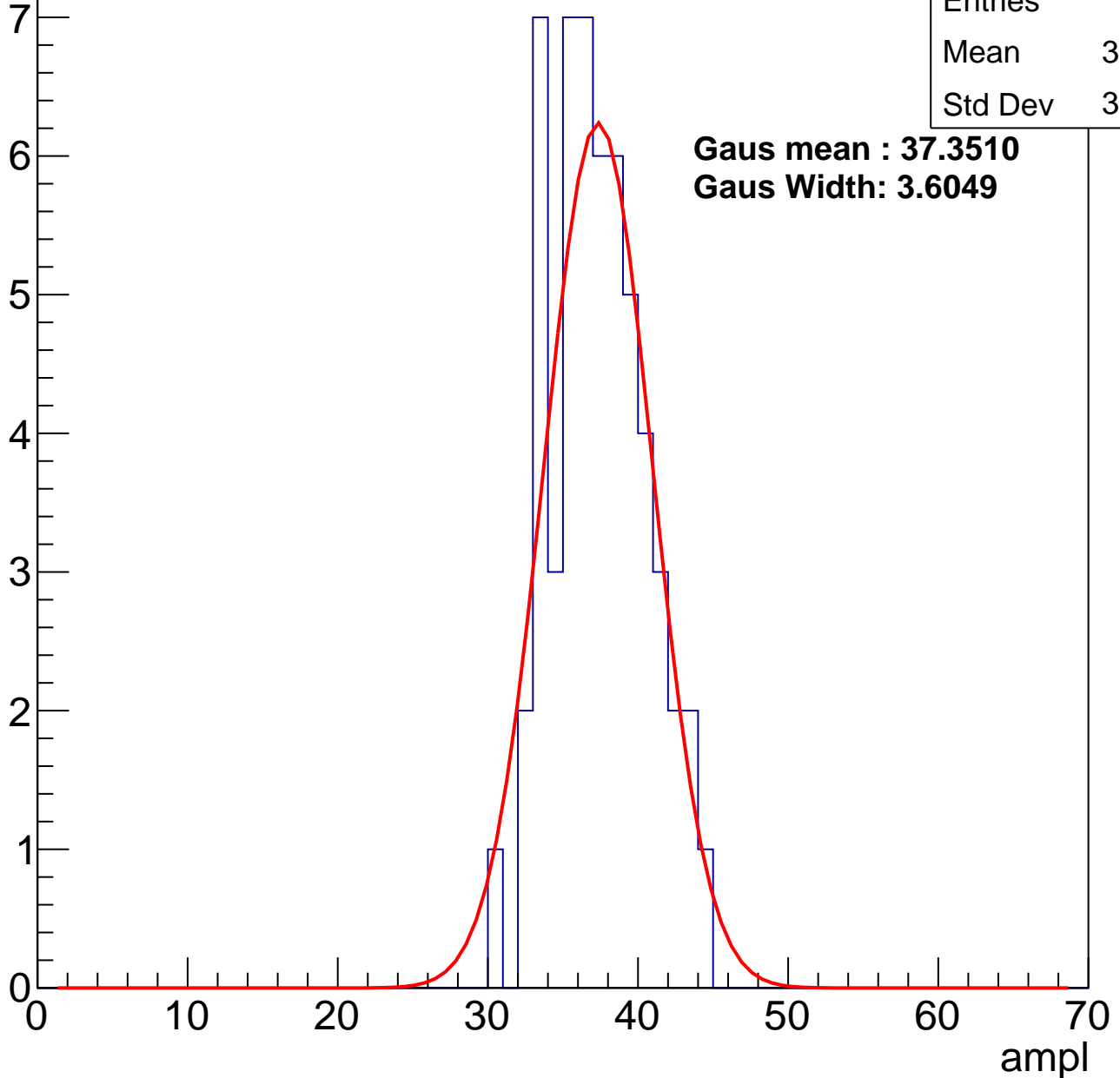
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.89
Std Dev	3.143

**Gaus mean : 37.3510**

**Gaus Width: 3.6049**



# B1L003S, U22-ch14, adc2

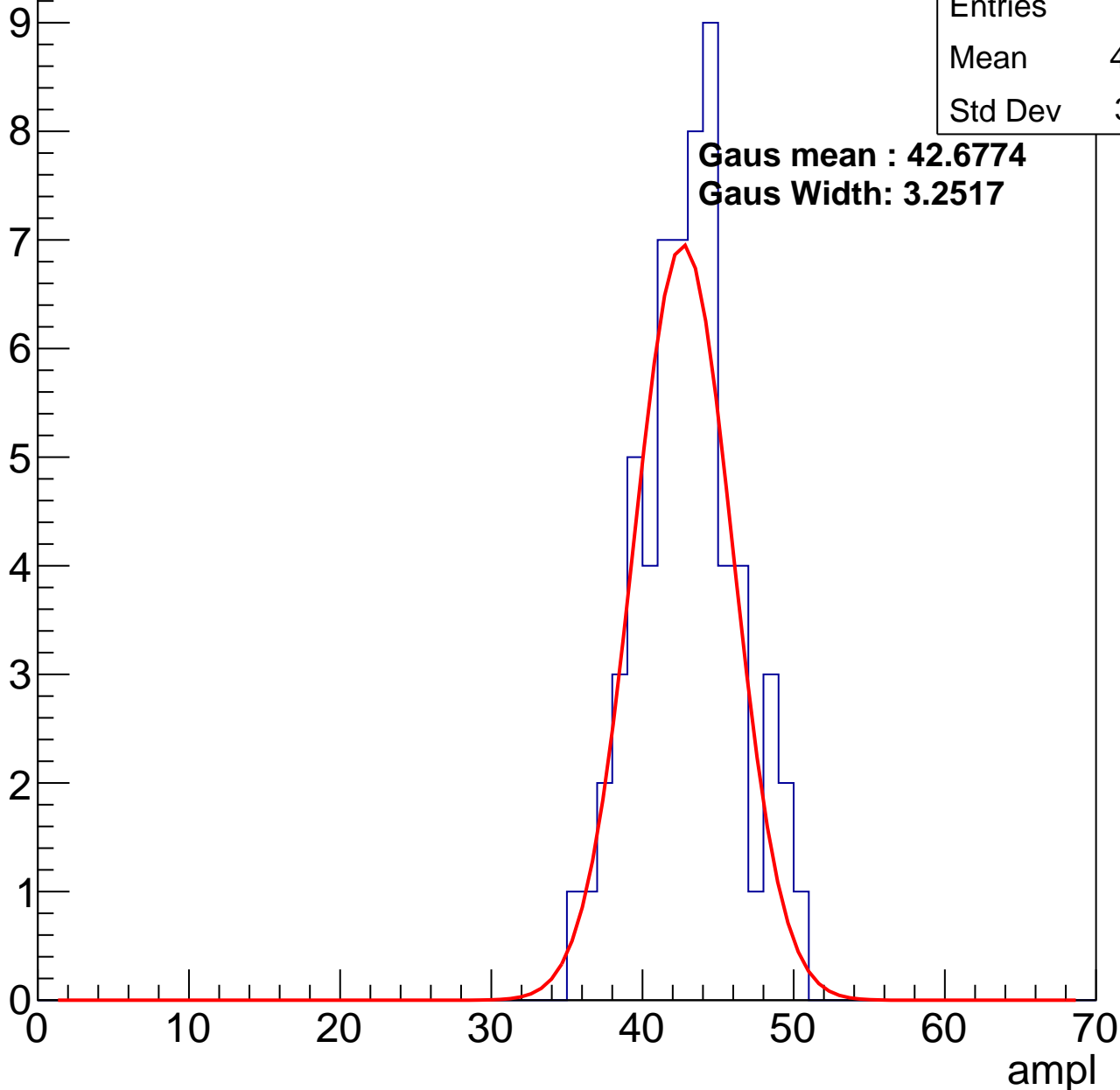
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	42.55
Std Dev	3.291

**Gaus mean : 42.6774**

**Gaus Width: 3.2517**

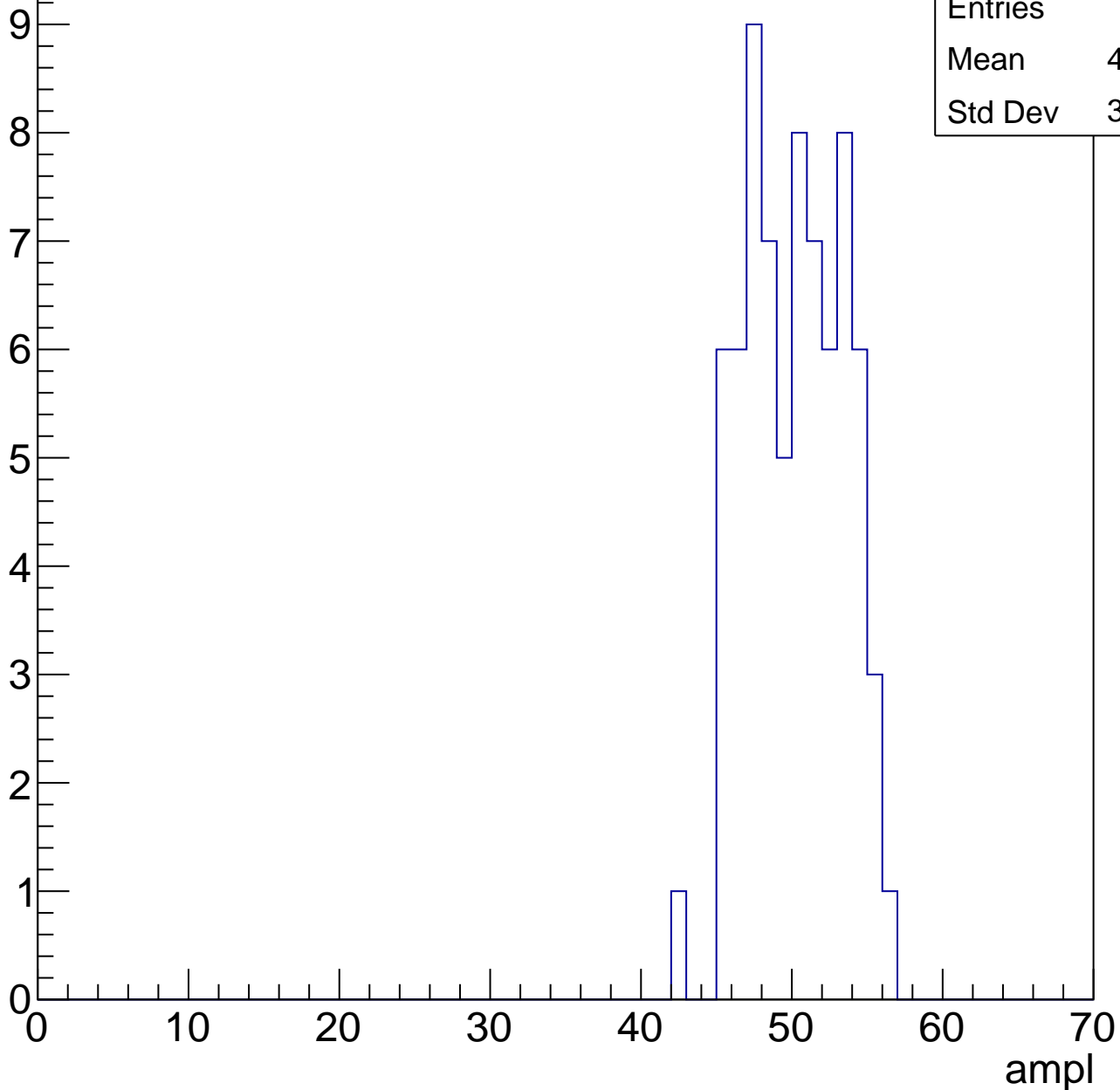


# B1L003S, U22-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	49.73
Std Dev	3.159

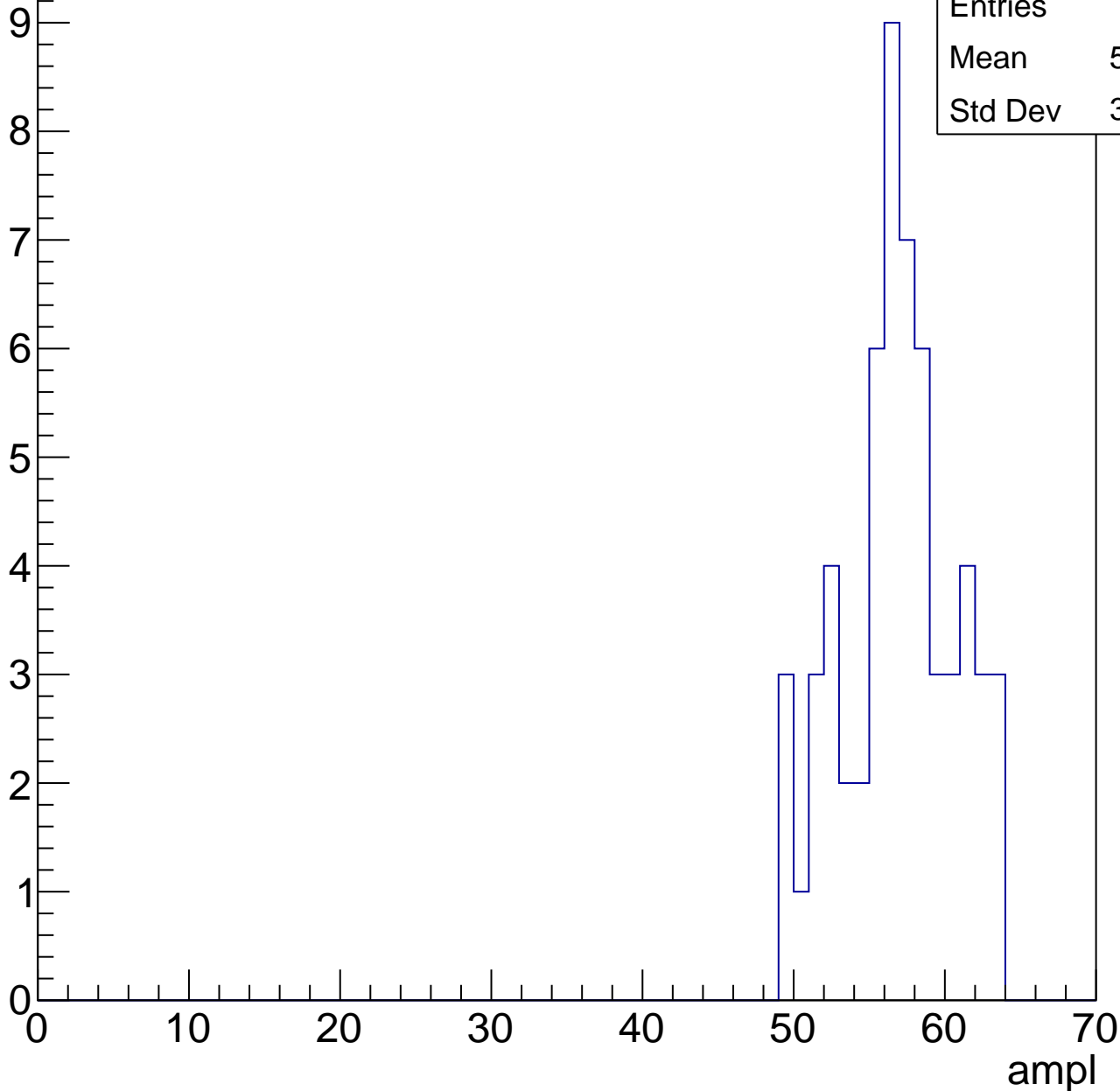


# B1L003S, U22-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.42
Std Dev	3.692

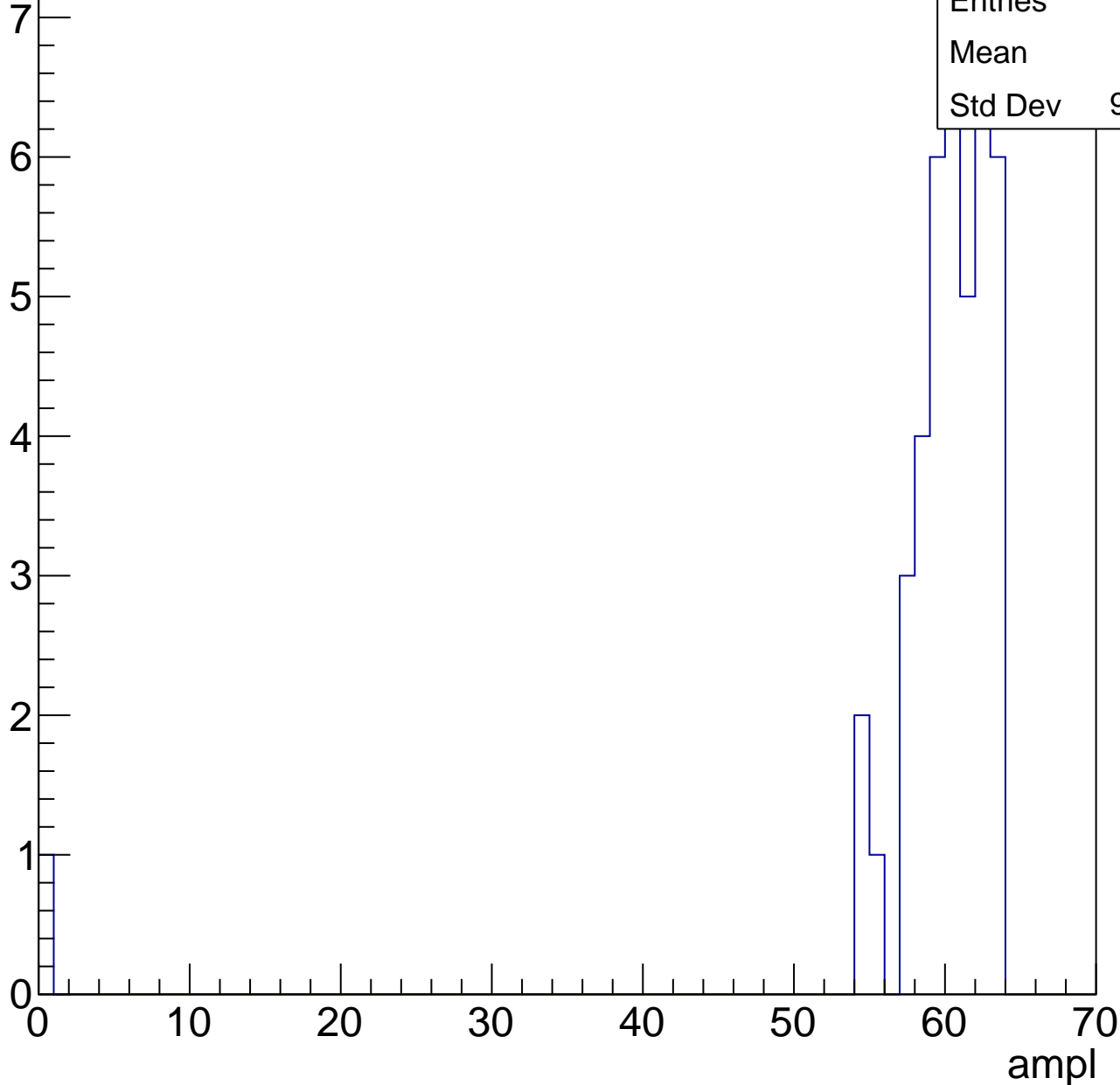


# B1L003S, U22-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

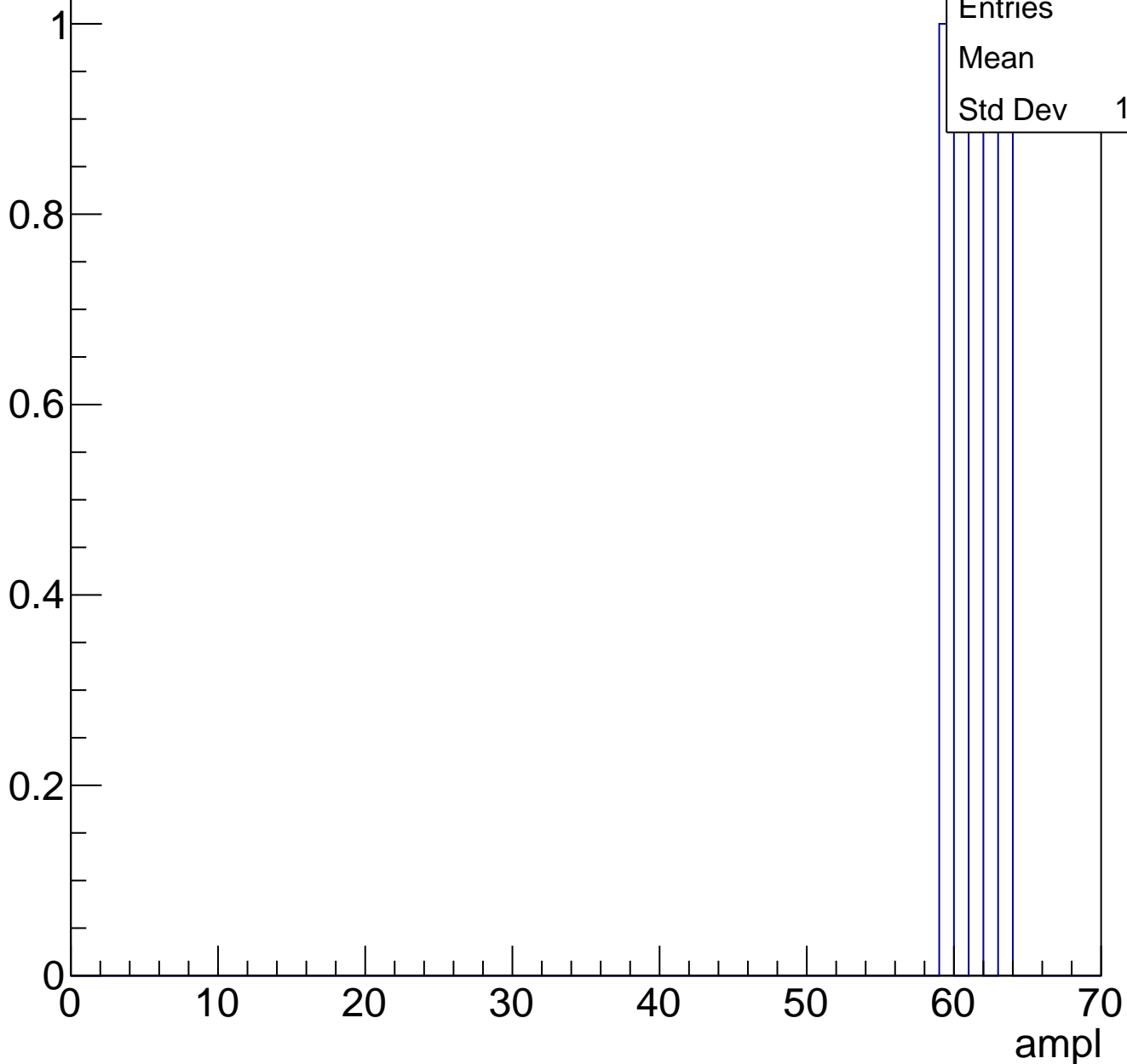
Entries	42
Mean	58.5
Std Dev	9.435



# B1L003S, U22-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch15, adc0

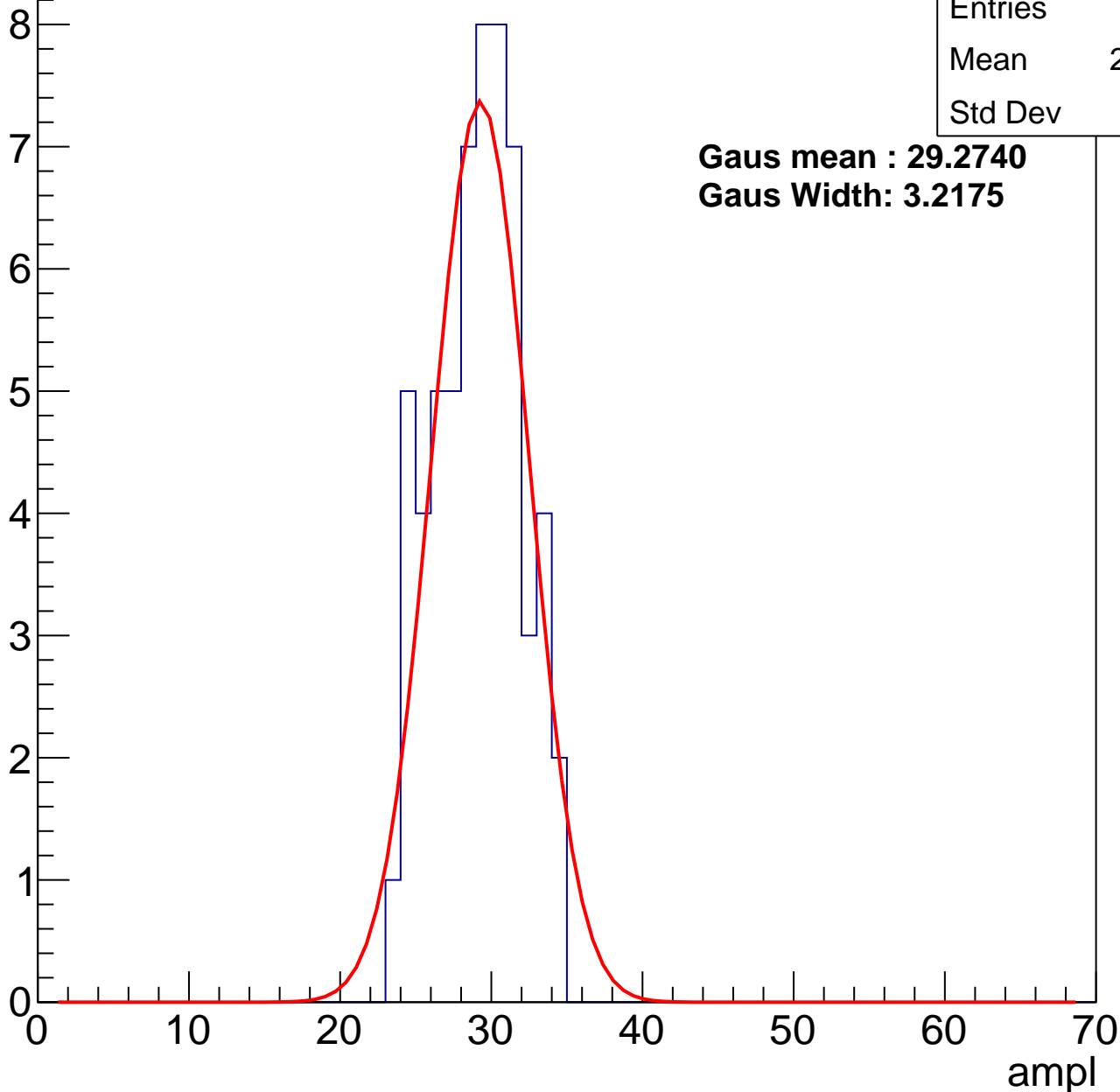
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	28.63
Std Dev	2.81

**Gaus mean : 29.2740**

**Gaus Width: 3.2175**



# B1L003S, U22-ch15, adc1

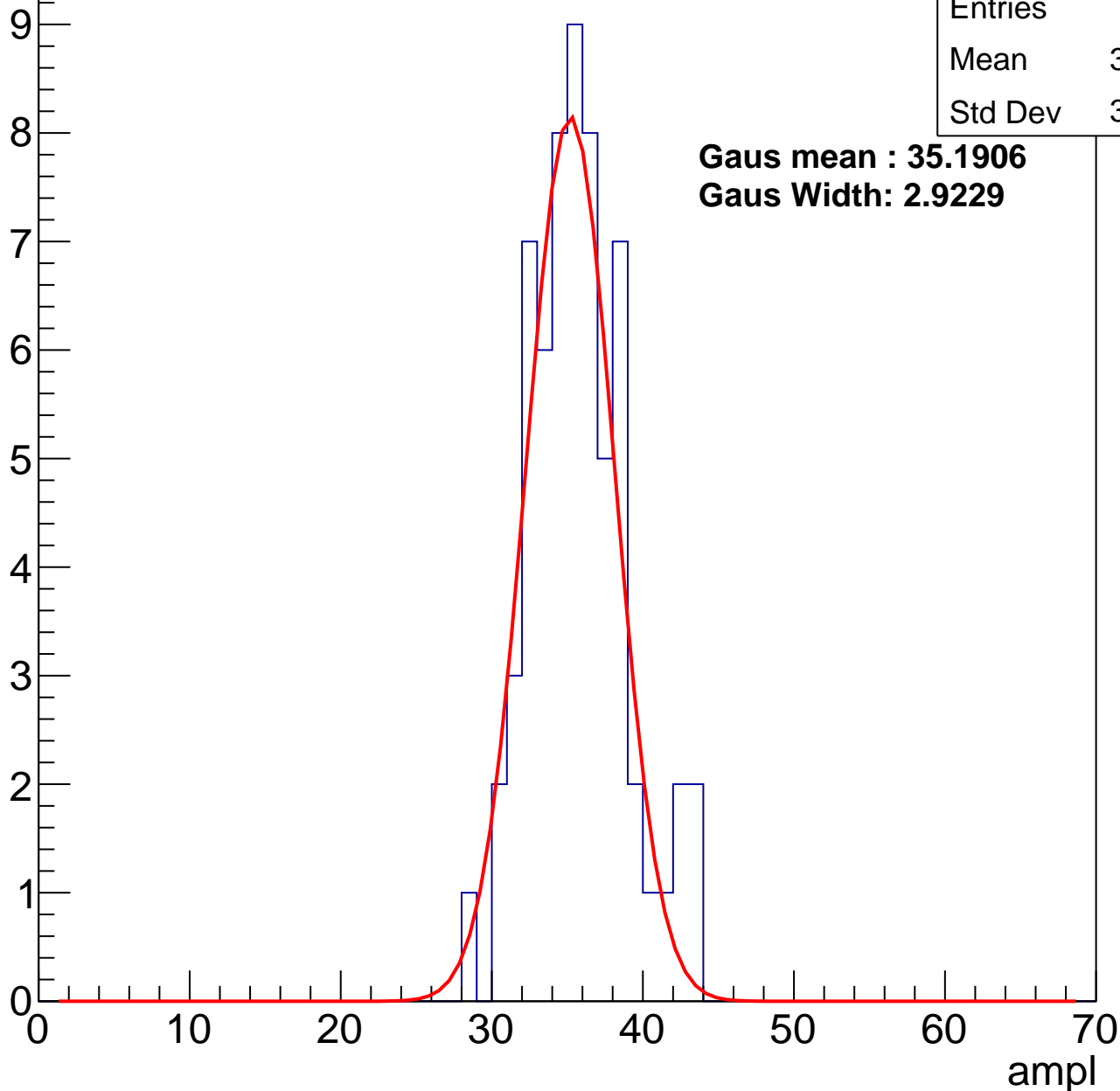
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.28
Std Dev	3.179

**Gaus mean : 35.1906**

**Gaus Width: 2.9229**



# B1L003S, U22-ch15, adc2

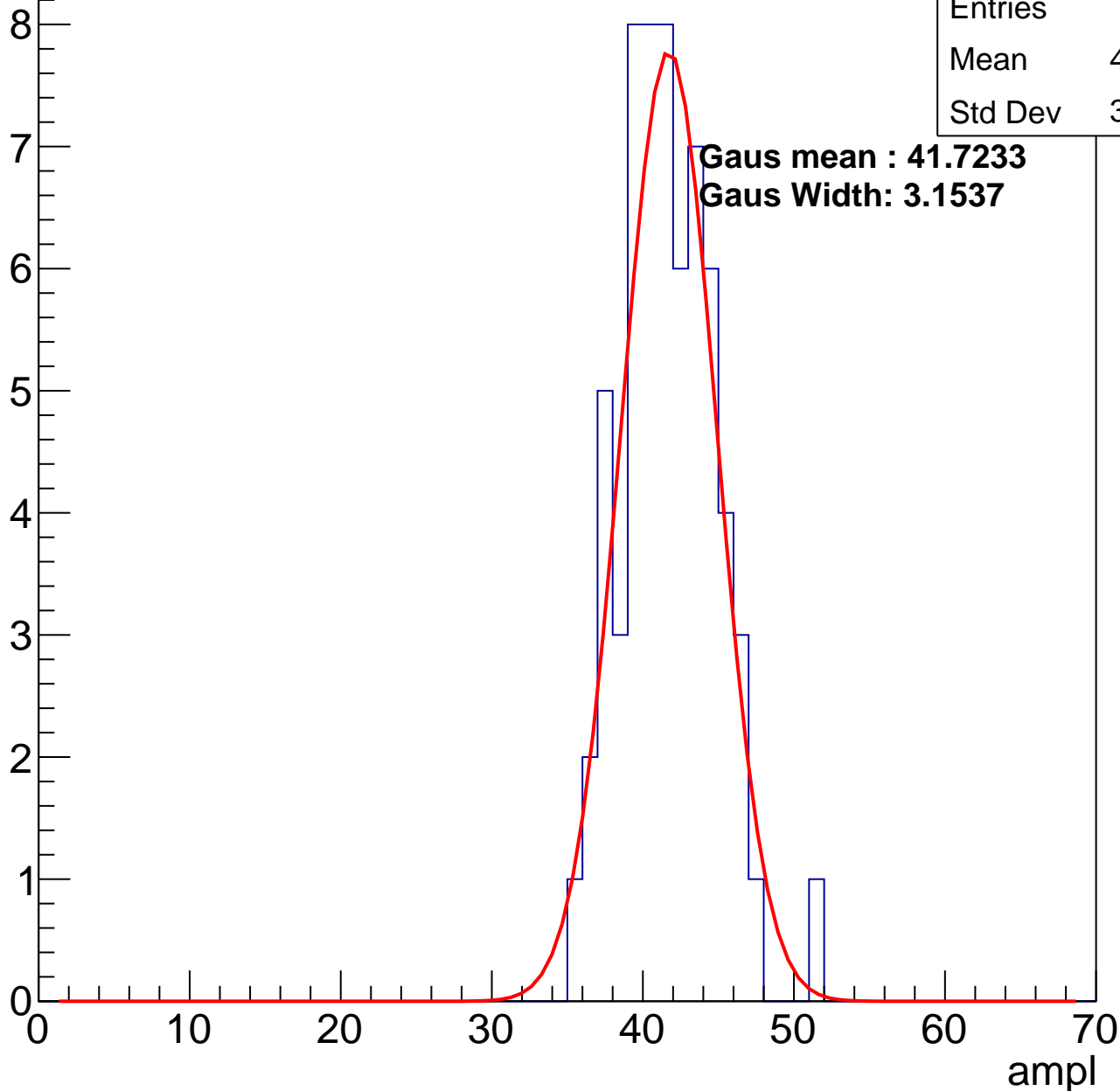
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.25
Std Dev	3.065

**Gaus mean : 41.7233**

**Gaus Width: 3.1537**



# B1L003S, U22-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

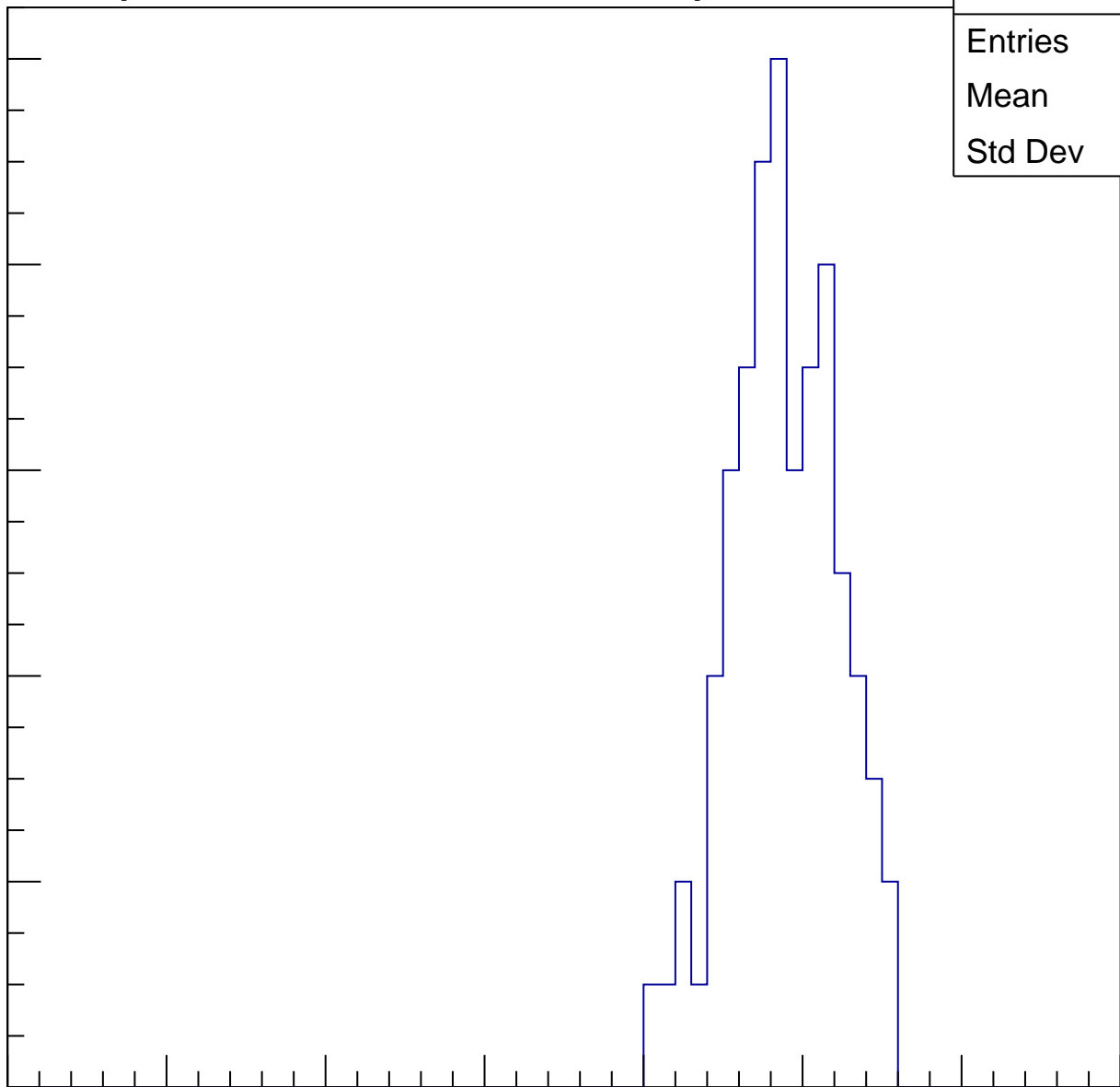
Entries	76
Mean	48.36
Std Dev	3.339

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

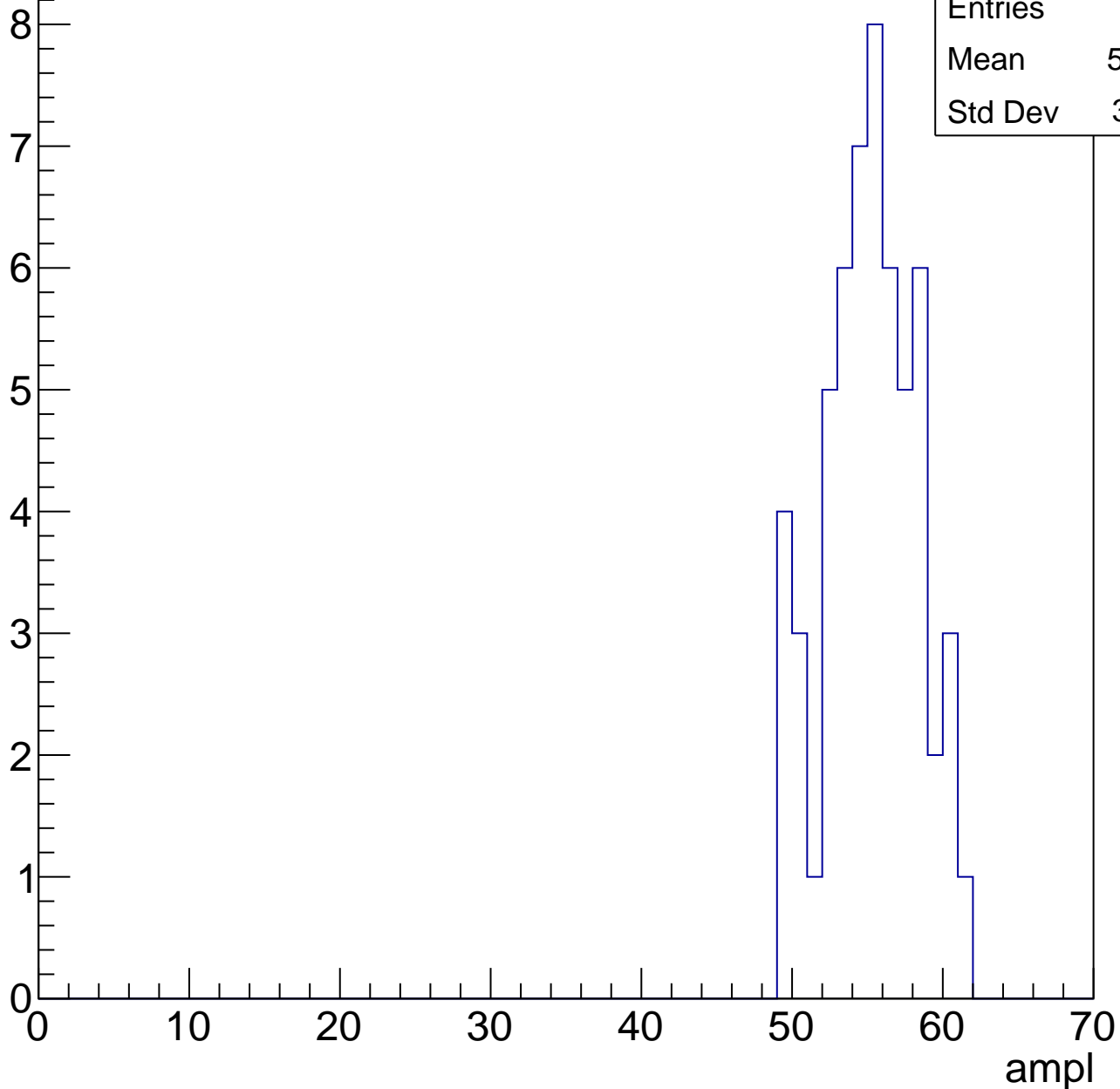
ampl



# B1L003S, U22-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

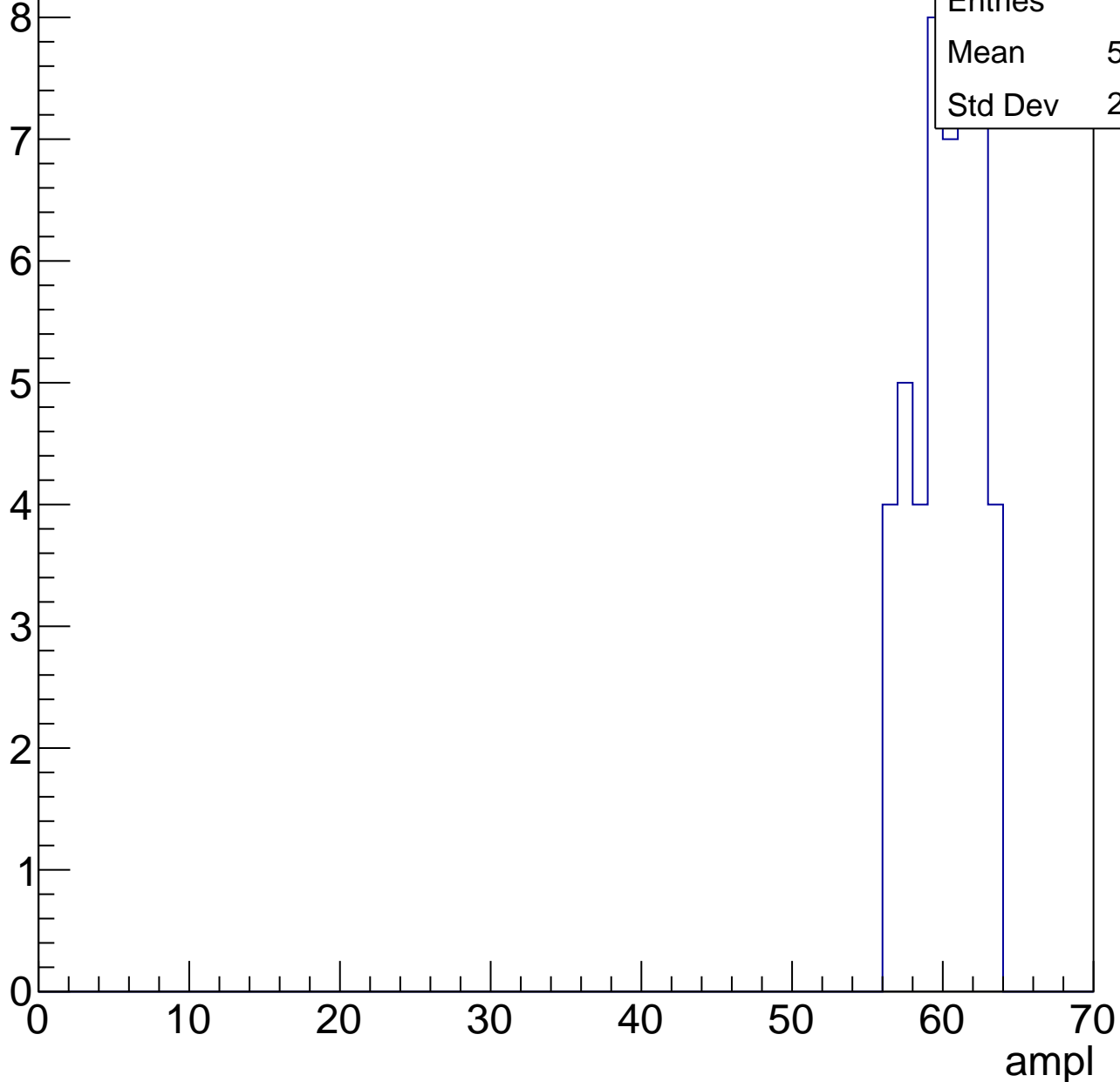
Entry



# B1L003S, U22-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



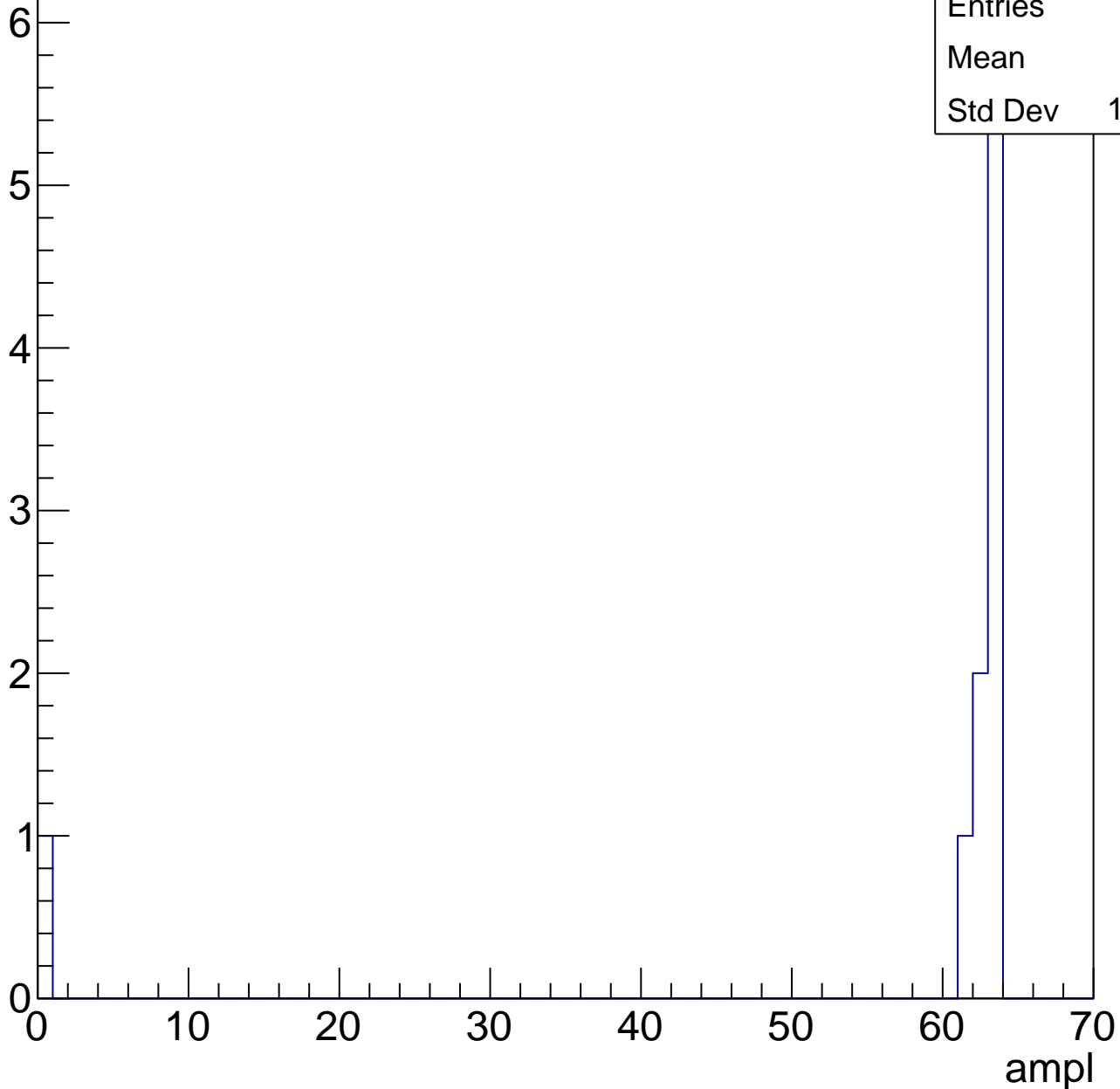
Entries	48
Mean	59.77
Std Dev	2.074

# B1L003S, U22-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	56.3
Std Dev	18.78

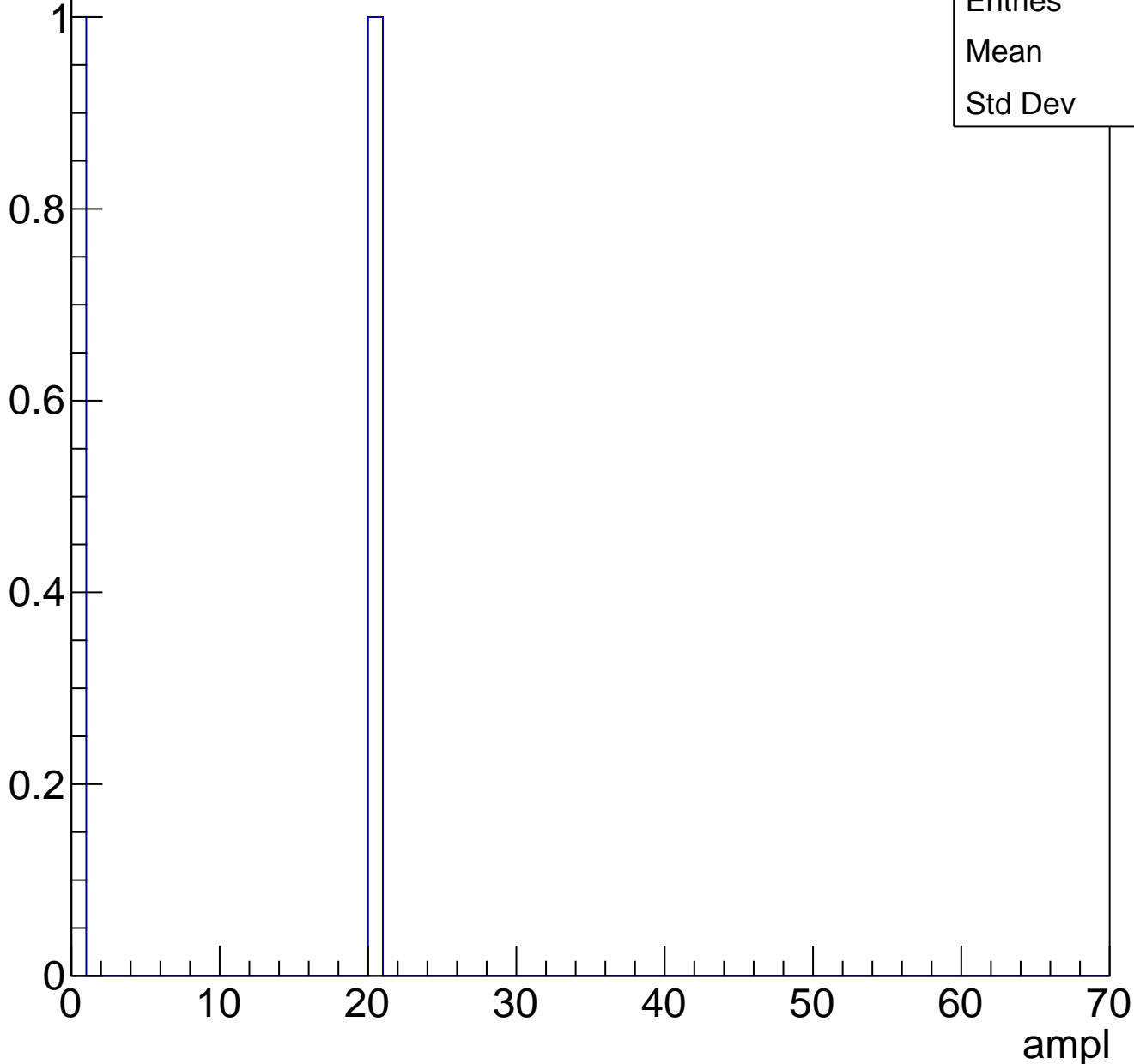




# B1L003S, U22-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	10
Std Dev	10

# B1L003S, U22-ch16, adc0

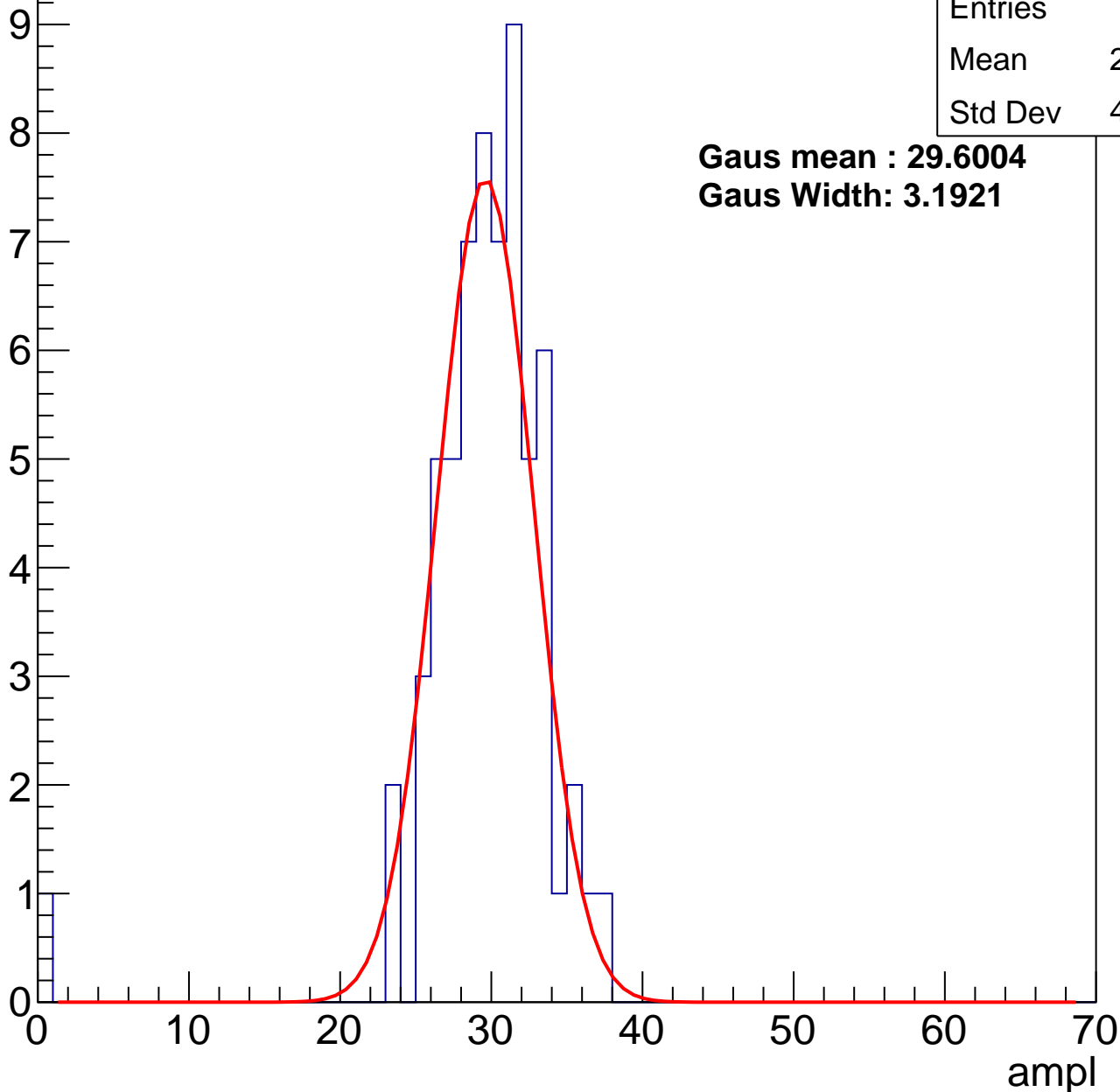
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.17
Std Dev	4.756

**Gaus mean : 29.6004**

**Gaus Width: 3.1921**



# B1L003S, U22-ch16, adc1

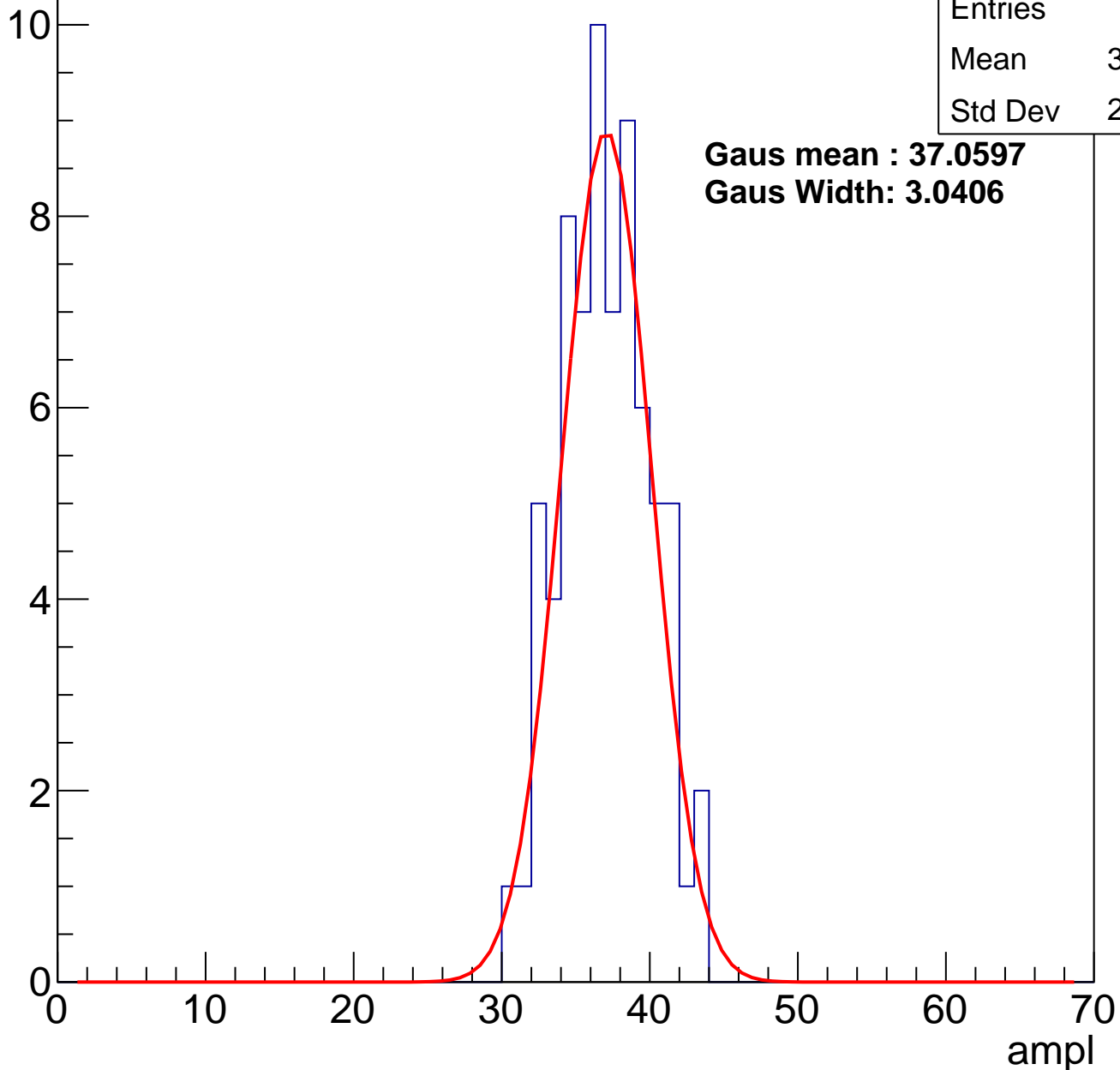
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	71
Mean	36.59
Std Dev	2.972

**Gaus mean : 37.0597**

**Gaus Width: 3.0406**

Entry



# B1L003S, U22-ch16, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	43.15
Std Dev	3.281

**Gaus mean : 44.1198**

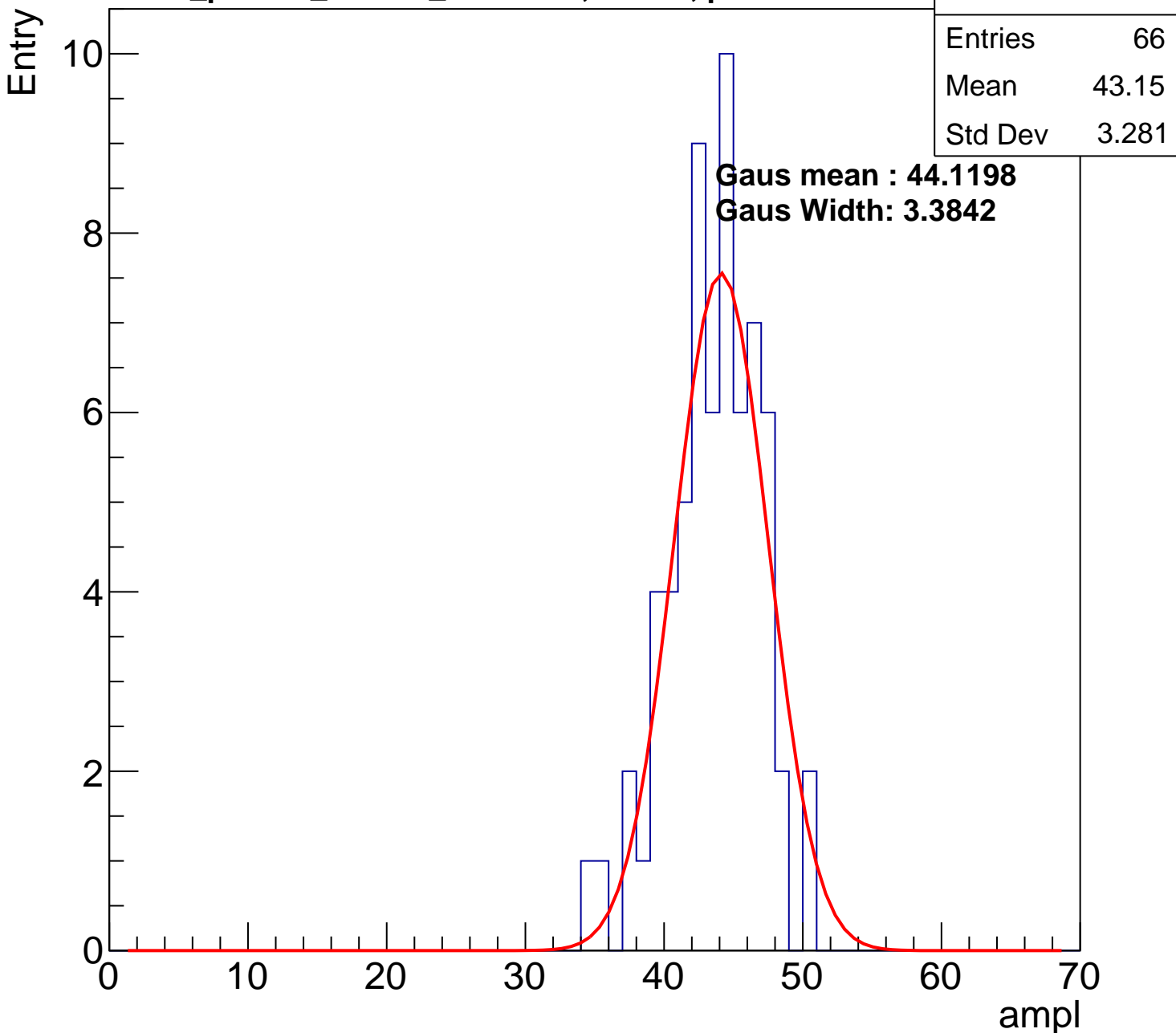
**Gaus Width: 3.3842**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

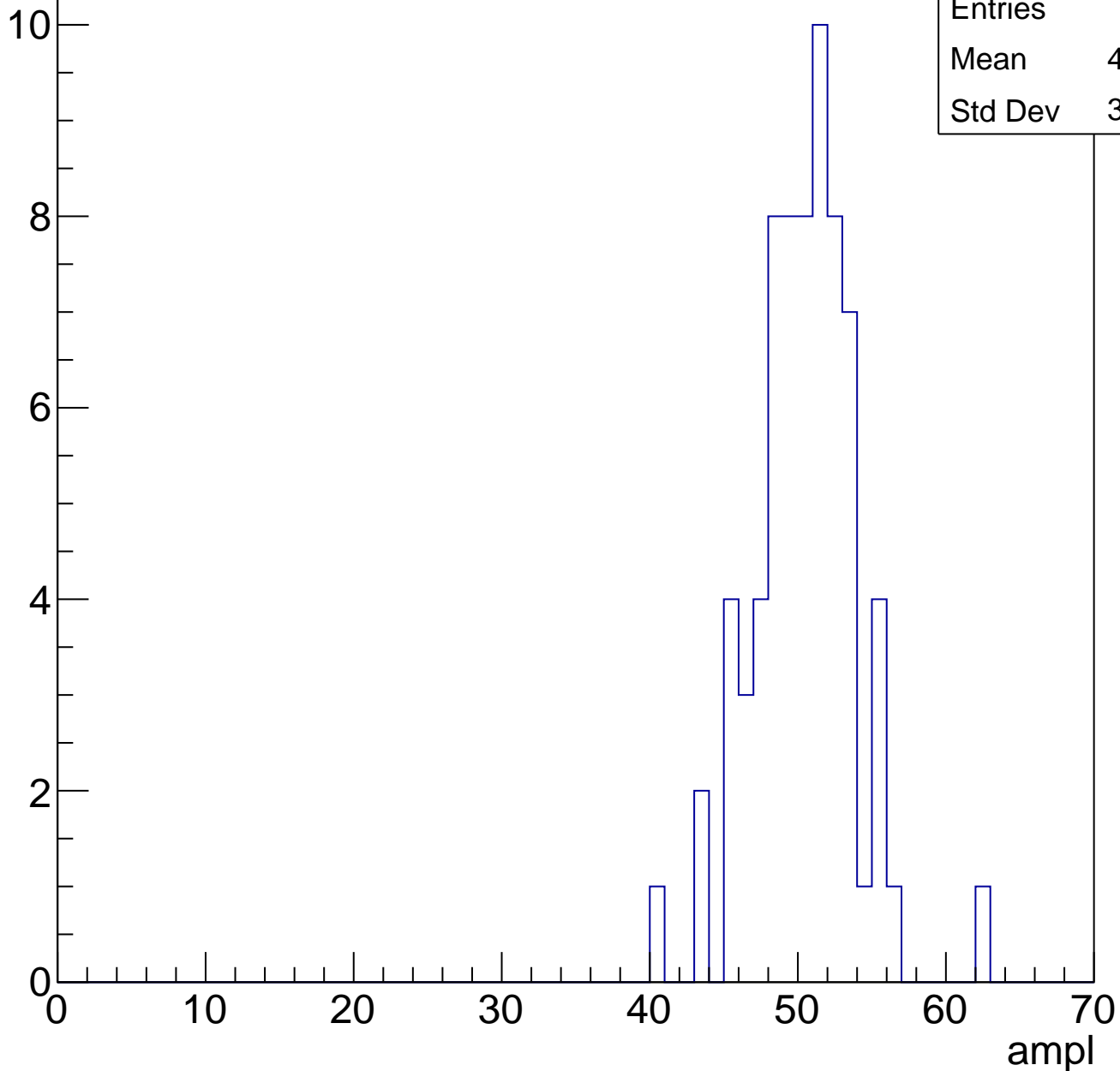


# B1L003S, U22-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	49.96
Std Dev	3.429

Entry

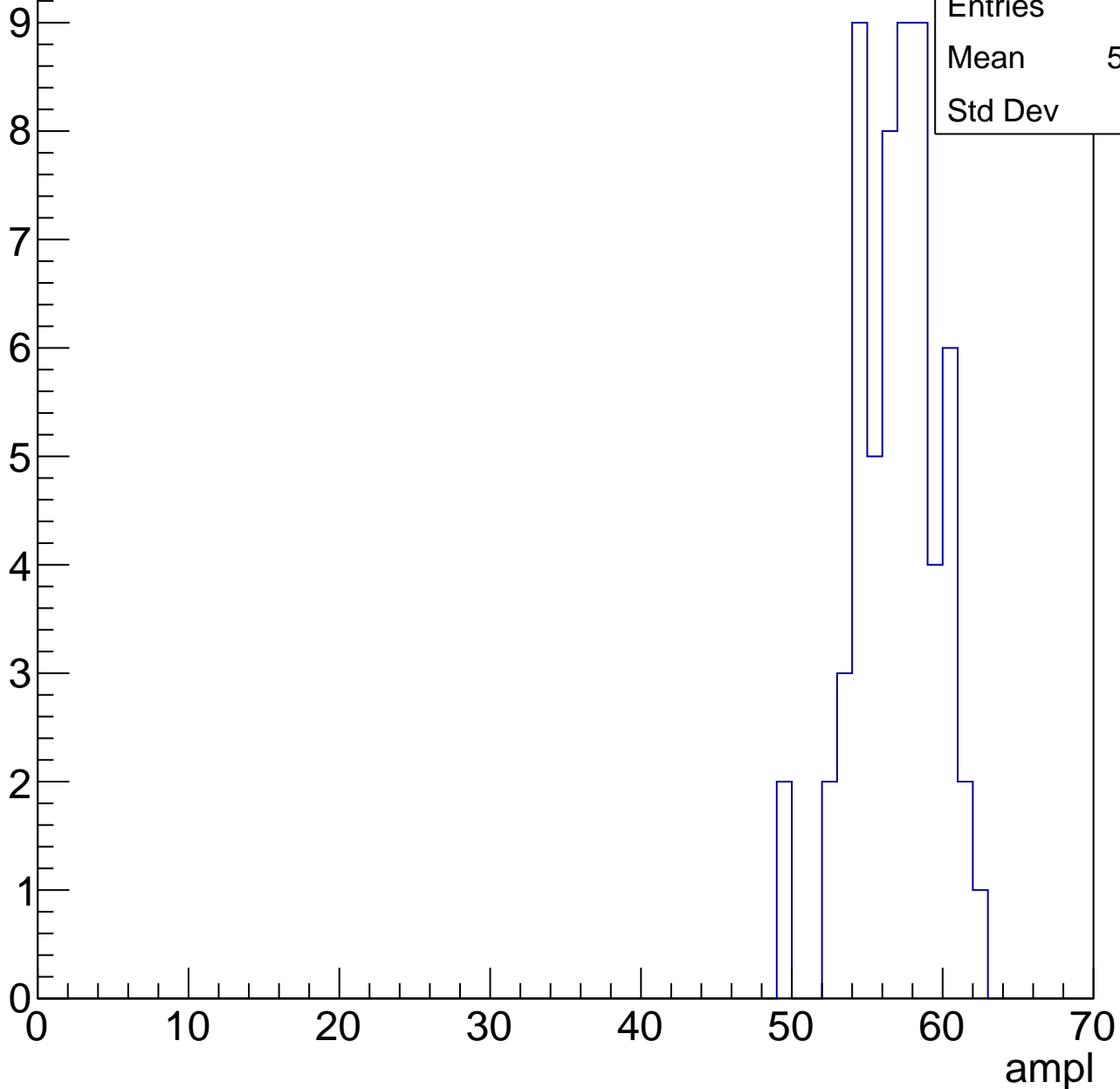


# B1L003S, U22-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	56.42
Std Dev	2.74

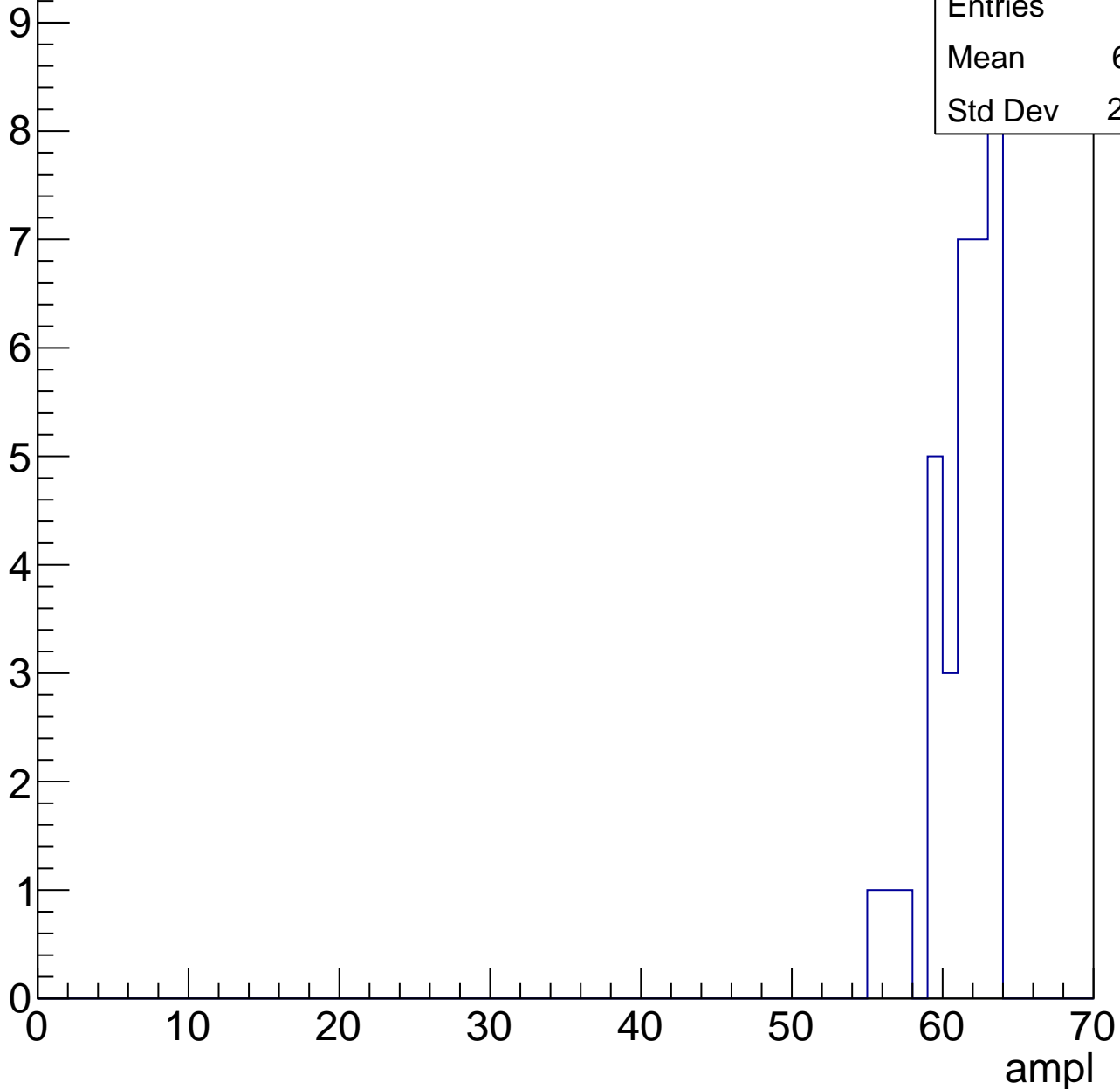


# B1L003S, U22-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

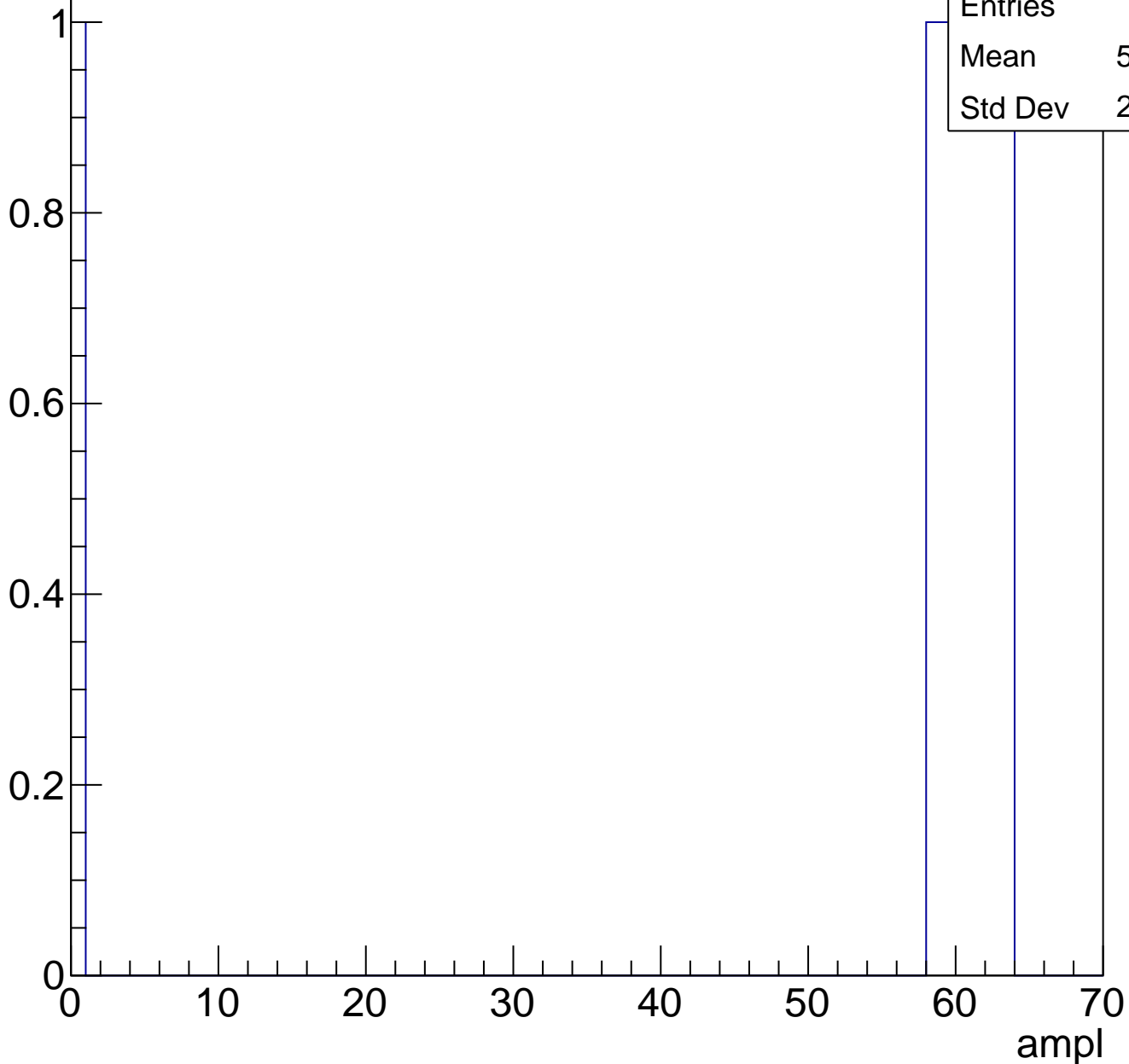
Entries	34
Mean	60.91
Std Dev	2.049



# B1L003S, U22-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch17, adc0

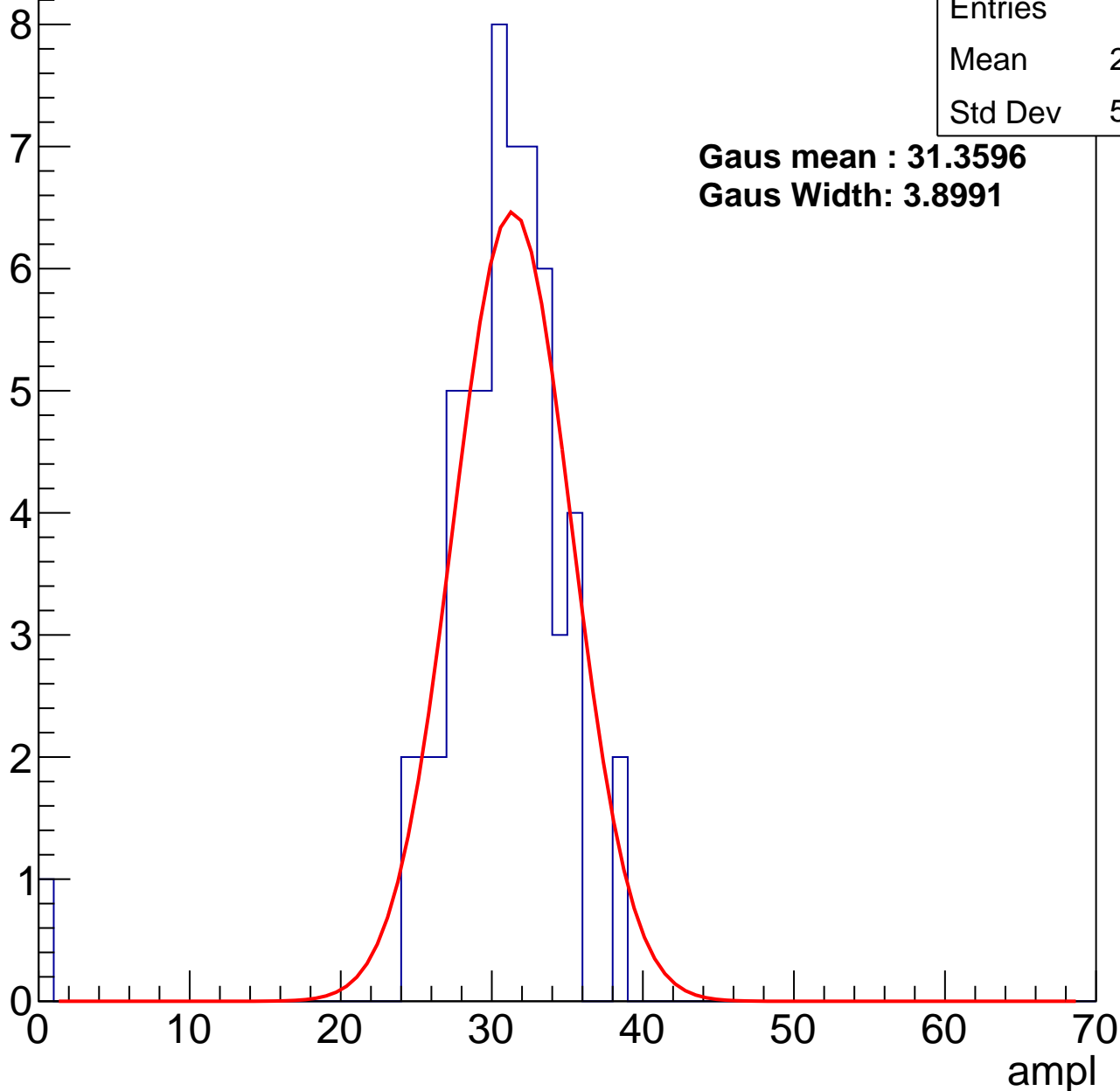
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.95
Std Dev	5.023

**Gaus mean : 31.3596**

**Gaus Width: 3.8991**



# B1L003S, U22-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	36.09
Std Dev	5.502

**Gaus mean : 37.7425**

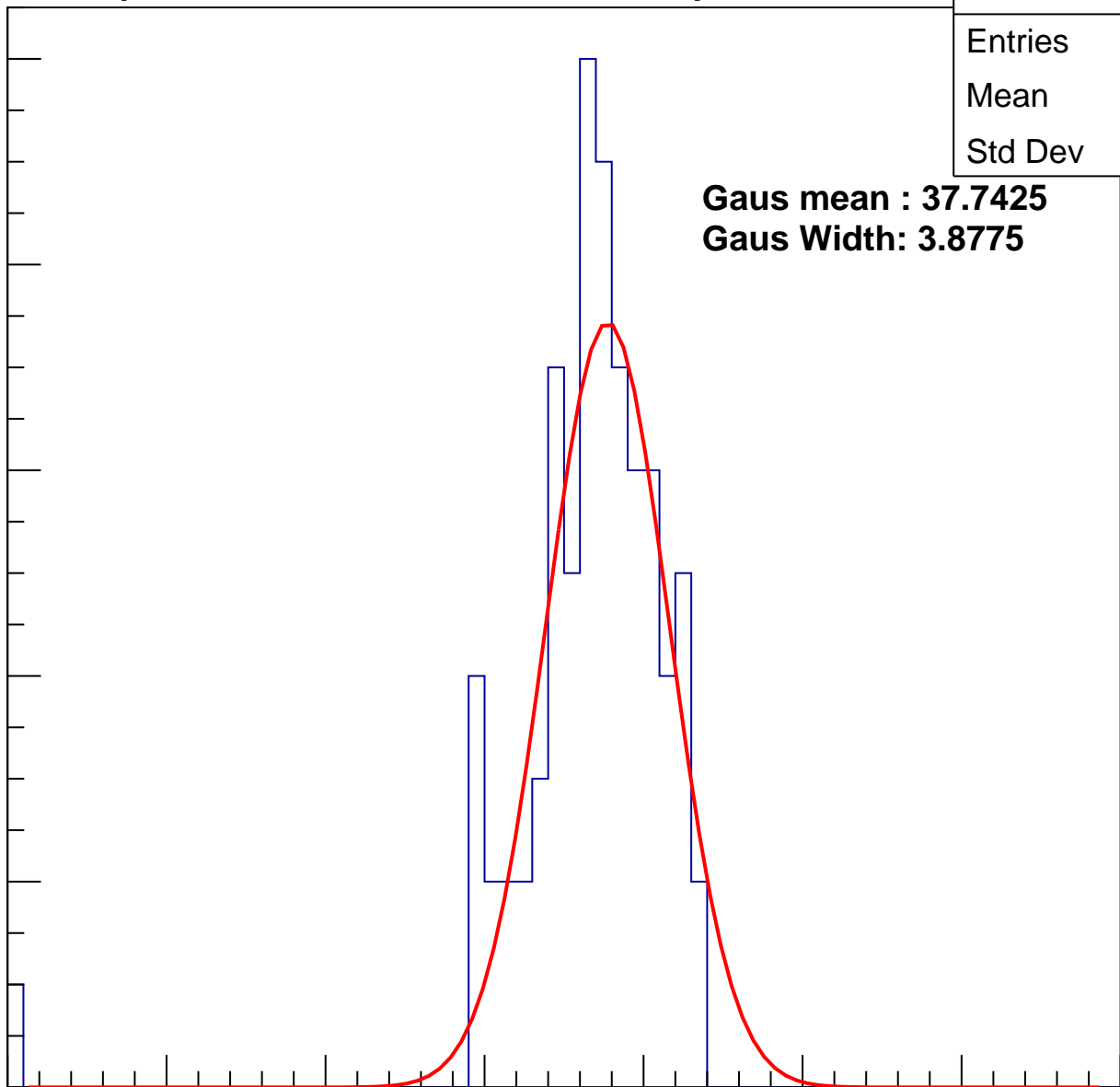
**Gaus Width: 3.8775**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch17, adc2

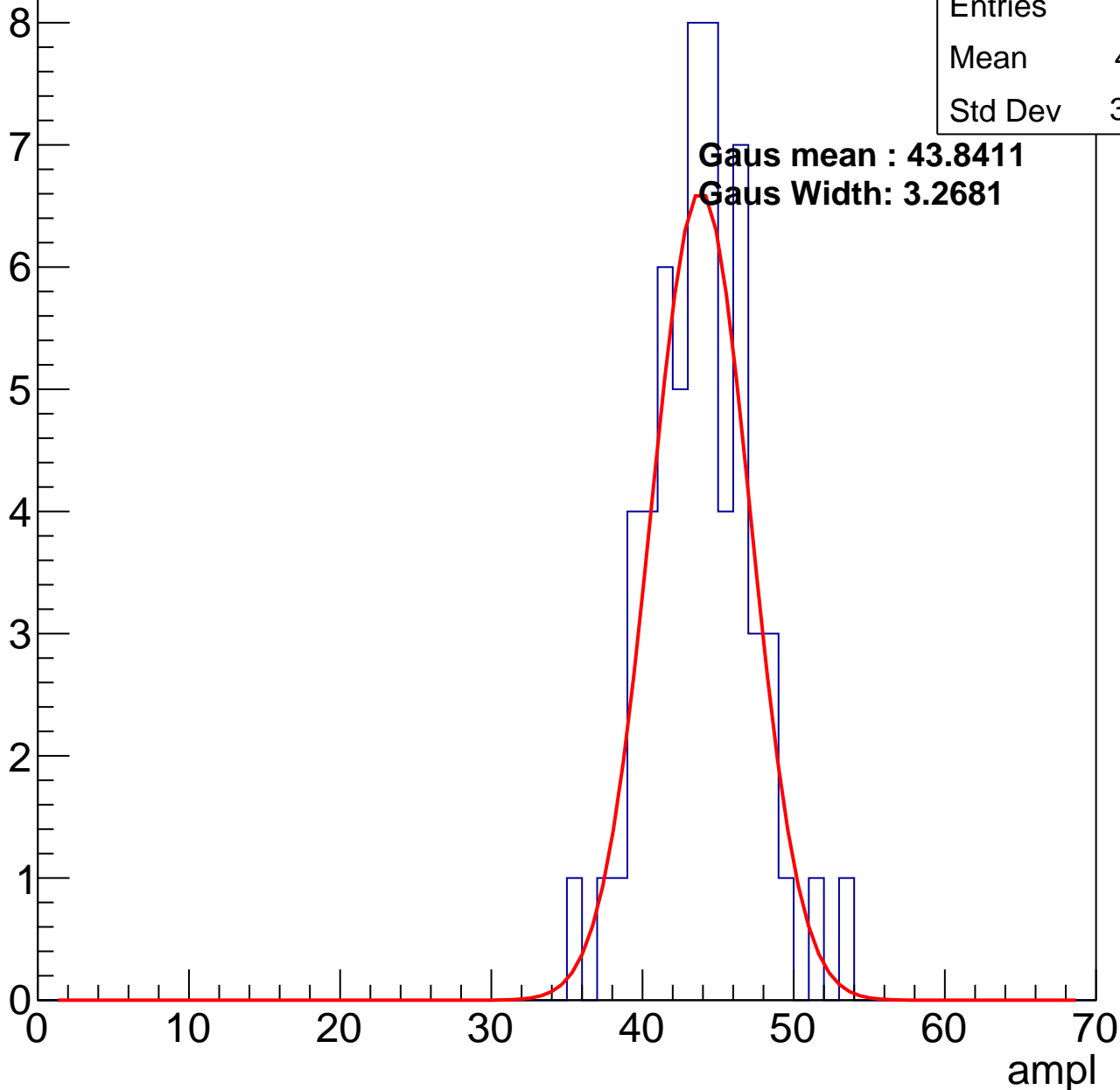
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.41
Std Dev	3.358

**Gaus mean : 43.8411**

**Gaus Width: 3.2681**

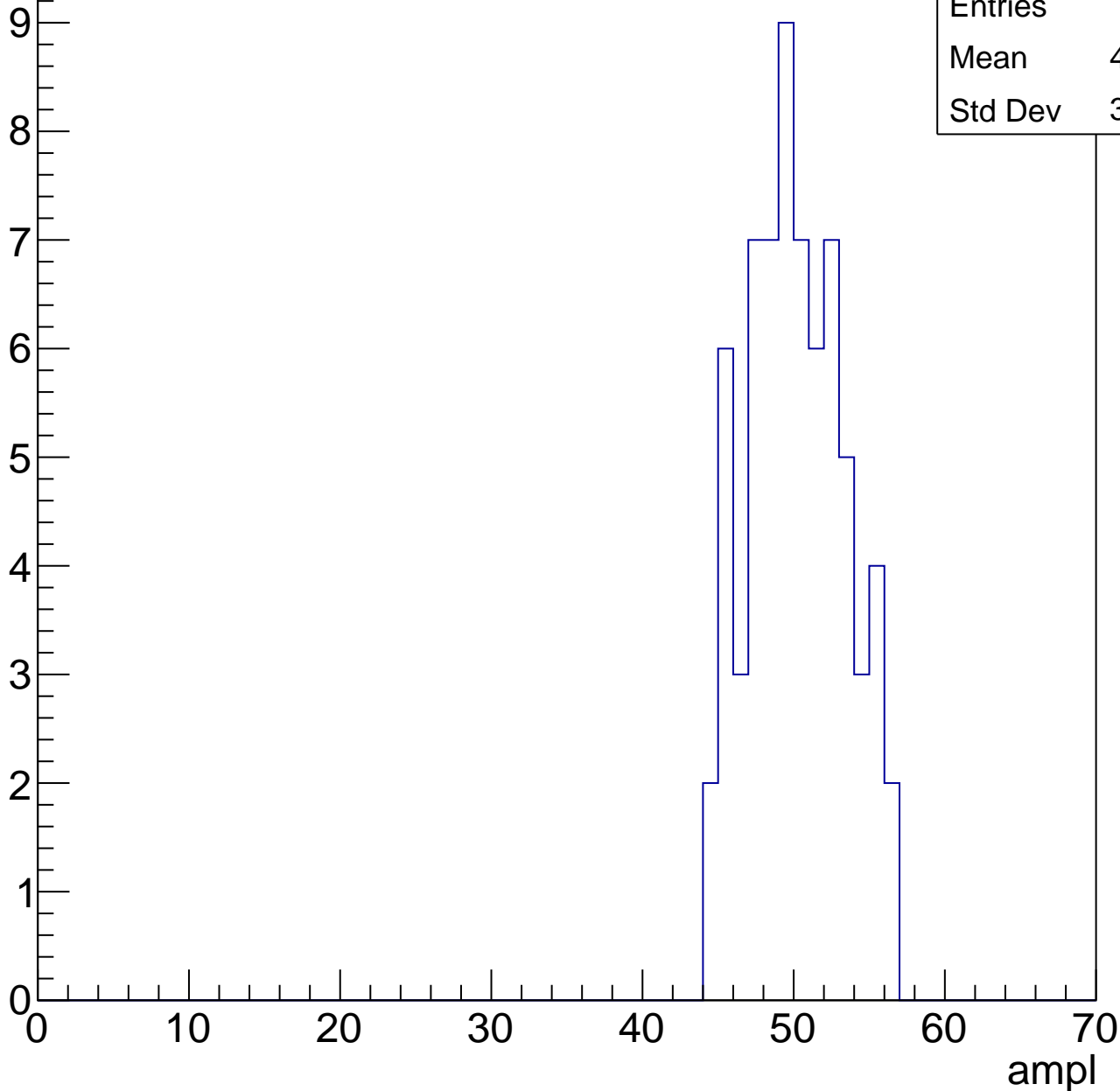


# B1L003S, U22-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.72
Std Dev	3.124



# B1L003S, U22-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	54.91
Std Dev	2.55

ampl

0

10

20

30

40

50

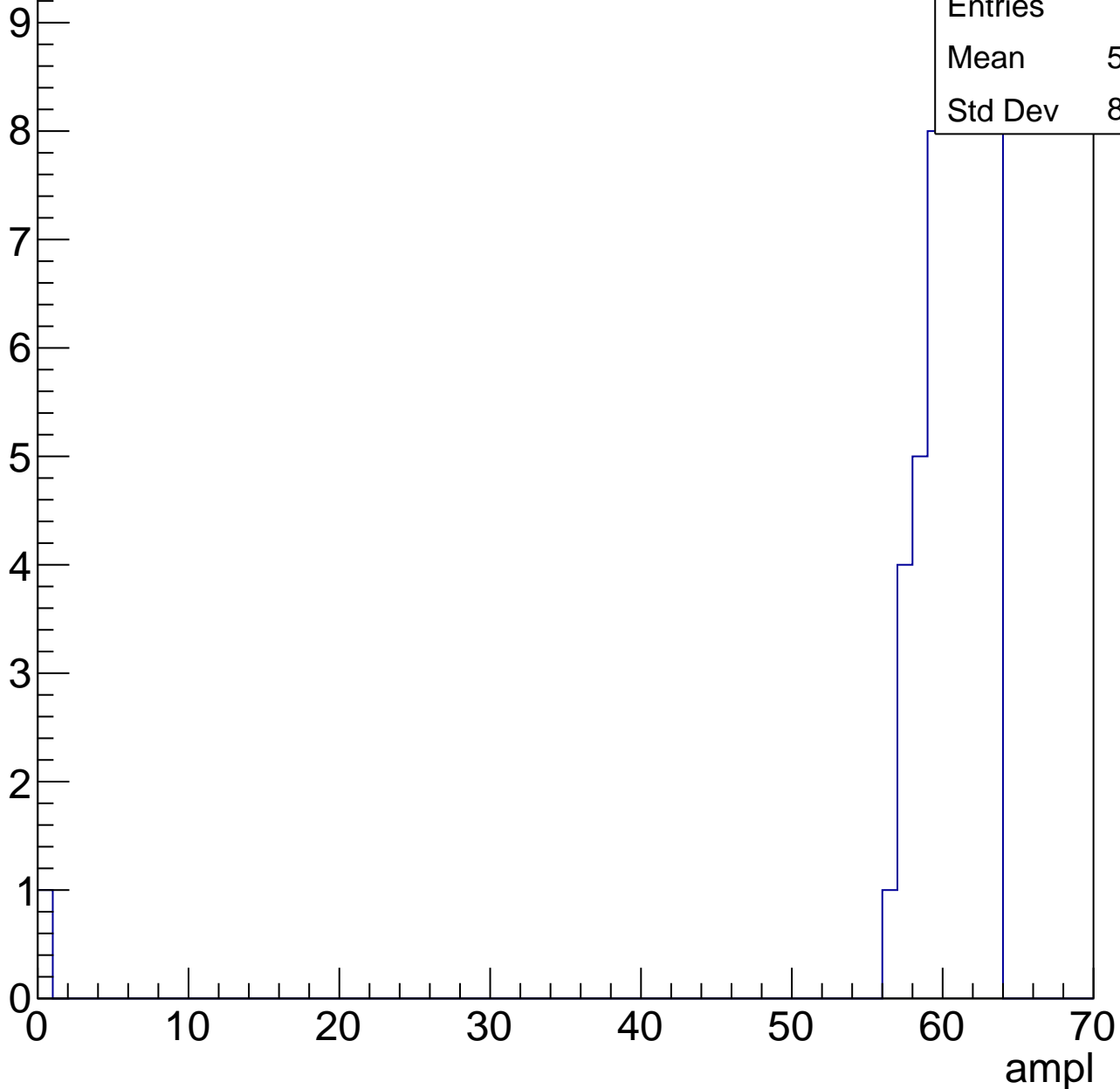
60

70

# B1L003S, U22-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch18, adc0

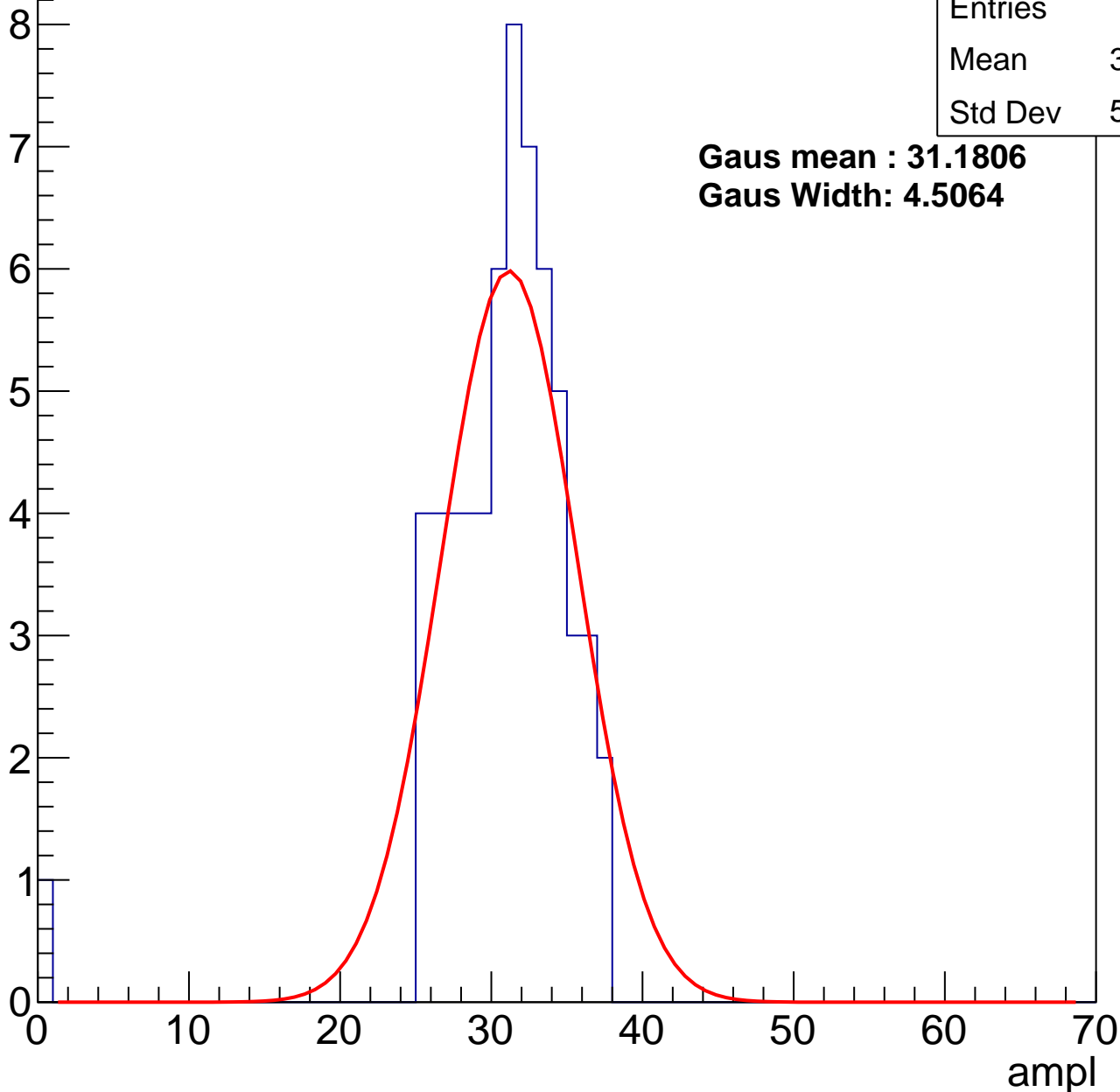
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	30.28
Std Dev	5.067

**Gaus mean : 31.1806**

**Gaus Width: 4.5064**



# B1L003S, U22-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	77
Mean	37.62
Std Dev	3.689

**Gaus mean : 38.2064**

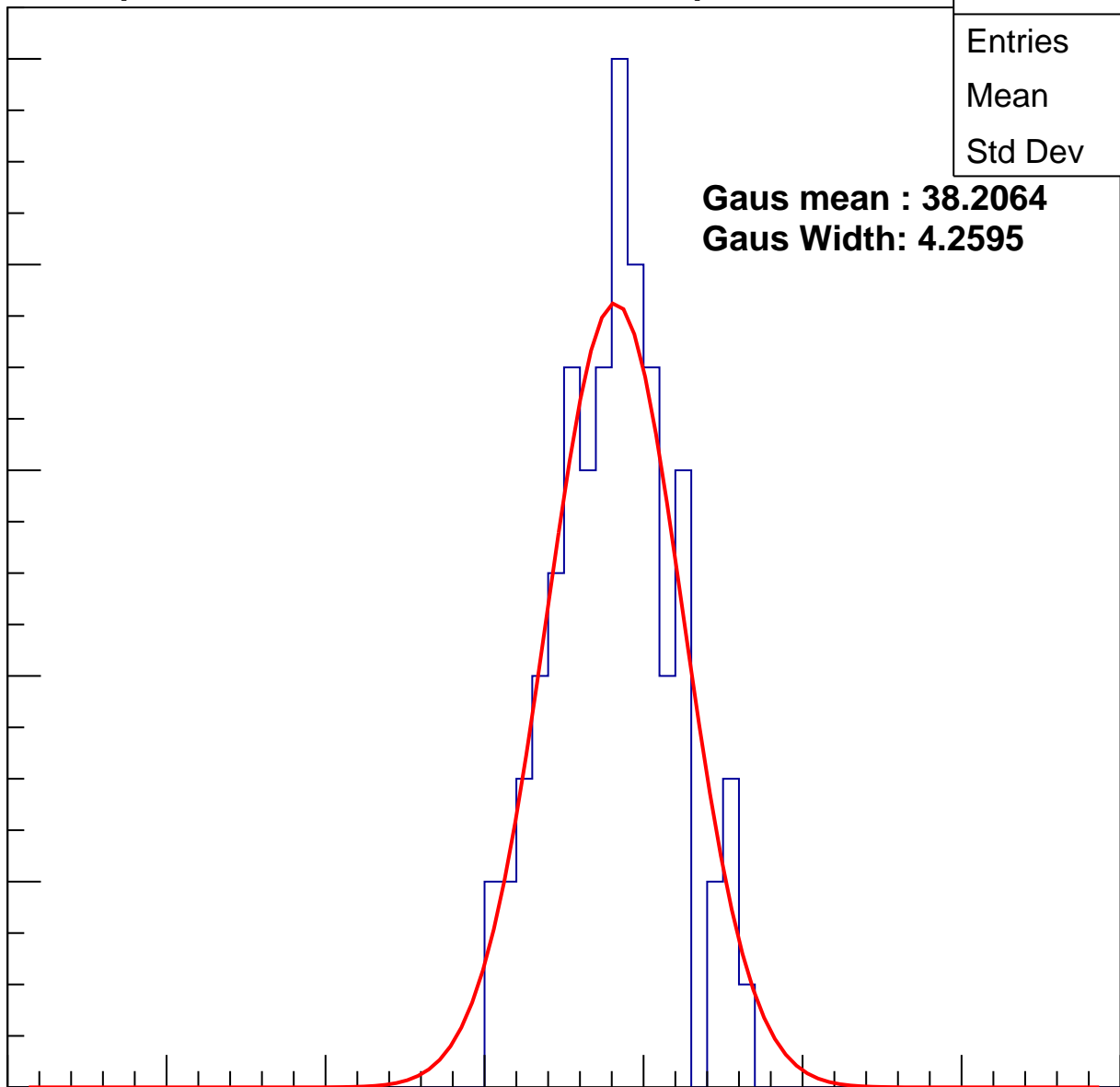
**Gaus Width: 4.2595**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch18, adc2

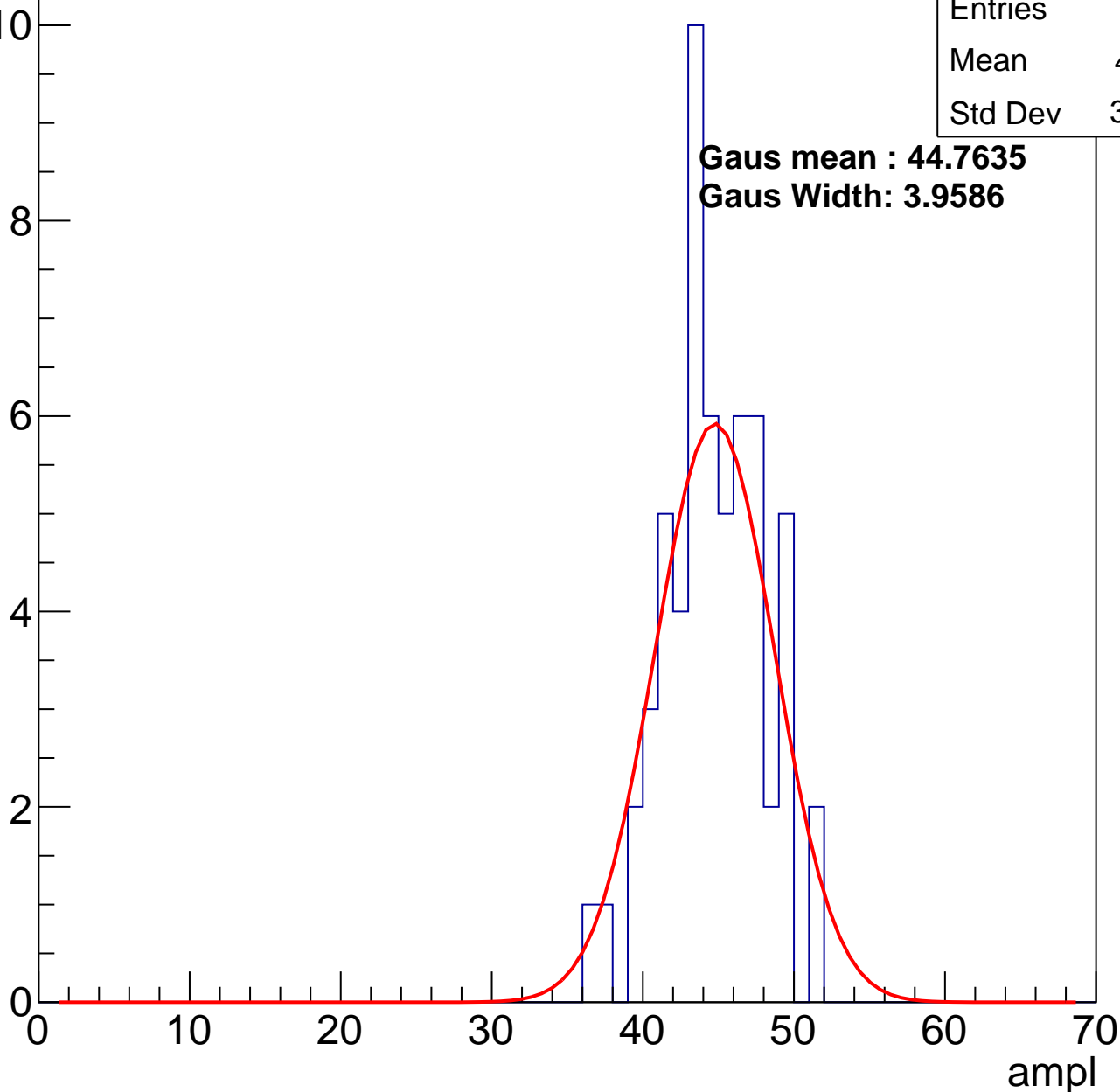
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	44.21
Std Dev	3.263

**Gaus mean : 44.7635**

**Gaus Width: 3.9586**



# B1L003S, U22-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

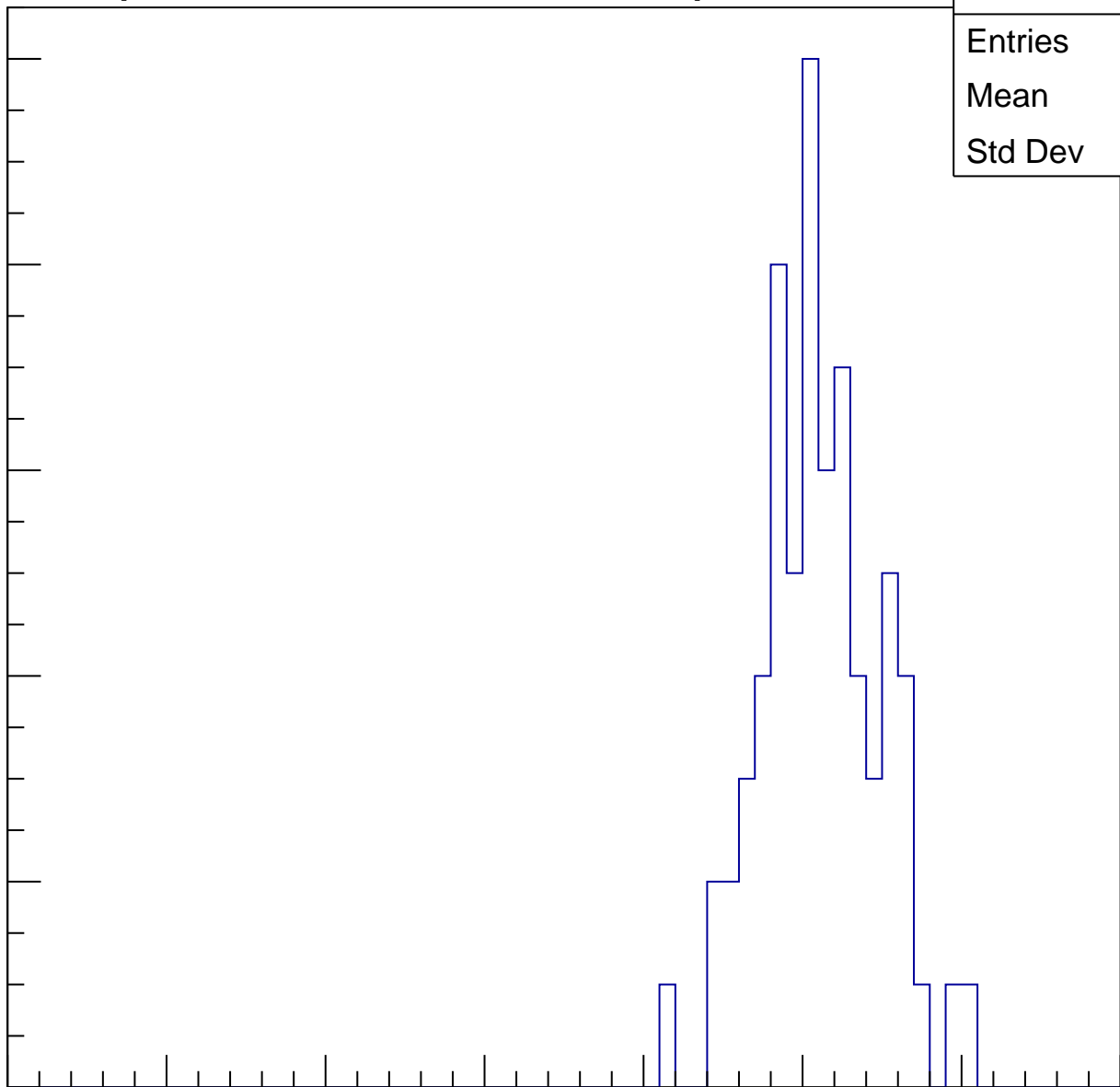
Entries	67
Mean	50.64
Std Dev	3.692

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

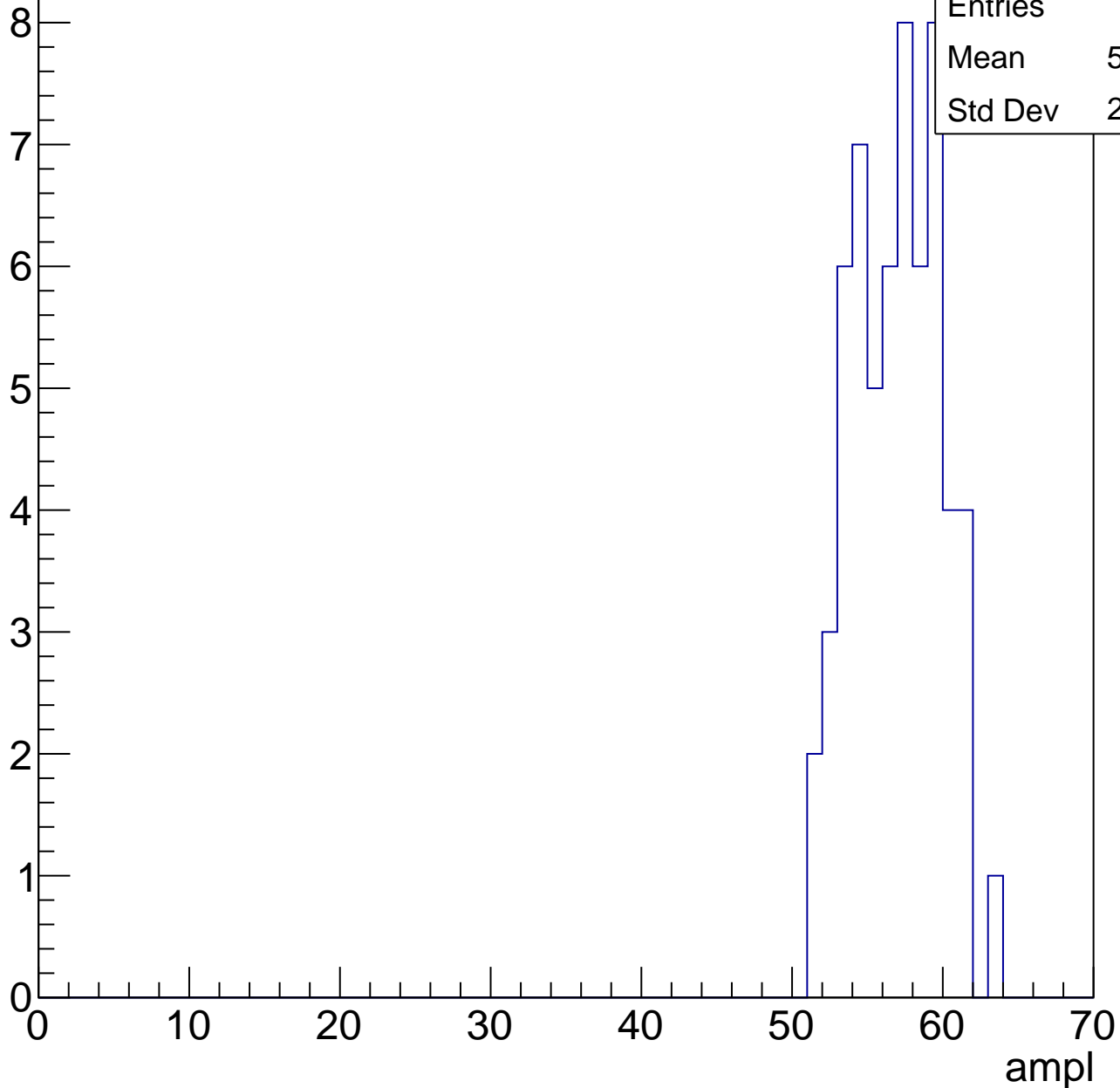
ampl



# B1L003S, U22-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



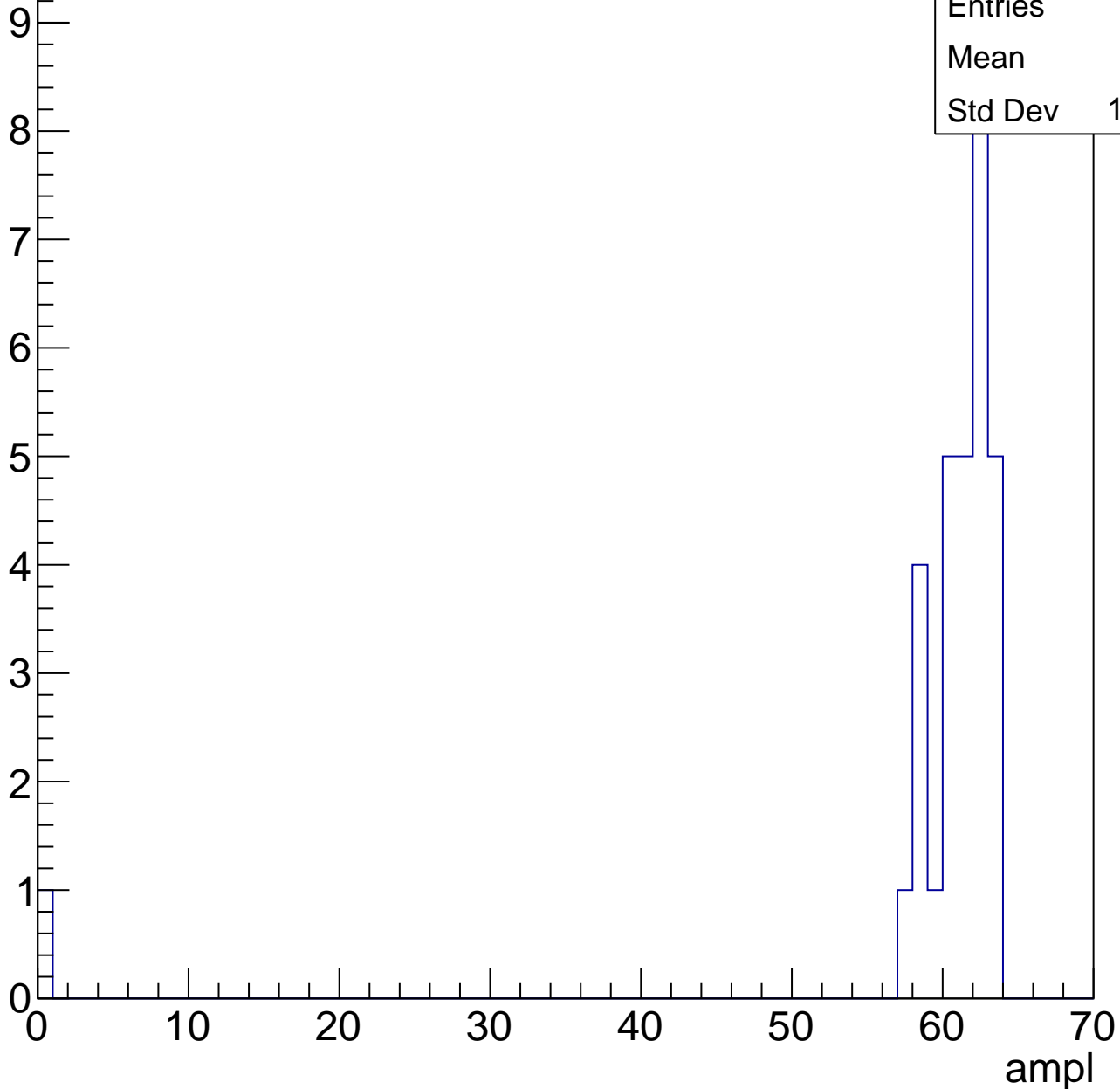
Entries	60
Mean	56.47
Std Dev	2.855

# B1L003S, U22-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

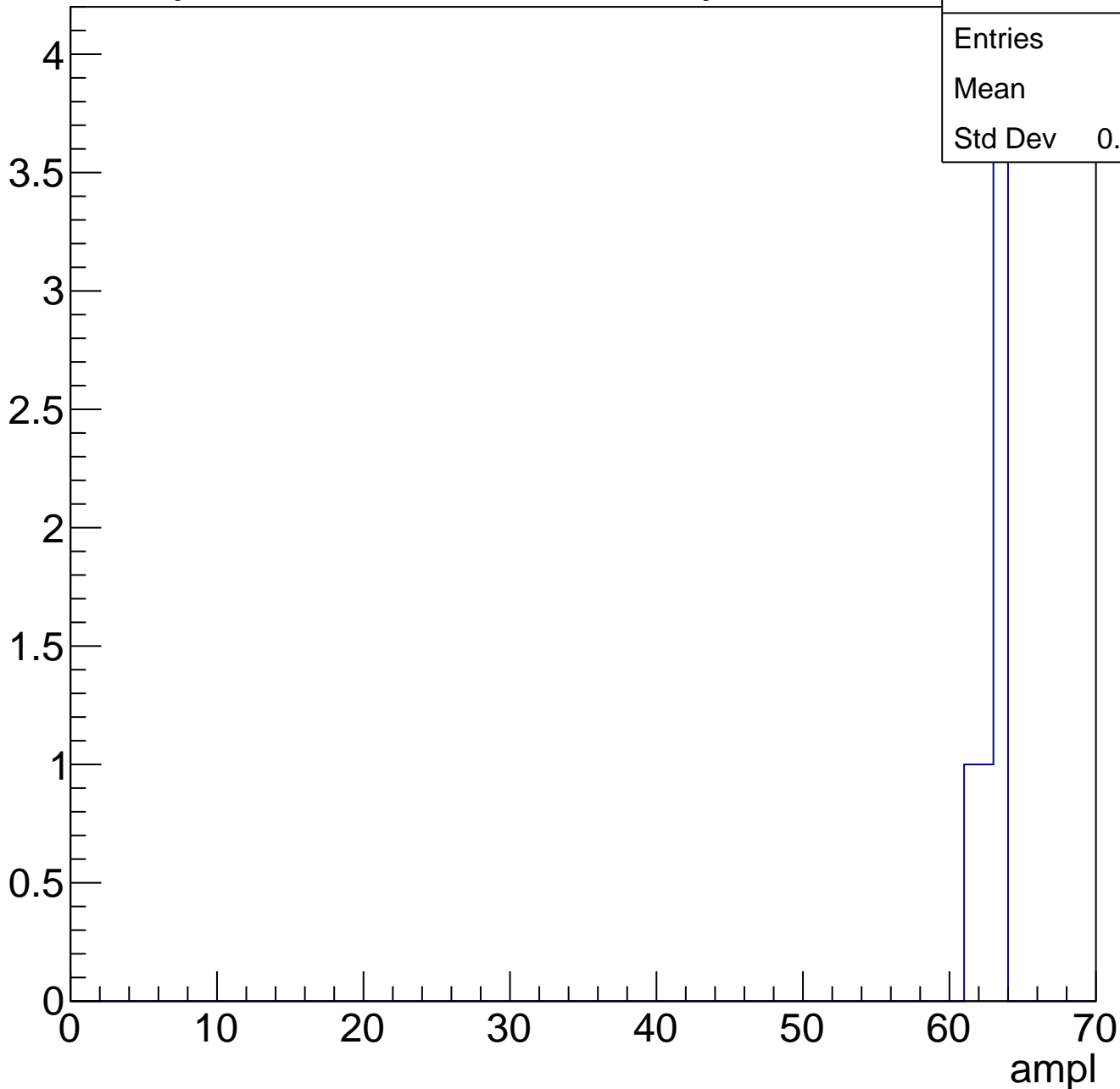
Entries	31
Mean	58.9
Std Dev	10.89



# B1L003S, U22-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	57
Mean	28.32
Std Dev	4.928

**Gaus mean : 28.9383**

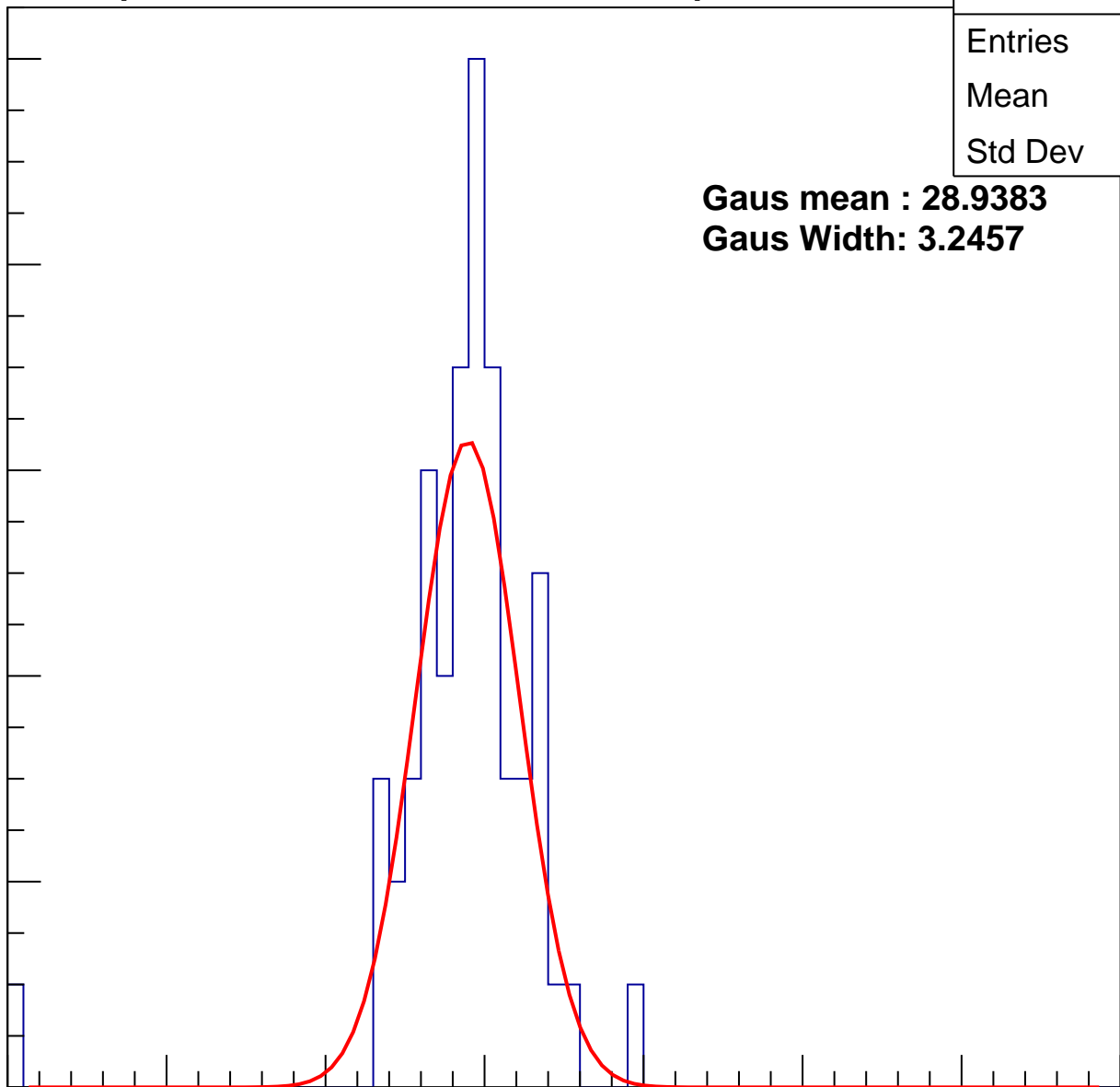
**Gaus Width: 3.2457**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



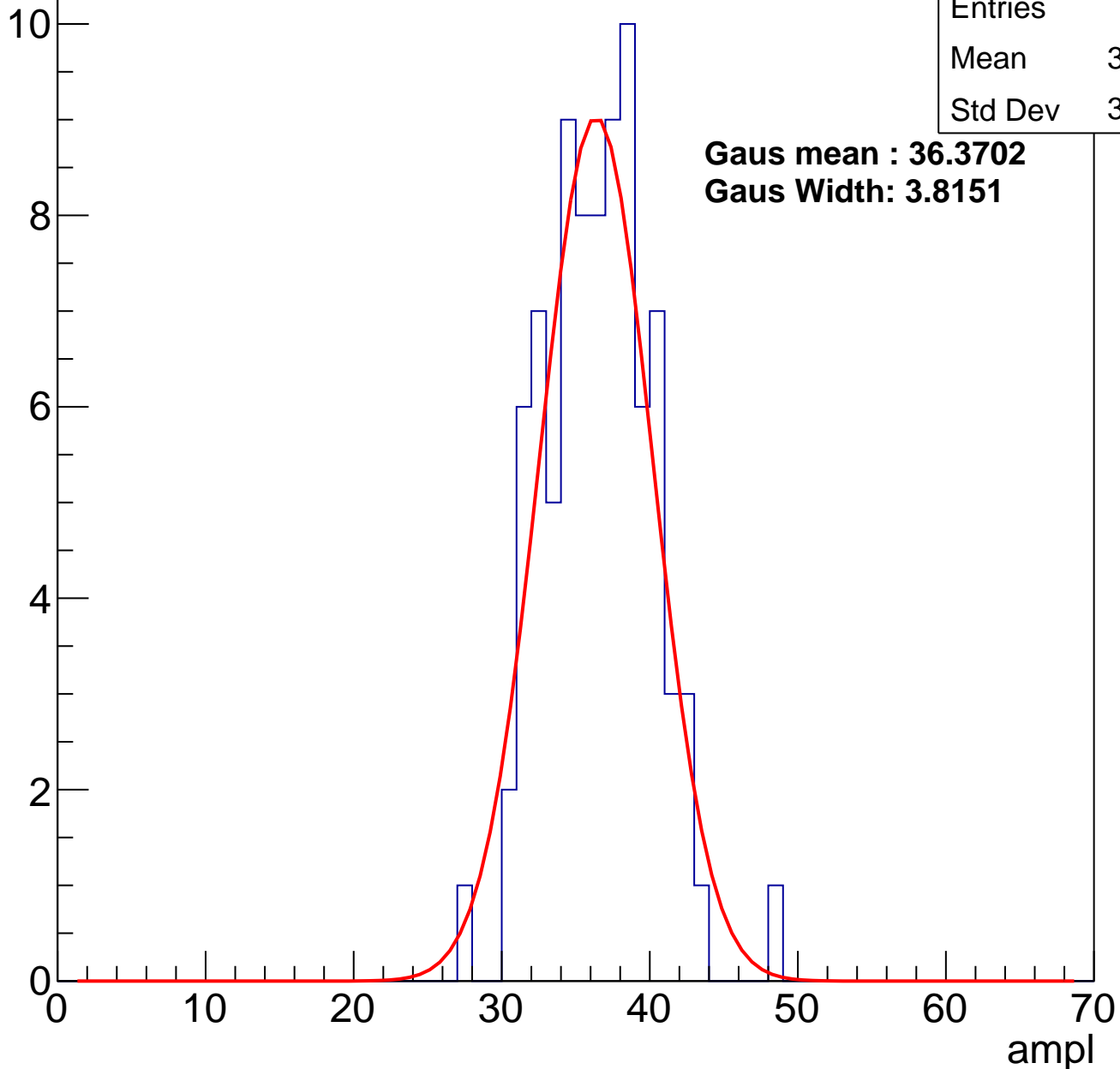
# B1L003S, U22-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	86
Mean	36.08
Std Dev	3.567

**Gaus mean : 36.3702**  
**Gaus Width: 3.8151**

Entry



# B1L003S, U22-ch19, adc2

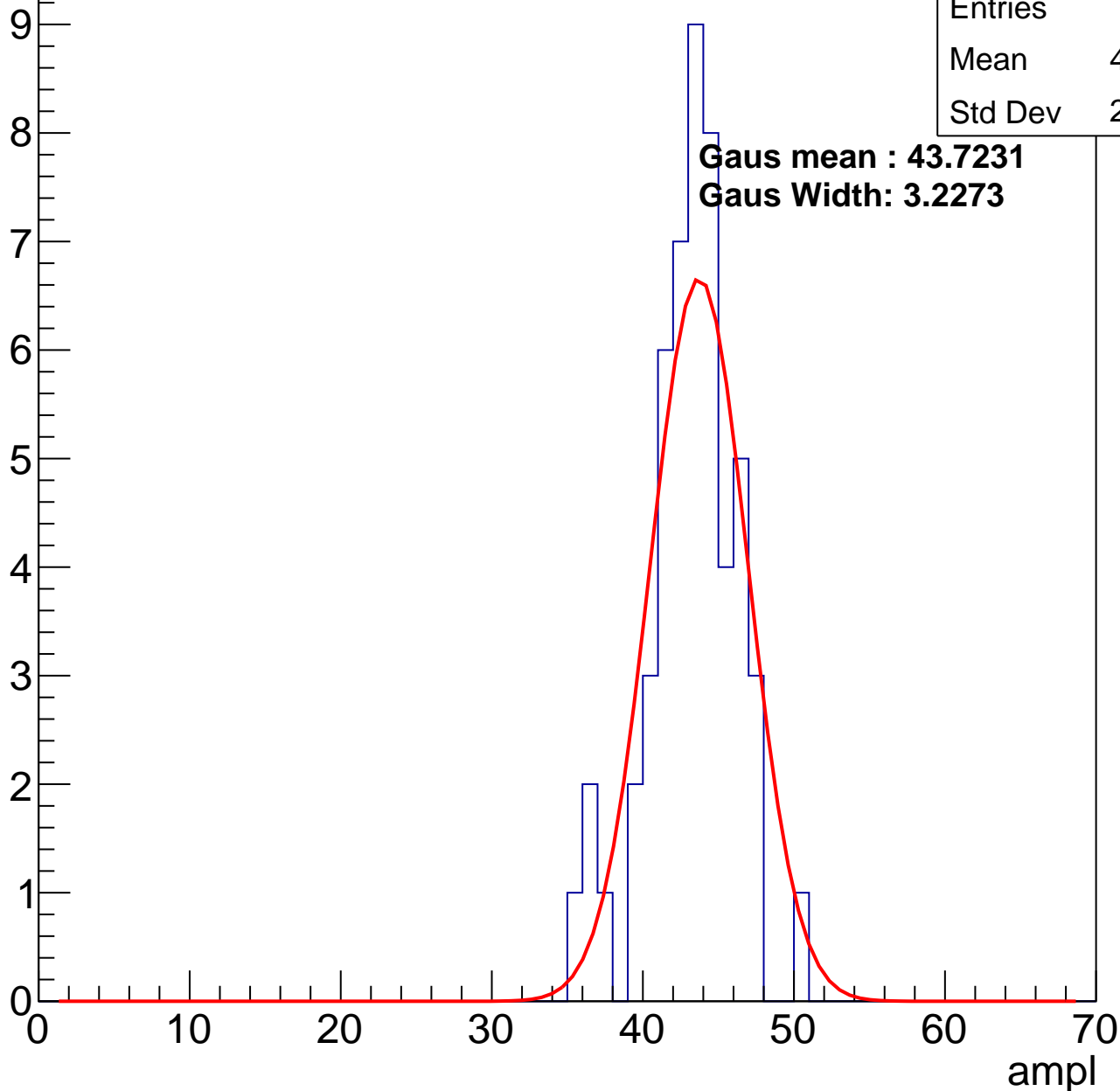
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.73
Std Dev	2.942

**Gaus mean : 43.7231**

**Gaus Width: 3.2273**

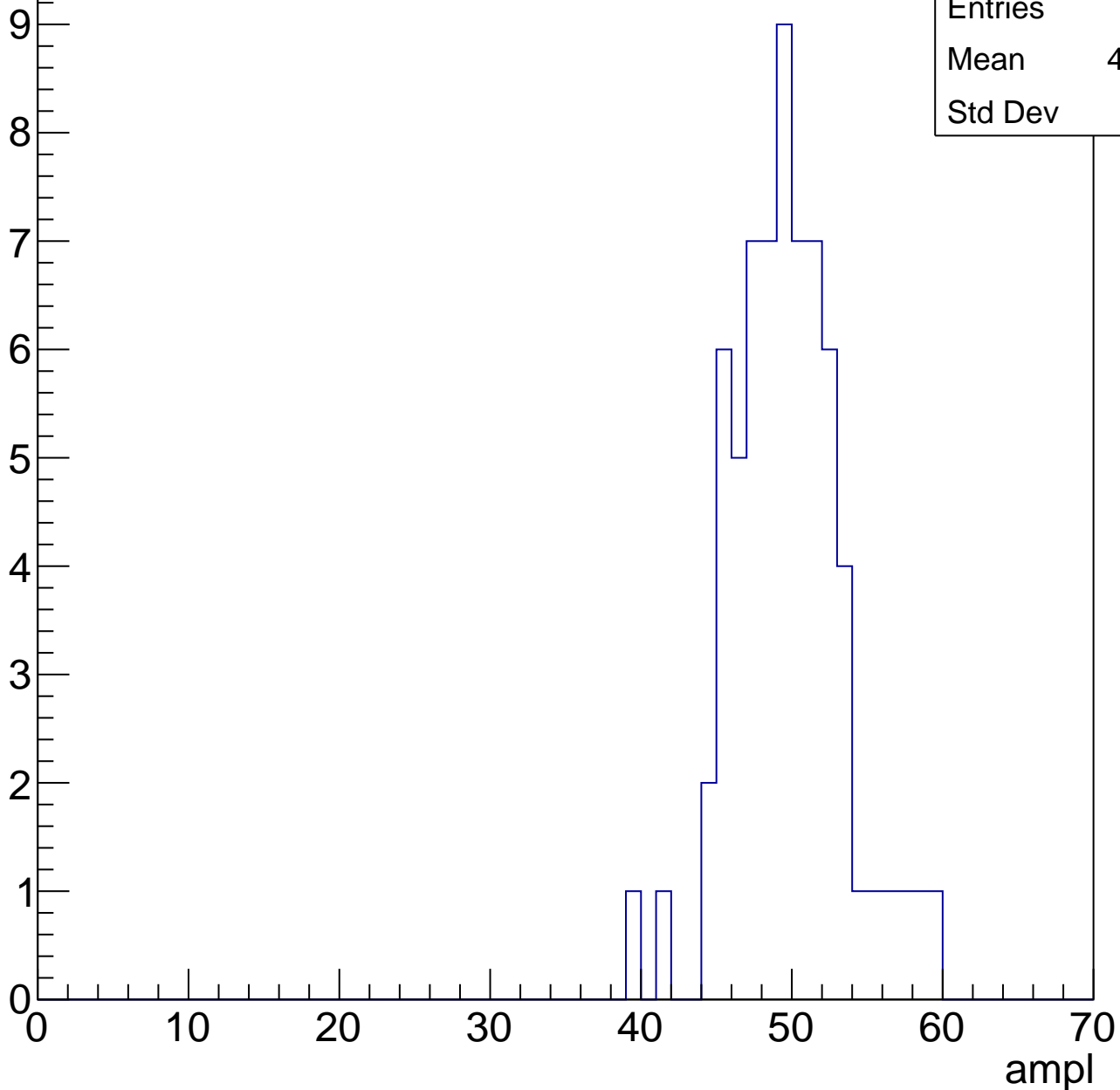


# B1L003S, U22-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.18
Std Dev	3.63

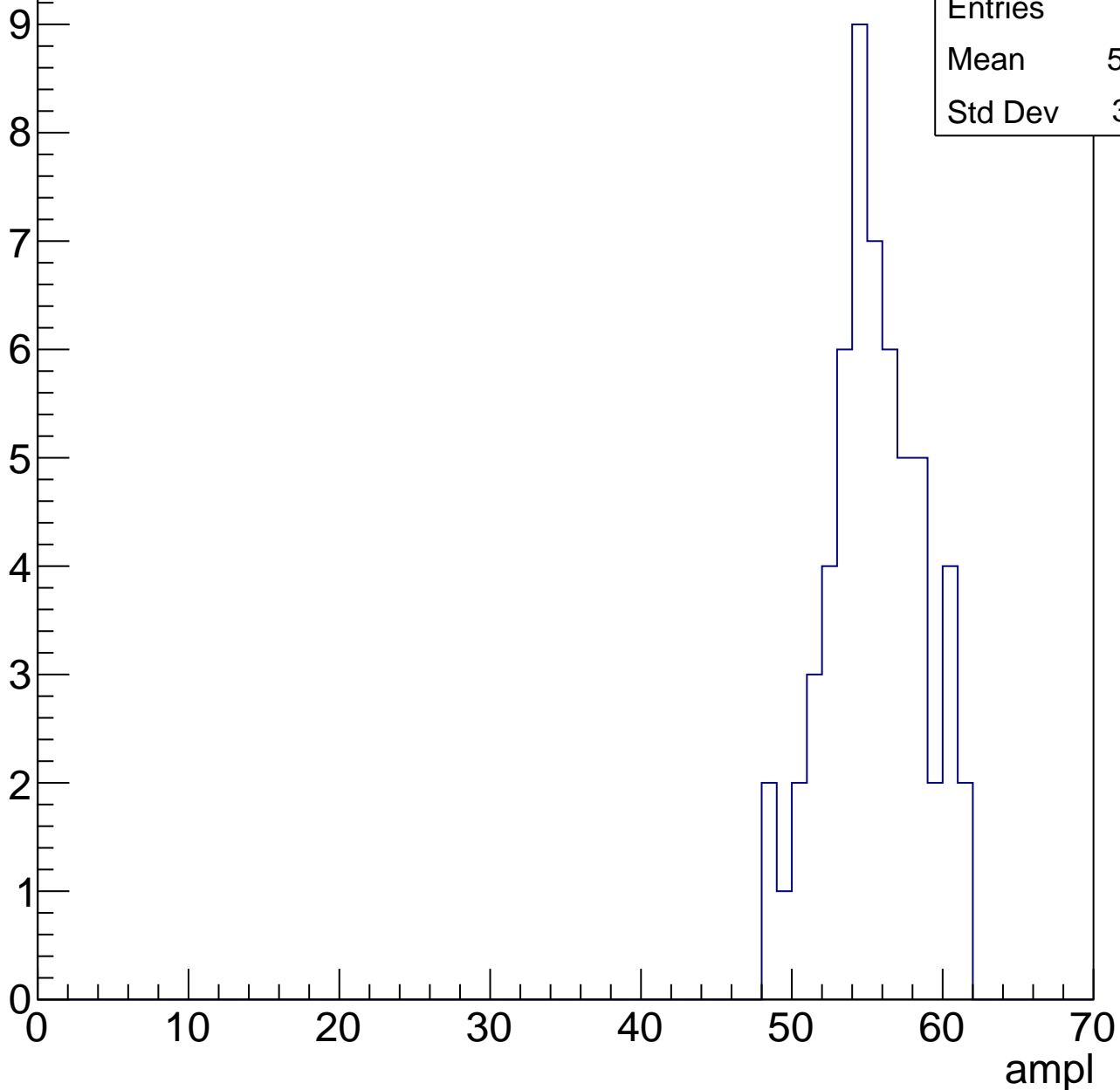


# B1L003S, U22-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

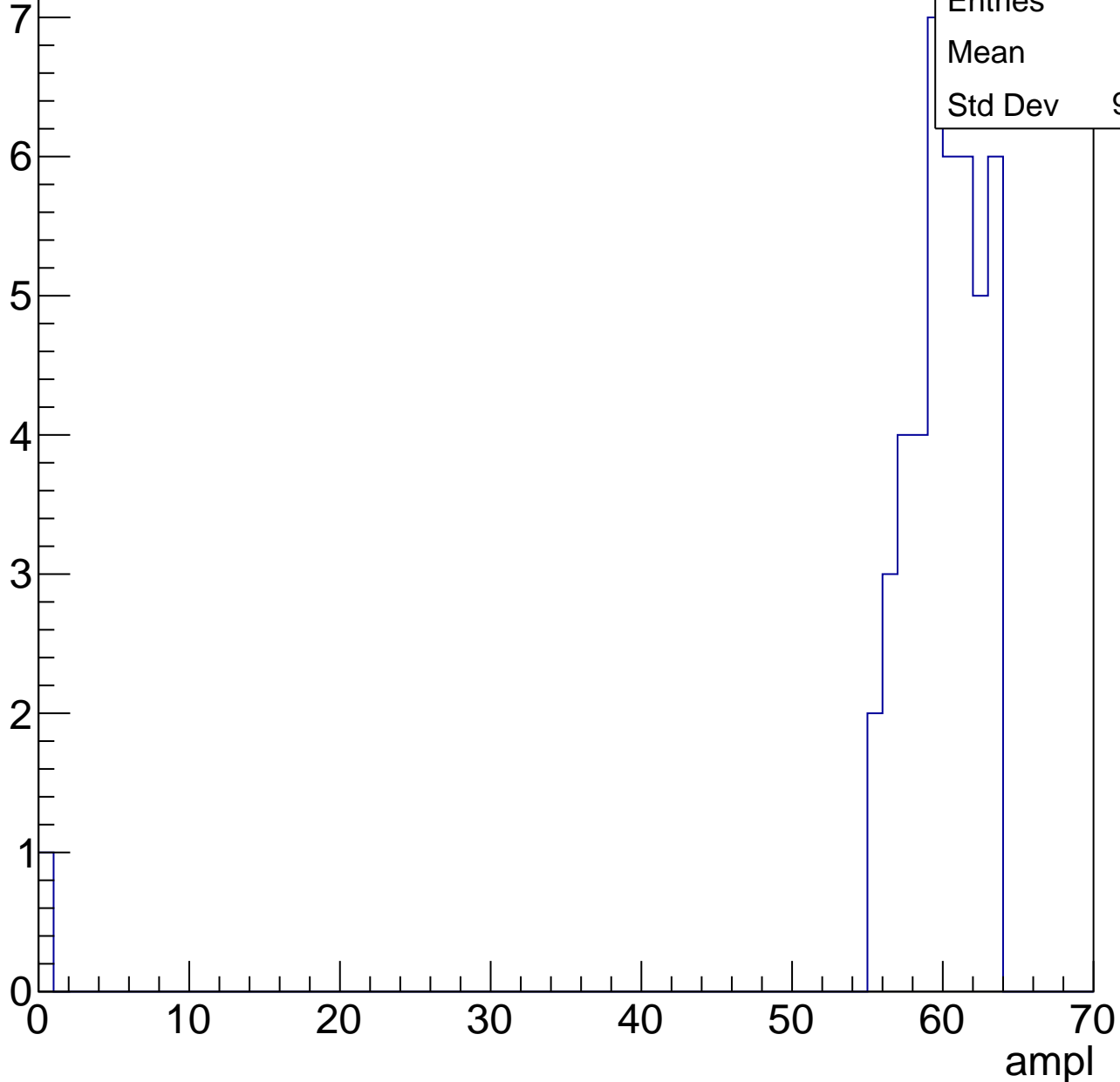
Entries	58
Mean	54.93
Std Dev	3.151



# B1L003S, U22-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

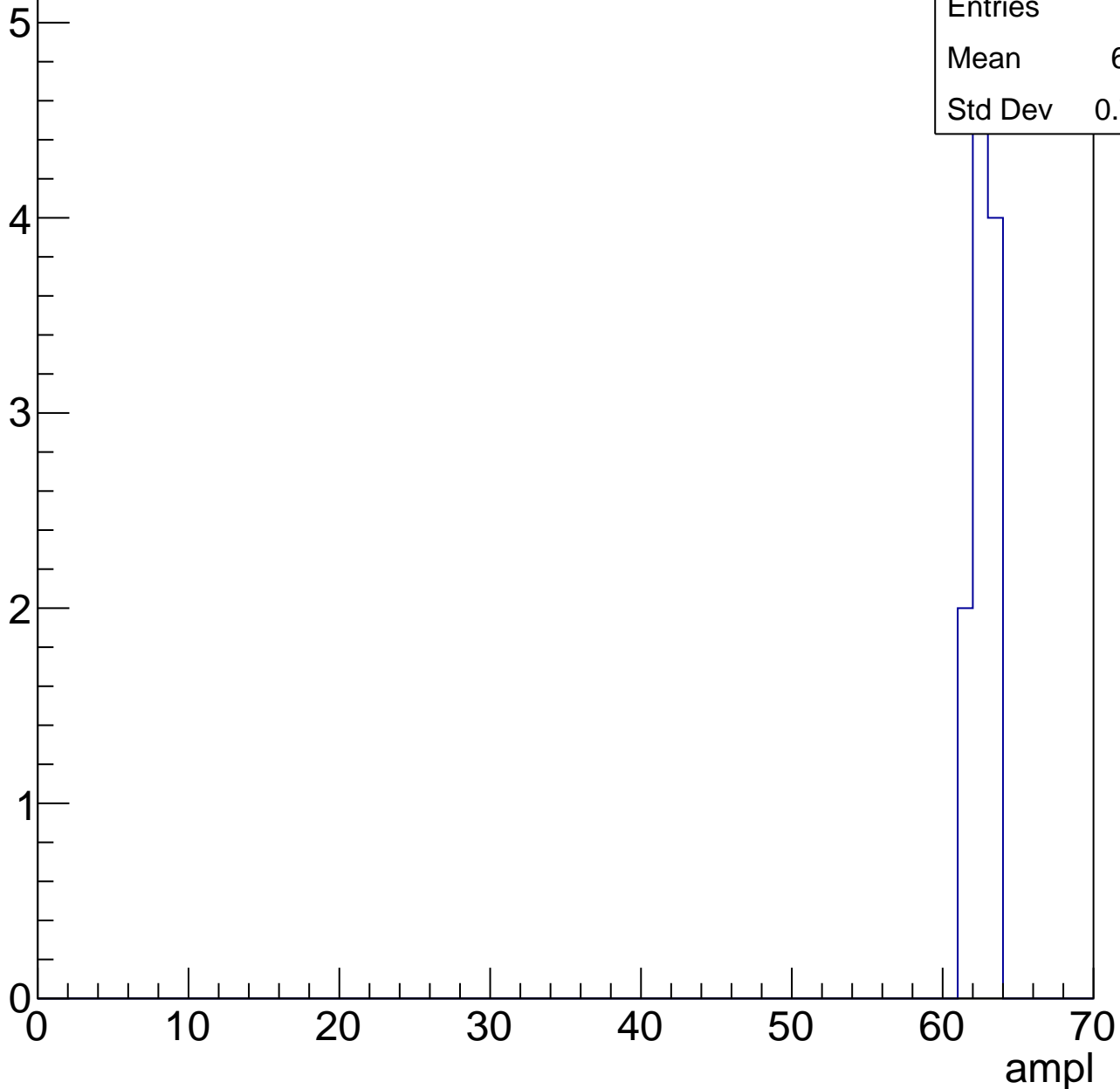


# B1L003S, U22-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.18
Std Dev	0.7158





# B1L003S, U22-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch20, adc0

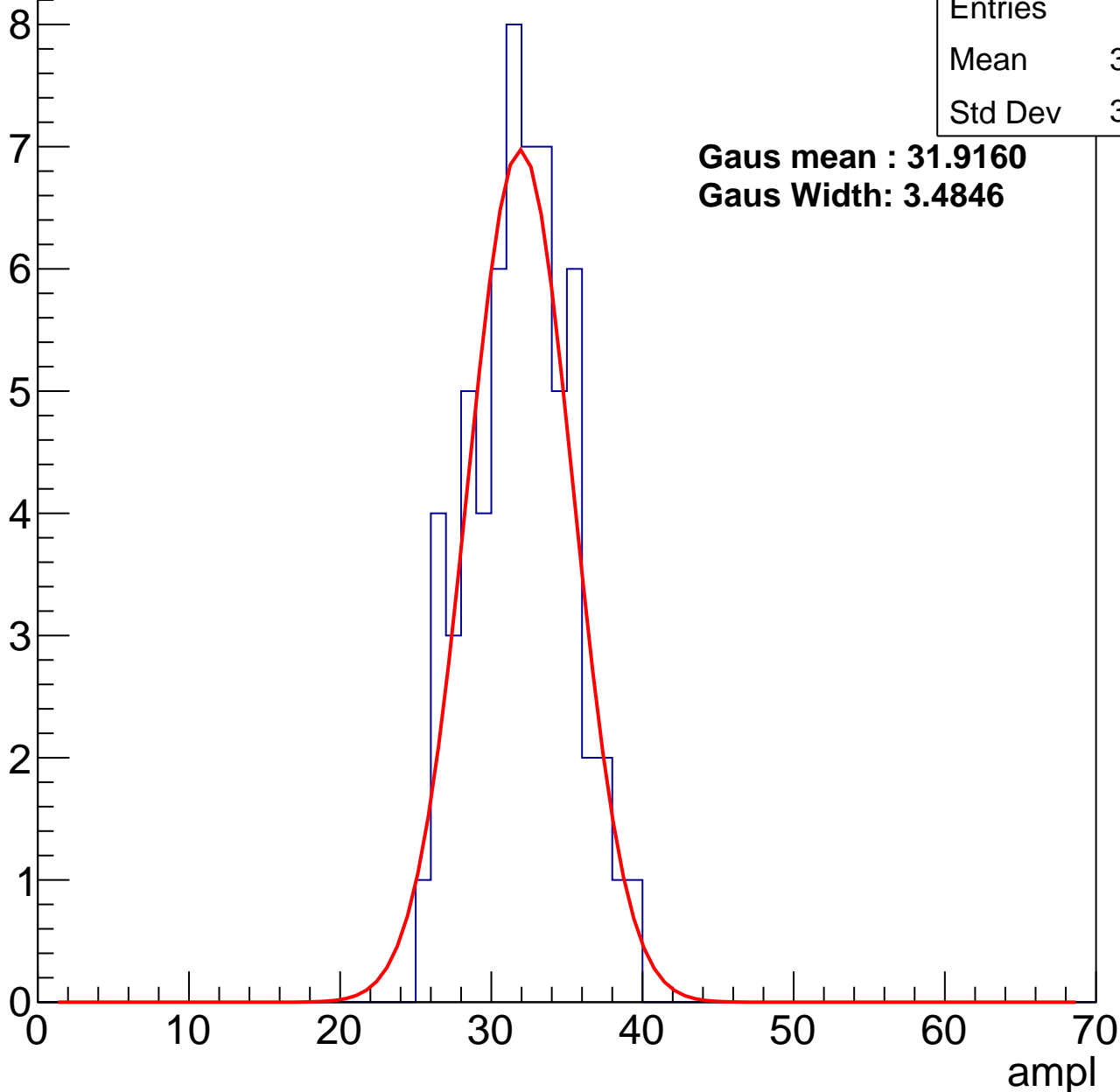
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	31.48
Std Dev	3.232

**Gaus mean : 31.9160**

**Gaus Width: 3.4846**



# B1L003S, U22-ch20, adc1

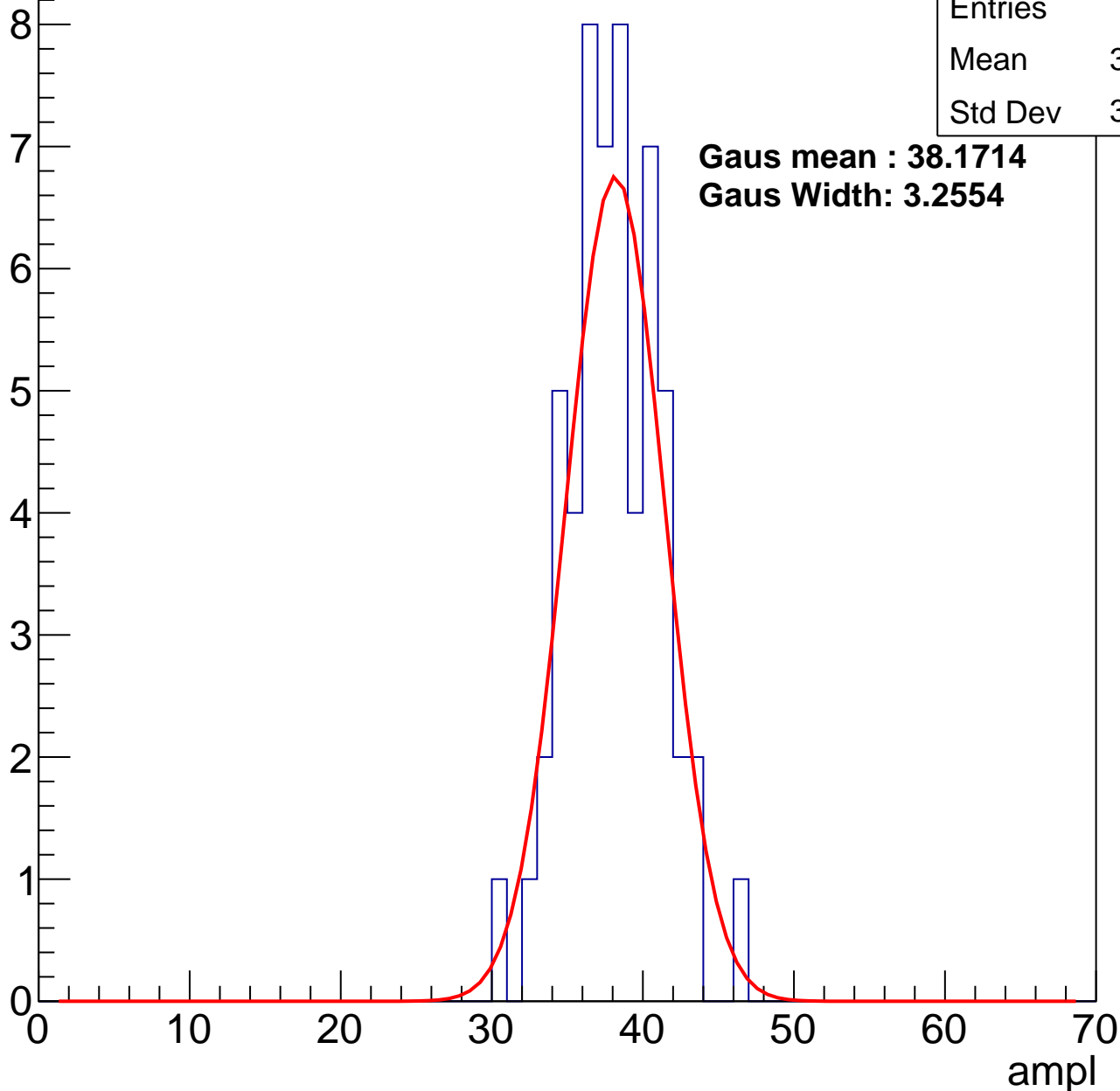
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	37.65
Std Dev	3.023

**Gaus mean : 38.1714**

**Gaus Width: 3.2554**



# B1L003S, U22-ch20, adc2

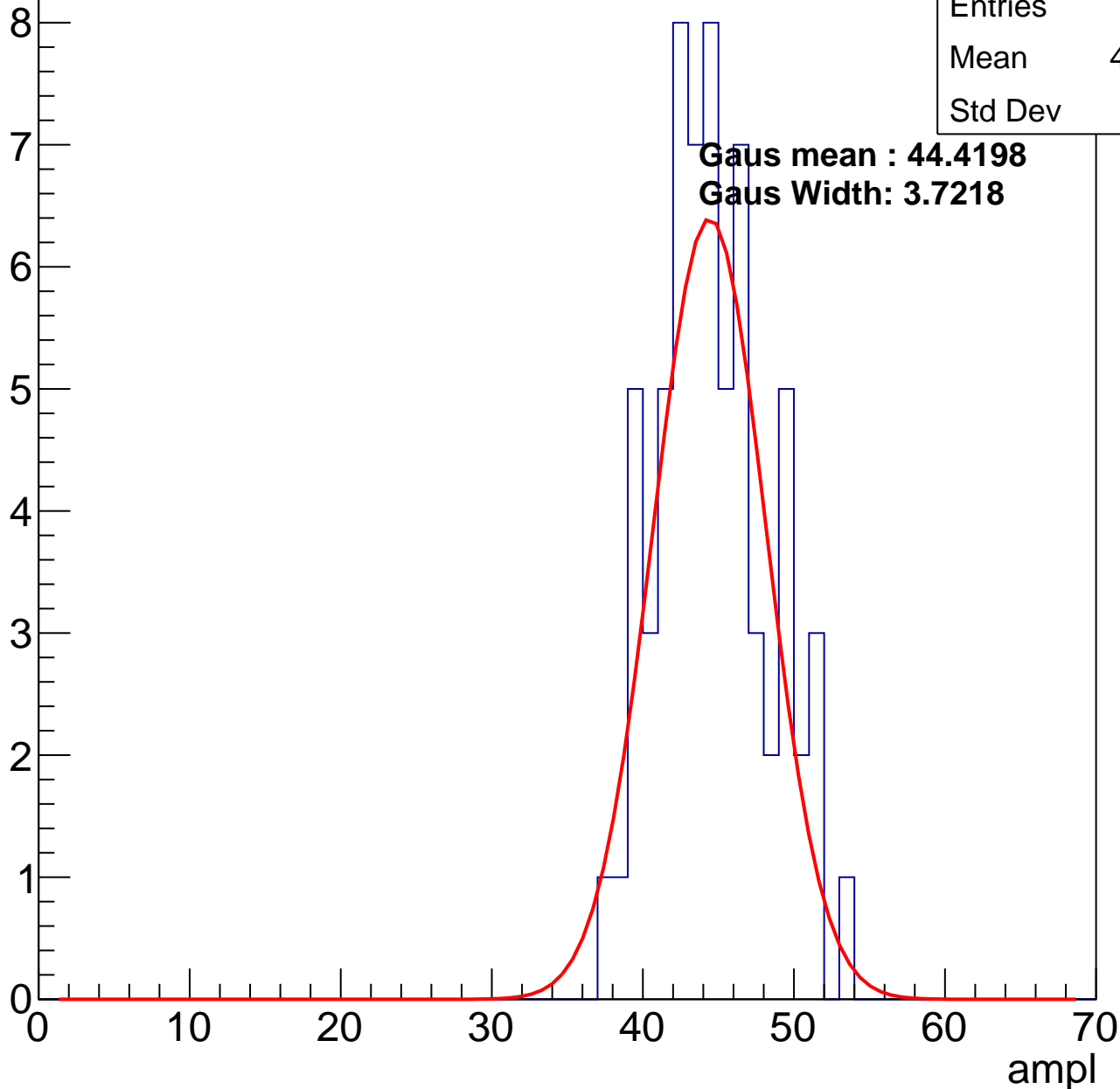
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.23
Std Dev	3.6

**Gaus mean : 44.4198**

**Gaus Width: 3.7218**

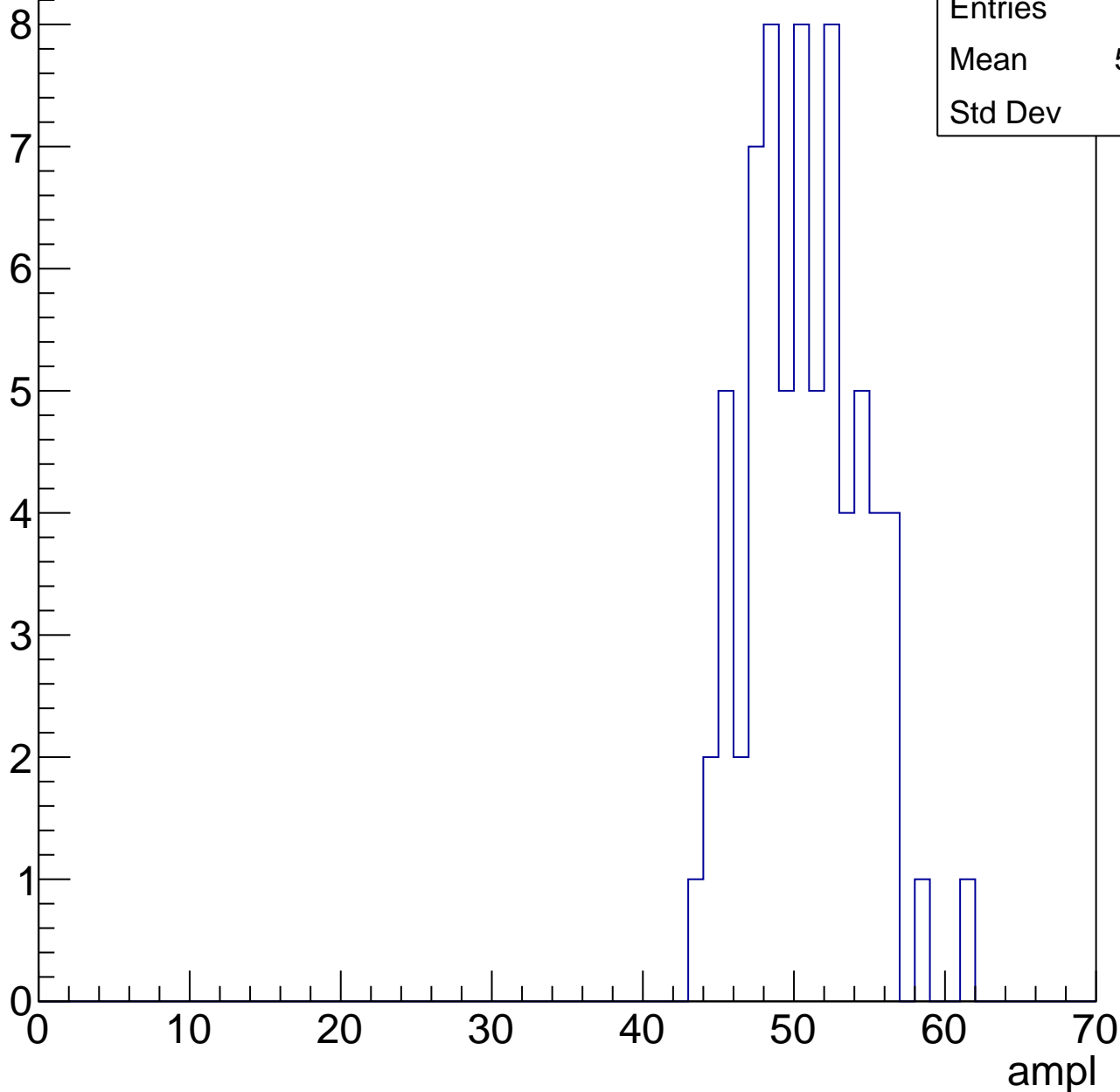


# B1L003S, U22-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	50.31
Std Dev	3.69

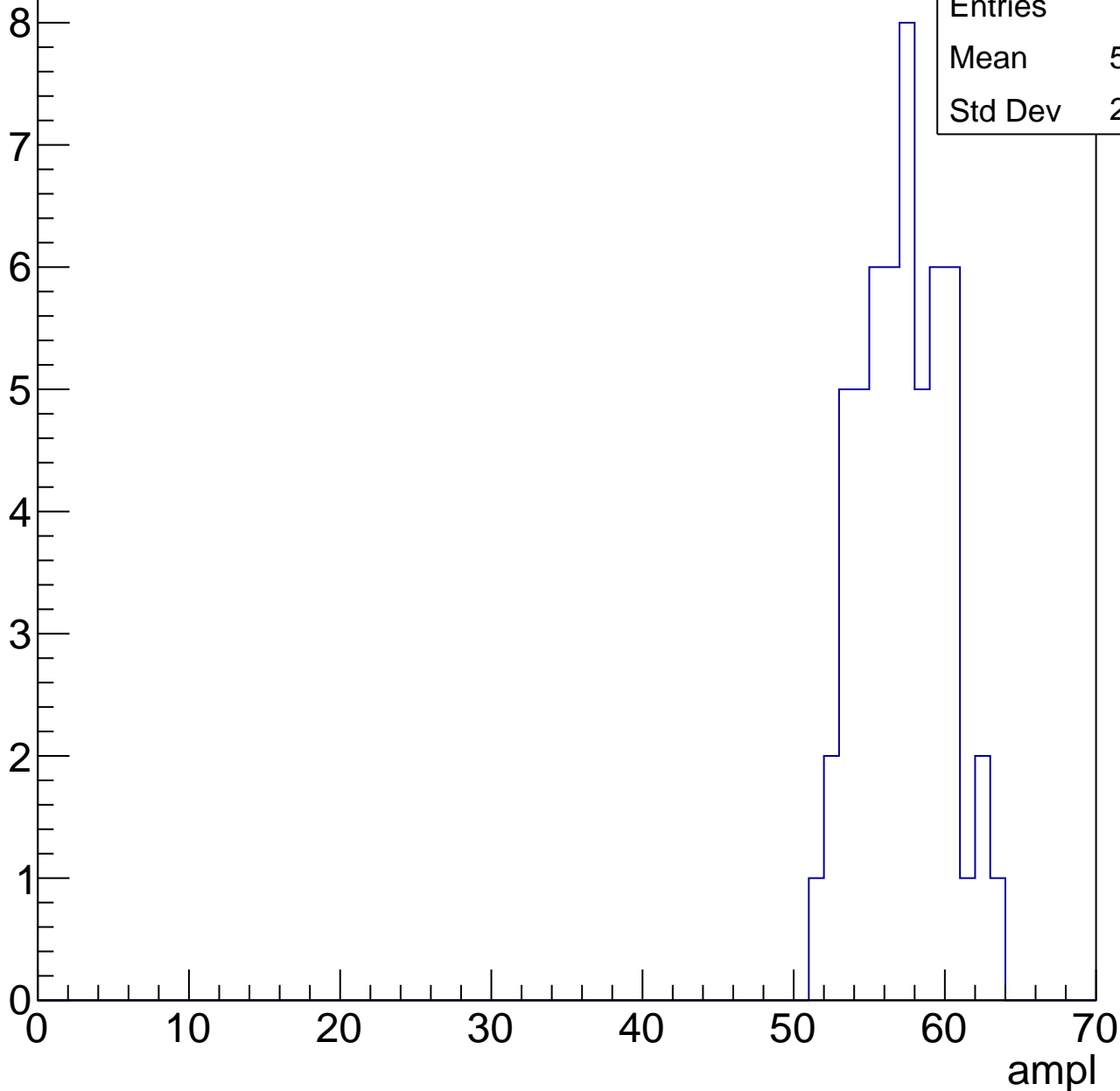


# B1L003S, U22-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	56.74
Std Dev	2.797

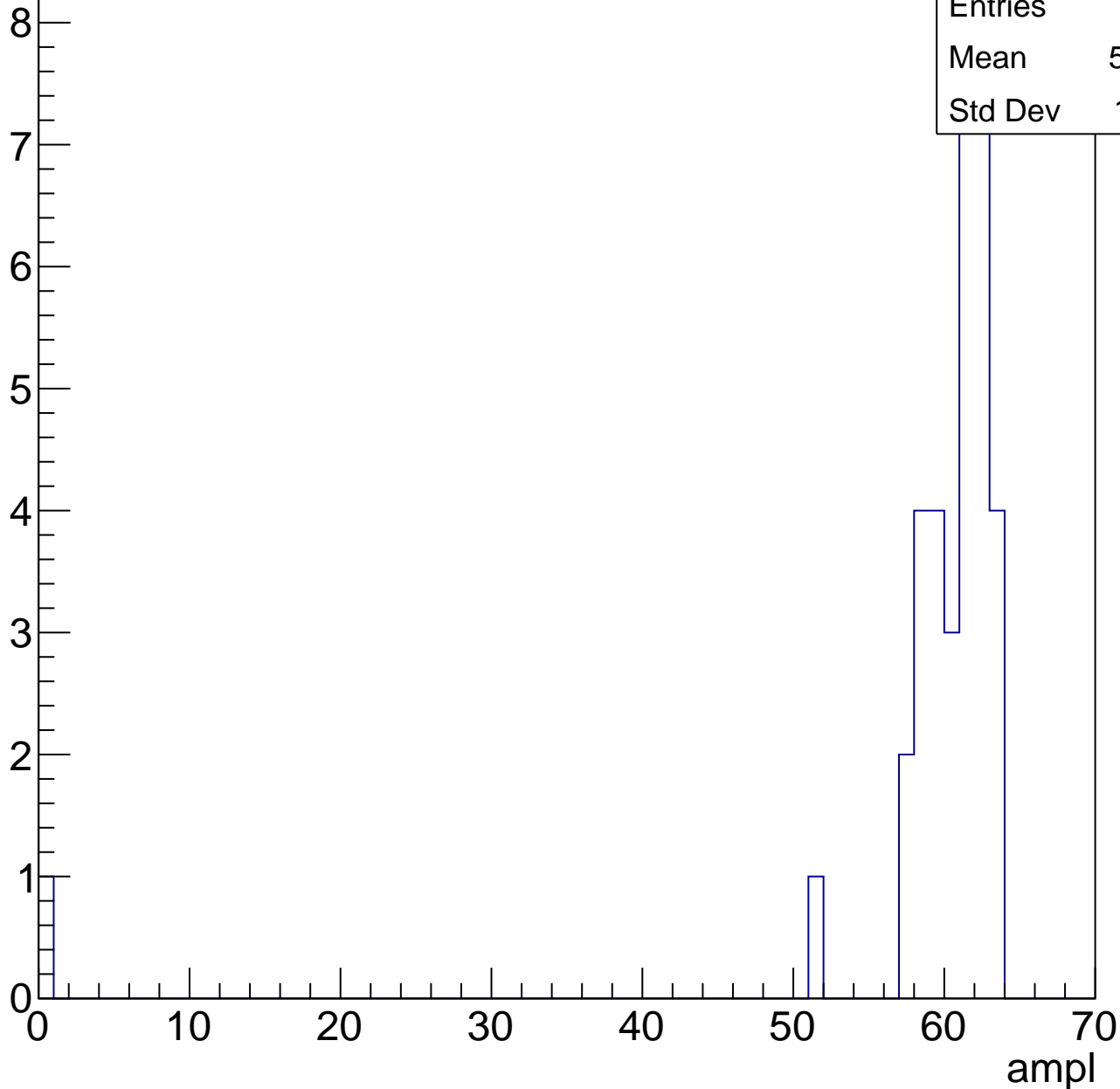


# B1L003S, U22-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	35
Mean	58.54
Std Dev	10.31

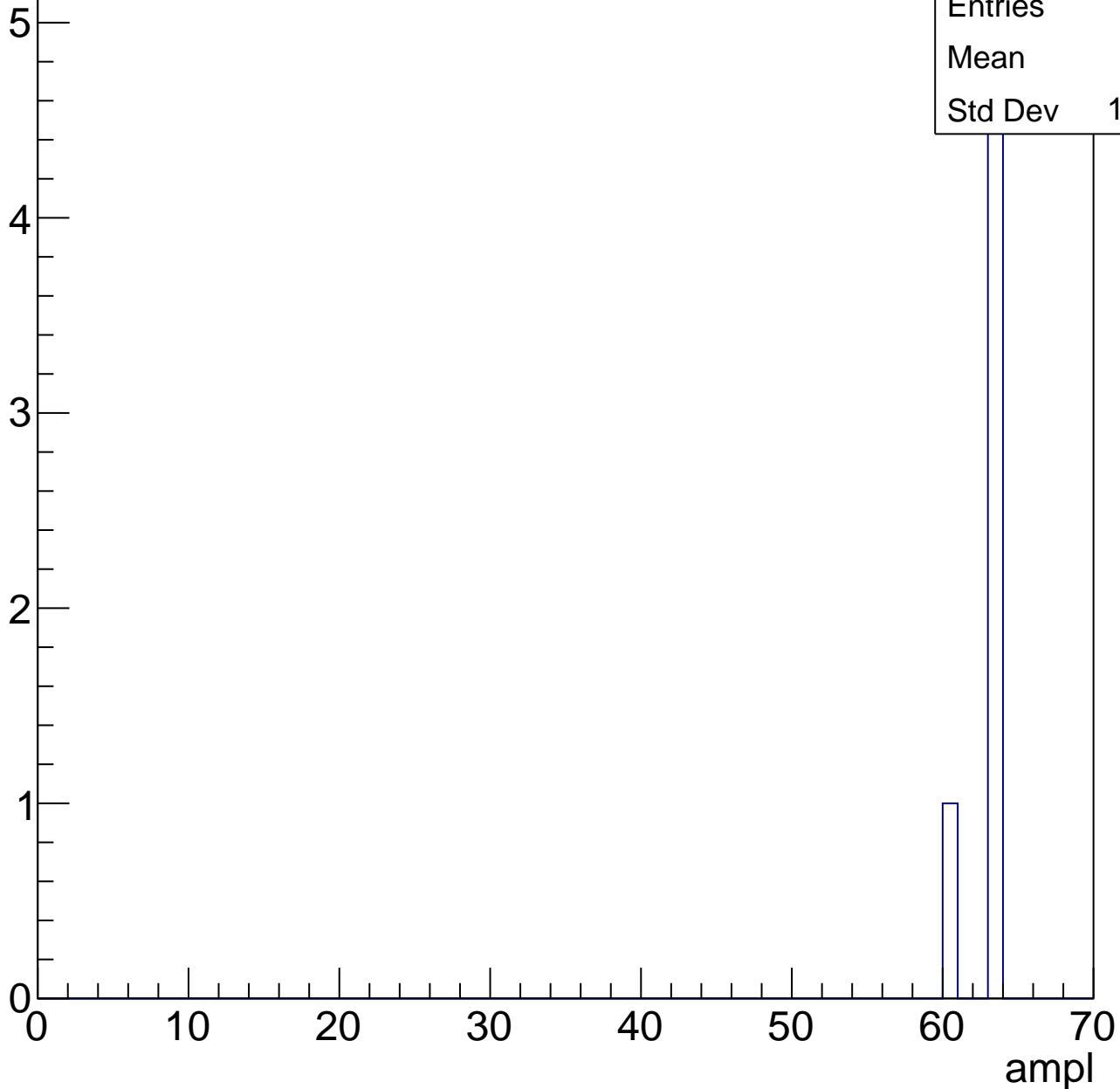


# B1L003S, U22-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	6
Mean	62.5
Std Dev	1.118





# B1L003S, U22-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B1L003S, U22-ch21, adc0

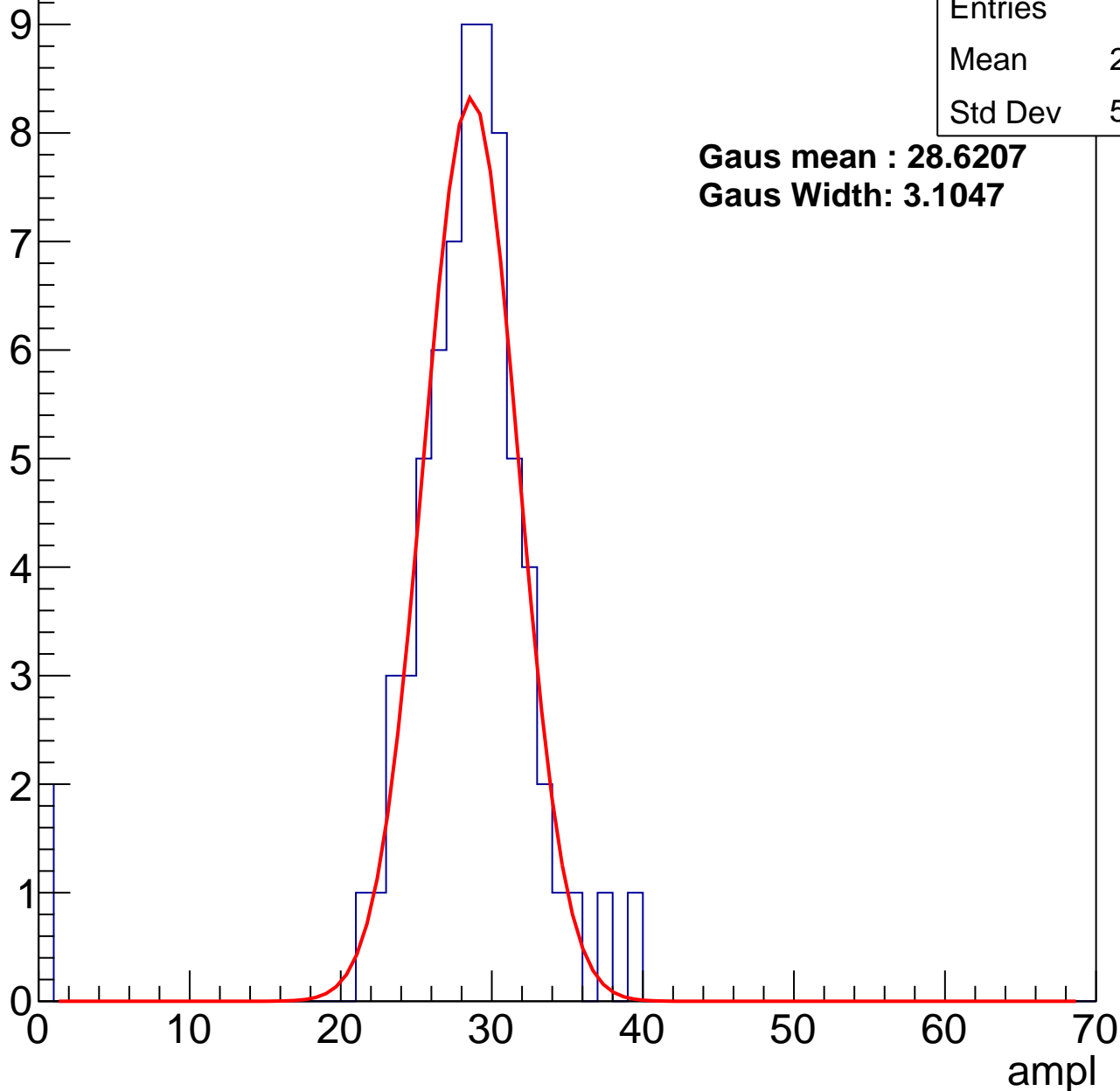
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	27.55
Std Dev	5.805

**Gaus mean : 28.6207**

**Gaus Width: 3.1047**

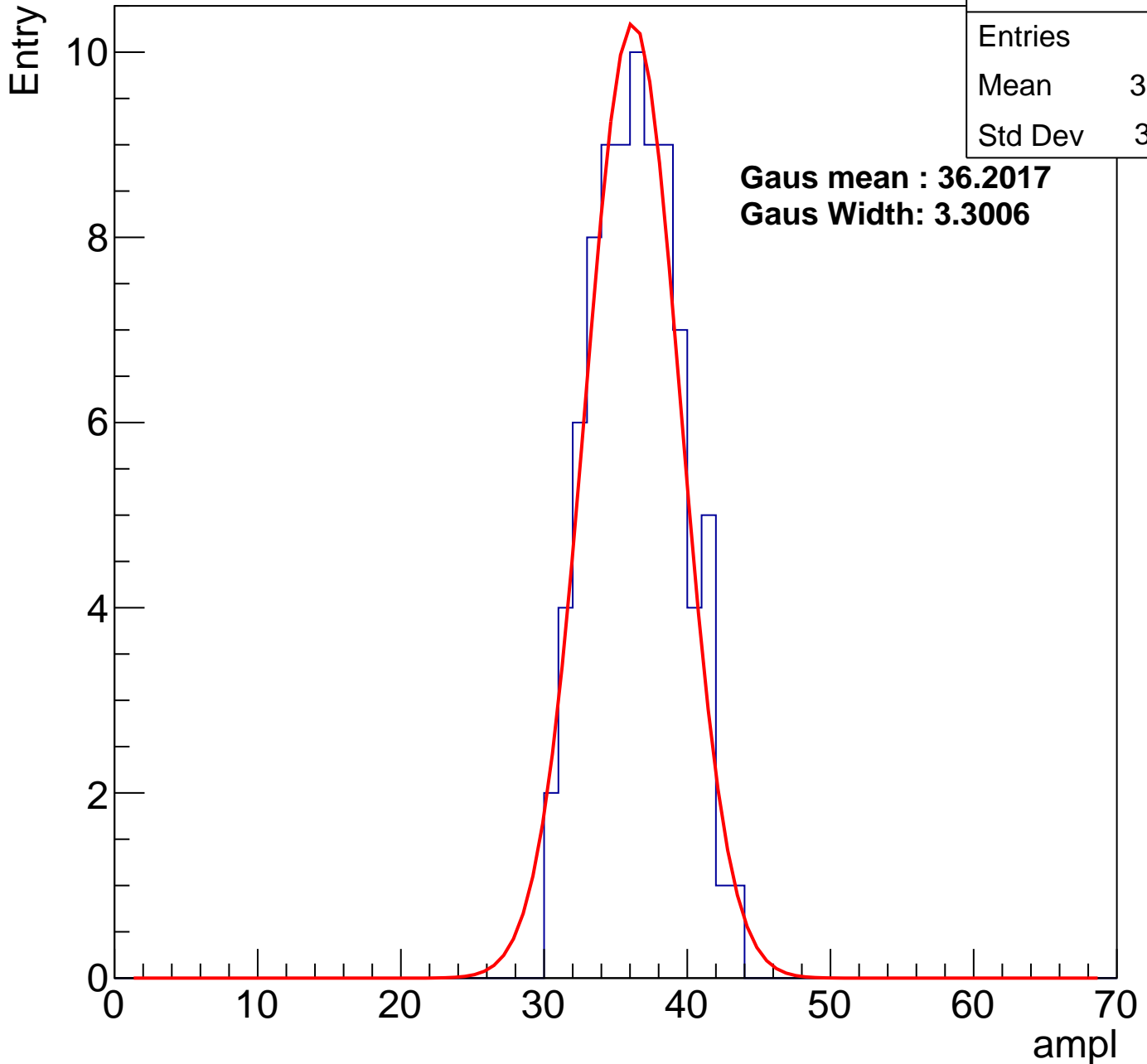


# B1L003S, U22-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	84
Mean	35.94
Std Dev	3.021

**Gaus mean : 36.2017**  
**Gaus Width: 3.3006**



# B1L003S, U22-ch21, adc2

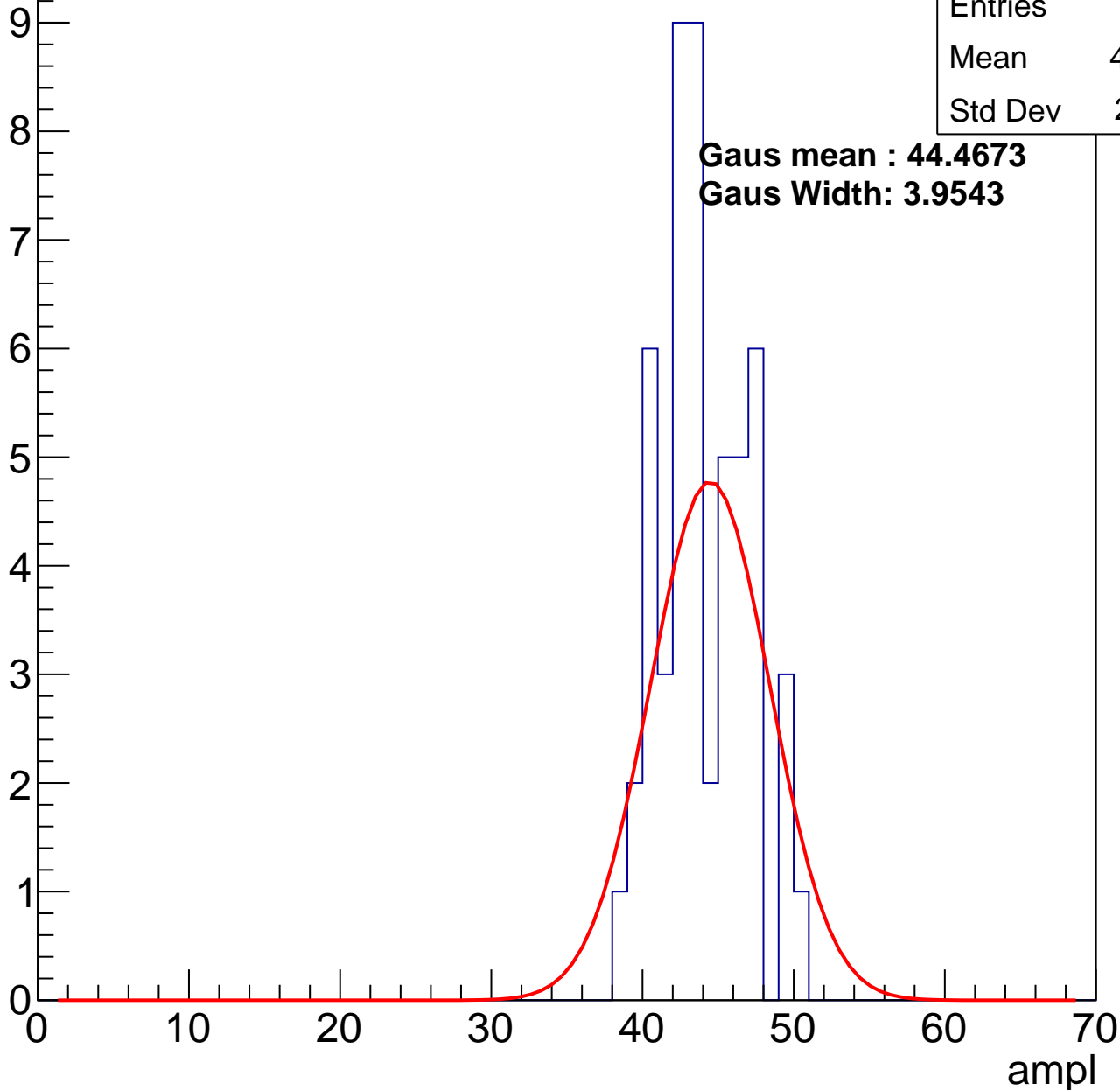
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.58
Std Dev	2.891

**Gaus mean : 44.4673**

**Gaus Width: 3.9543**

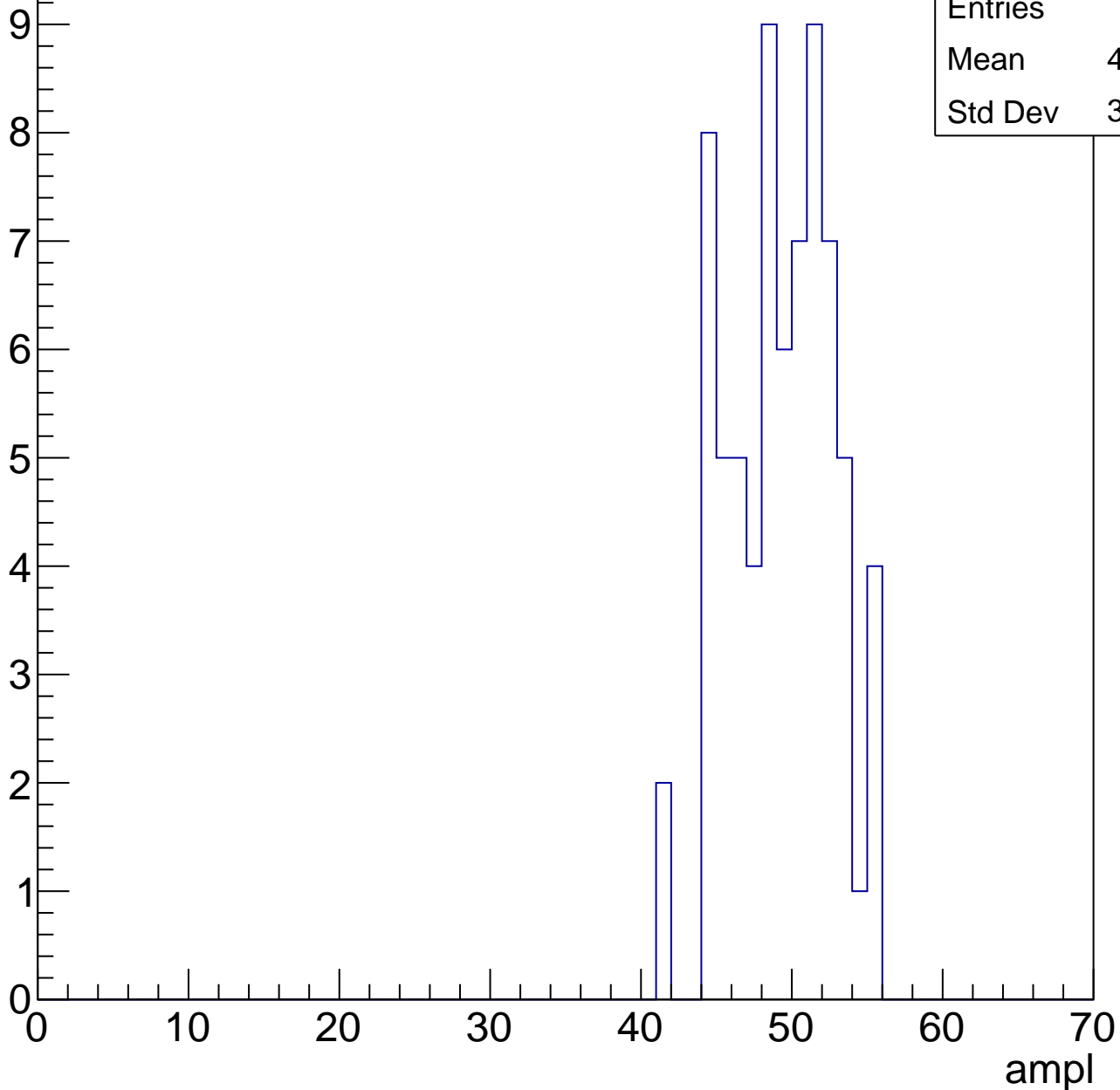


# B1L003S, U22-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.82
Std Dev	3.397

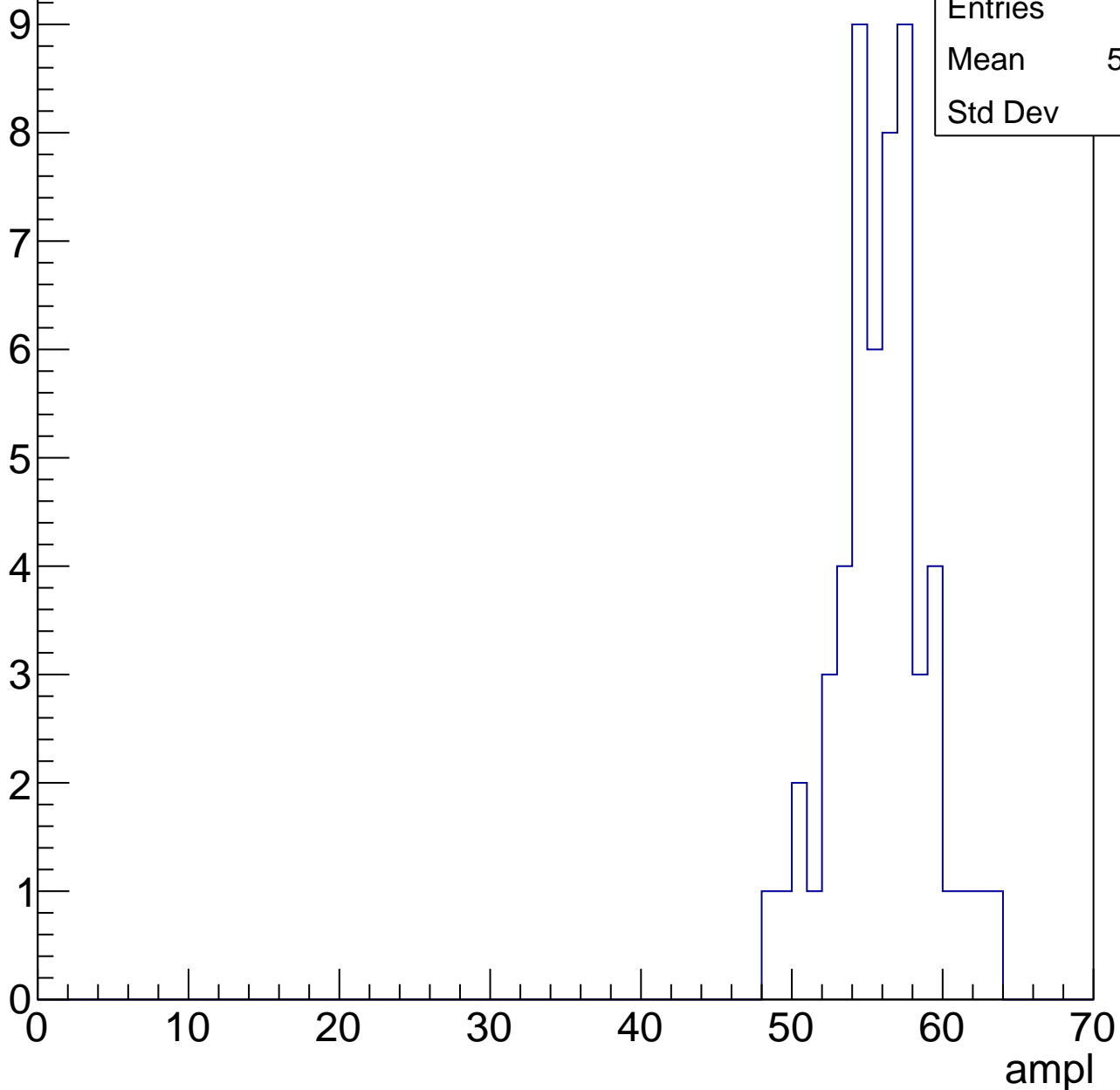


# B1L003S, U22-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.44
Std Dev	3.02

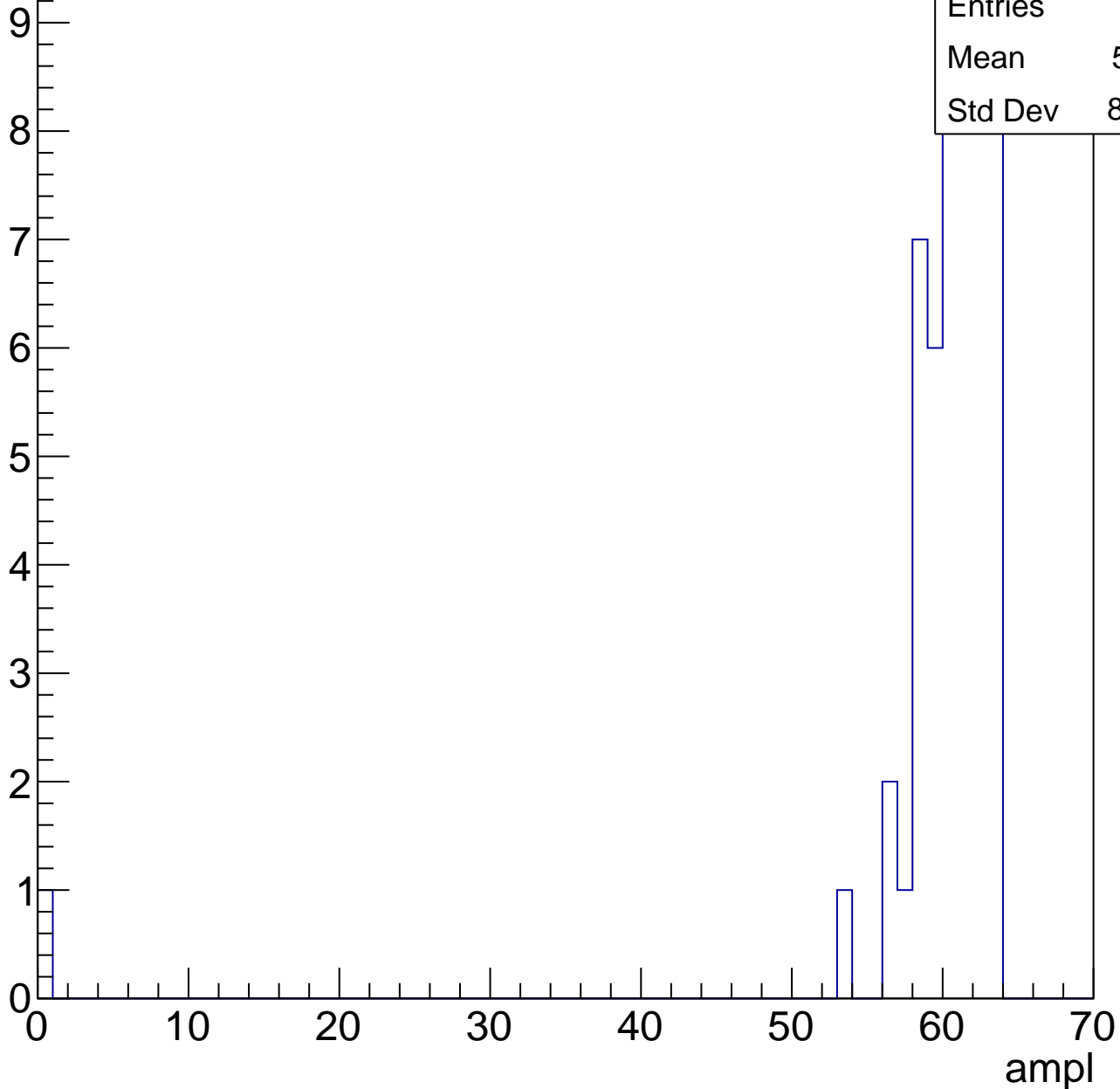


# B1L003S, U22-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59.11
Std Dev	8.467



# B1L003S, U22-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch22, adc0

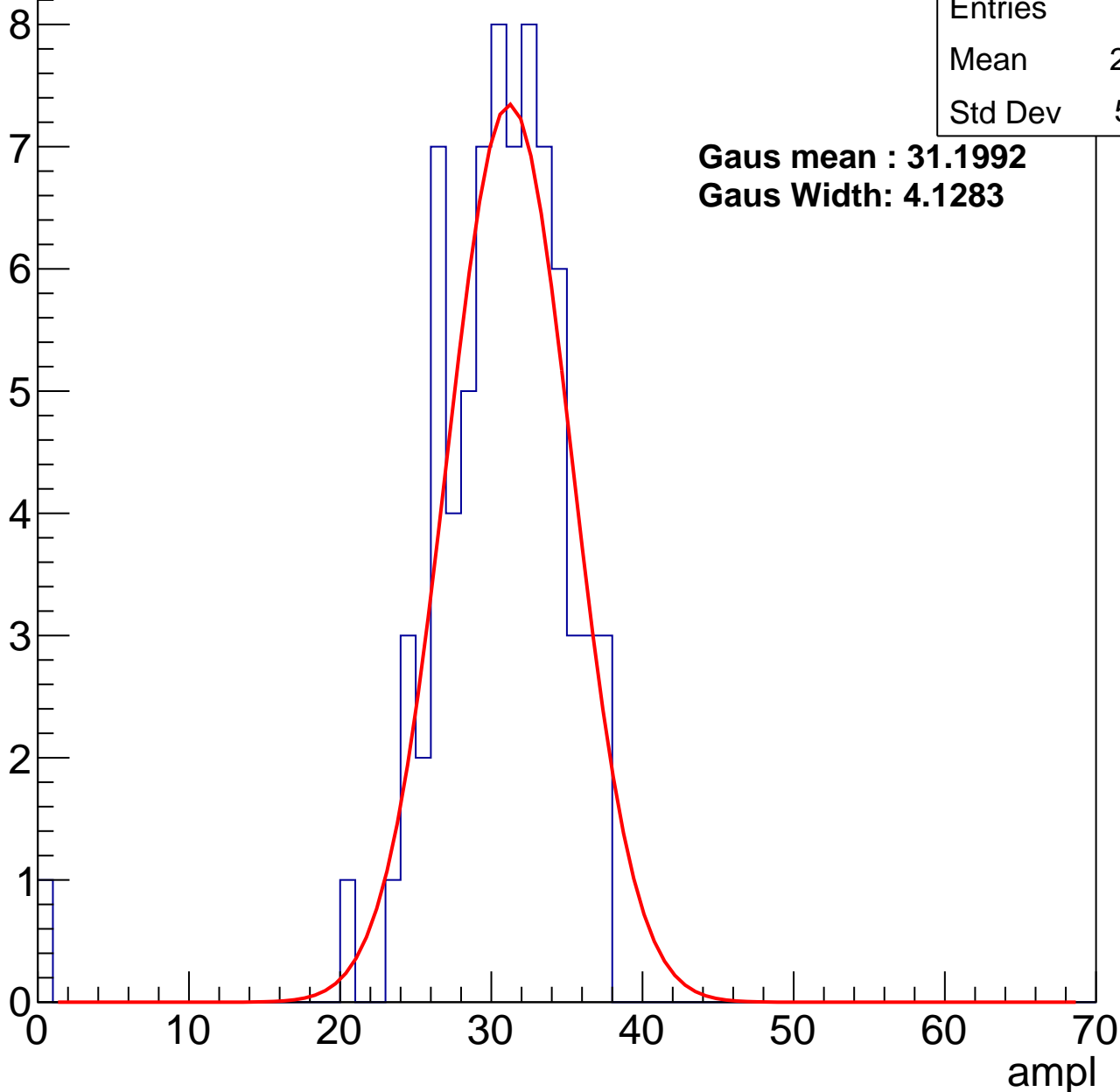
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	29.87
Std Dev	5.011

**Gaus mean : 31.1992**

**Gaus Width: 4.1283**



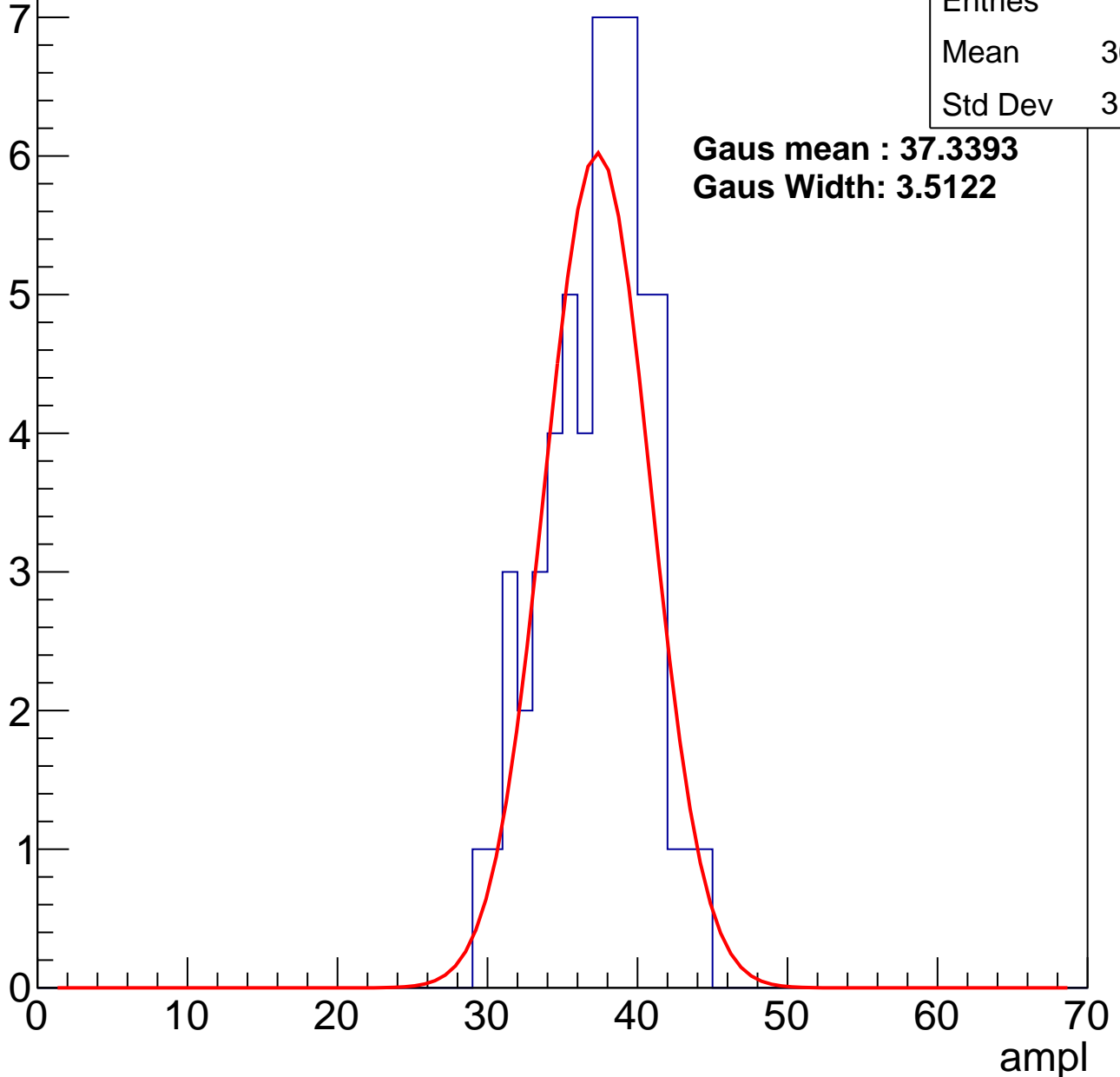
# B1L003S, U22-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.88
Std Dev	3.372

**Gaus mean : 37.3393**  
**Gaus Width: 3.5122**



# B1L003S, U22-ch22, adc2

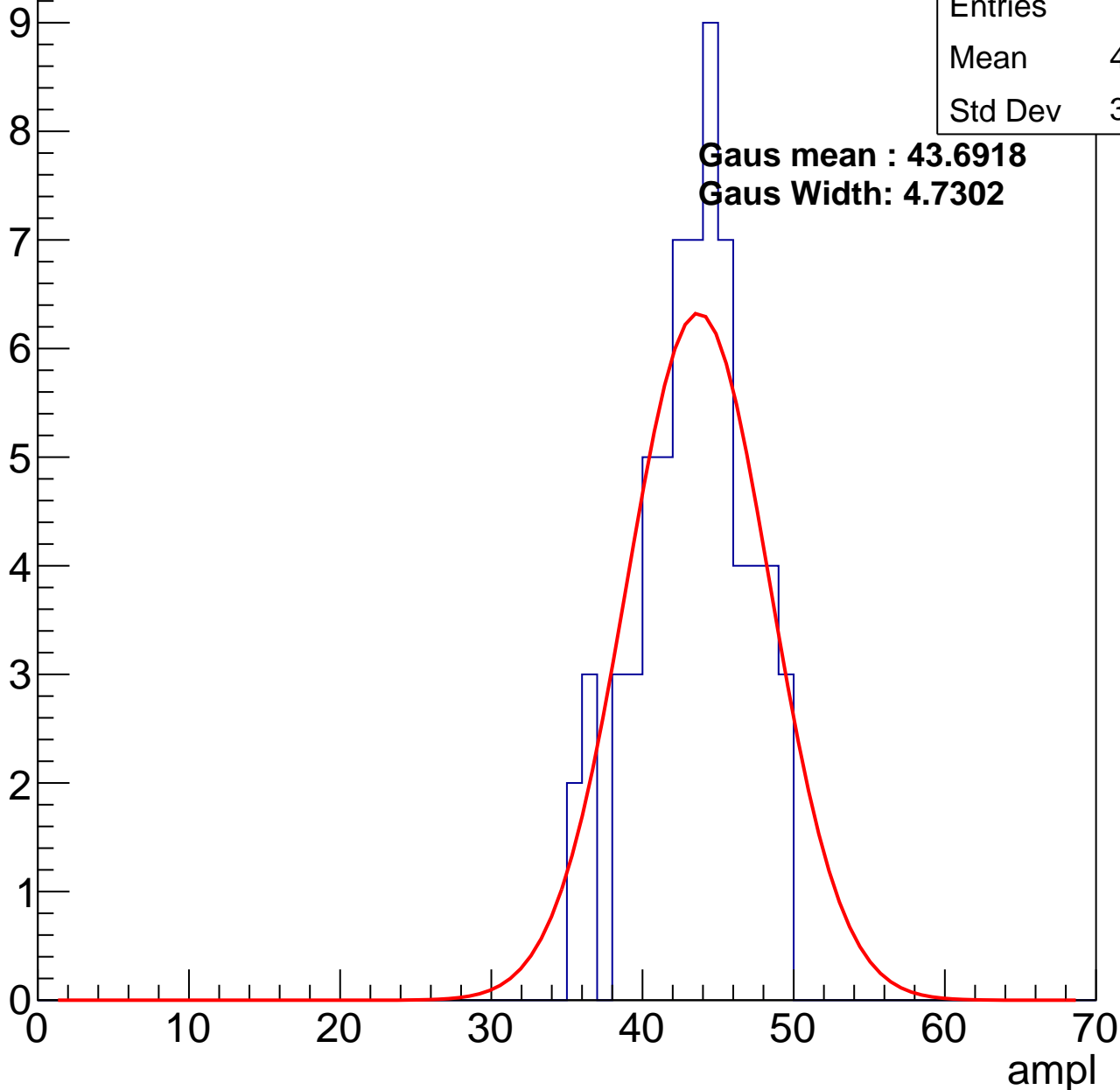
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.89
Std Dev	3.512

**Gaus mean : 43.6918**

**Gaus Width: 4.7302**

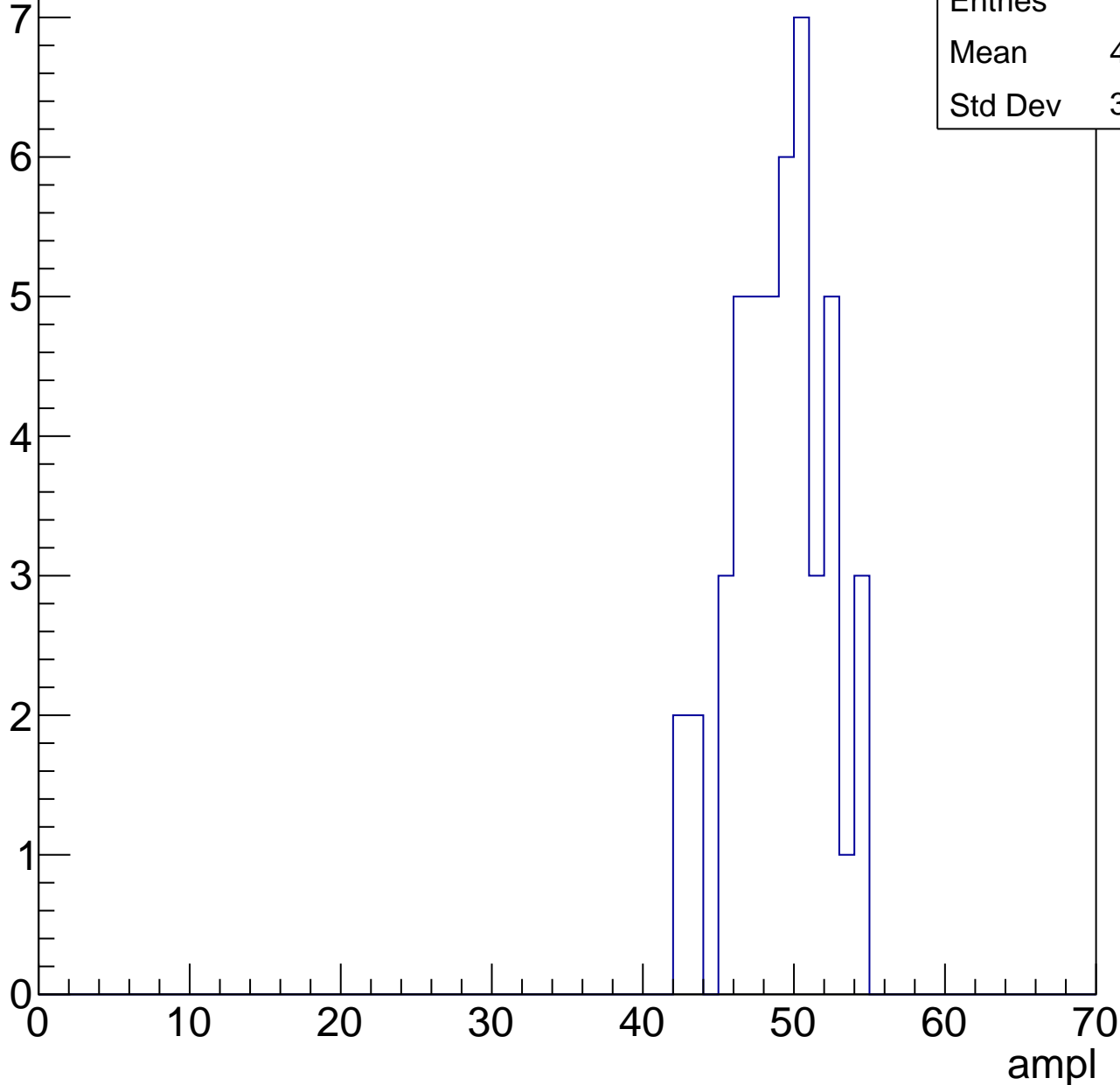


# B1L003S, U22-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	48.55
Std Dev	3.037



# B1L003S, U22-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	71
Mean	53.77
Std Dev	7.21

Entry

10

8

6

4

2

0

0

10

20

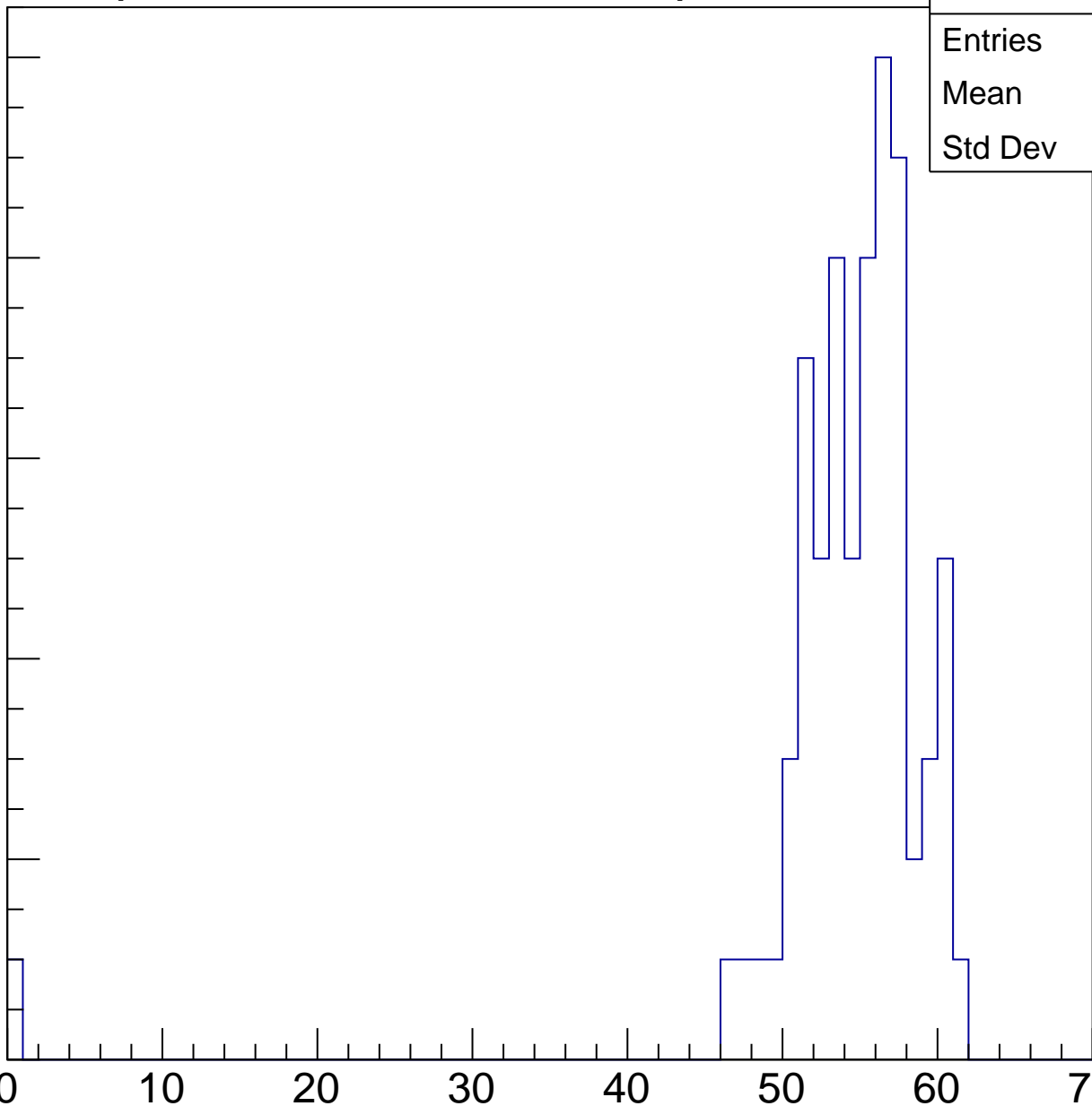
30

40

50

60

ampl



# B1L003S, U22-ch22, adc5

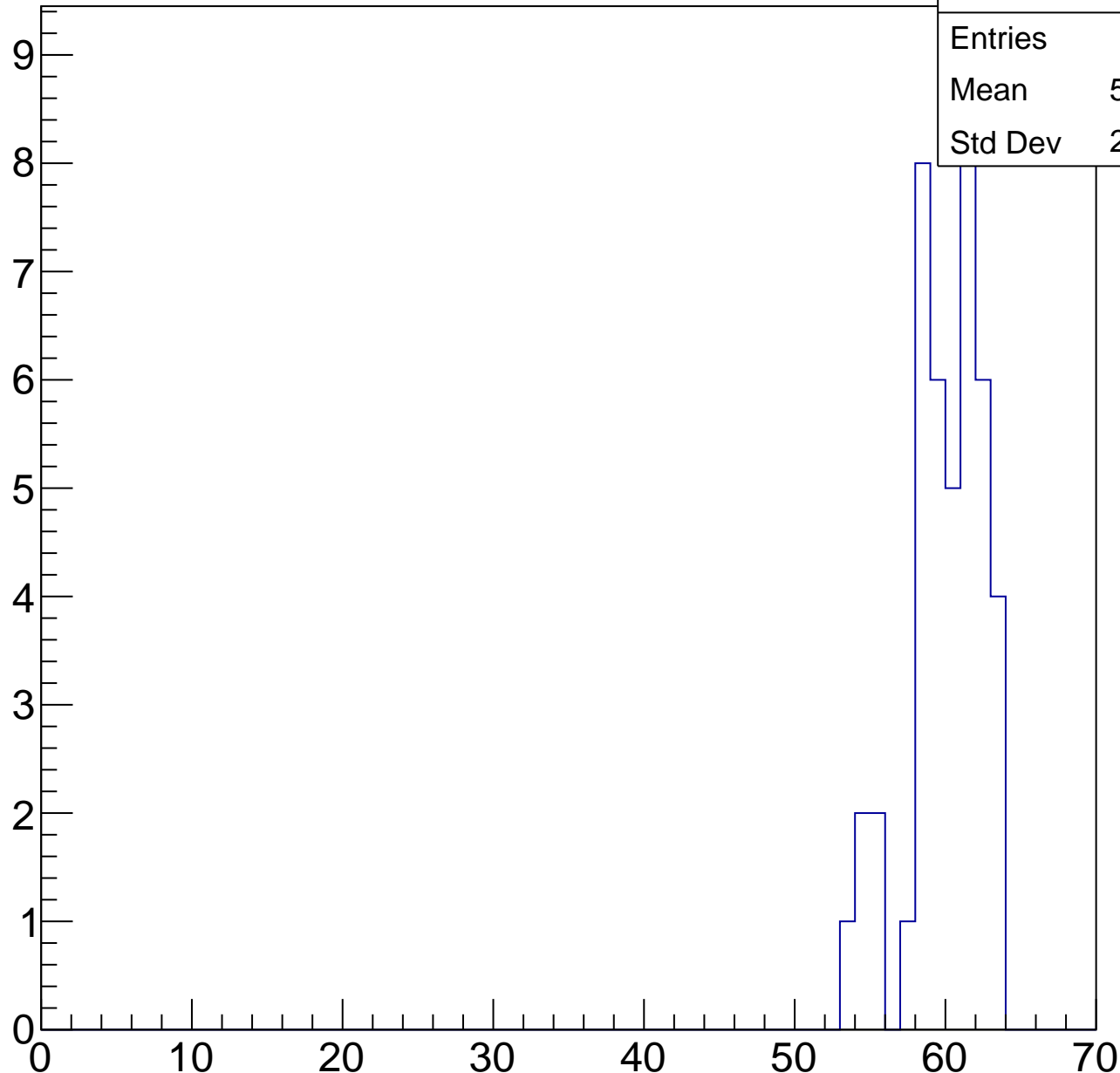
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	59.52
Std Dev	2.509

ampl

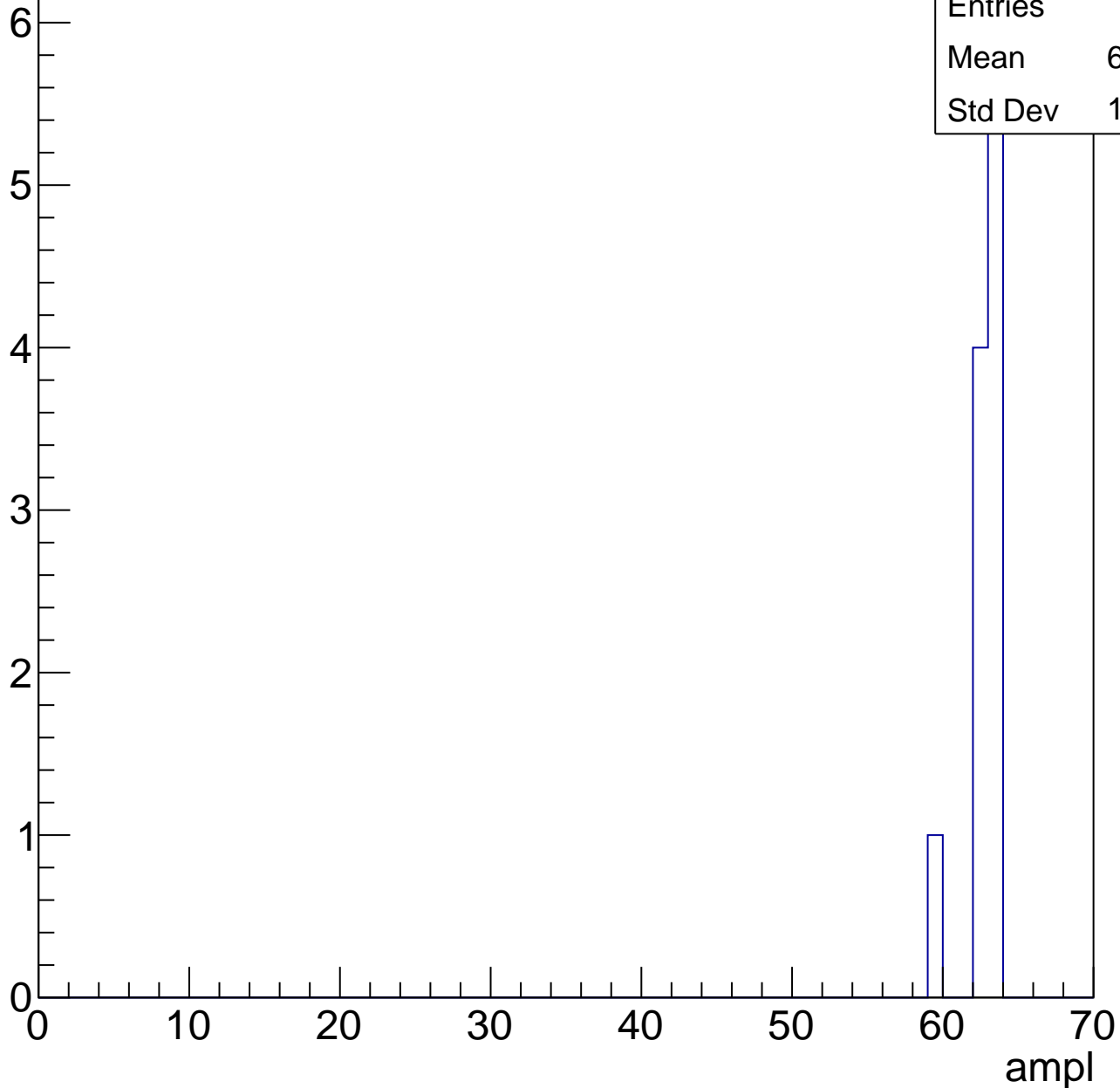


# B1L003S, U22-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.27
Std Dev	1.135





# B1L003S, U22-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	29.15
Std Dev	4.923

**Gaus mean : 30.3538**

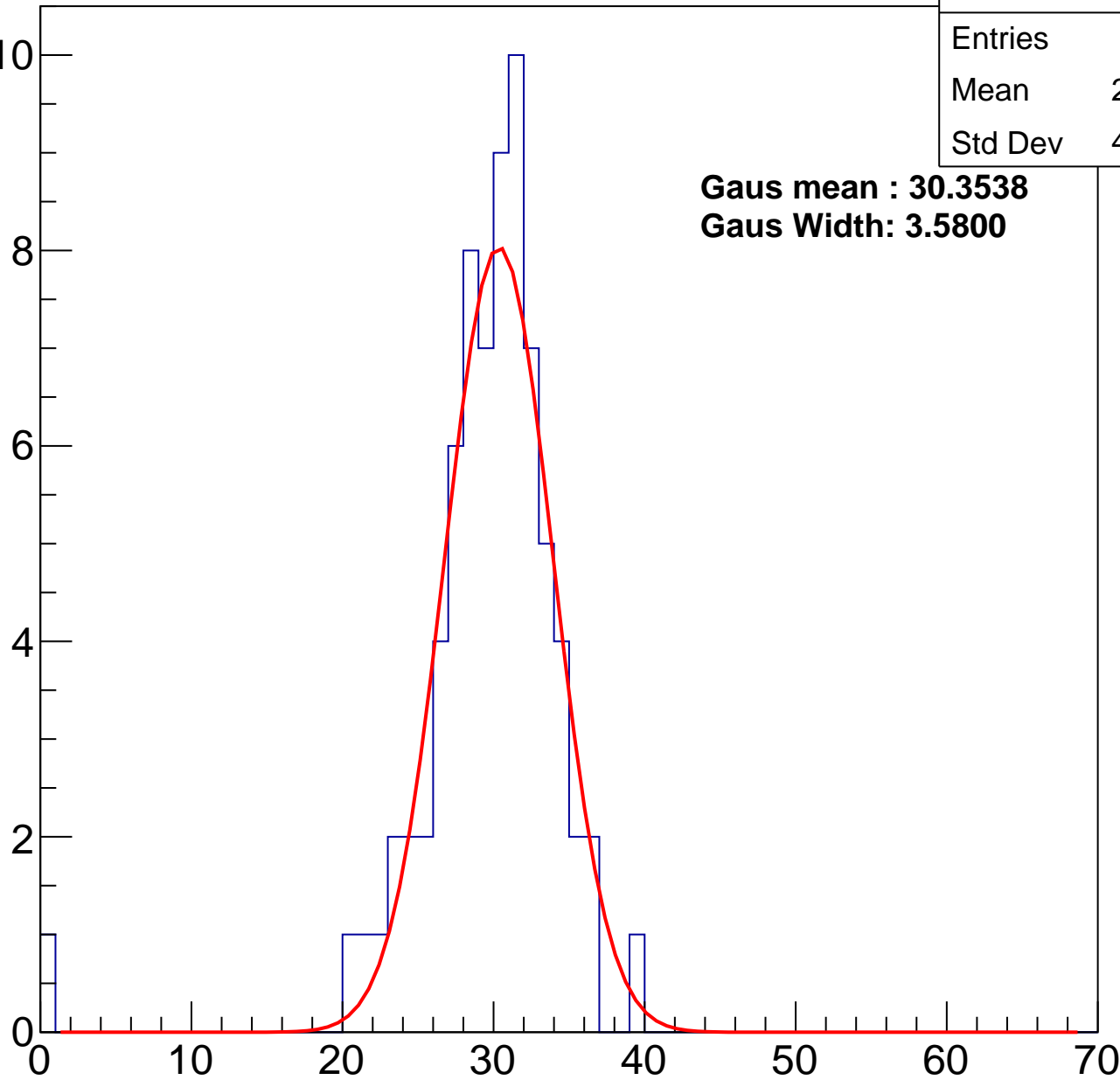
**Gaus Width: 3.5800**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U22-ch23, adc1

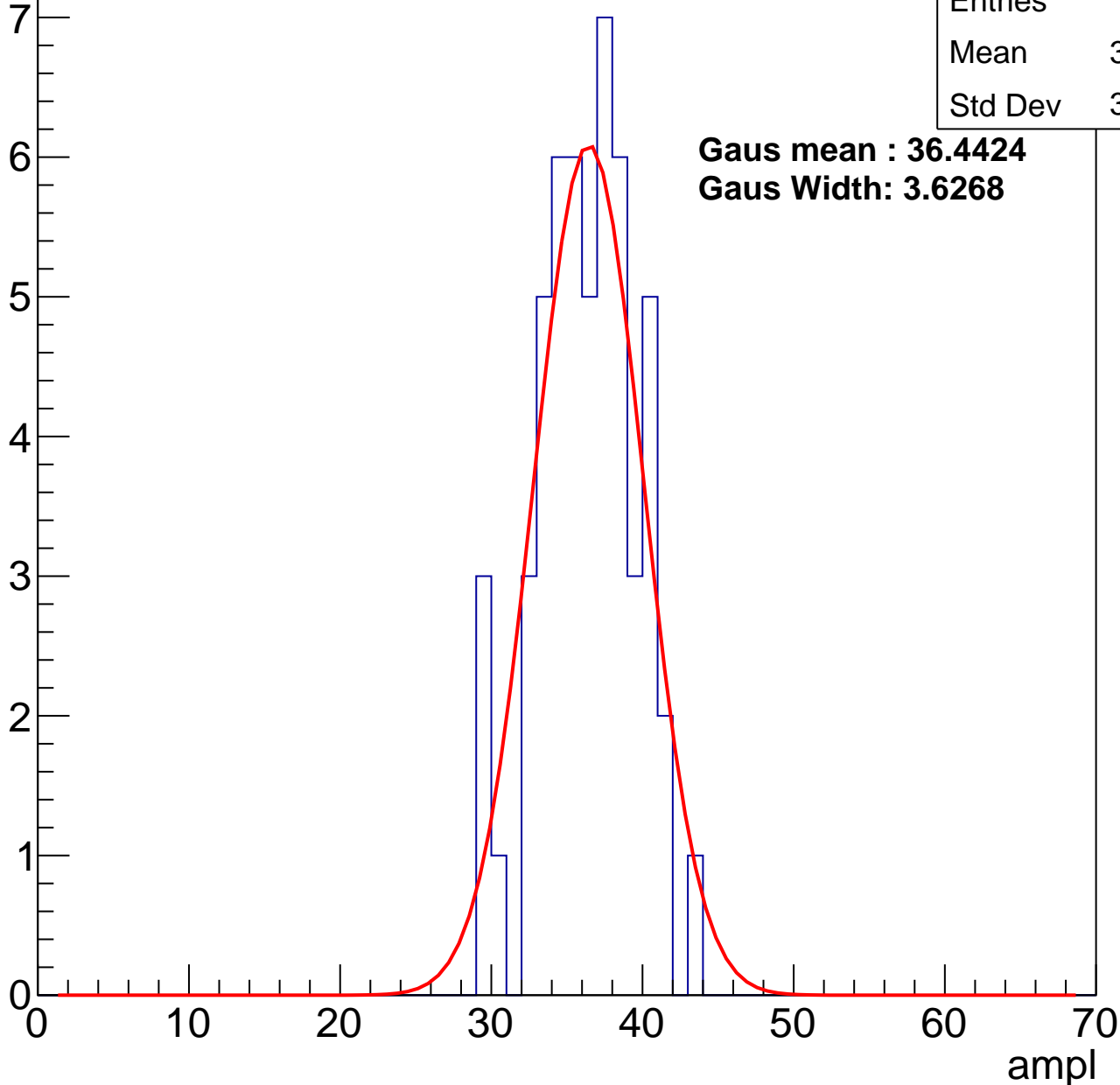
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	35.87
Std Dev	3.198

**Gaus mean : 36.4424**

**Gaus Width: 3.6268**



# B1L003S, U22-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	84
Mean	42.54
Std Dev	3.403

**Gaus mean : 43.1182**

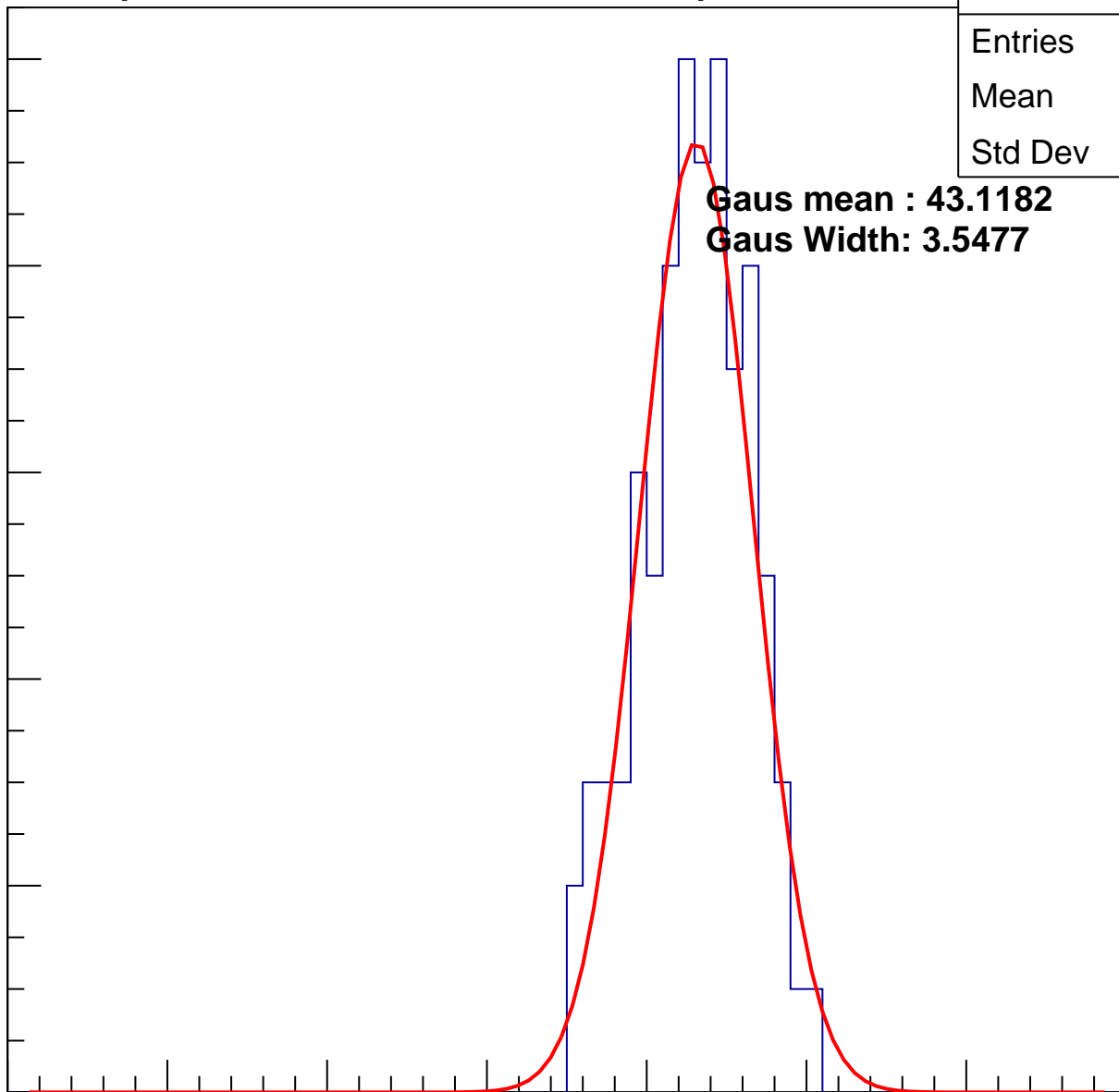
**Gaus Width: 3.5477**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

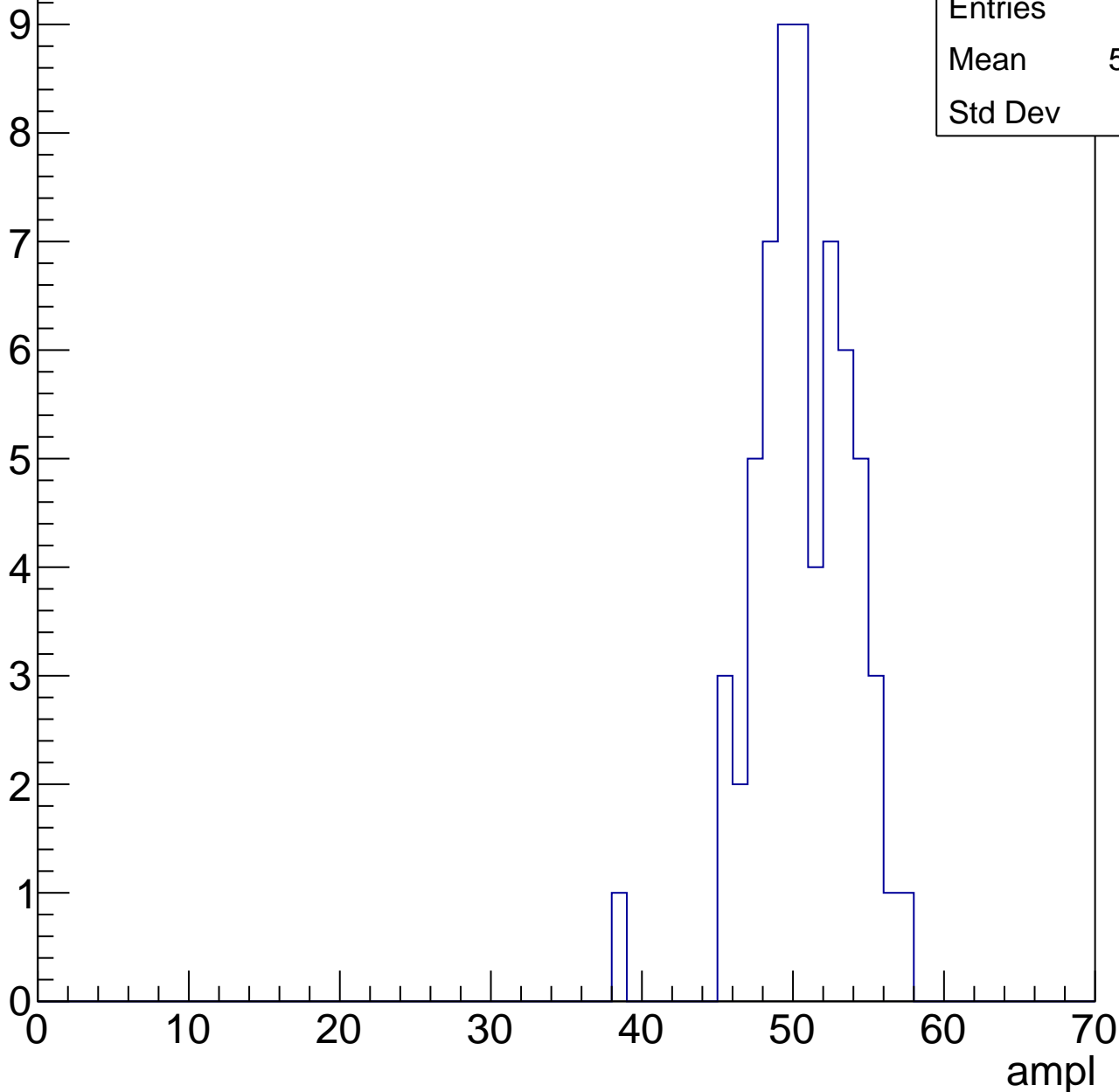


# B1L003S, U22-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	50.17
Std Dev	3.23

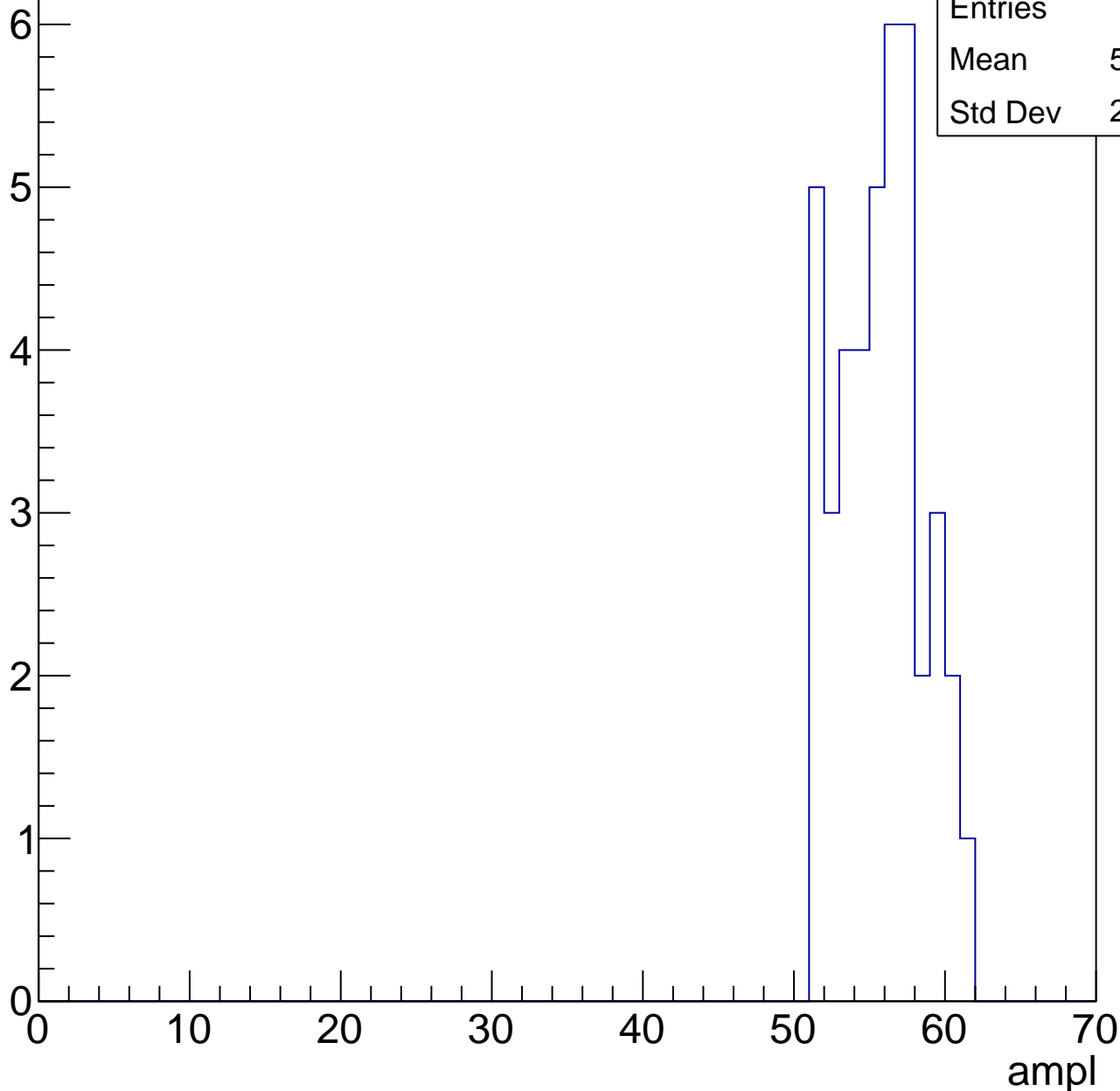


# B1L003S, U22-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	41
Mean	55.27
Std Dev	2.732

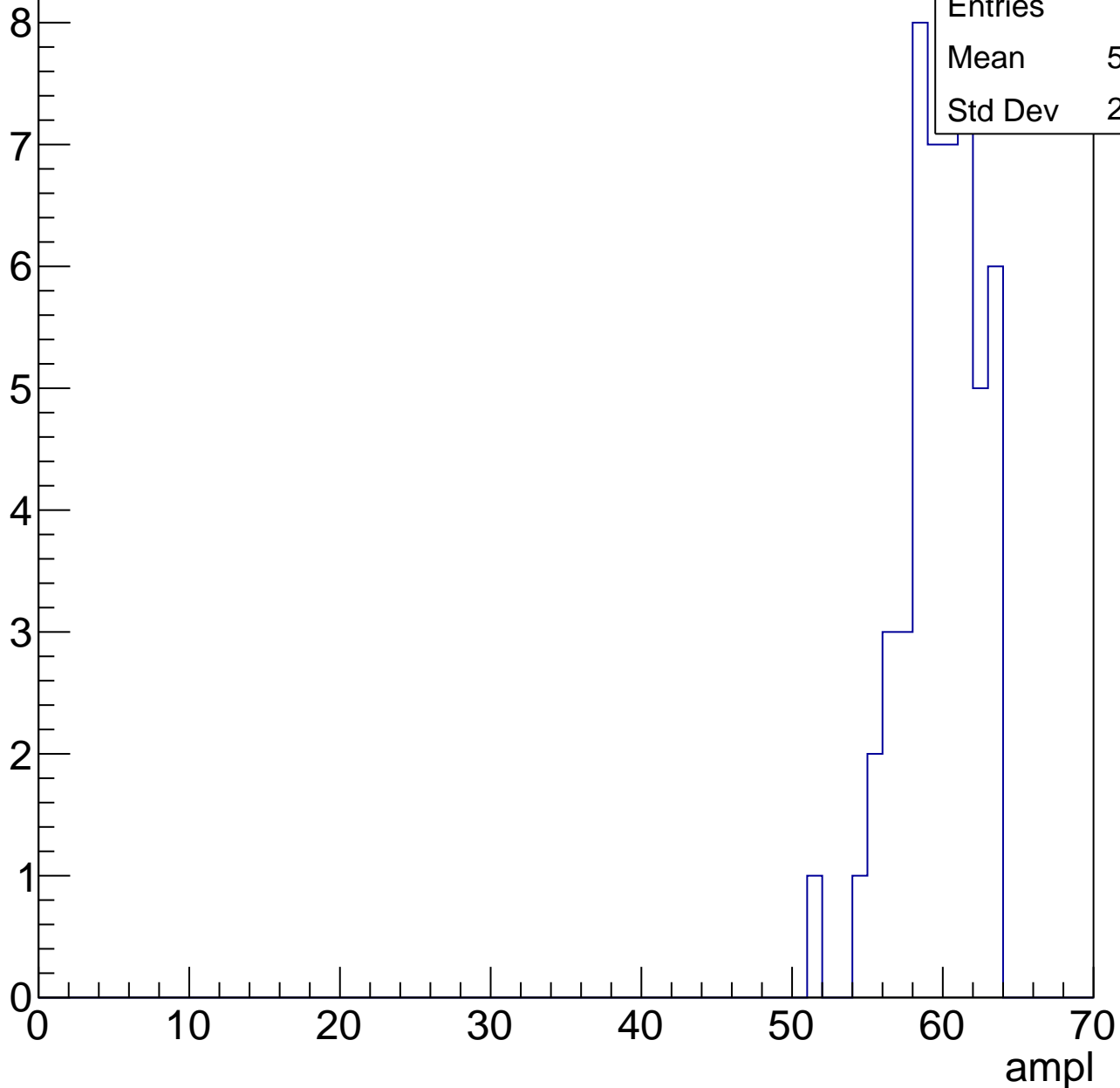


# B1L003S, U22-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.35
Std Dev	2.588

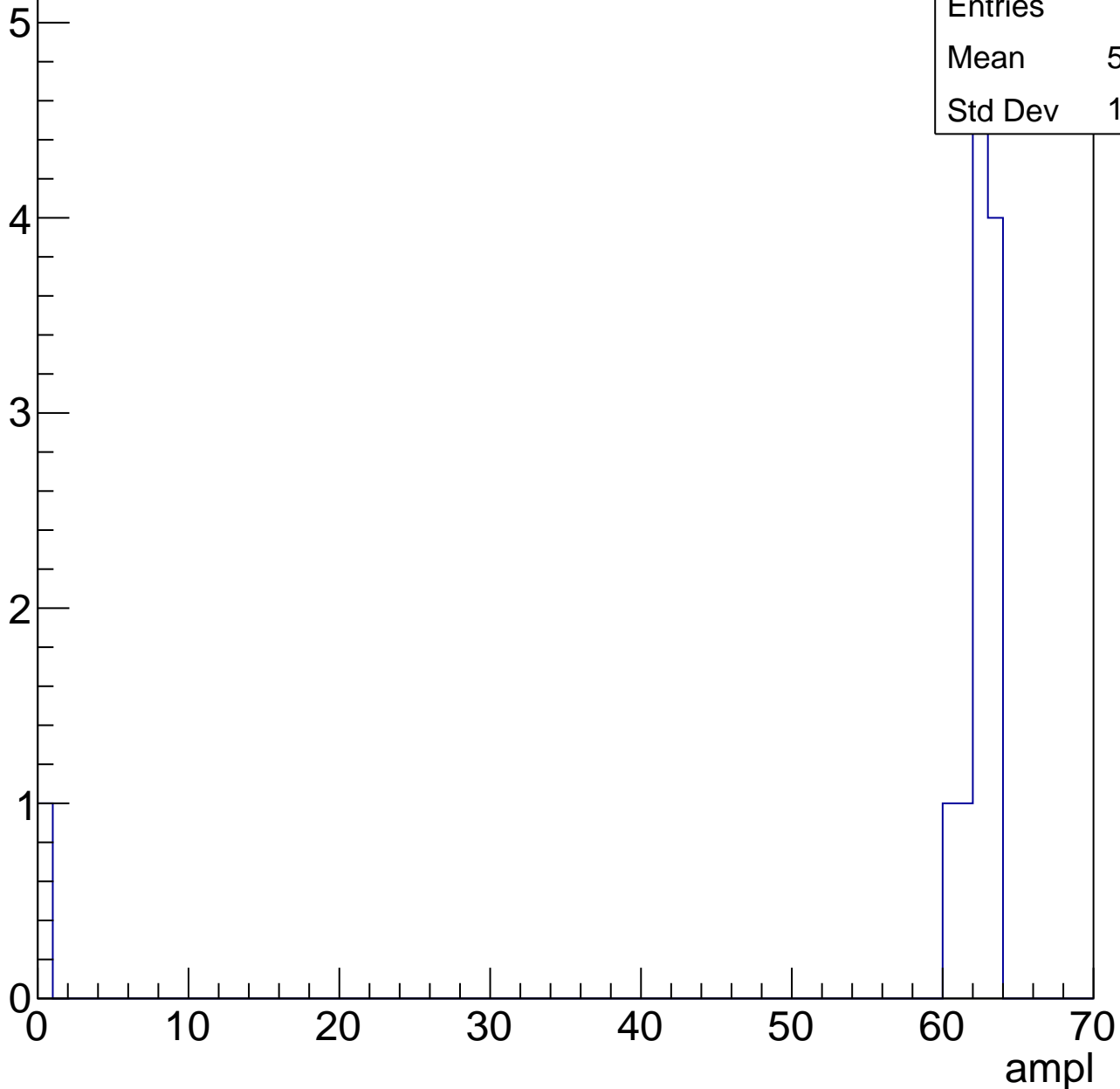


# B1L003S, U22-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	56.92
Std Dev	17.18





# B1L003S, U22-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch24, adc0

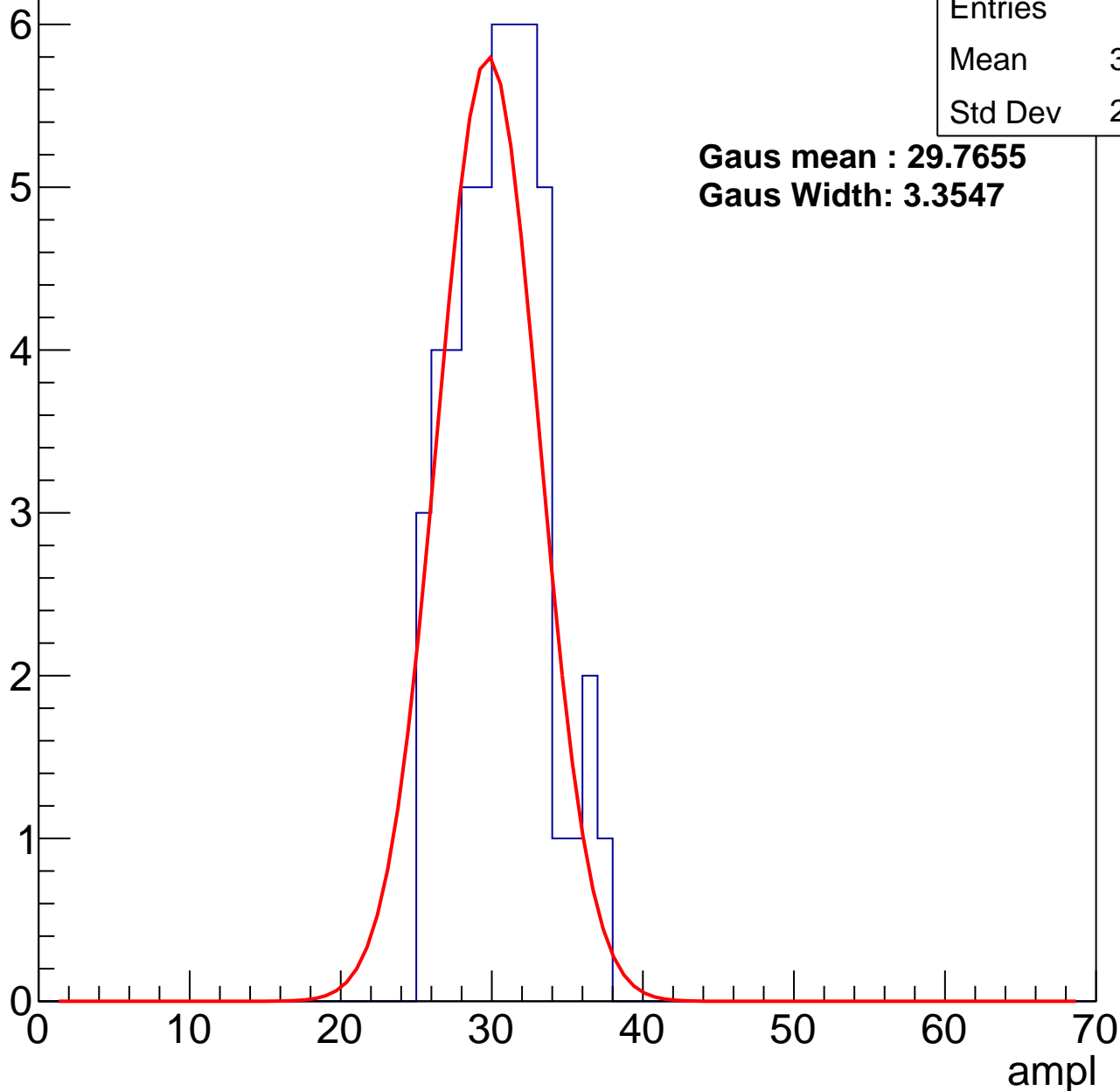
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	30.06
Std Dev	2.986

**Gaus mean : 29.7655**

**Gaus Width: 3.3547**



# B1L003S, U22-ch24, adc1

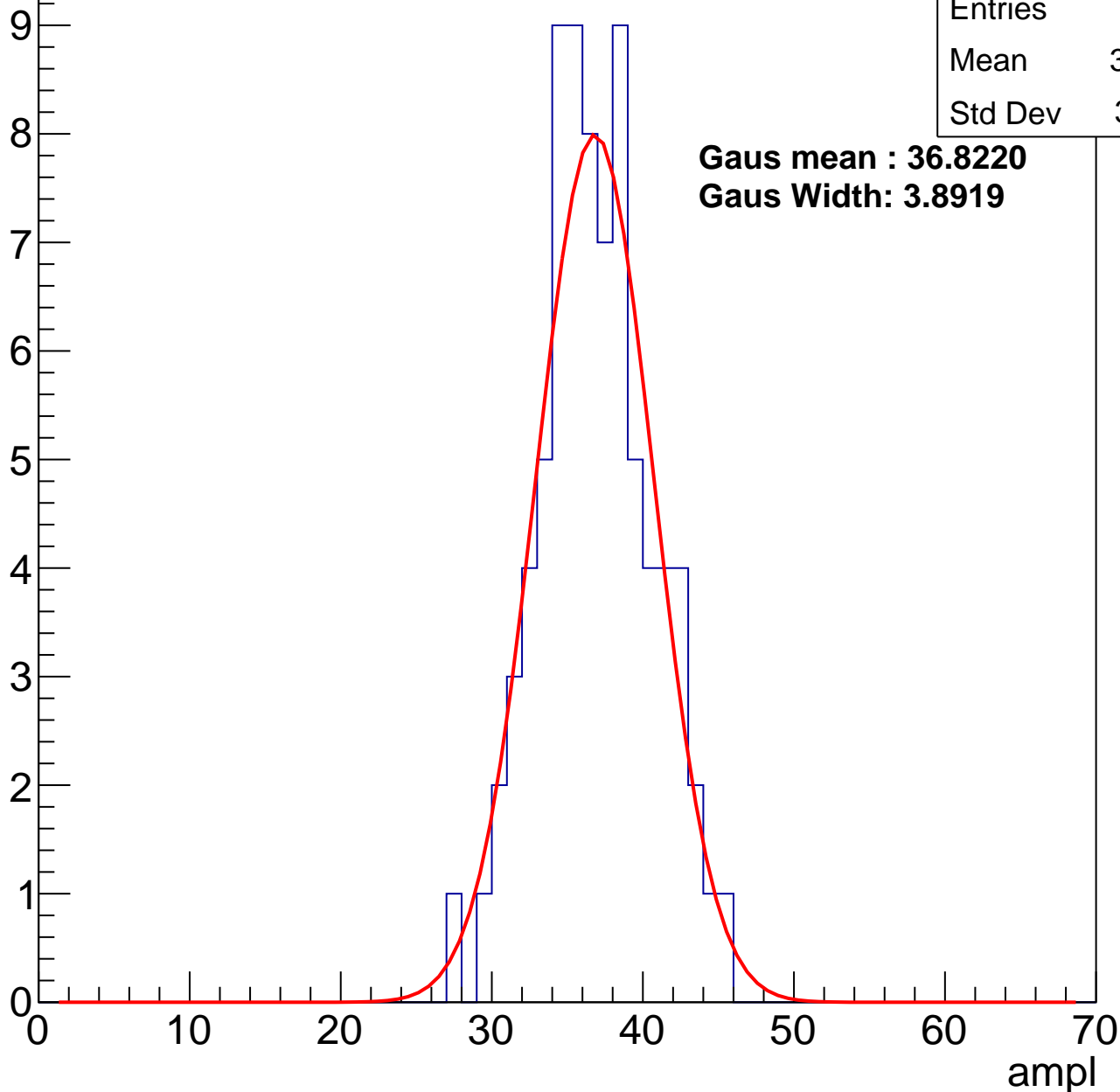
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	36.38
Std Dev	3.661

**Gaus mean : 36.8220**

**Gaus Width: 3.8919**



# B1L003S, U22-ch24, adc2

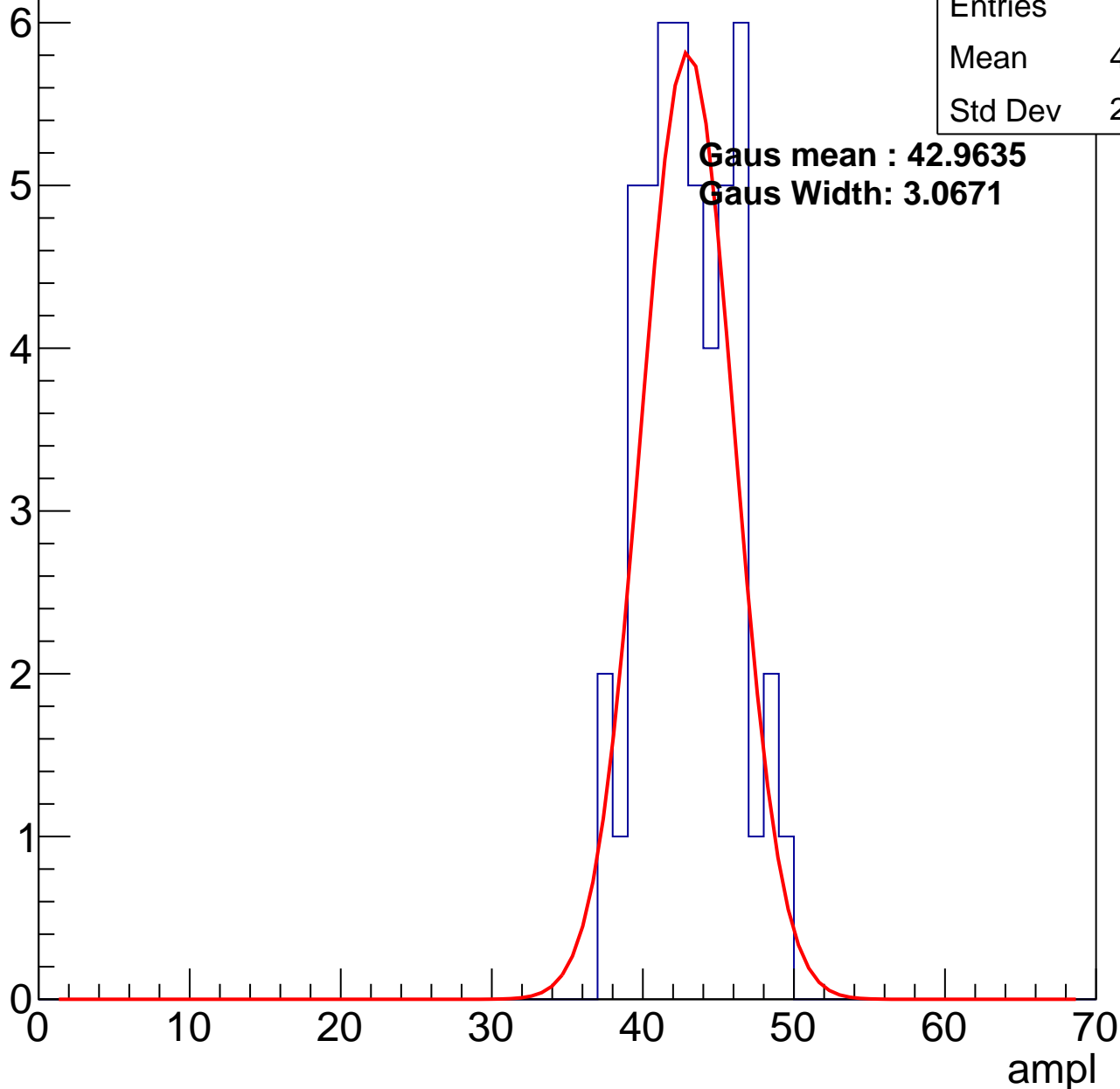
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.63
Std Dev	2.946

**Gaus mean : 42.9635**

**Gaus Width: 3.0671**



# B1L003S, U22-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	74
Mean	48.8
Std Dev	3.154

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

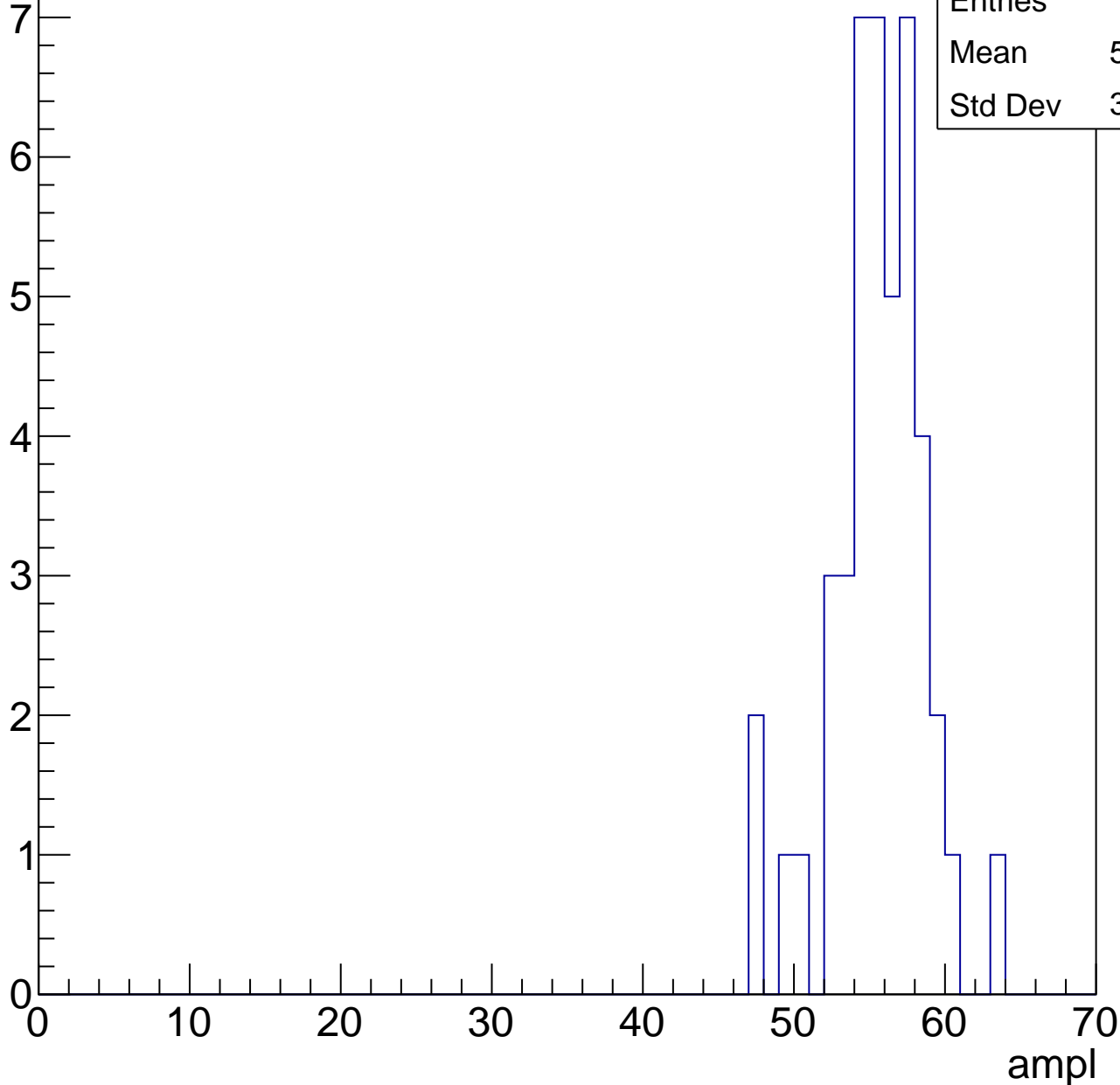
10

# B1L003S, U22-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	55.07
Std Dev	3.107

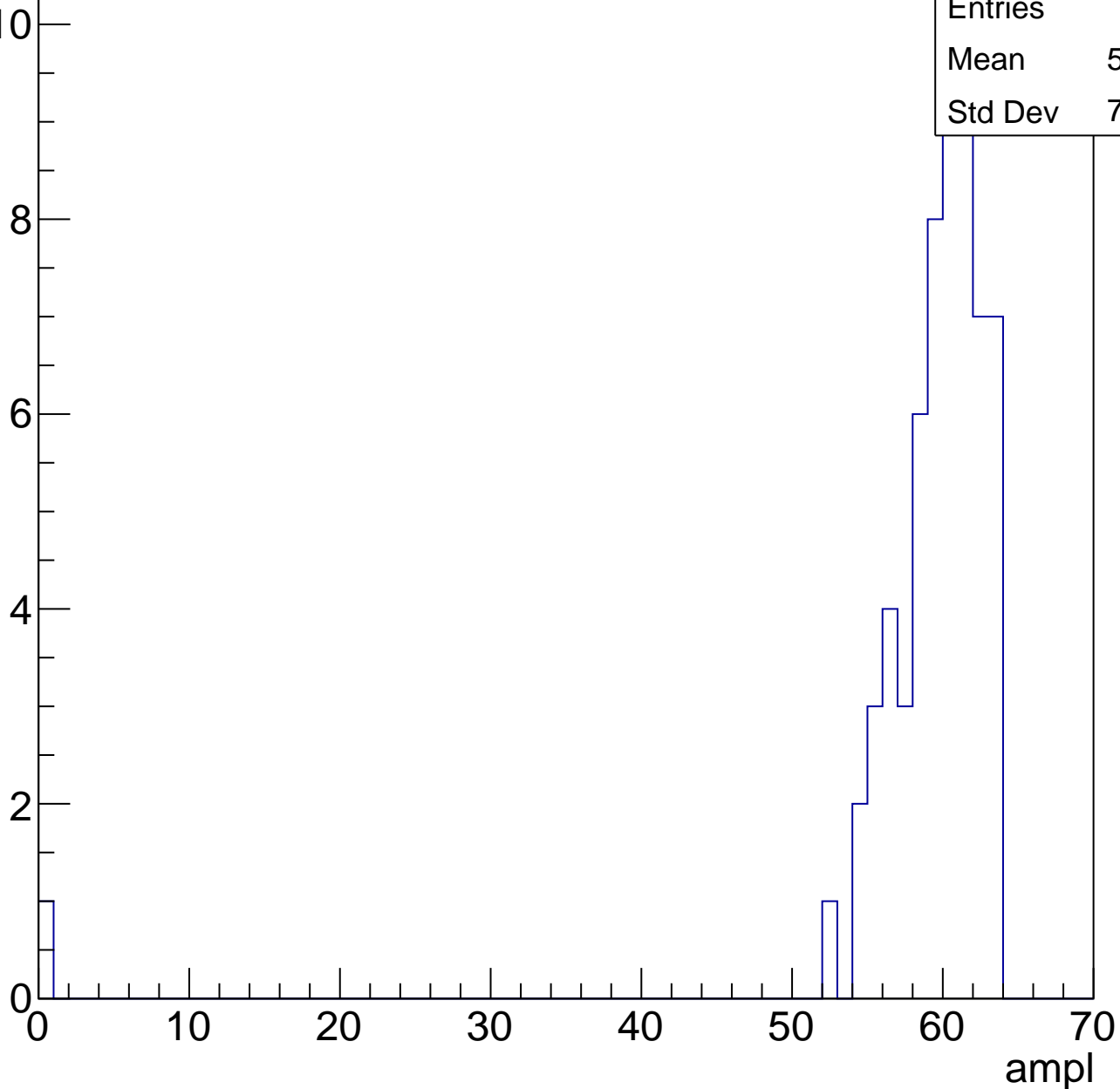


# B1L003S, U22-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	58.44
Std Dev	7.983



# B1L003S, U22-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

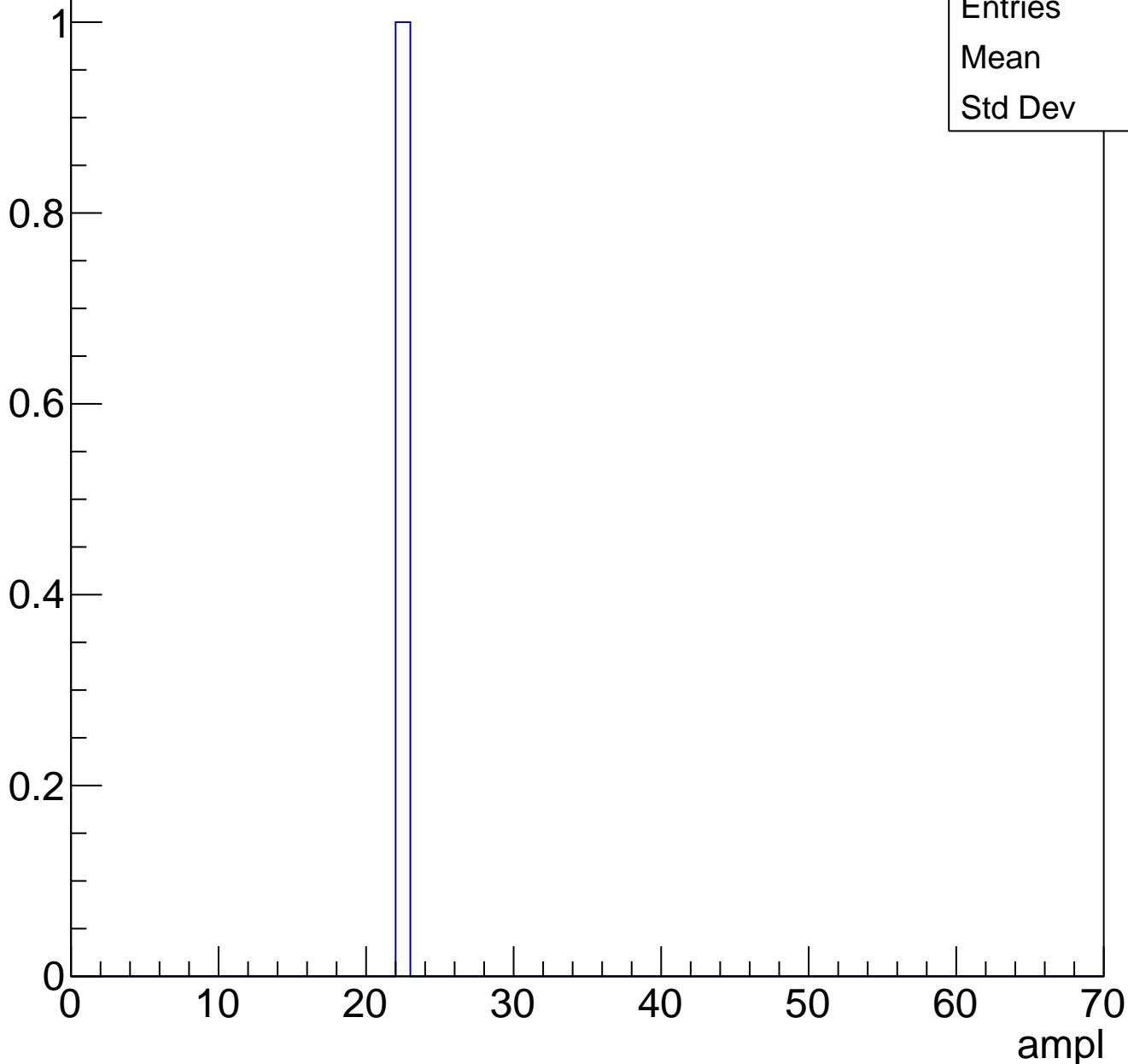




# B1L003S, U22-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch25, adc0

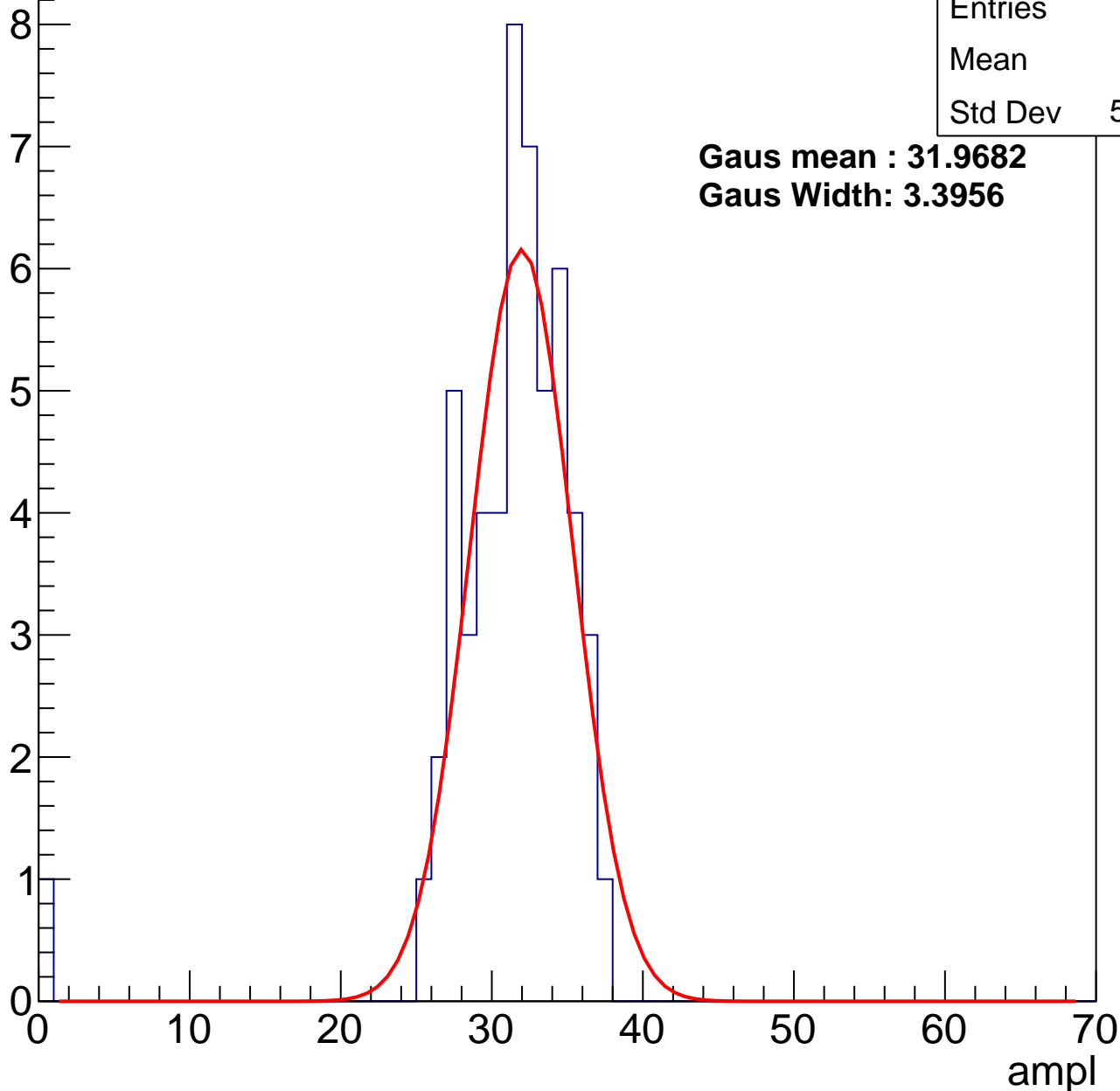
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	30.7
Std Dev	5.137

**Gaus mean : 31.9682**

**Gaus Width: 3.3956**



# B1L003S, U22-ch25, adc1

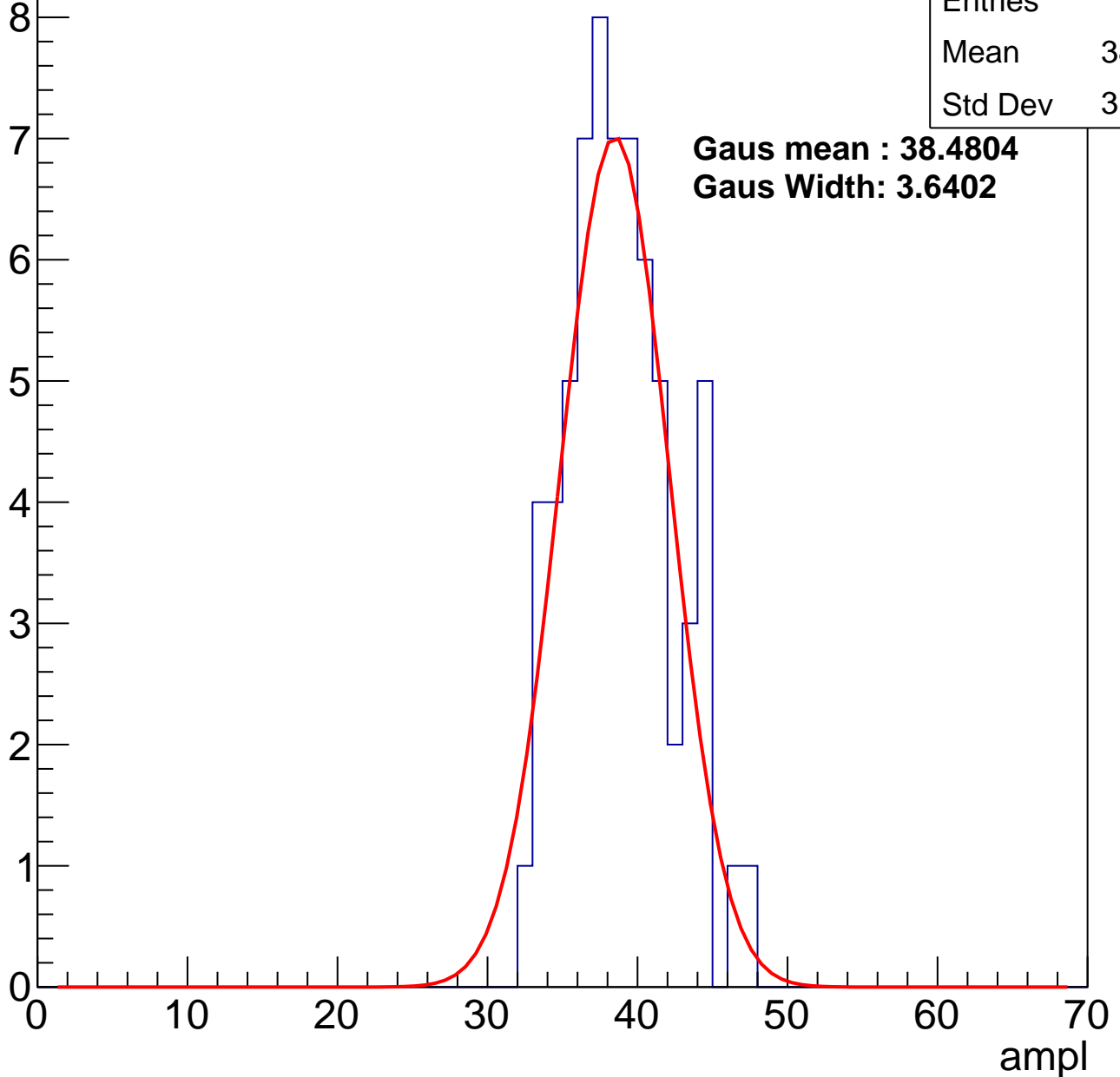
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	38.38
Std Dev	3.437

**Gaus mean : 38.4804**

**Gaus Width: 3.6402**



# B1L003S, U22-ch25, adc2

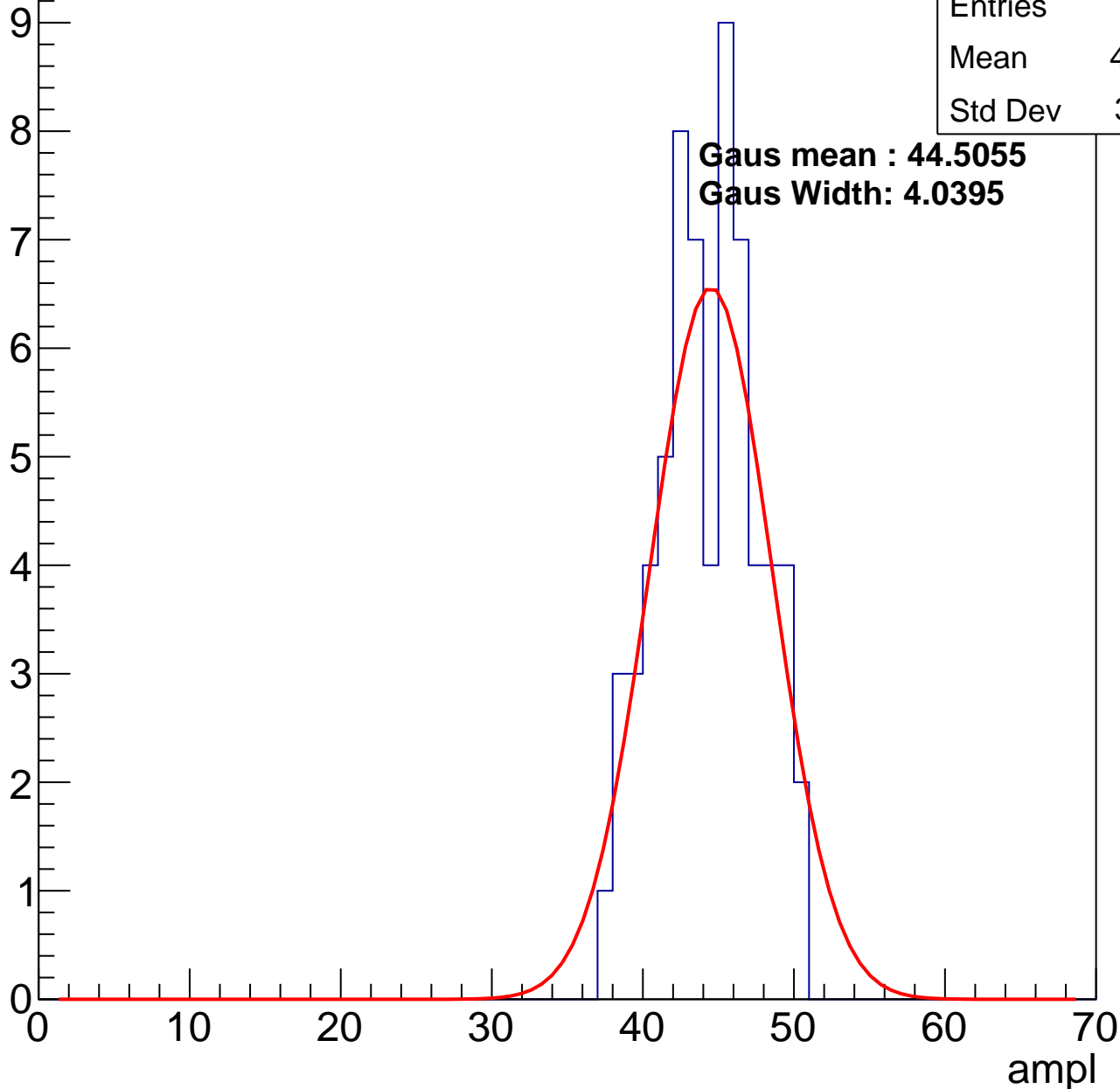
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.83
Std Dev	3.251

**Gaus mean : 44.5055**

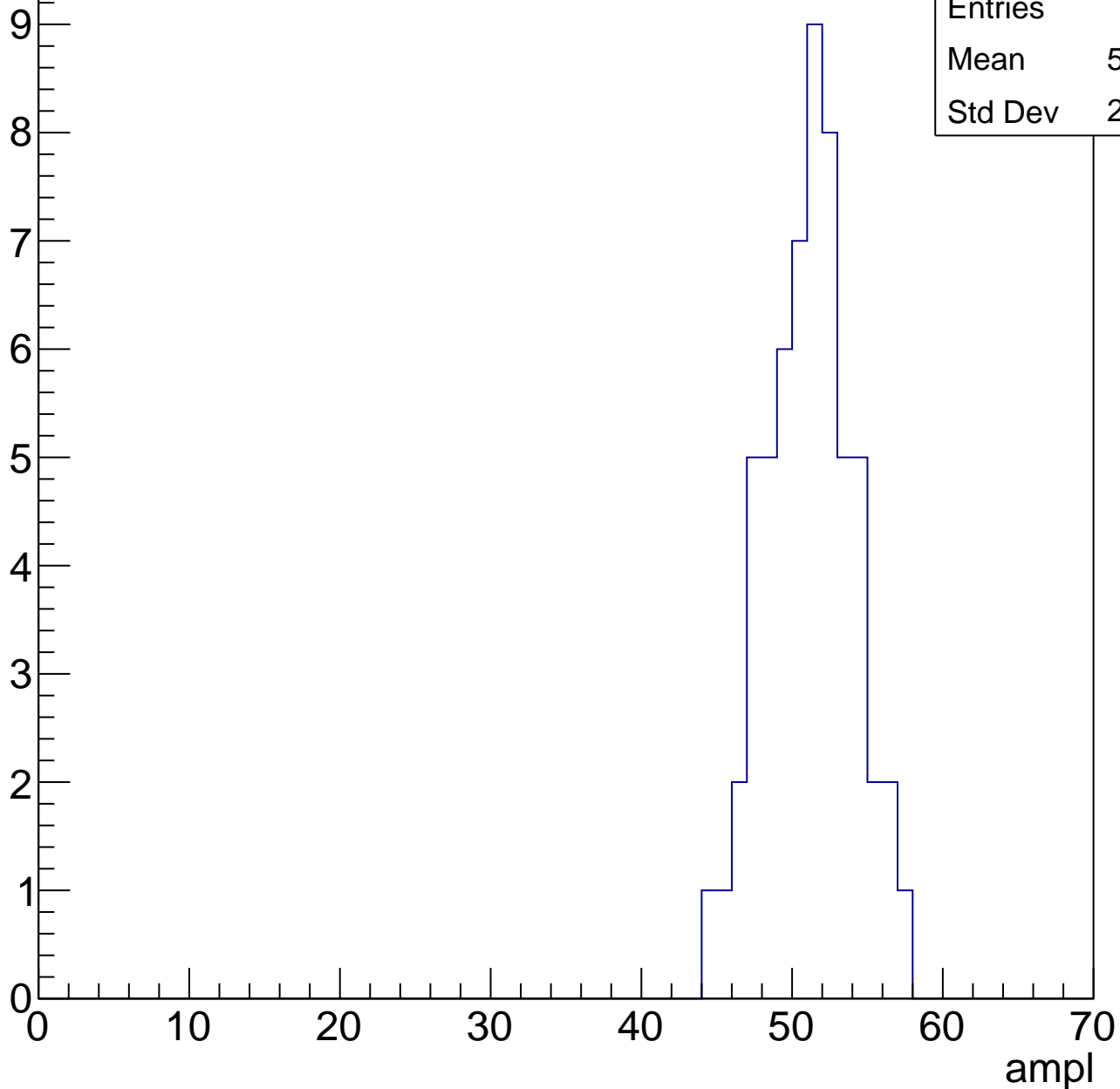
**Gaus Width: 4.0395**



# B1L003S, U22-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



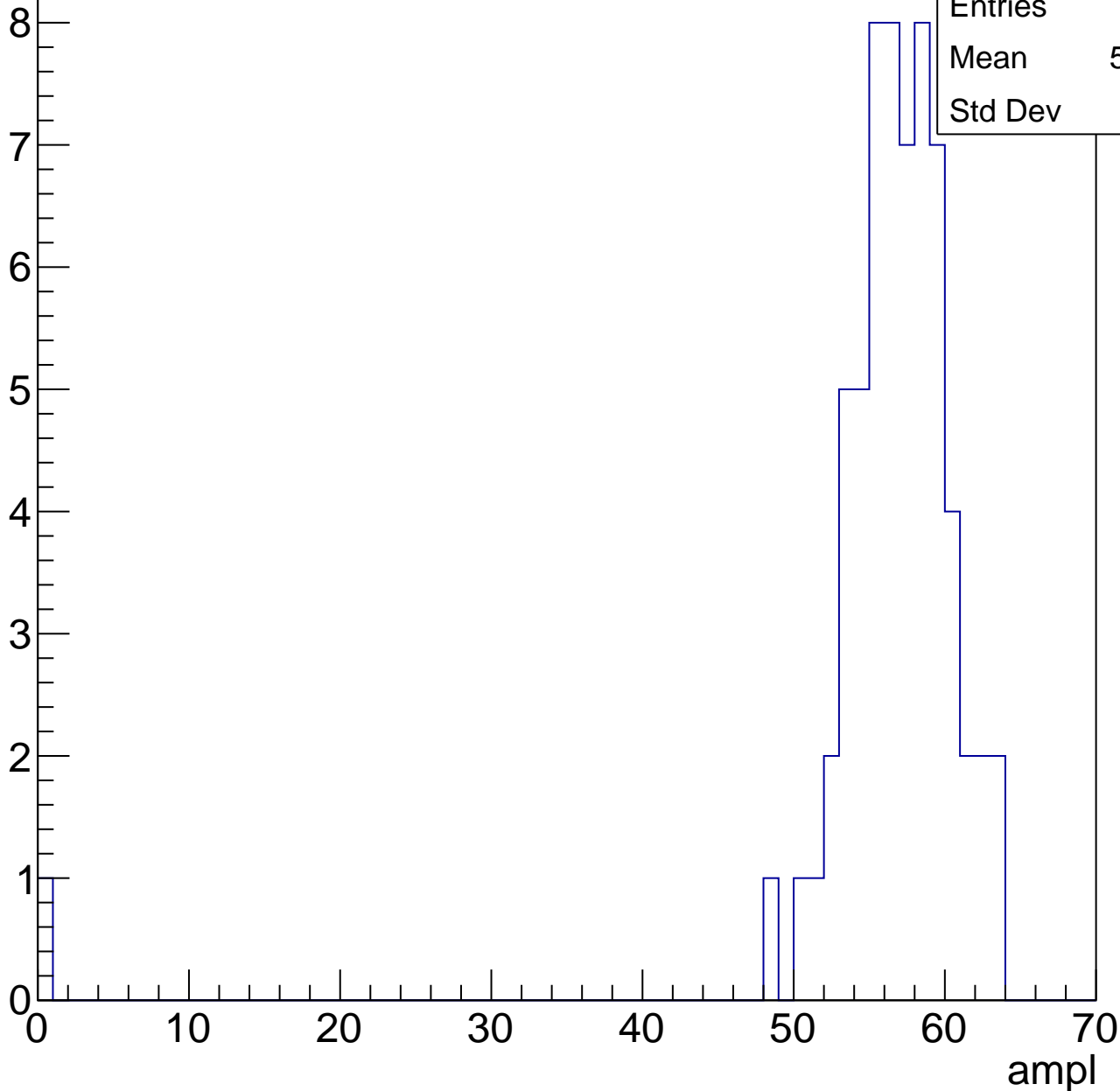
Entries	59
Mean	50.66
Std Dev	2.838

# B1L003S, U22-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	55.69
Std Dev	7.65

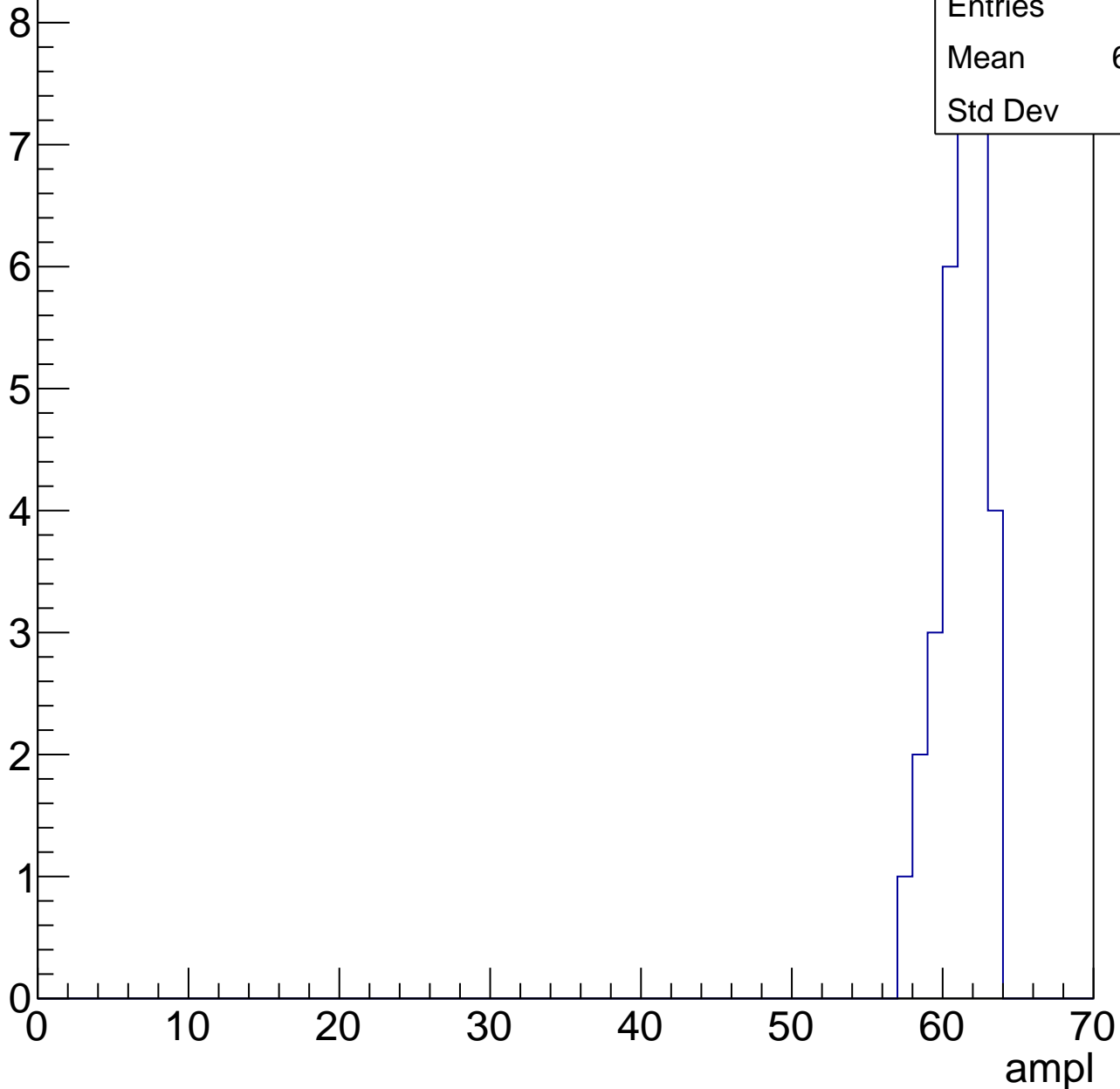


# B1L003S, U22-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

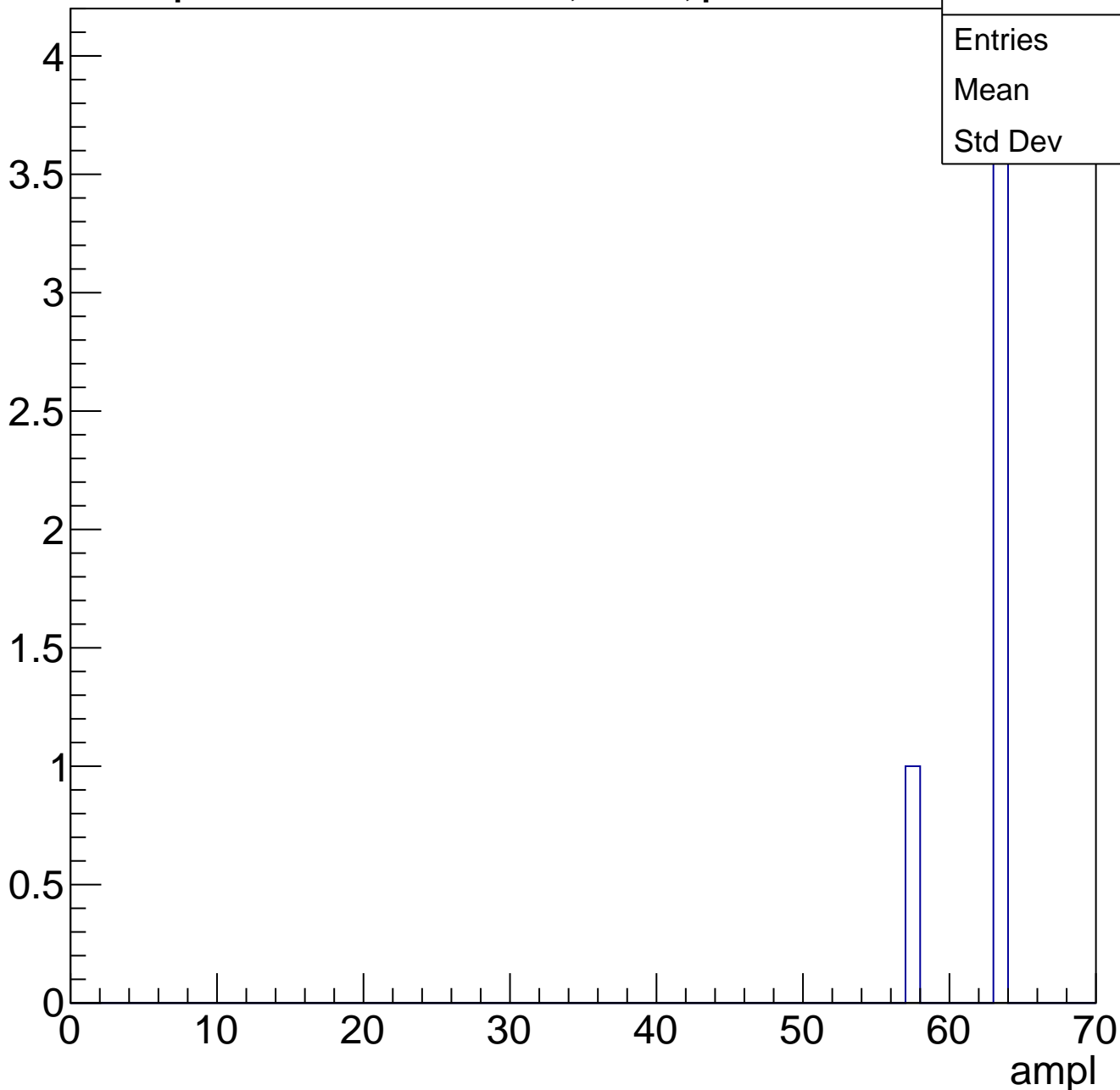
Entries	32
Mean	60.81
Std Dev	1.53



# B1L003S, U22-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch26, adc0

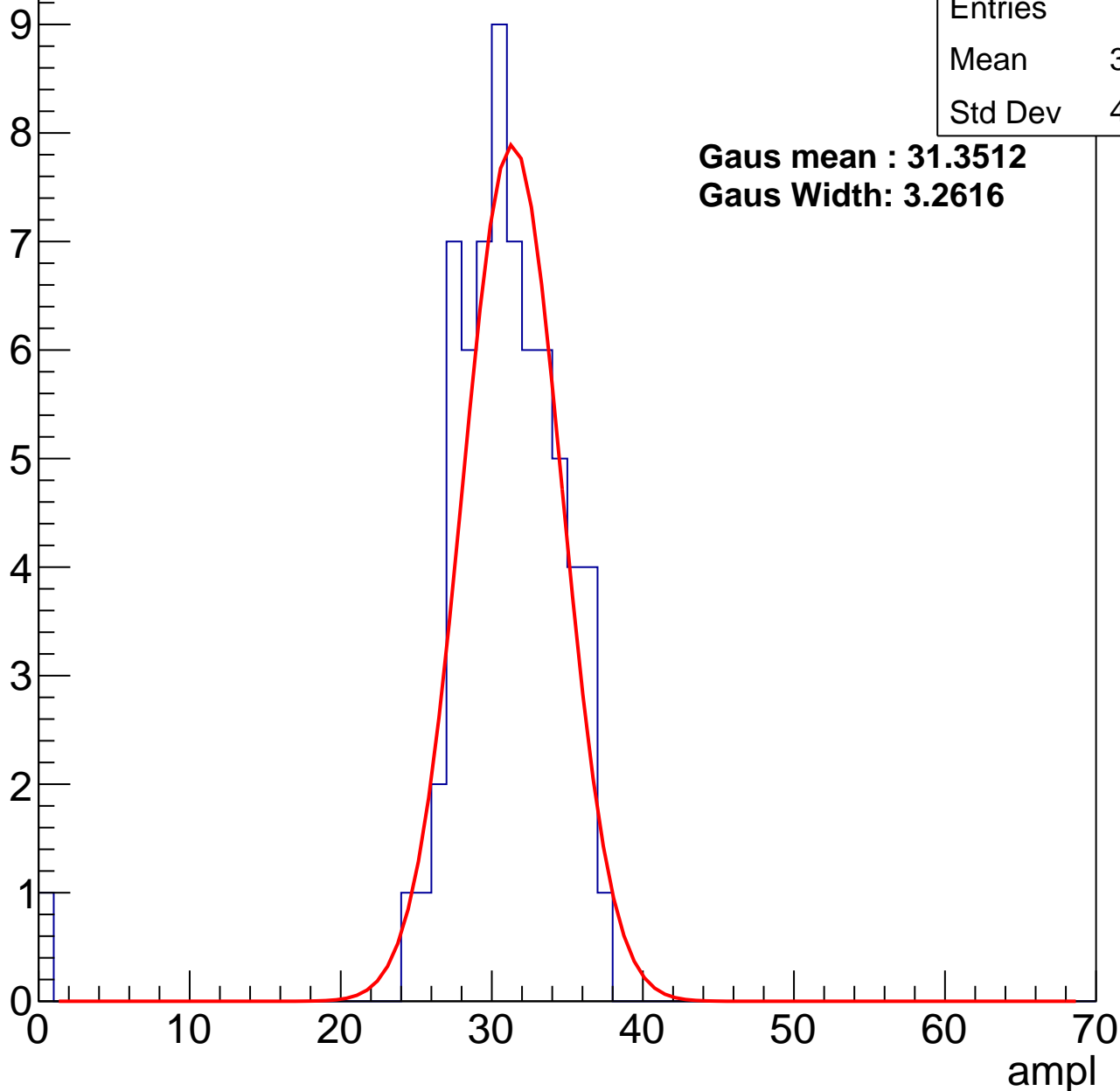
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	30.28
Std Dev	4.794

**Gaus mean : 31.3512**

**Gaus Width: 3.2616**



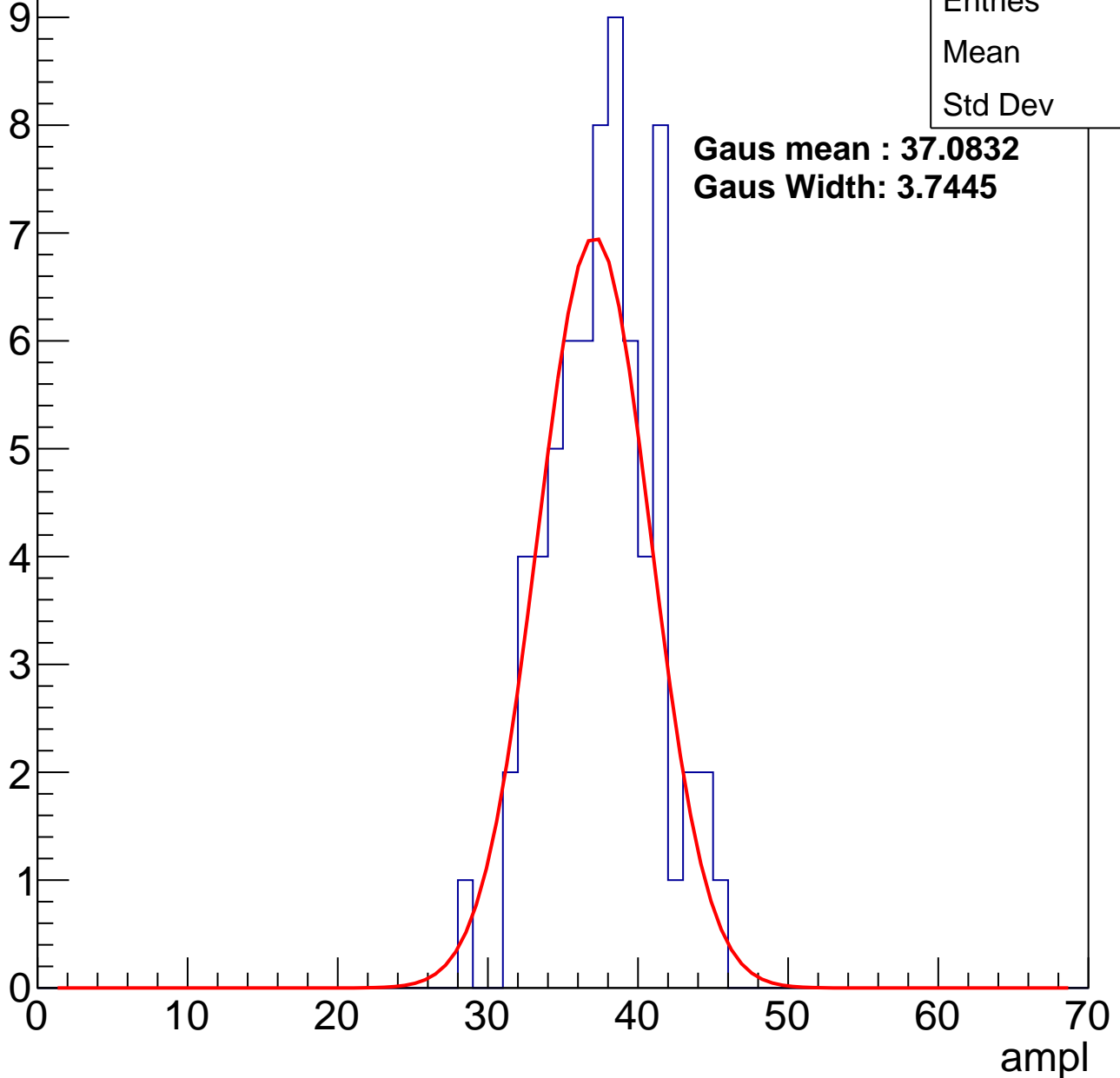
# B1L003S, U22-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	37.2
Std Dev	3.5

**Gaus mean : 37.0832**  
**Gaus Width: 3.7445**



# B1L003S, U22-ch26, adc2

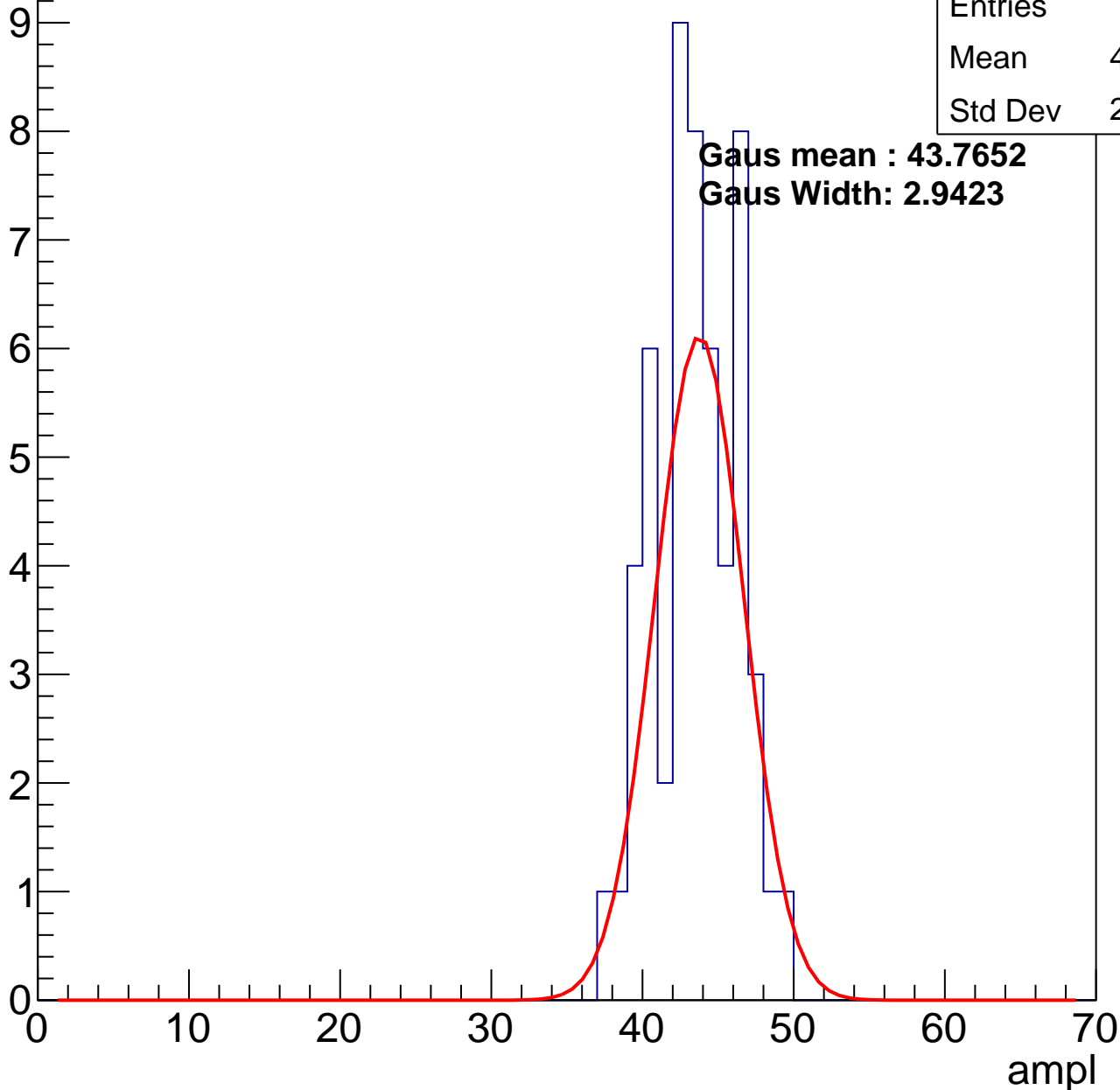
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.06
Std Dev	2.718

**Gaus mean : 43.7652**

**Gaus Width: 2.9423**

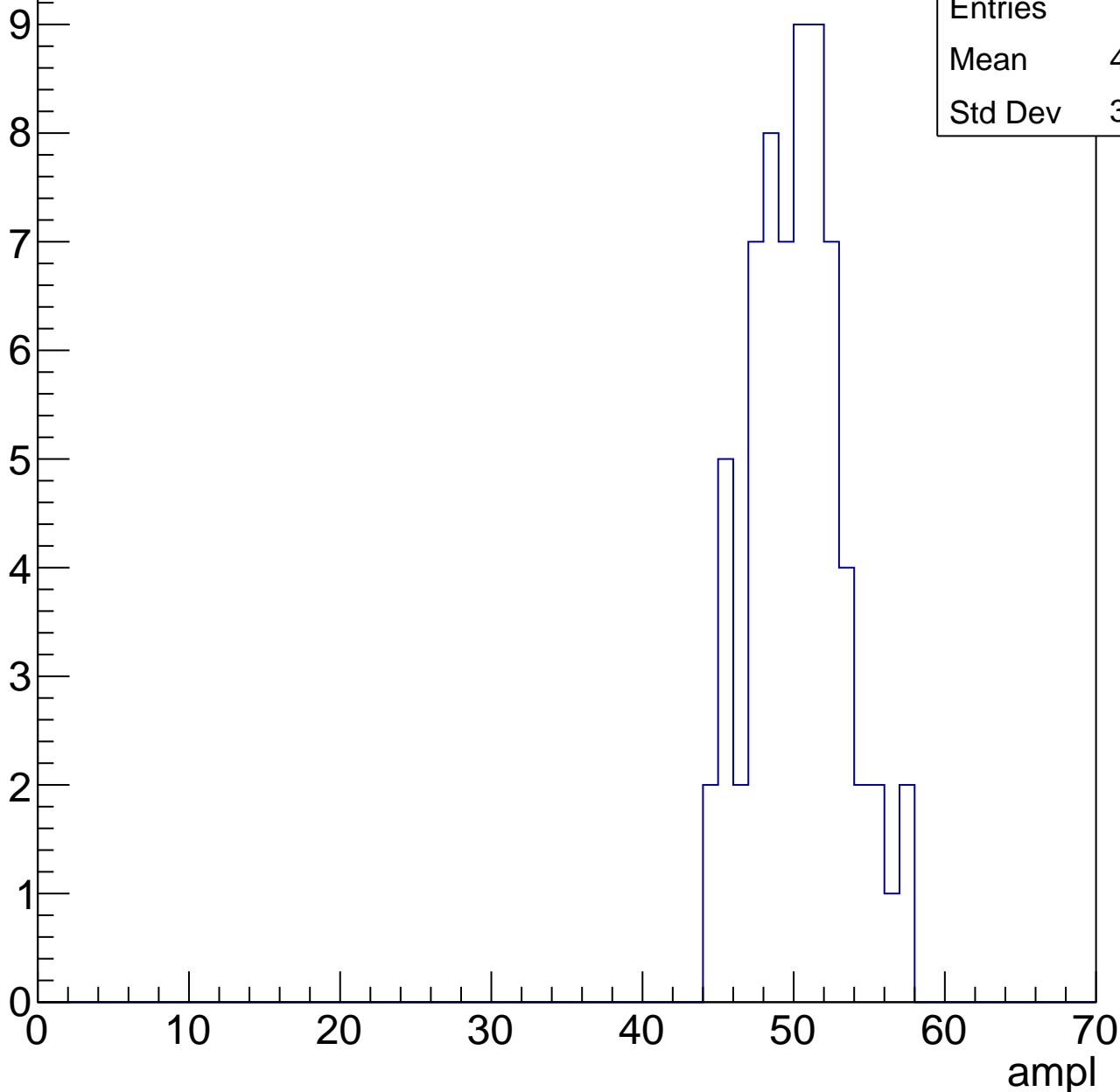


# B1L003S, U22-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	49.76
Std Dev	3.033

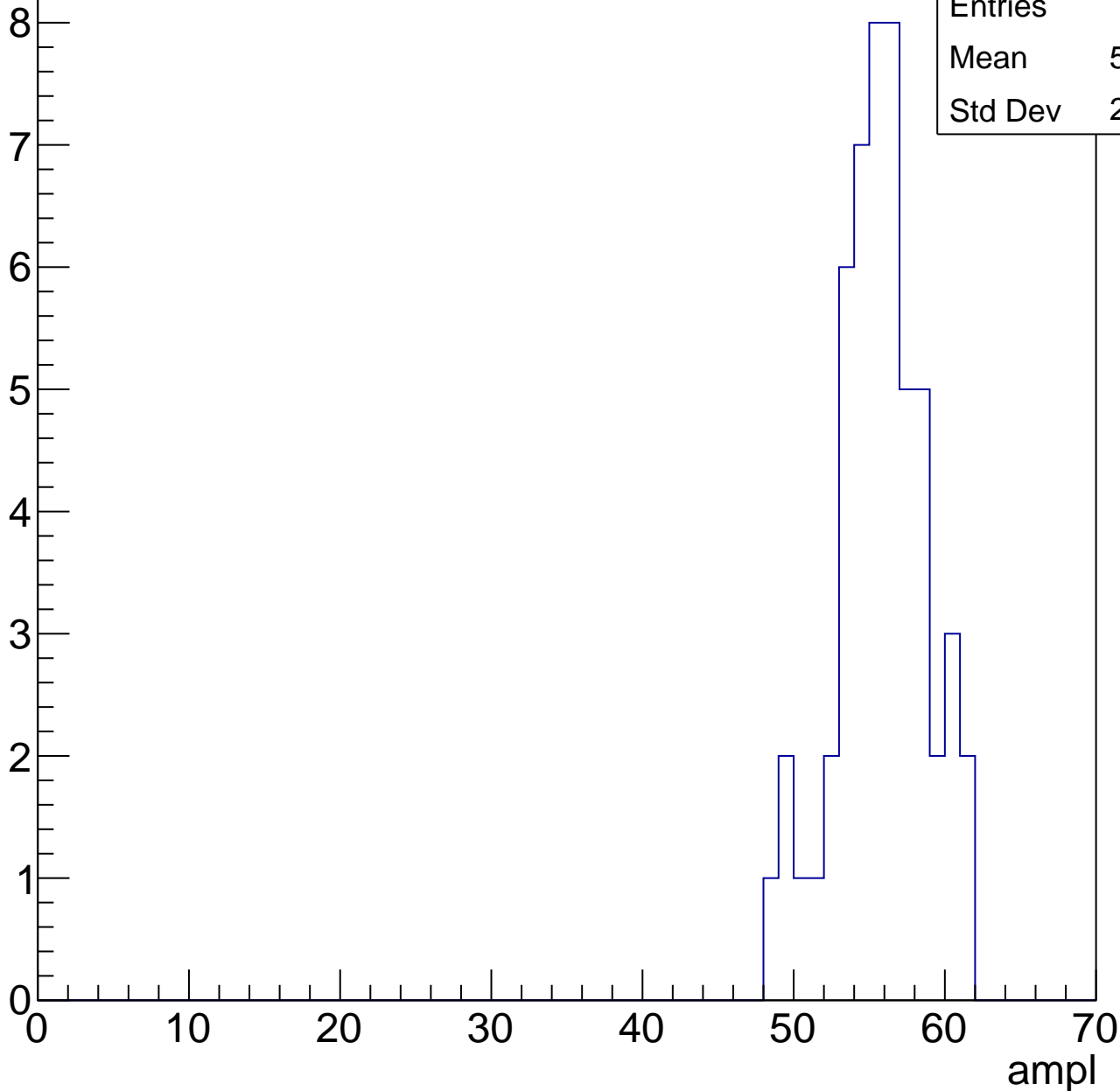


# B1L003S, U22-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.28
Std Dev	2.942

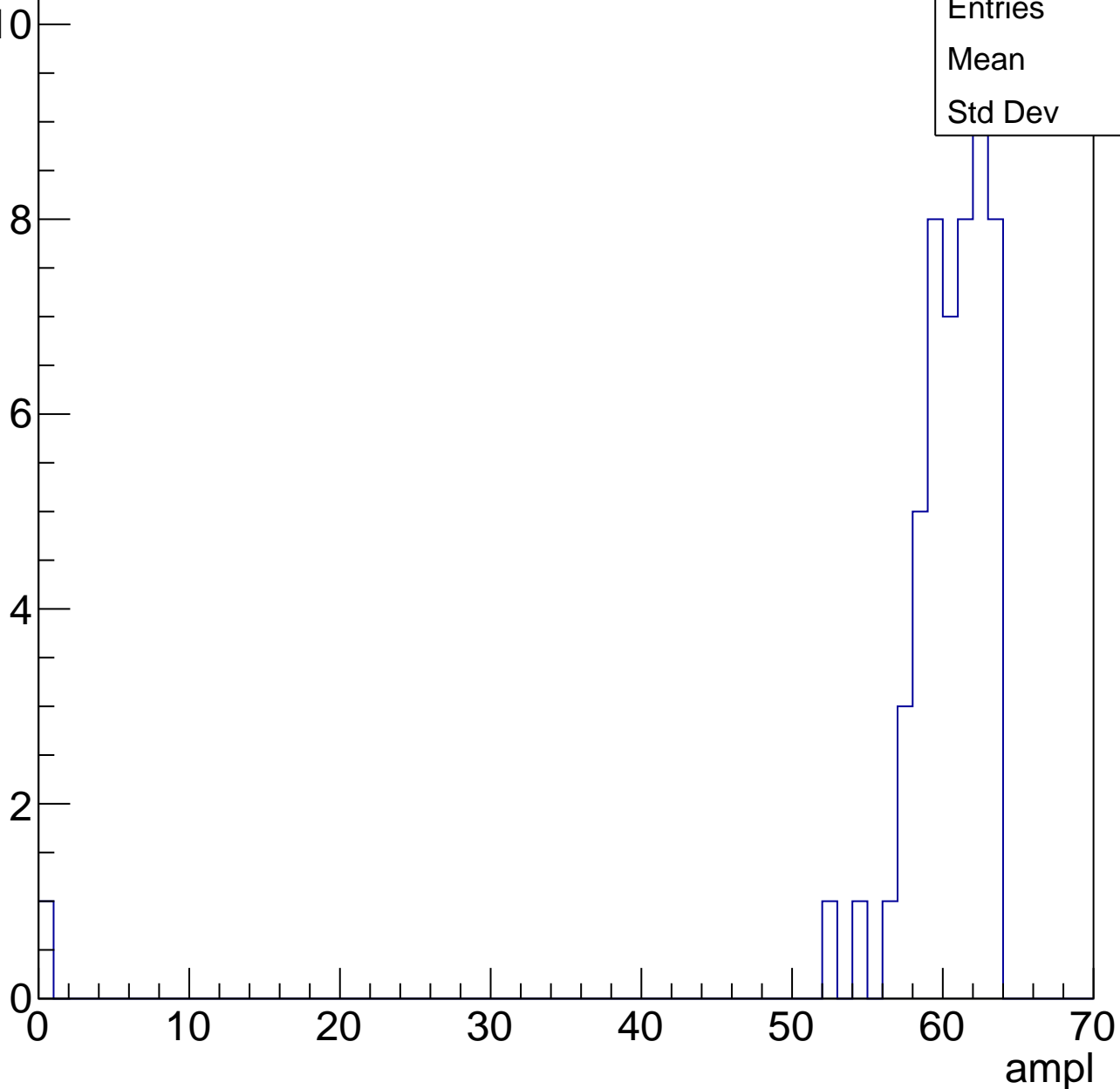


# B1L003S, U22-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	59
Std Dev	8.51



# B1L003S, U22-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U22-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch27, adc0

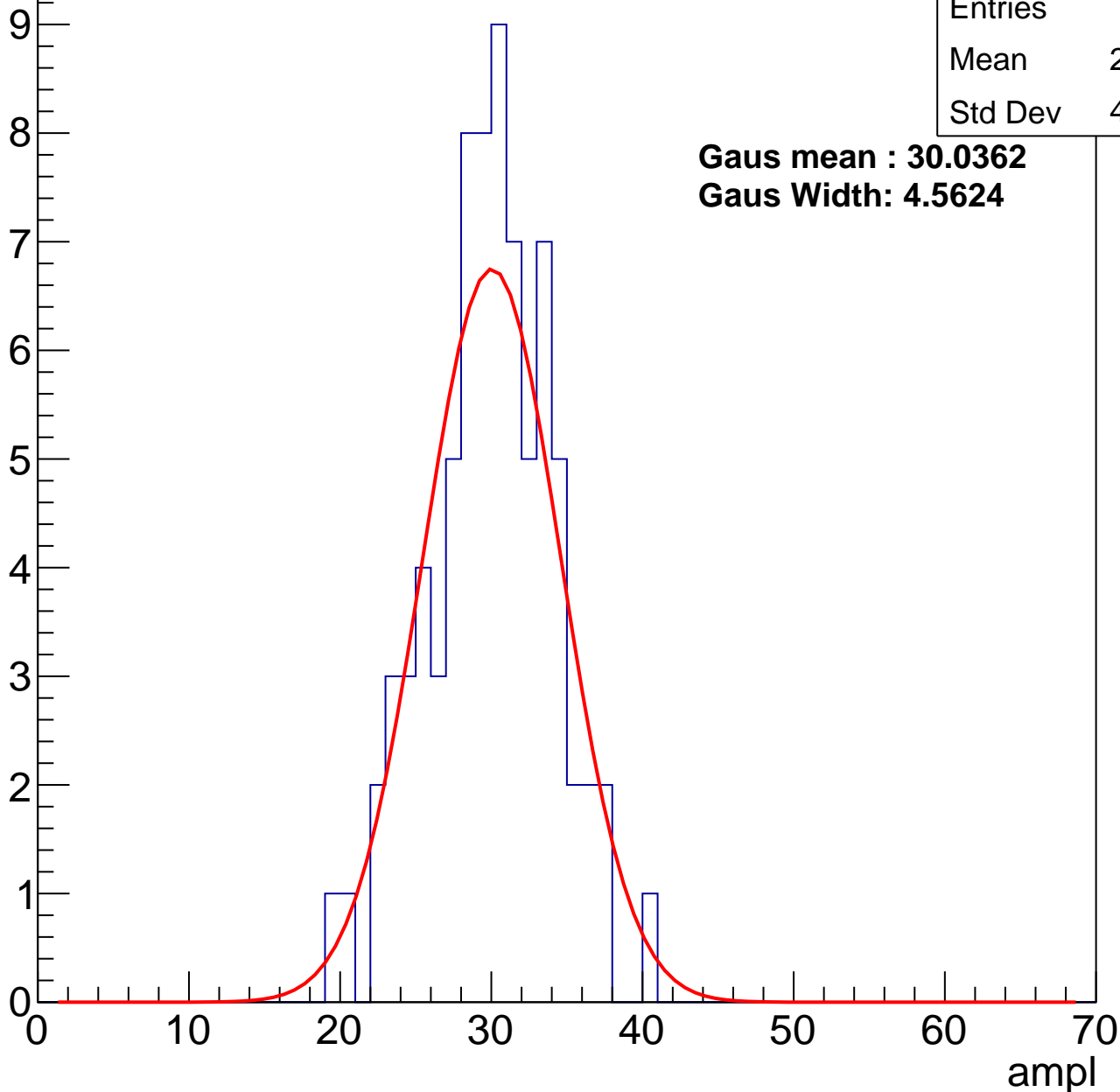
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	29.45
Std Dev	4.088

**Gaus mean : 30.0362**

**Gaus Width: 4.5624**



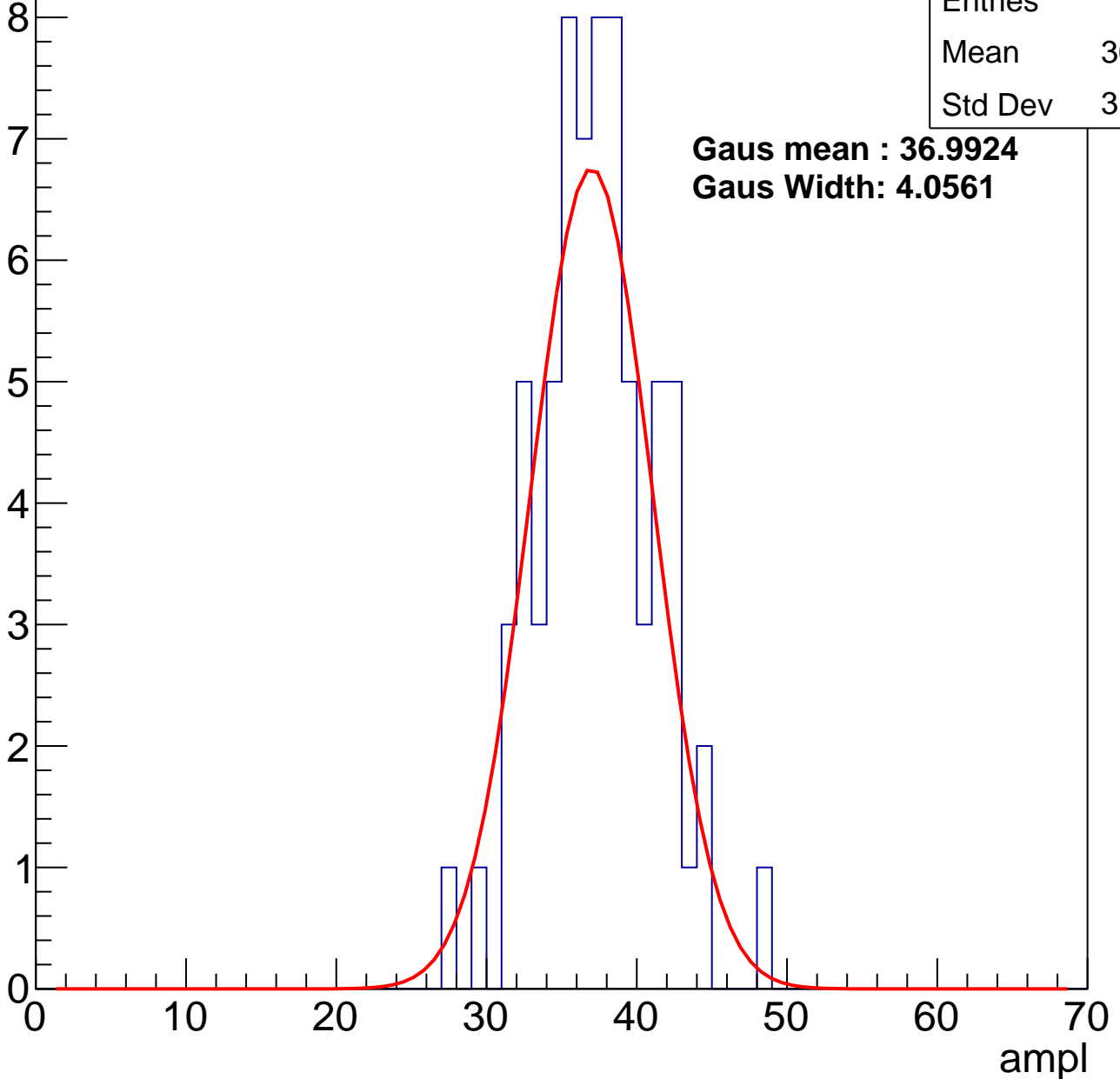
# B1L003S, U22-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.89
Std Dev	3.833

**Gaus mean : 36.9924**  
**Gaus Width: 4.0561**

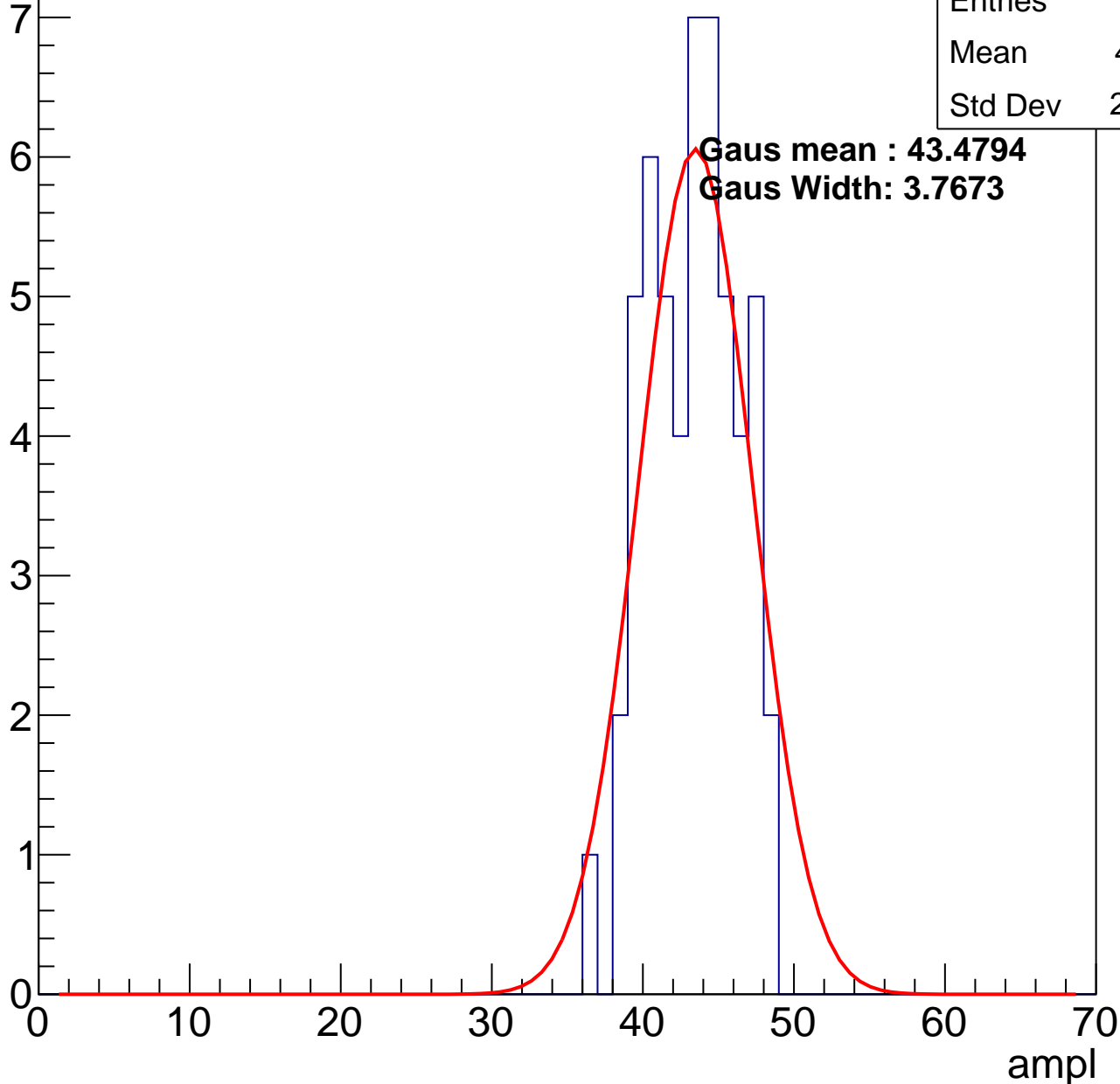


# B1L003S, U22-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.81
Std Dev	2.908



# B1L003S, U22-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

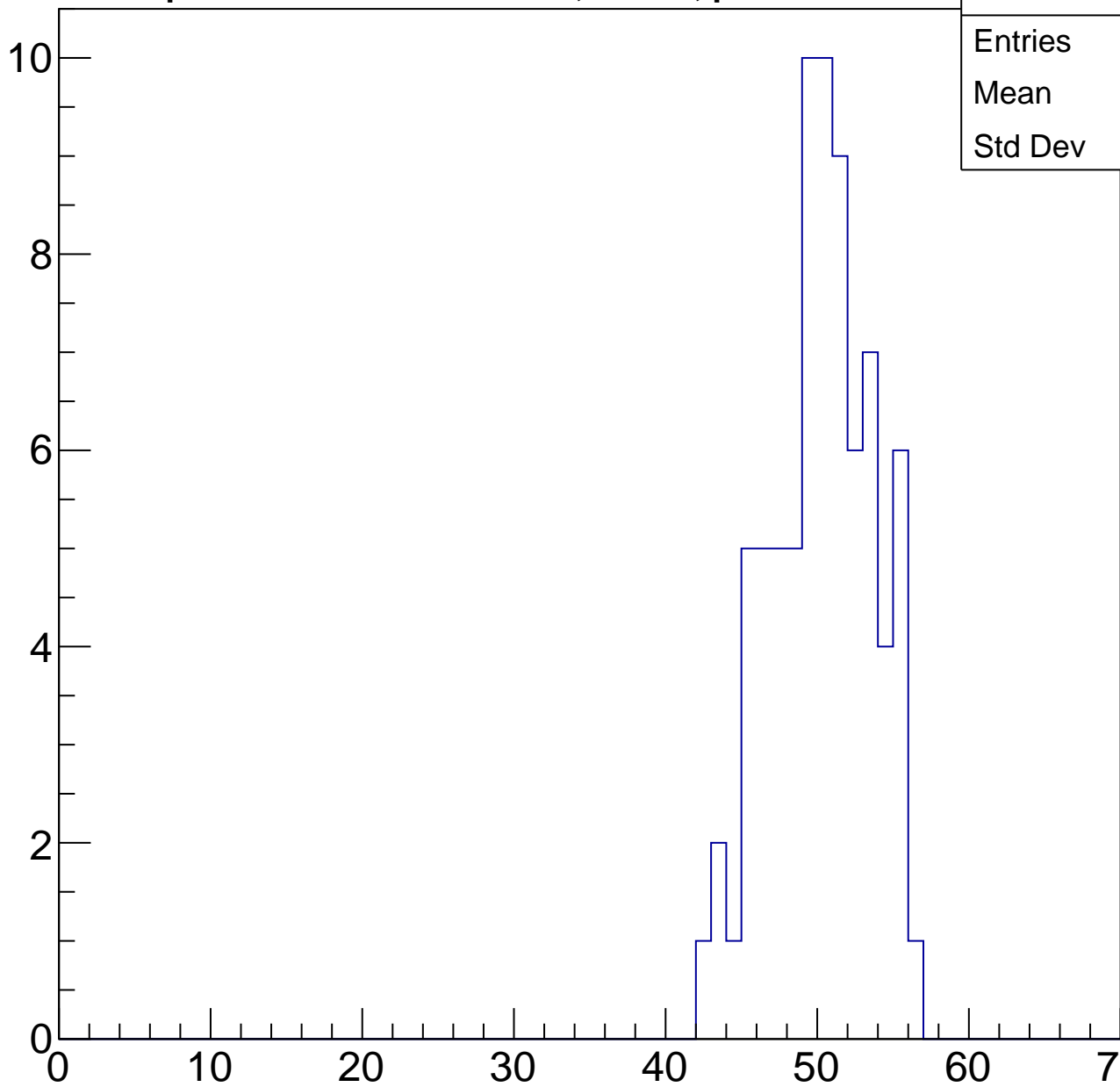
Entries	77
Mean	49.82
Std Dev	3.266

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

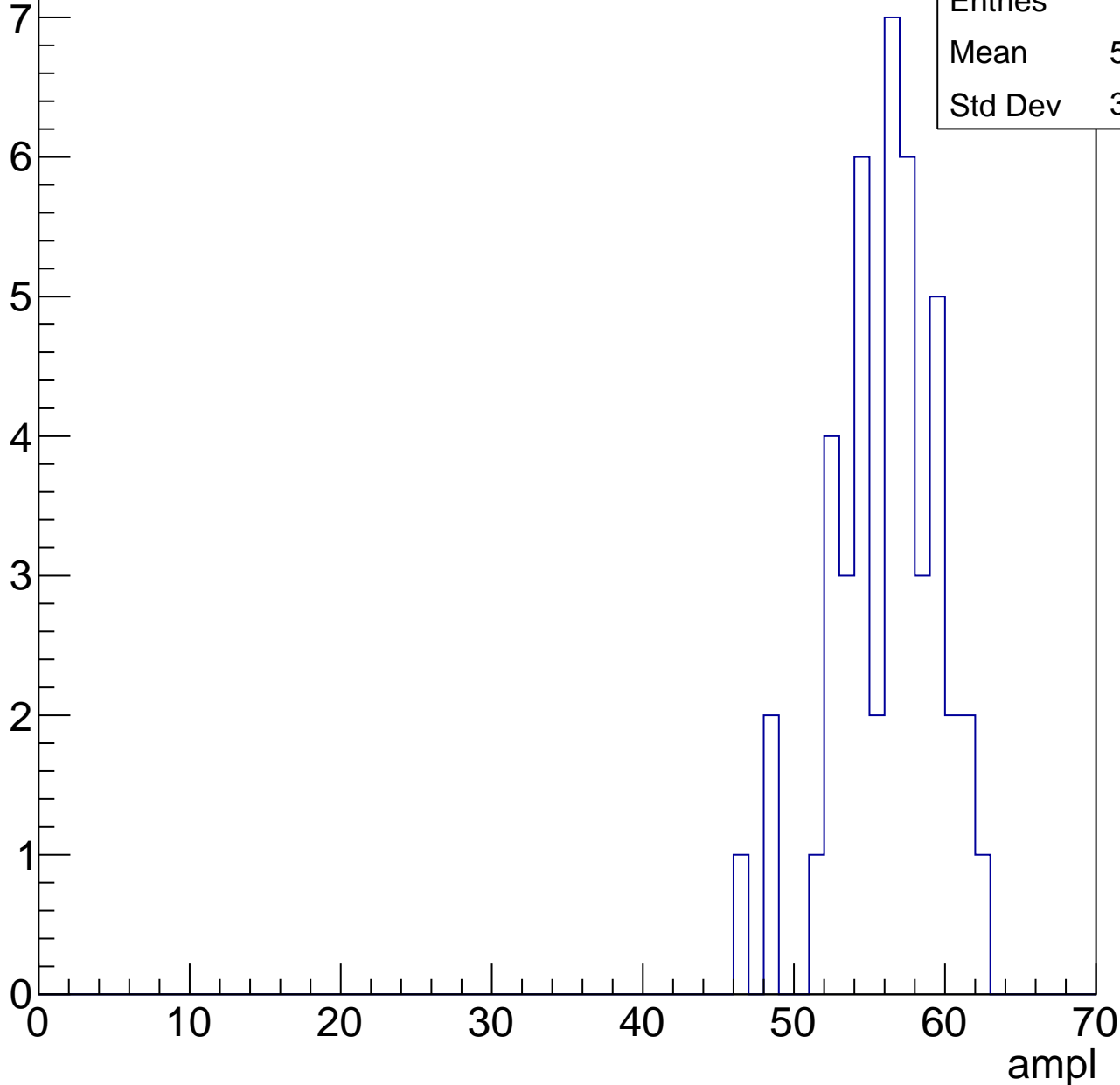


# B1L003S, U22-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	55.58
Std Dev	3.474



# B1L003S, U22-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	58.55
Std Dev	8.927

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

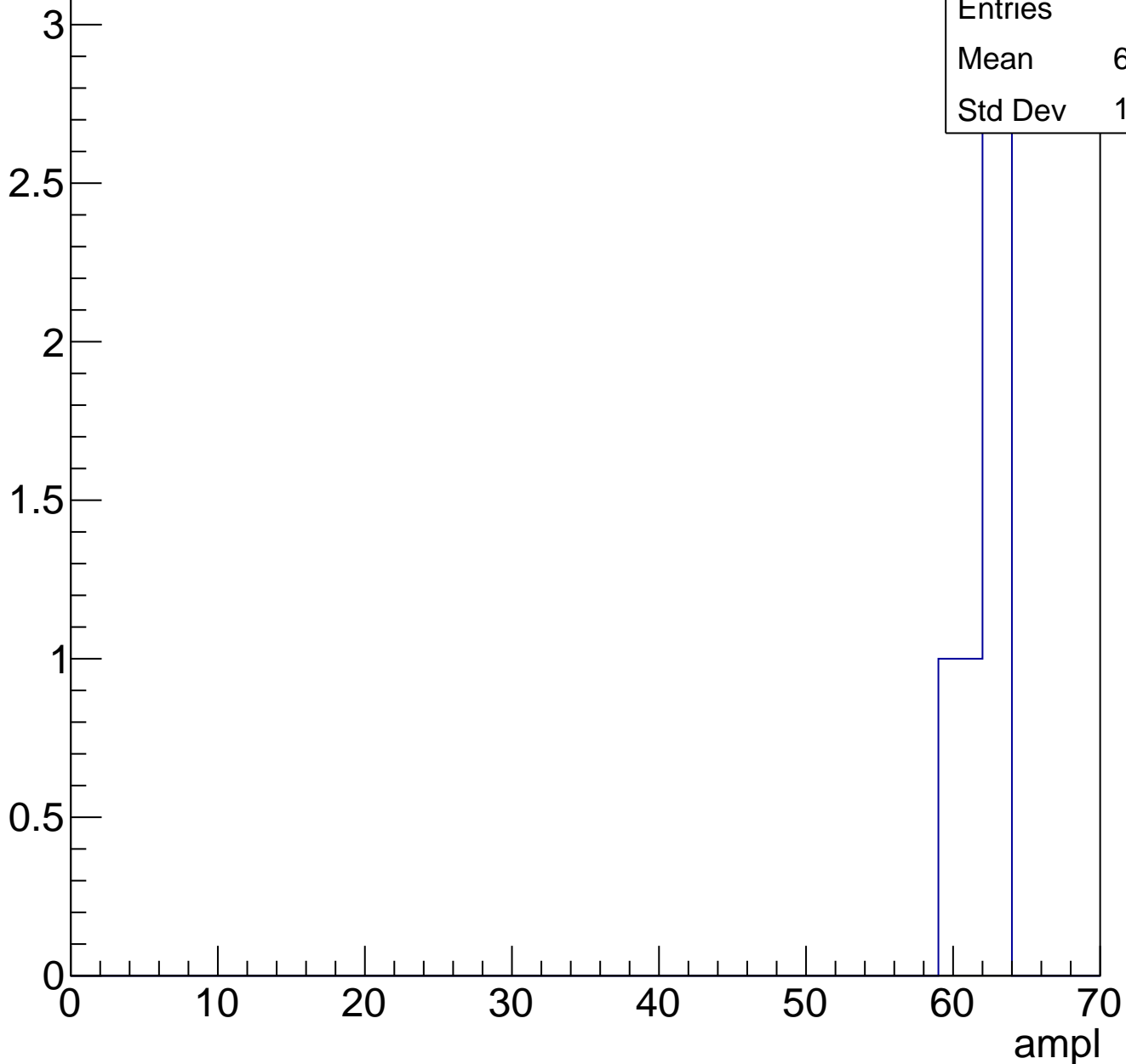
6

7

# B1L003S, U22-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch28, adc0

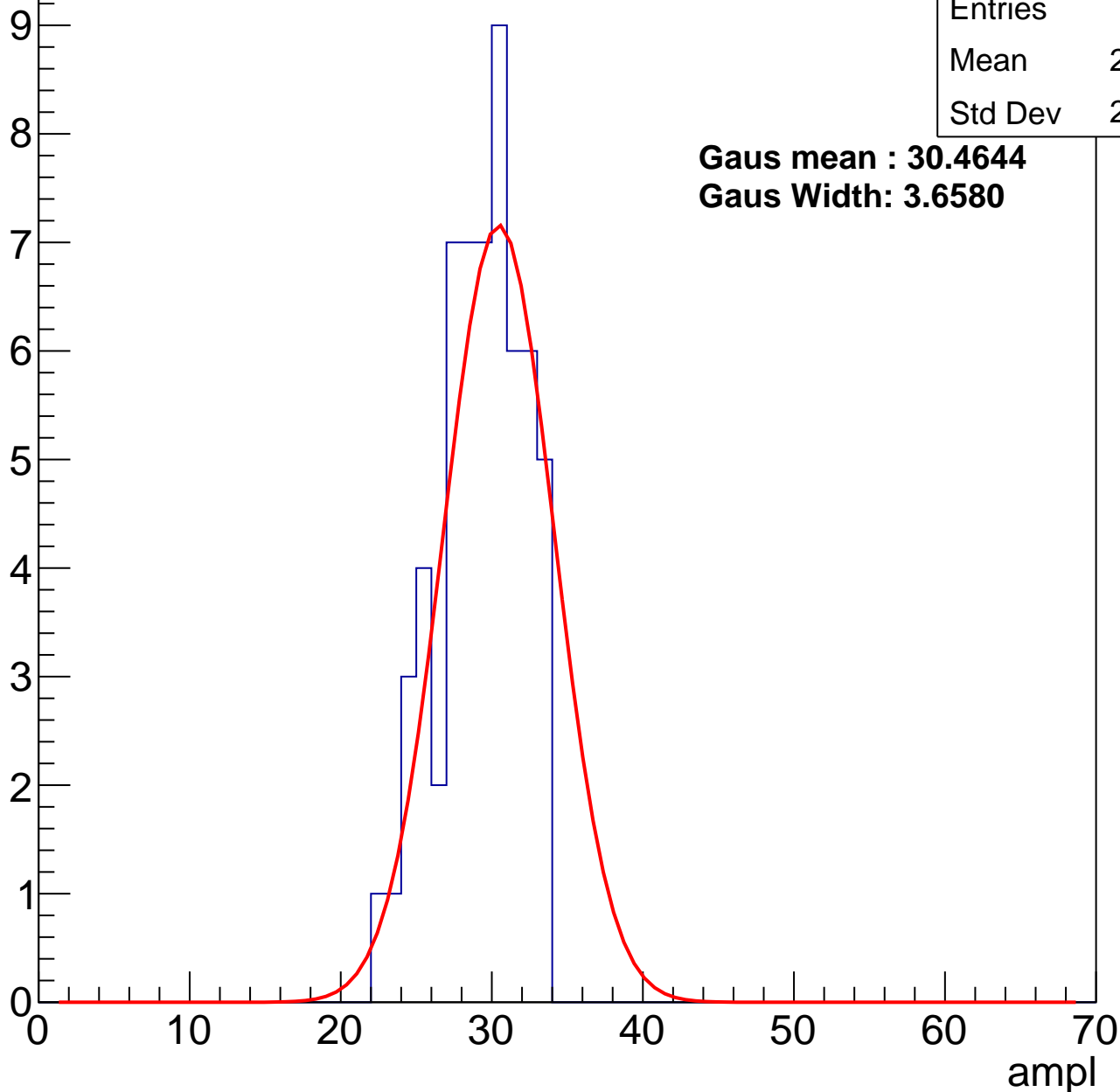
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.79
Std Dev	2.759

**Gaus mean : 30.4644**

**Gaus Width: 3.6580**



# B1L003S, U22-ch28, adc1

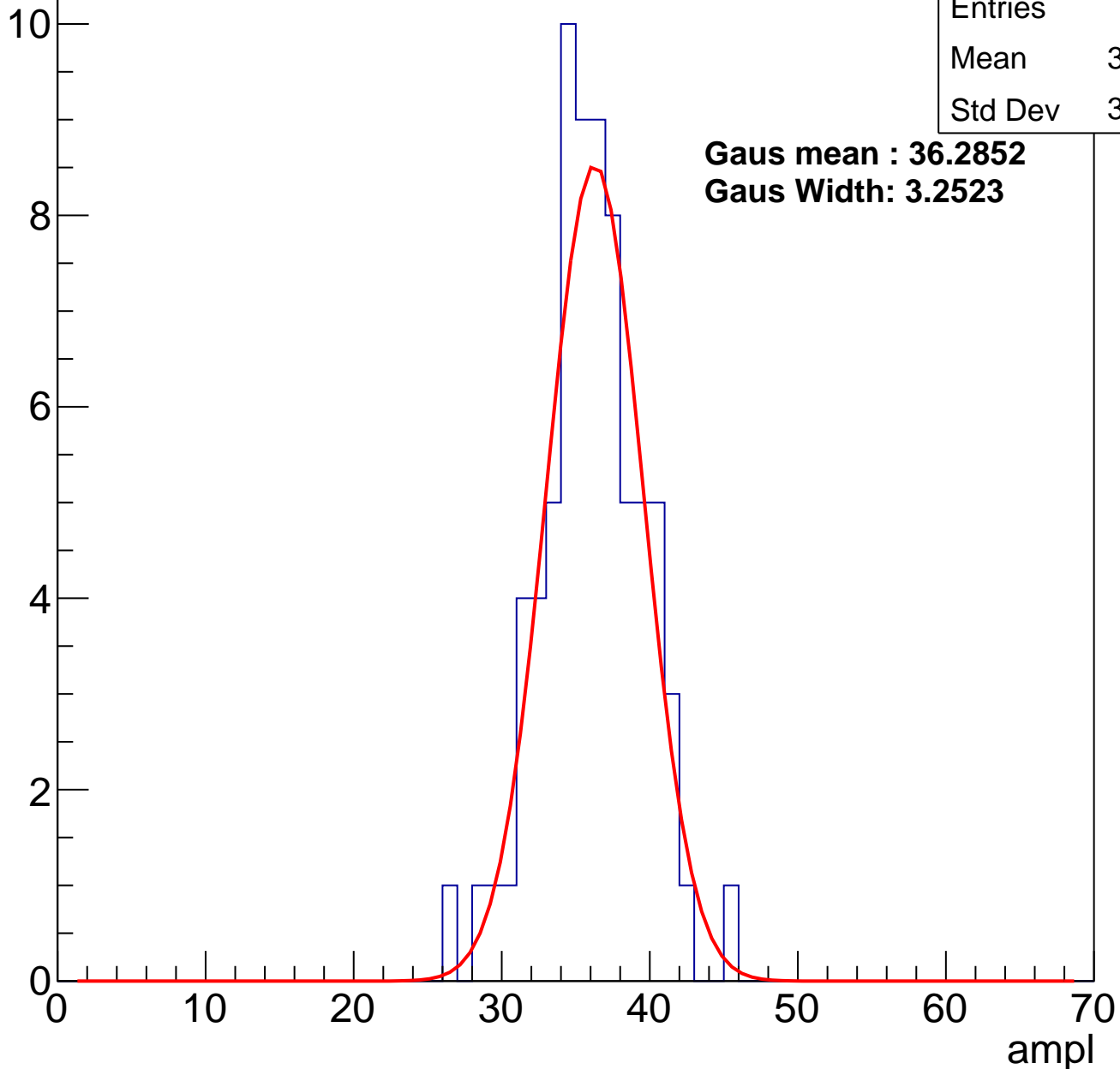
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	35.62
Std Dev	3.395

**Gaus mean : 36.2852**

**Gaus Width: 3.2523**

Entry



# B1L003S, U22-ch28, adc2

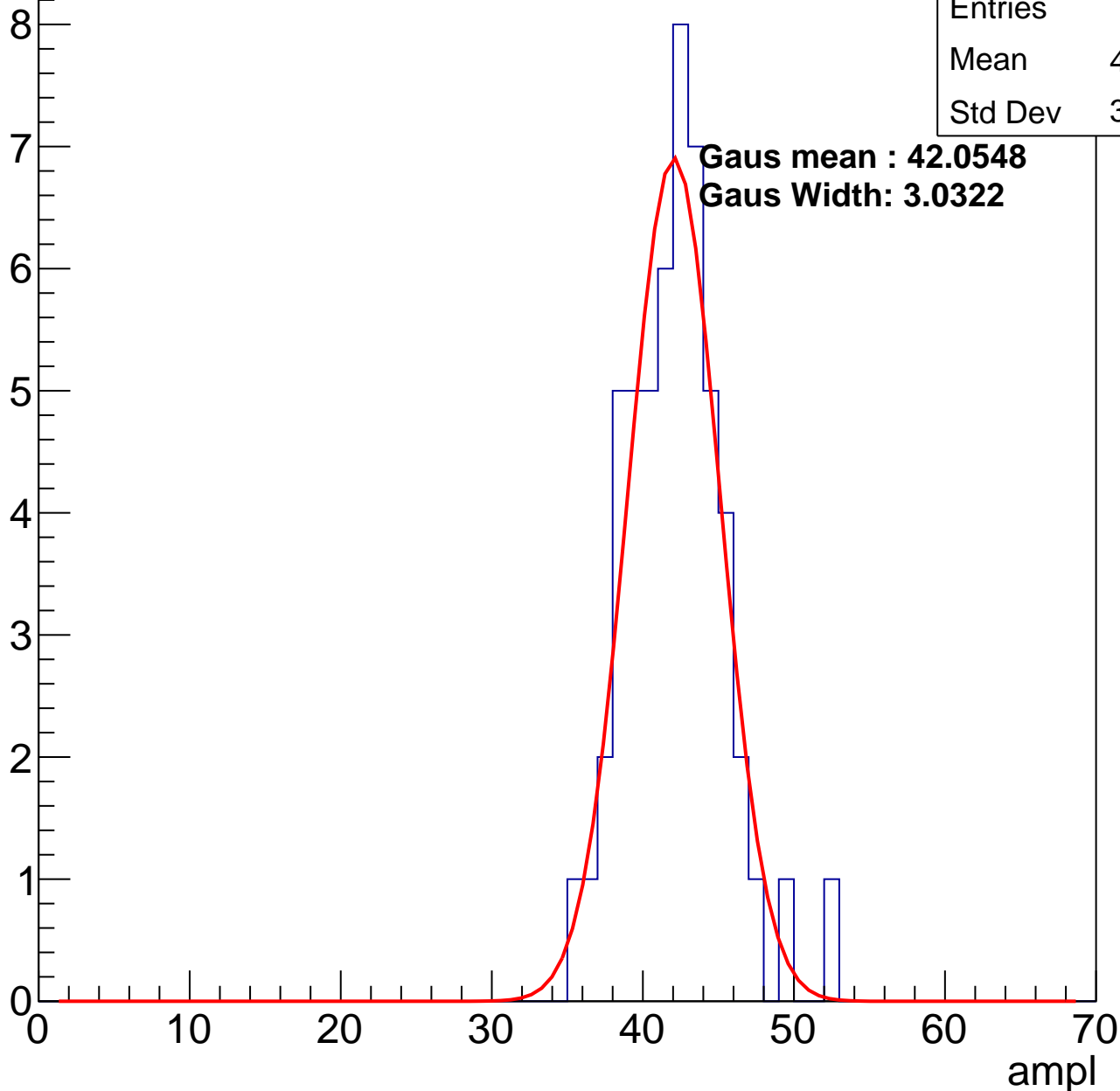
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.72
Std Dev	3.194

**Gaus mean : 42.0548**

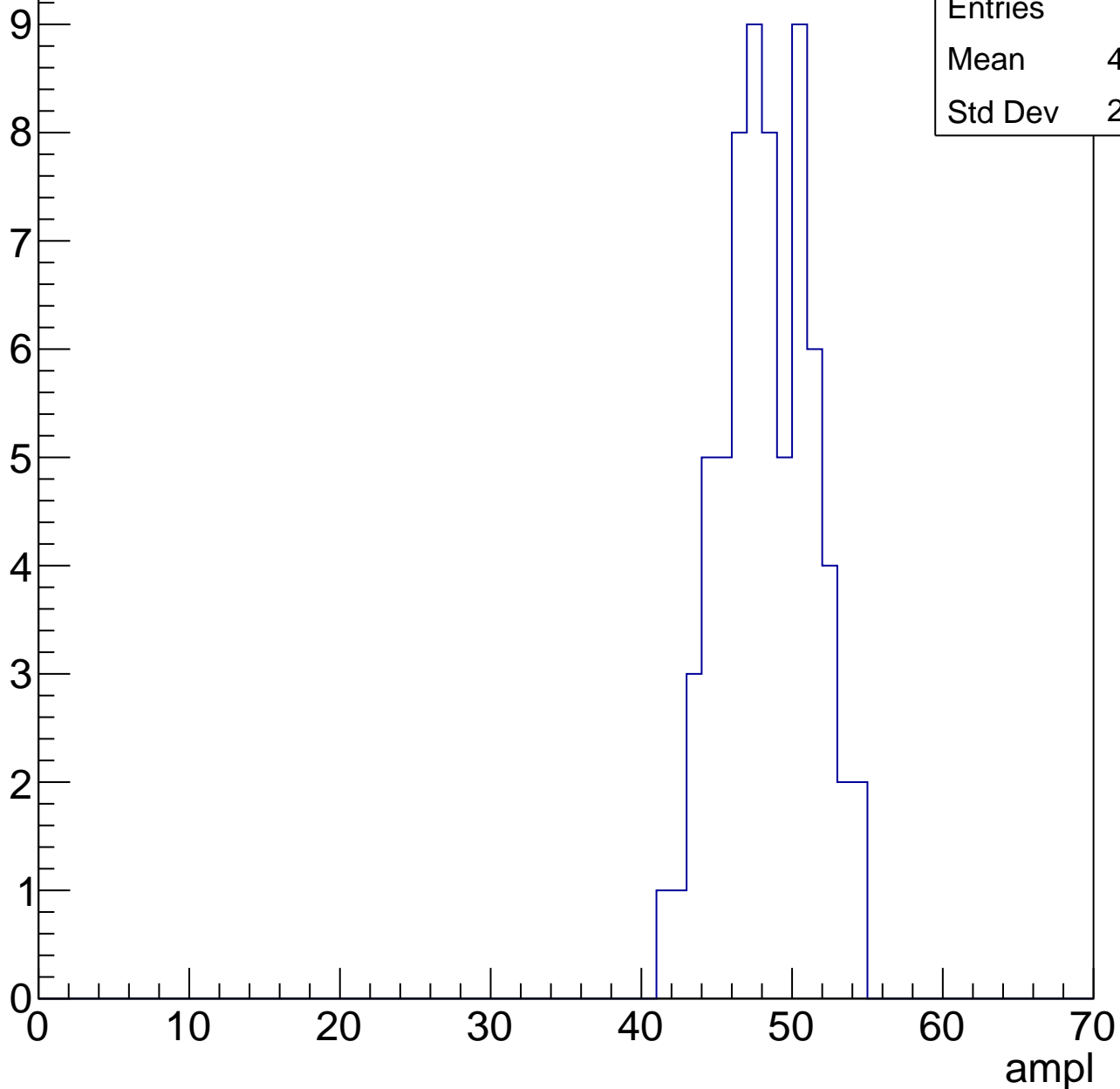
**Gaus Width: 3.0322**



# B1L003S, U22-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

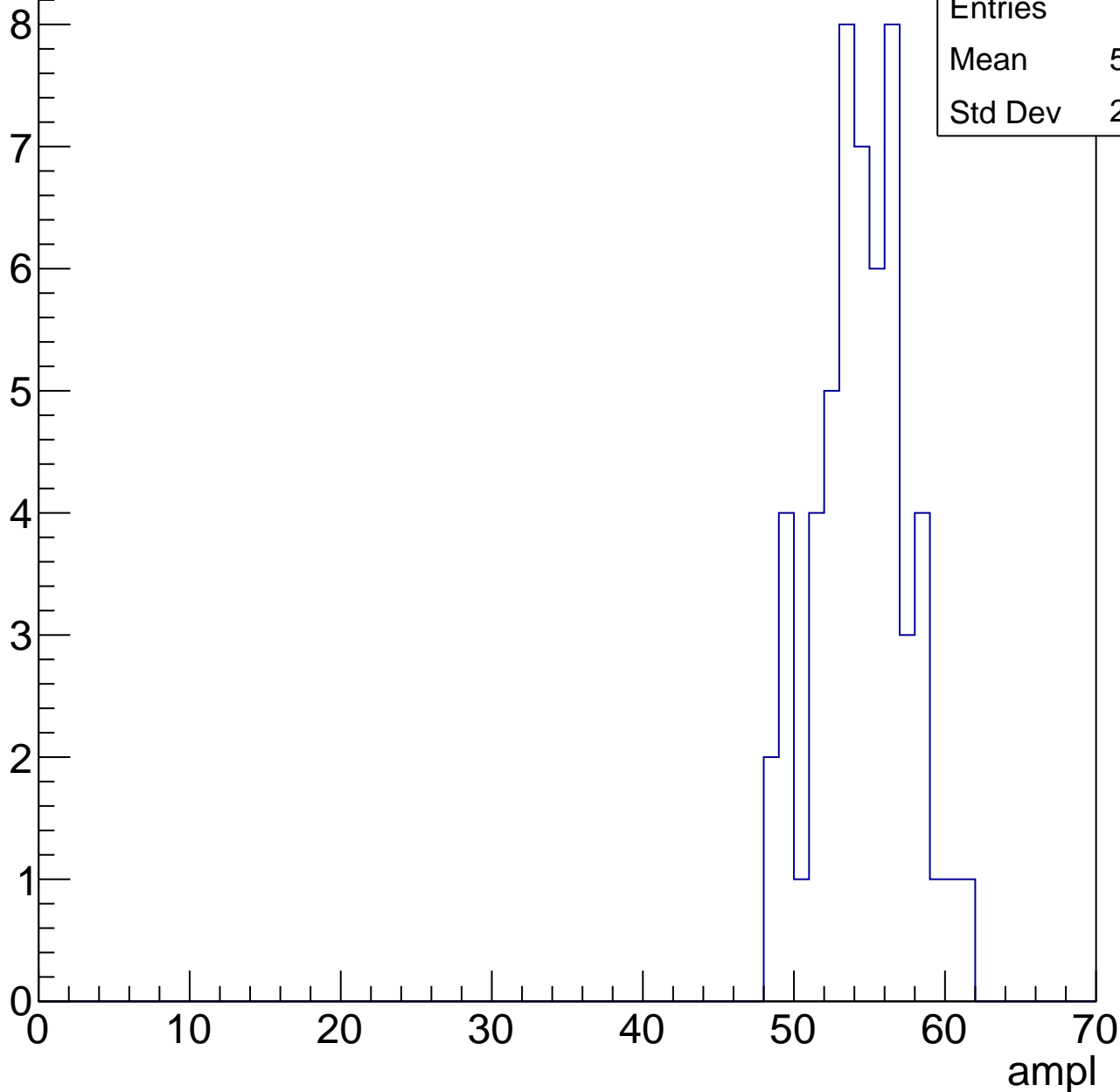
Entry



# B1L003S, U22-ch28, adc4

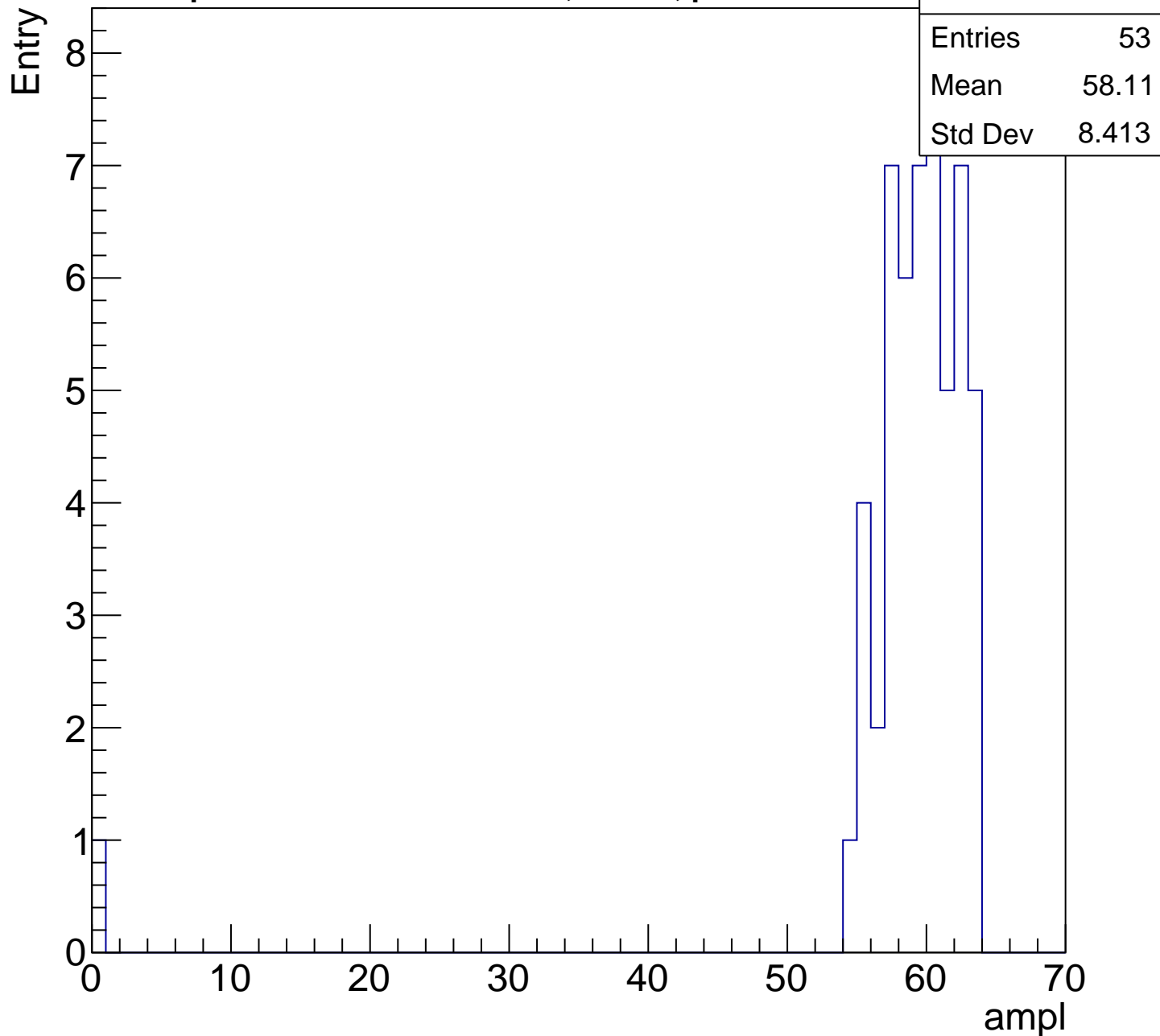
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch28, adc5

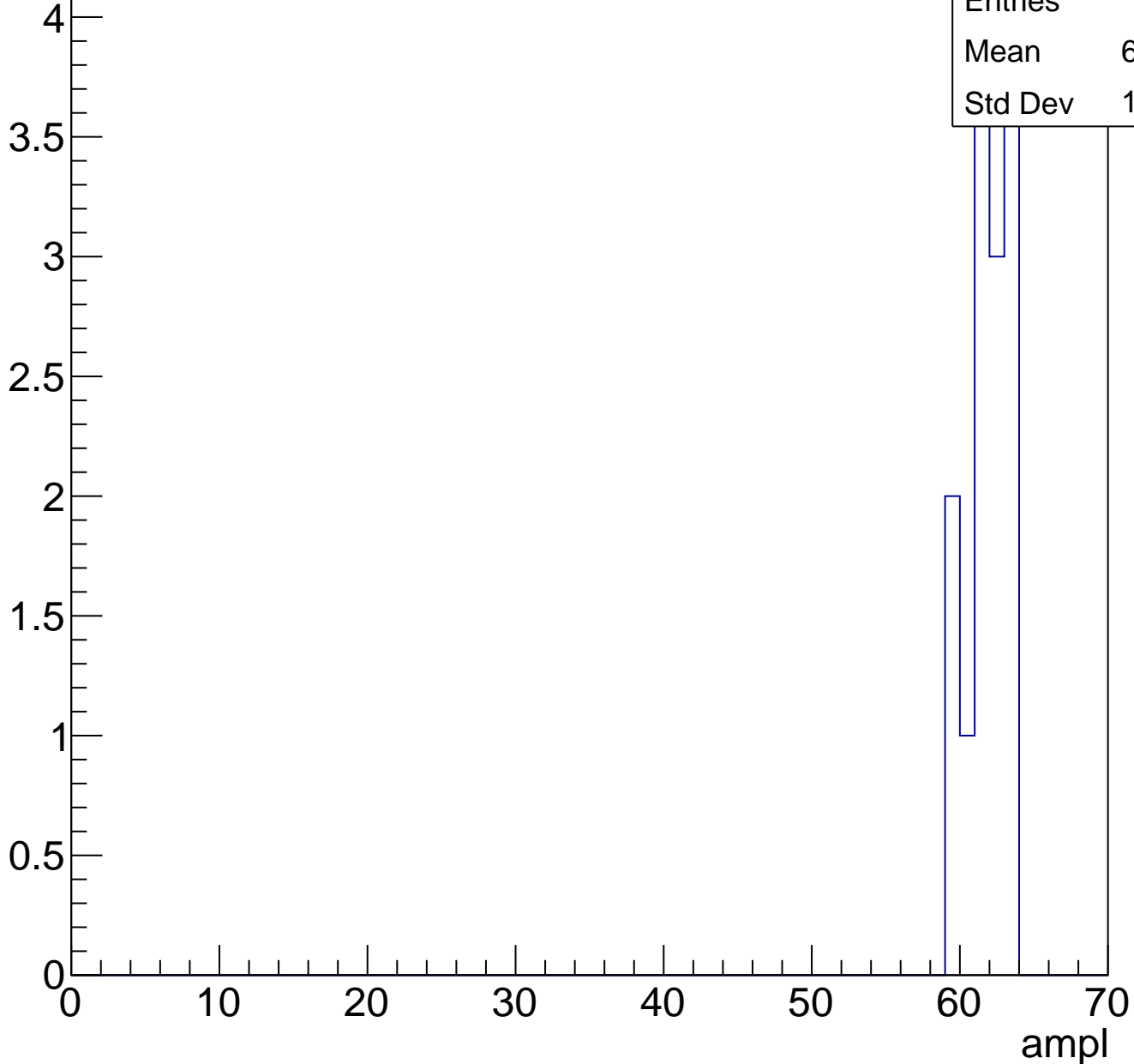
calib\_packv5\_042523\_0143.root, FC#13, port D2



# B1L003S, U22-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch29, adc0

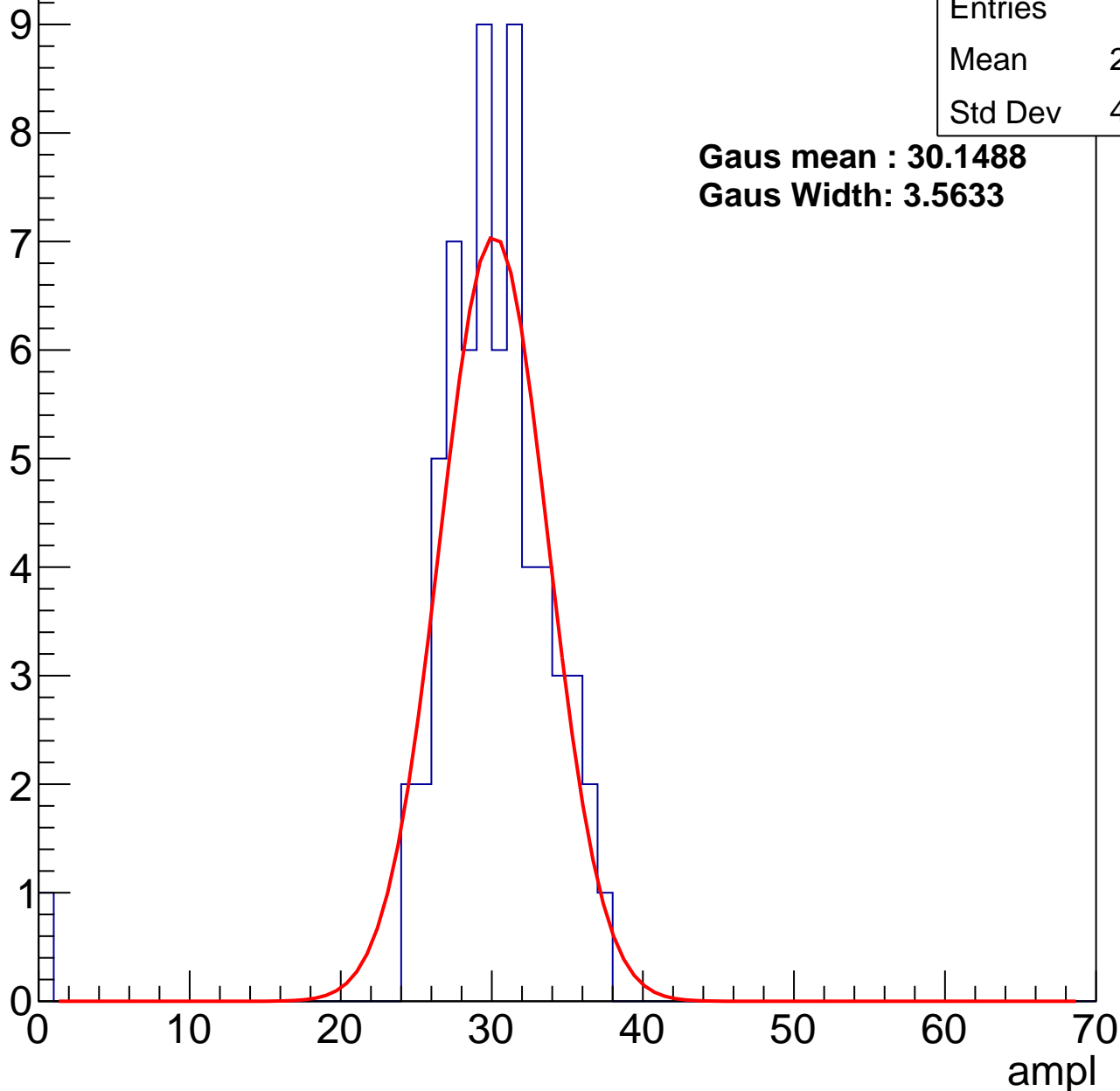
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.39
Std Dev	4.808

**Gaus mean : 30.1488**

**Gaus Width: 3.5633**



# B1L003S, U22-ch29, adc1

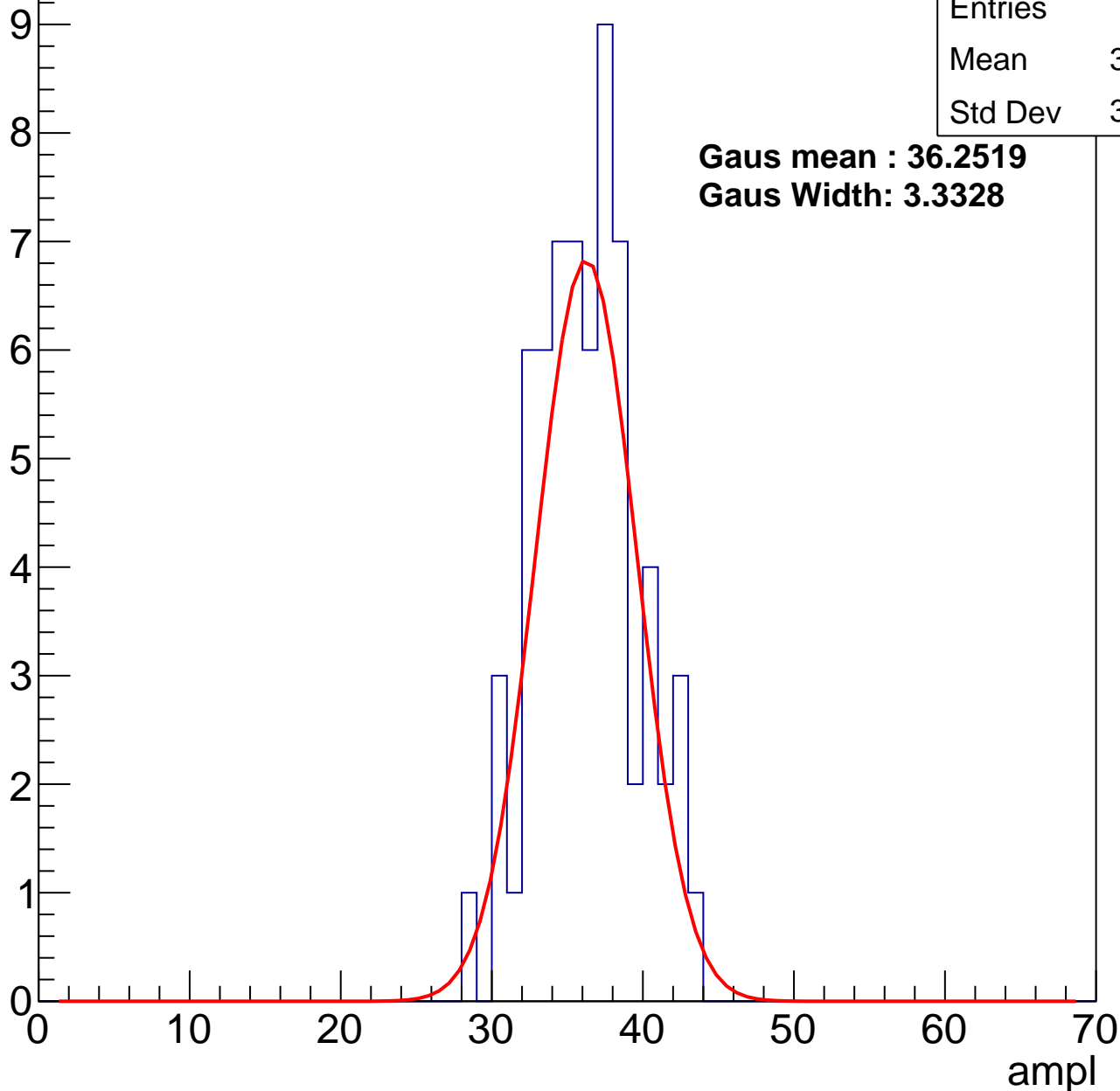
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	35.78
Std Dev	3.293

**Gaus mean : 36.2519**

**Gaus Width: 3.3328**



# B1L003S, U22-ch29, adc2

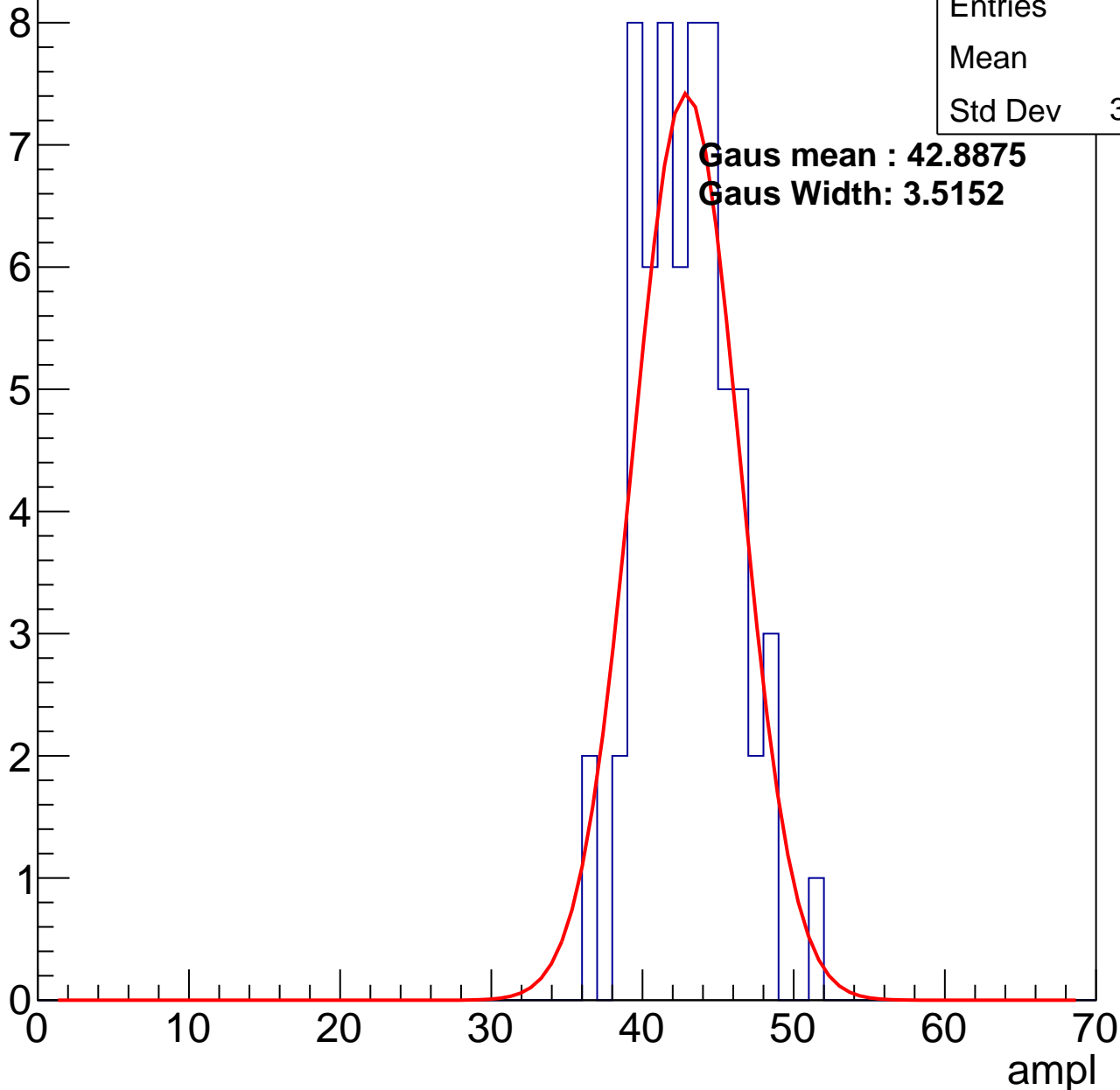
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.5
Std Dev	3.052

**Gaus mean : 42.8875**

**Gaus Width: 3.5152**

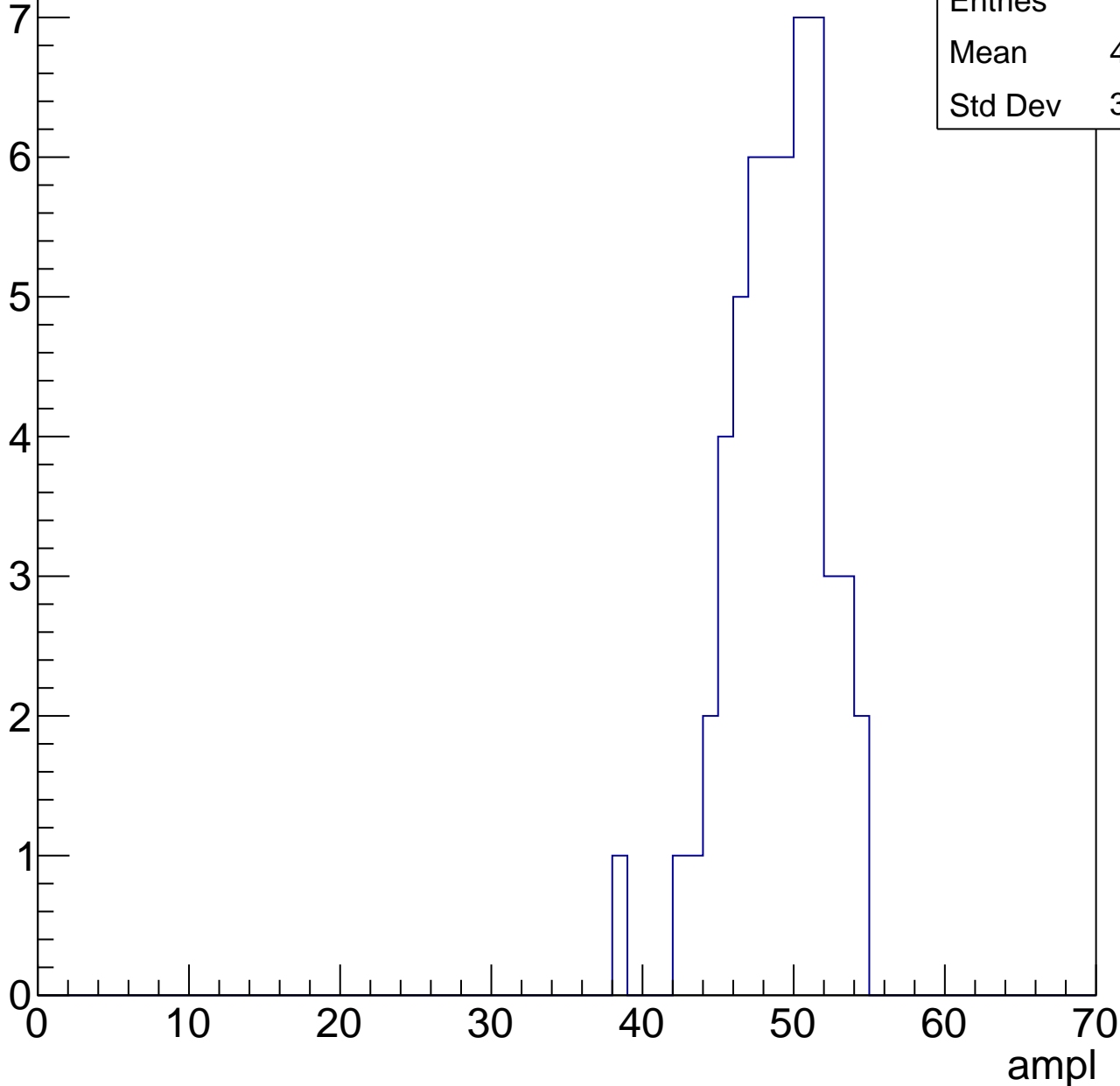


# B1L003S, U22-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.43
Std Dev	3.154

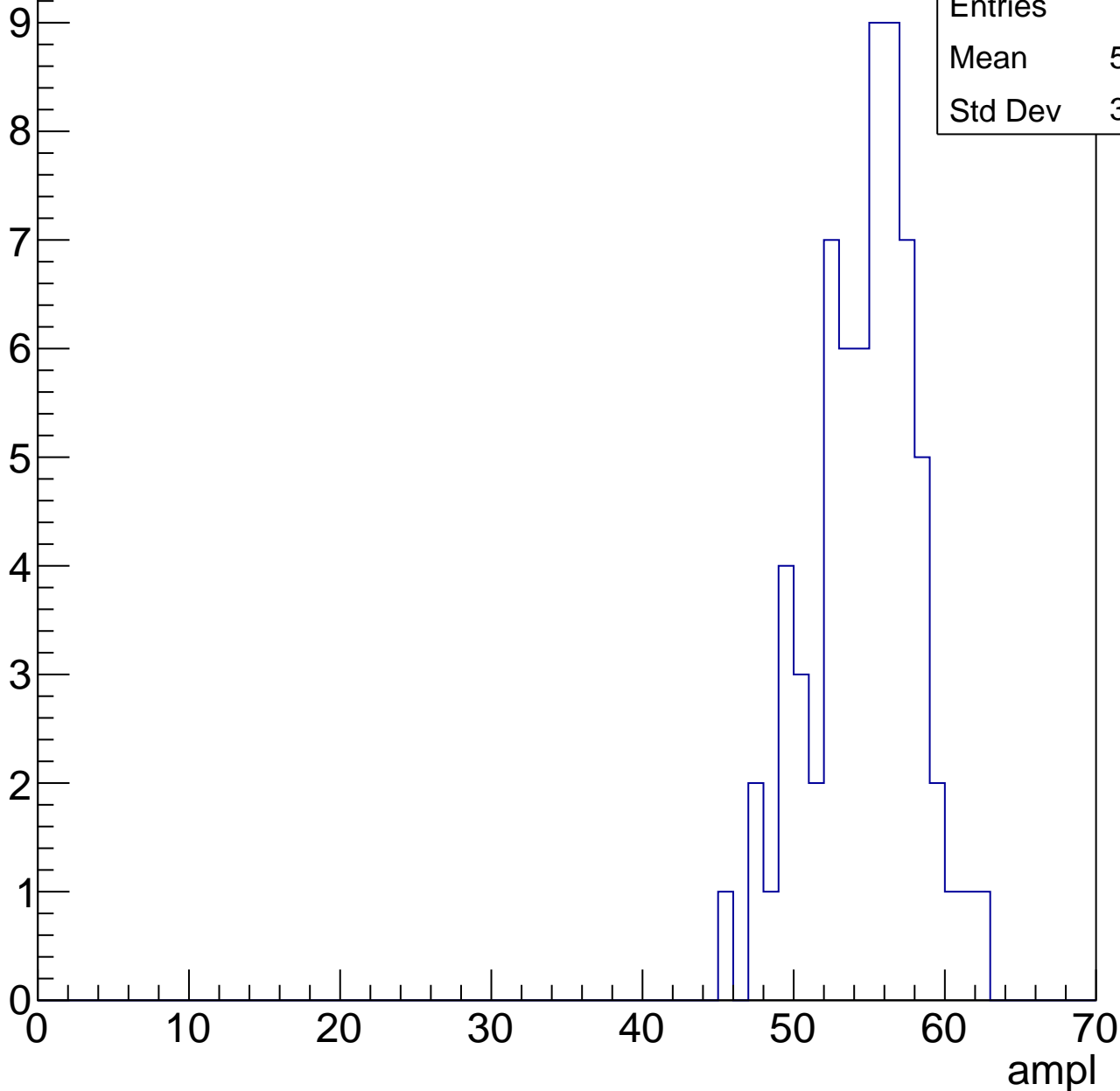


# B1L003S, U22-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	54.18
Std Dev	3.455

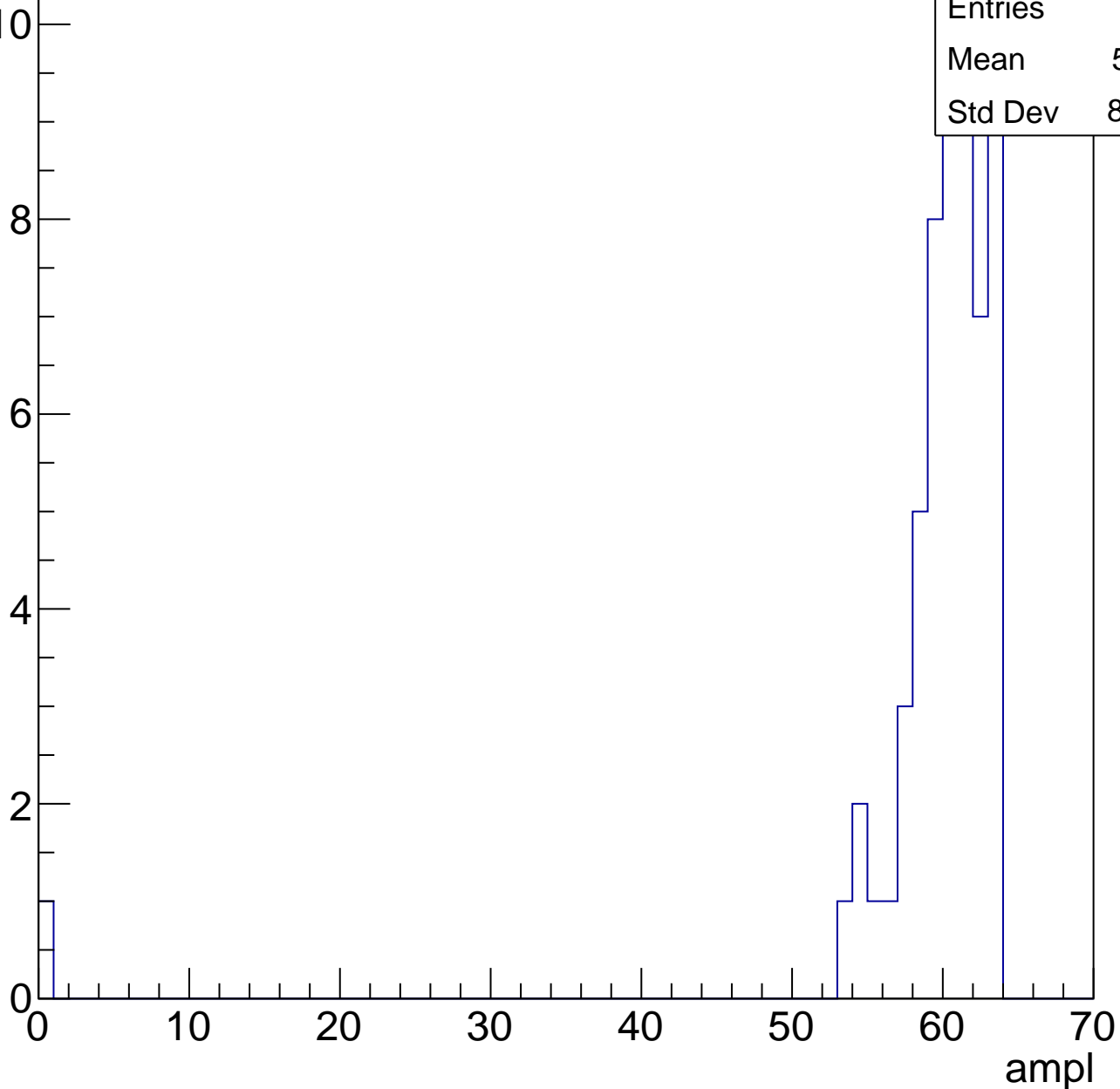


# B1L003S, U22-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.91
Std Dev	8.247



# B1L003S, U22-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

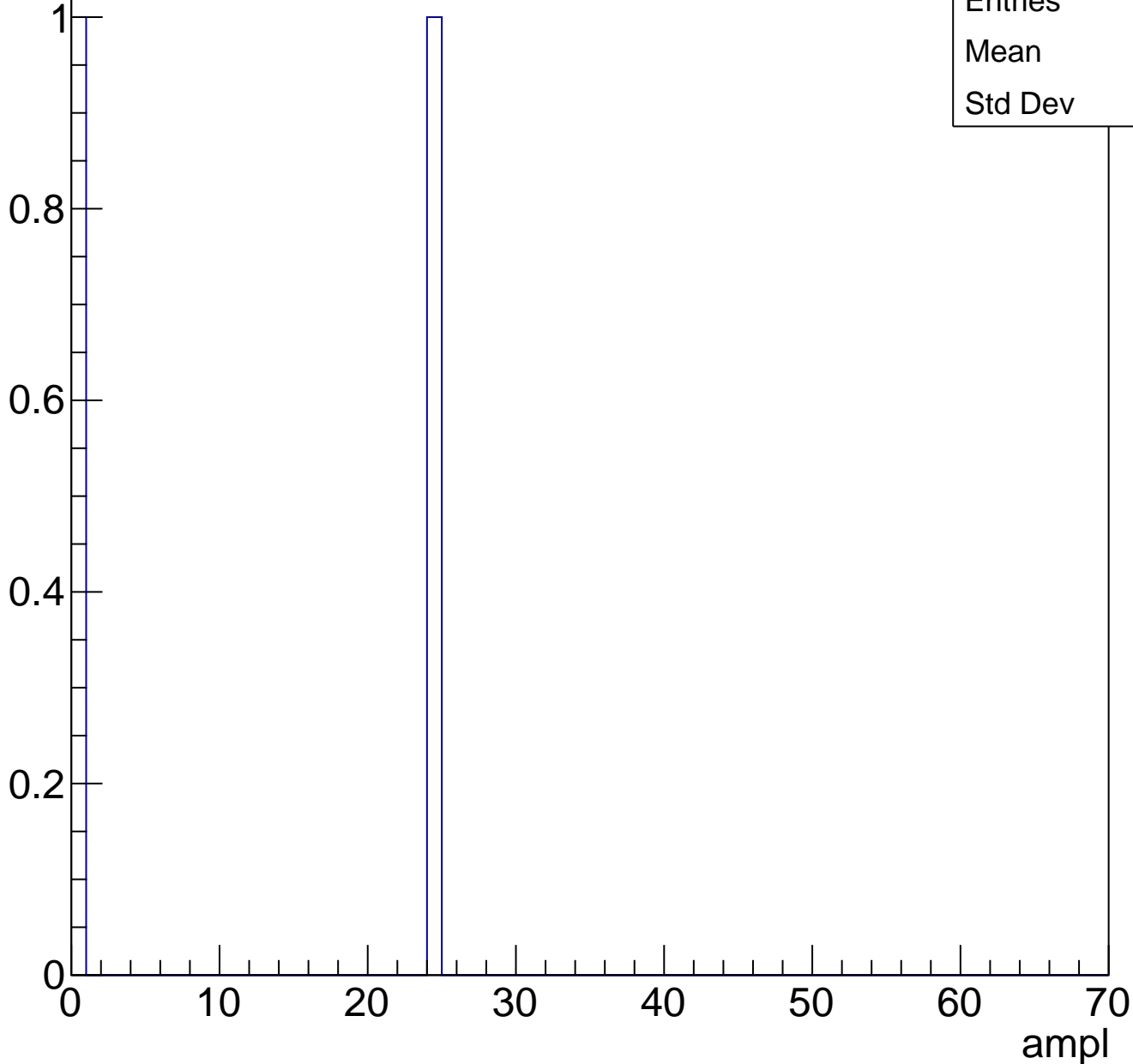




# B1L003S, U22-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	30.38
Std Dev	3.325

**Gaus mean : 30.6180**

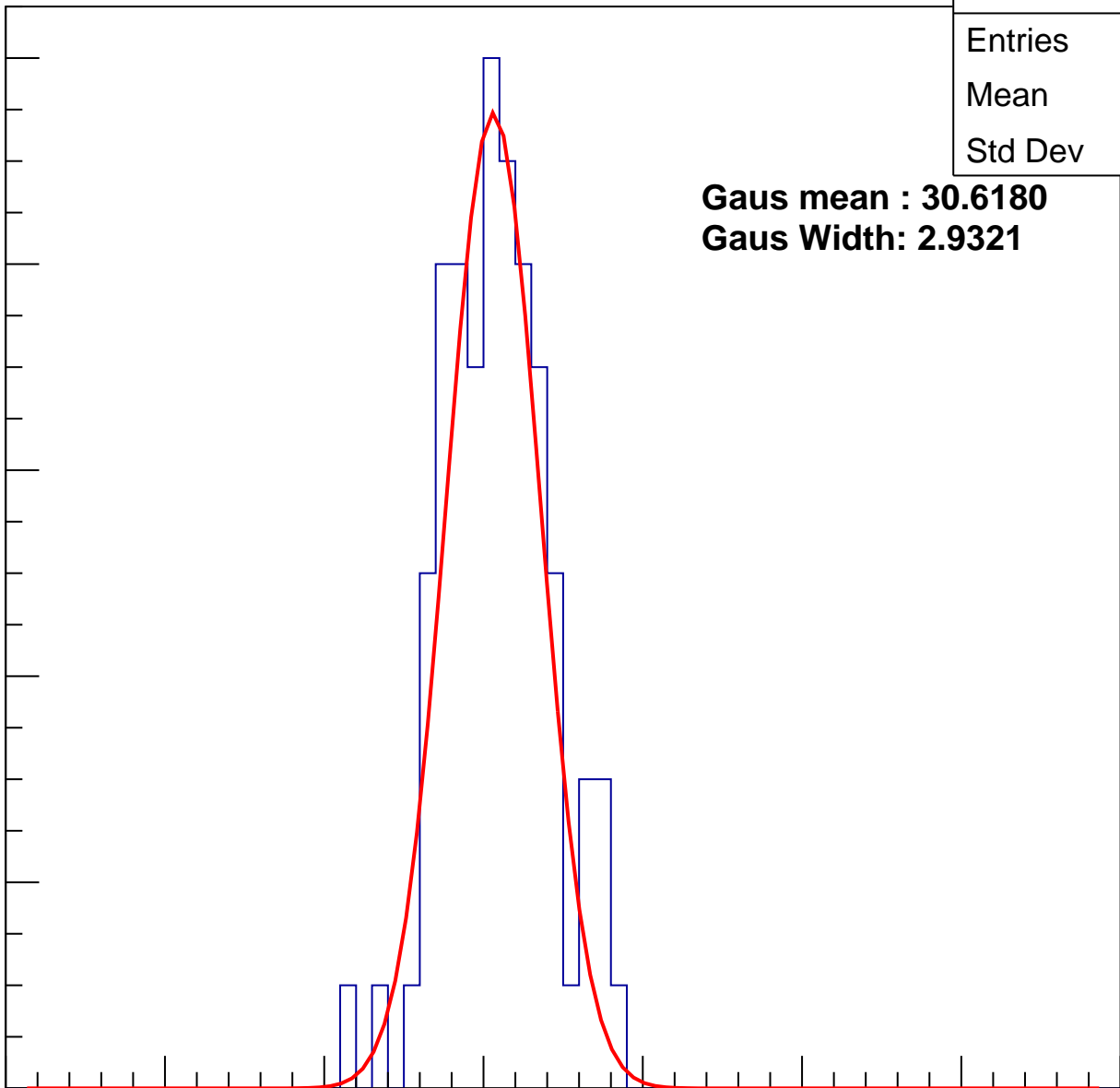
**Gaus Width: 2.9321**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch30, adc1

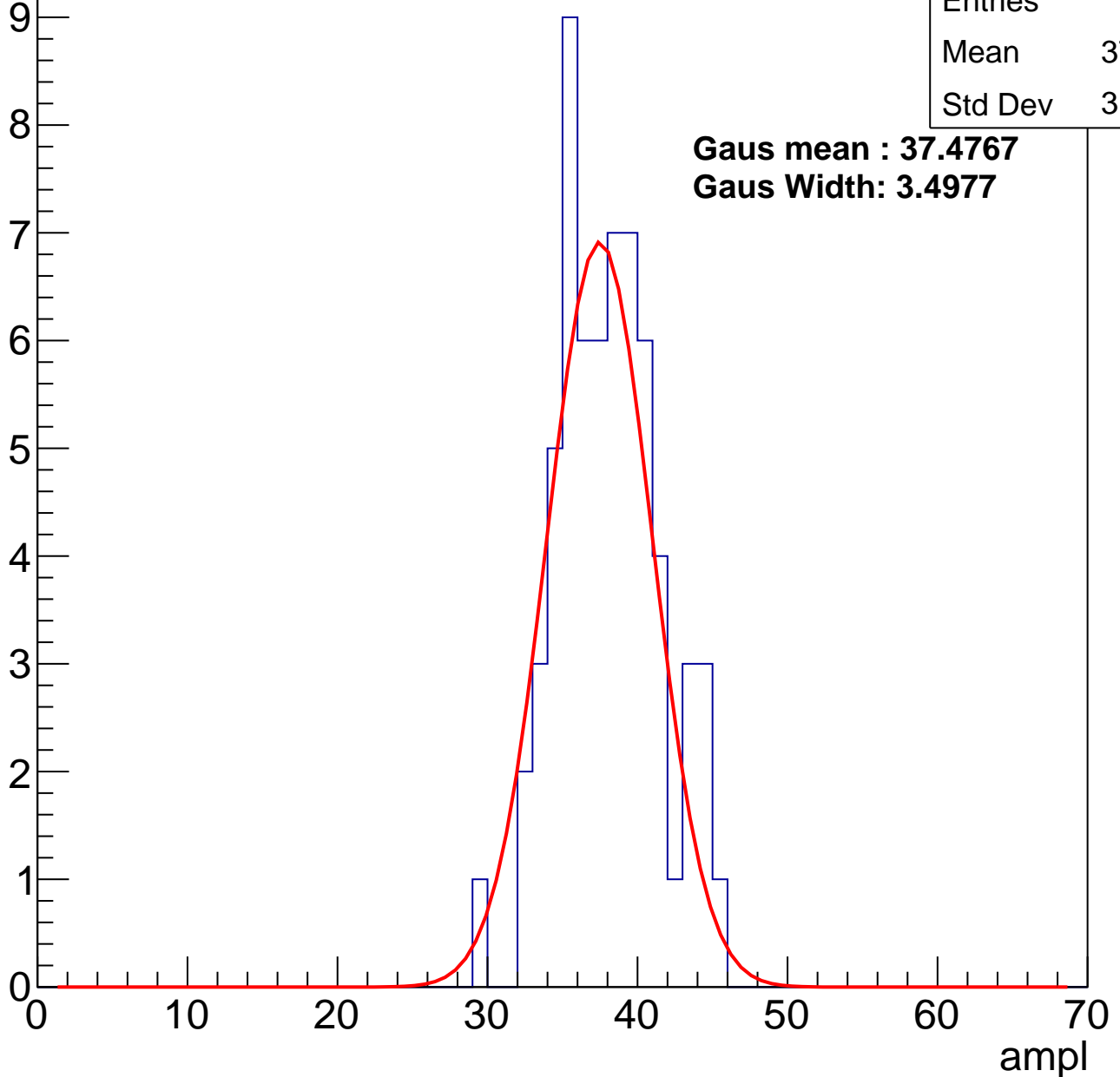
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.59
Std Dev	3.362

**Gaus mean : 37.4767**

**Gaus Width: 3.4977**



# B1L003S, U22-ch30, adc2

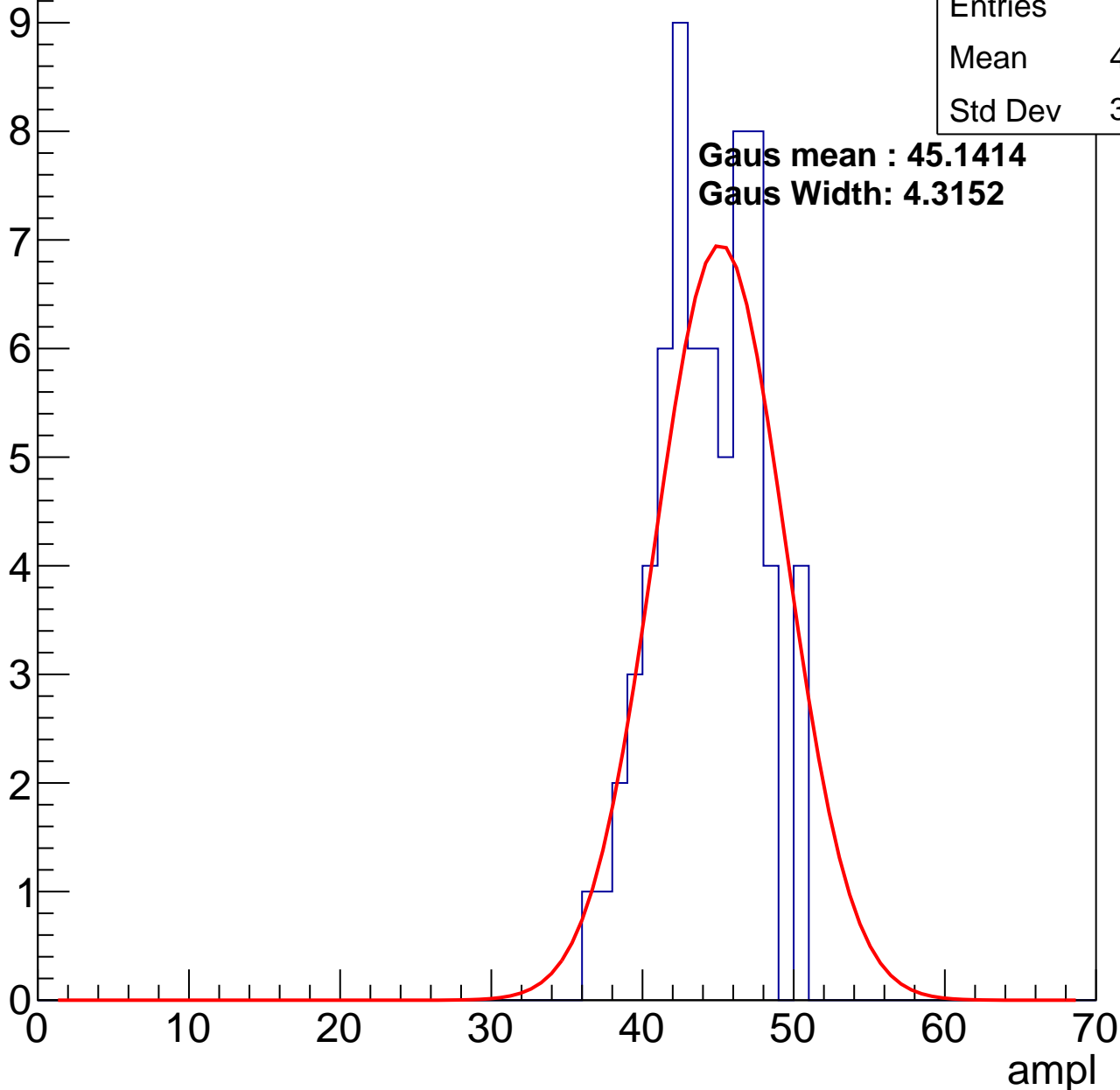
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.78
Std Dev	3.309

**Gaus mean : 45.1414**

**Gaus Width: 4.3152**

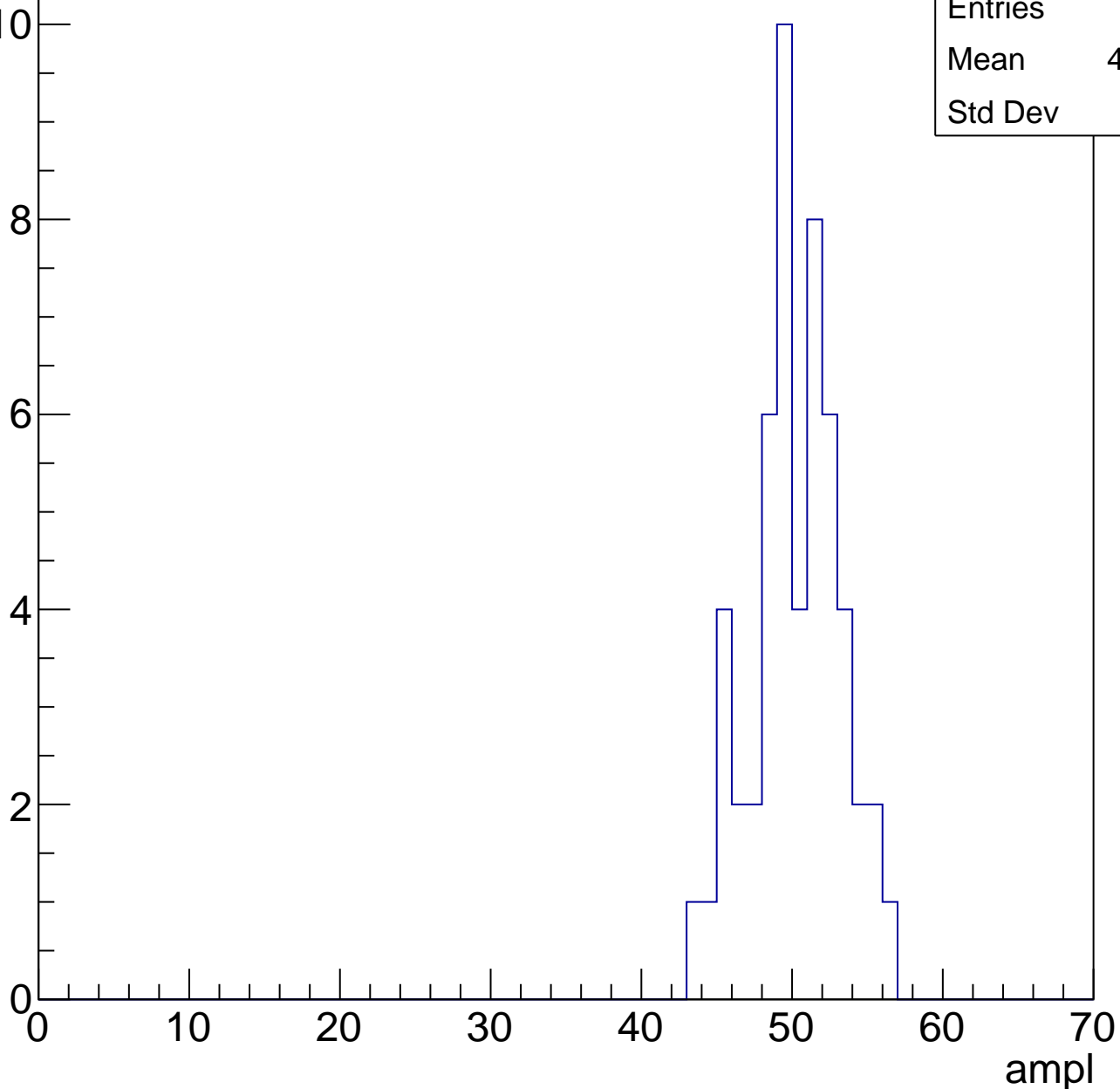


# B1L003S, U22-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	49.75
Std Dev	2.92

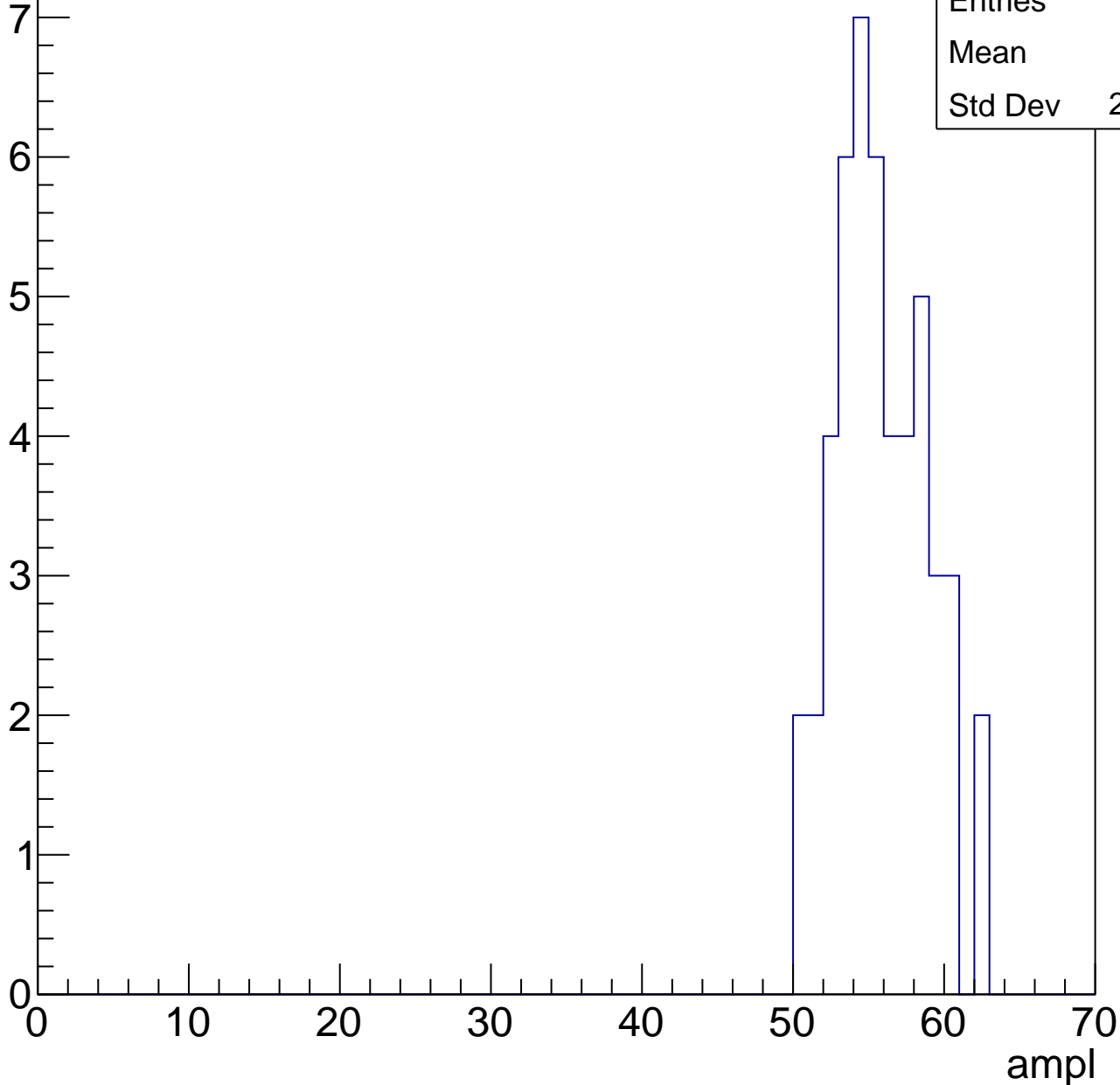


# B1L003S, U22-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	55.4
Std Dev	2.984



# B1L003S, U22-ch30, adc5

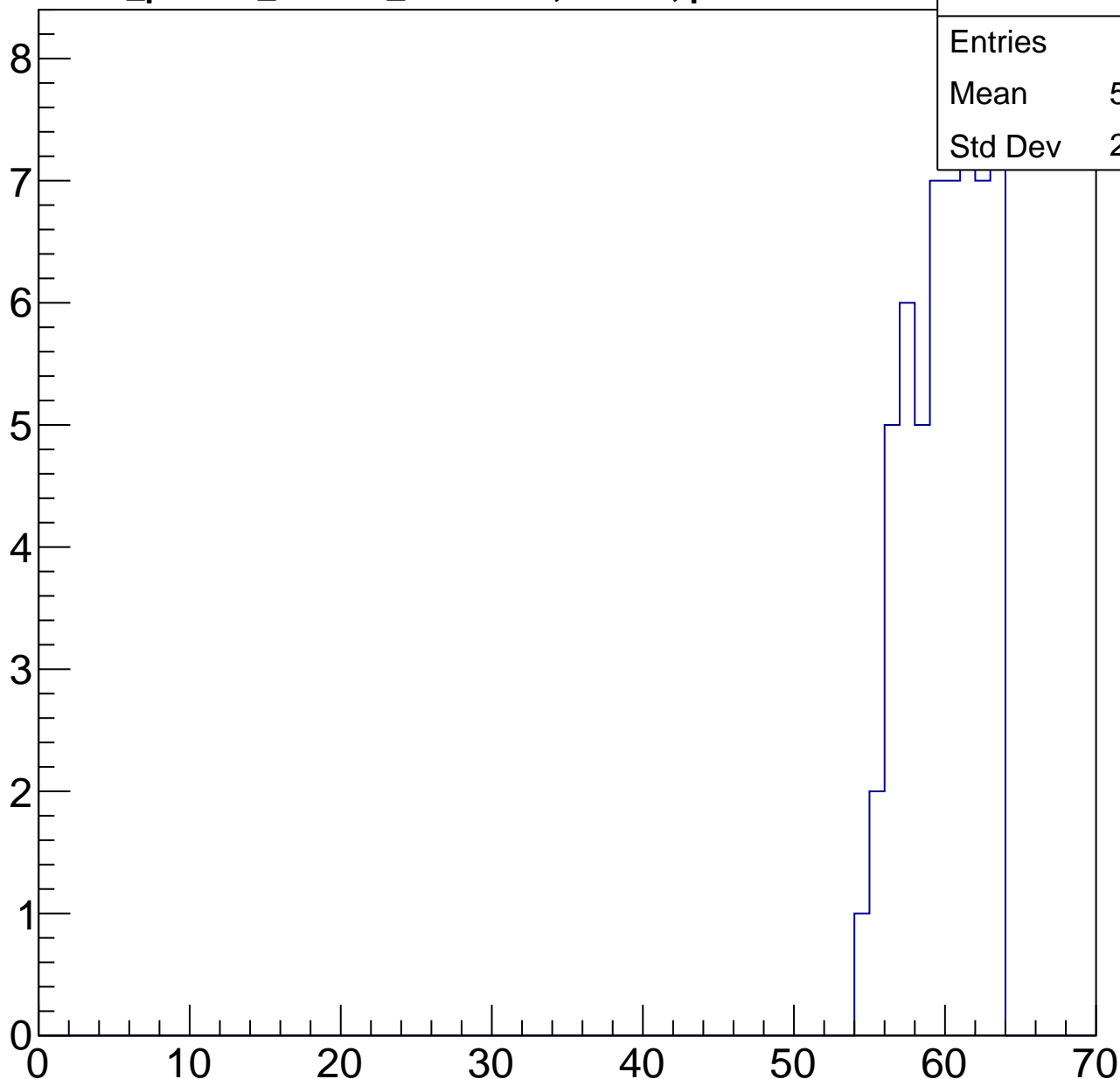
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	59.55
Std Dev	2.478

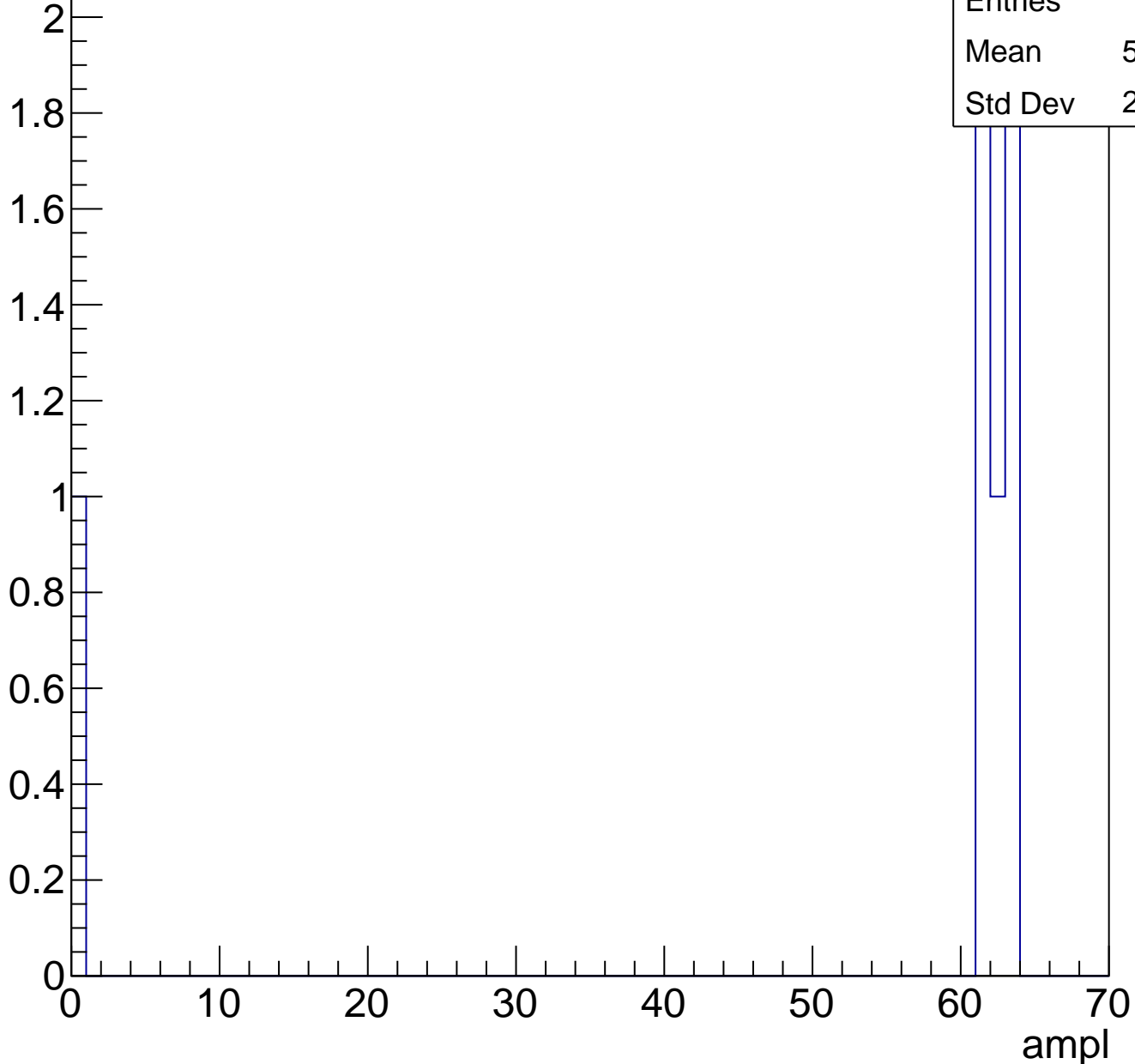
ampl



# B1L003S, U22-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch31, adc0

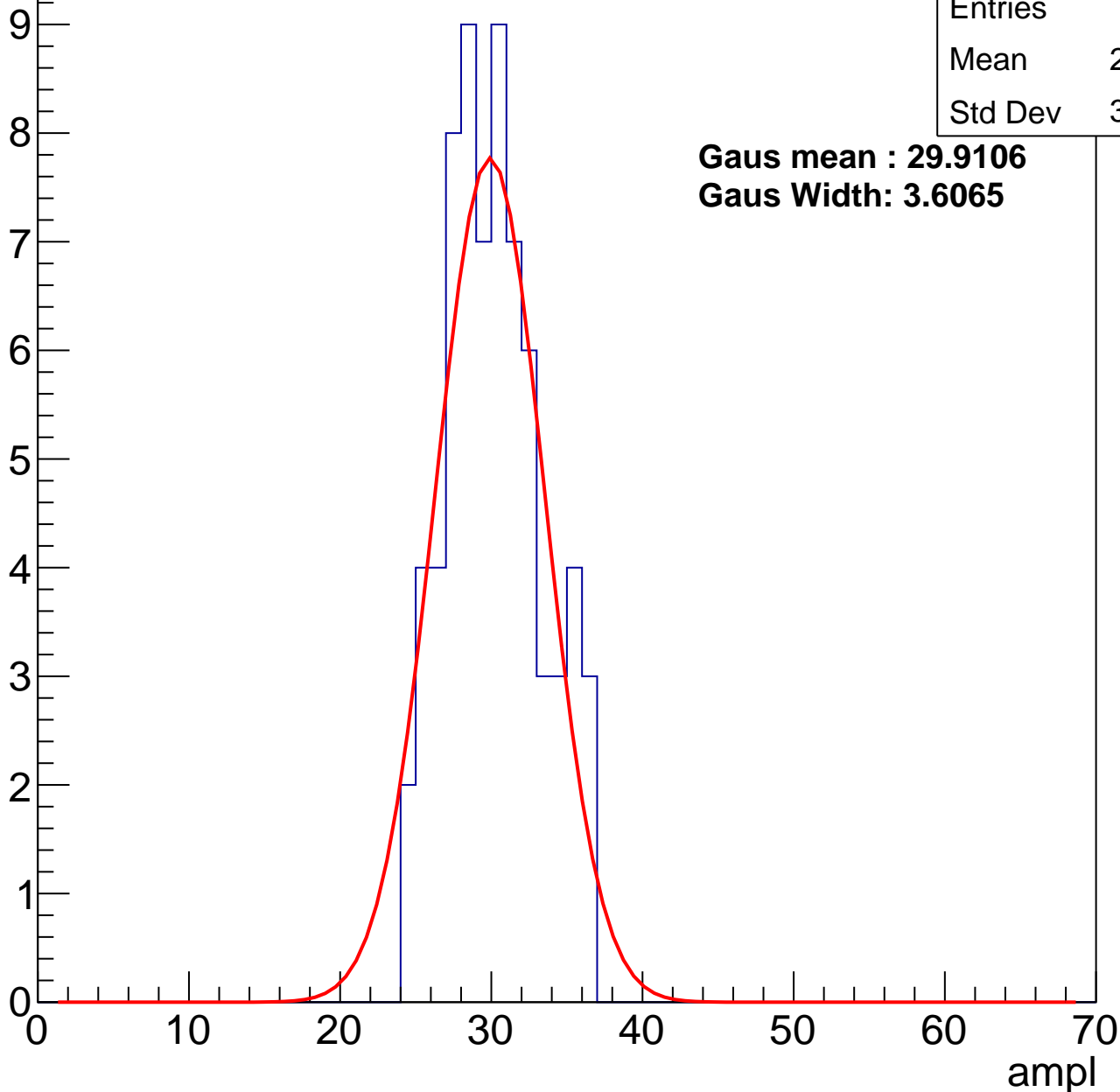
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.72
Std Dev	3.092

**Gaus mean : 29.9106**

**Gaus Width: 3.6065**



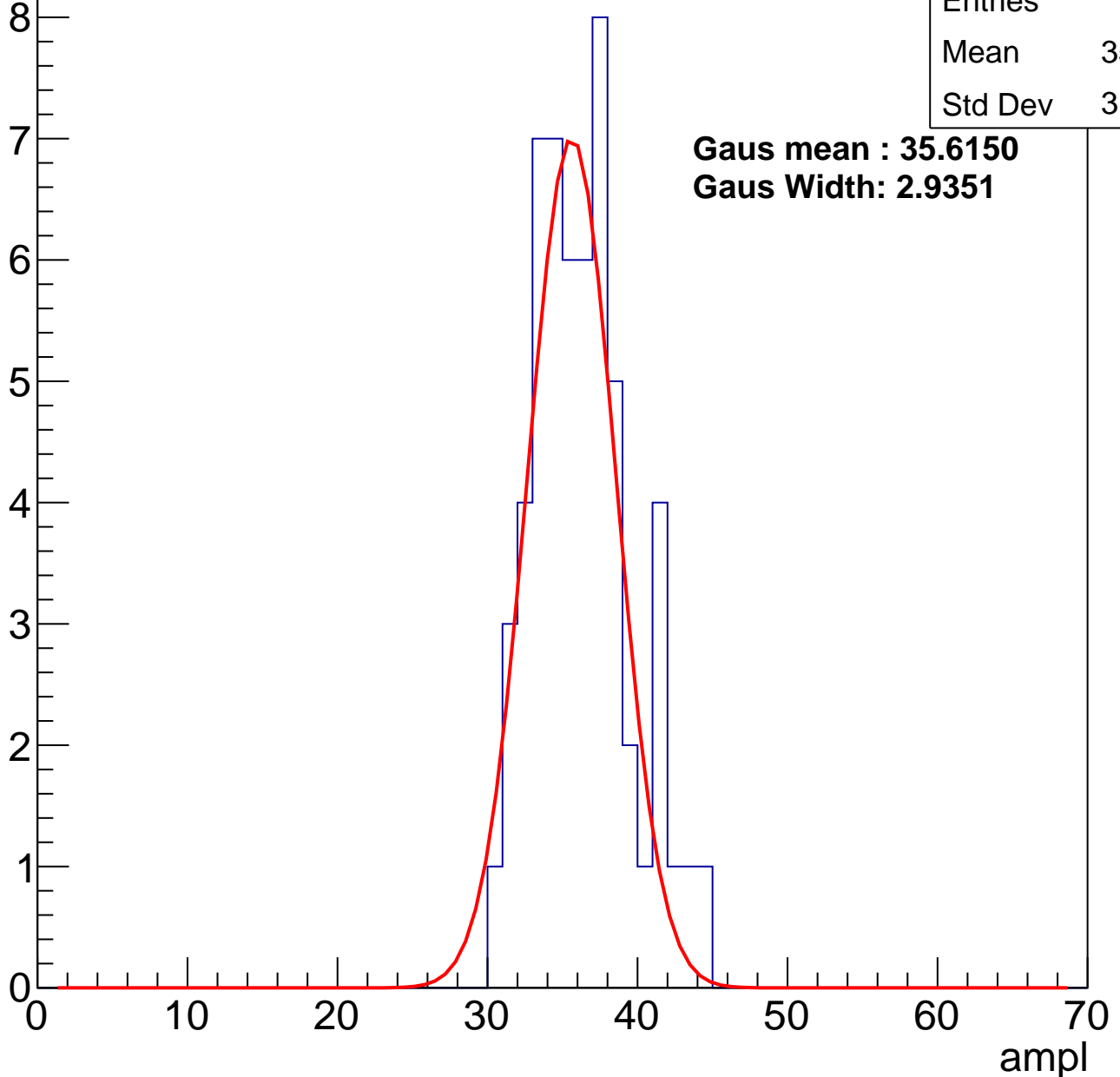
# B1L003S, U22-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.84
Std Dev	3.194

**Gaus mean : 35.6150**  
**Gaus Width: 2.9351**



# B1L003S, U22-ch31, adc2

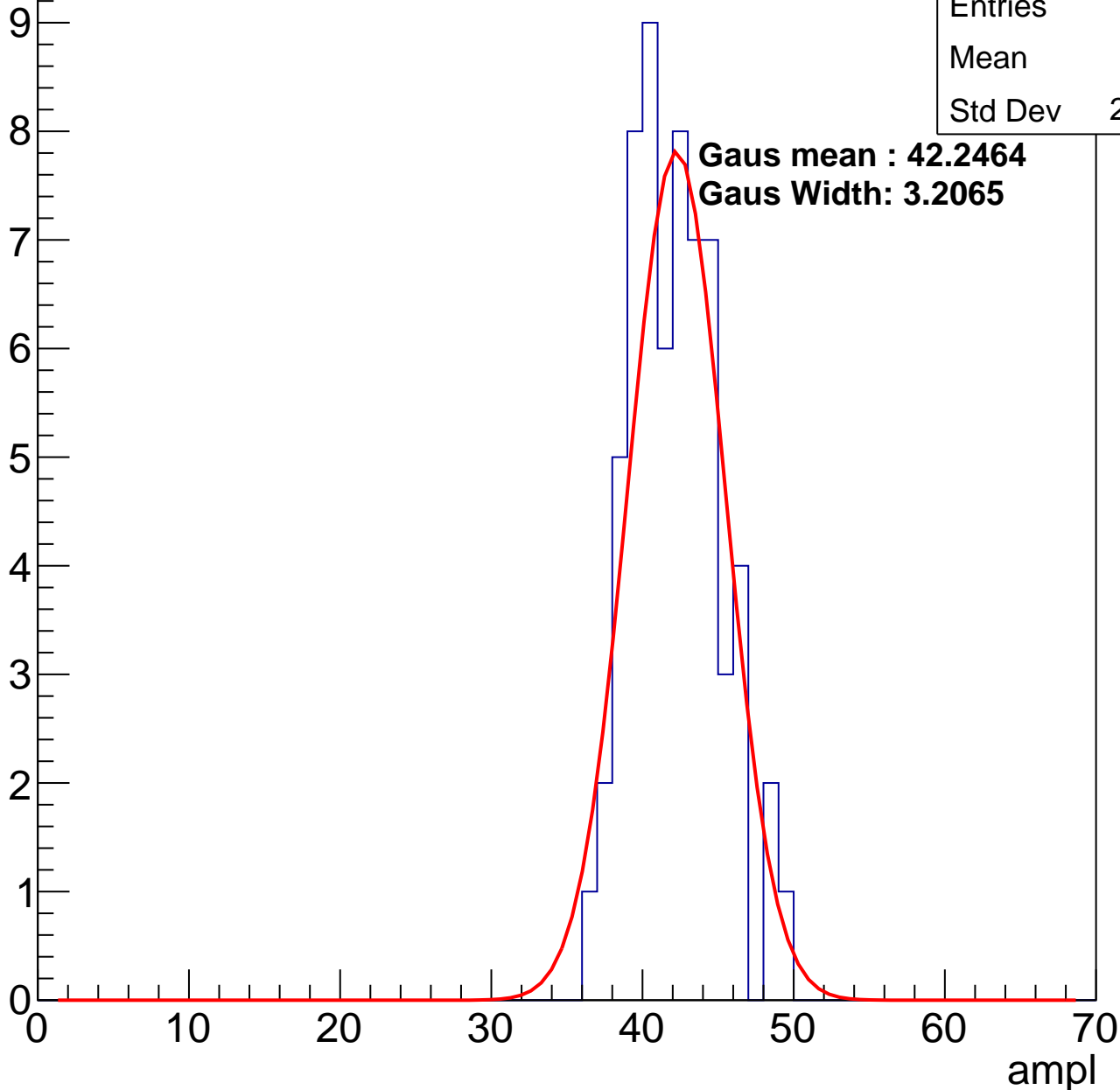
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.7
Std Dev	2.876

**Gaus mean : 42.2464**

**Gaus Width: 3.2065**

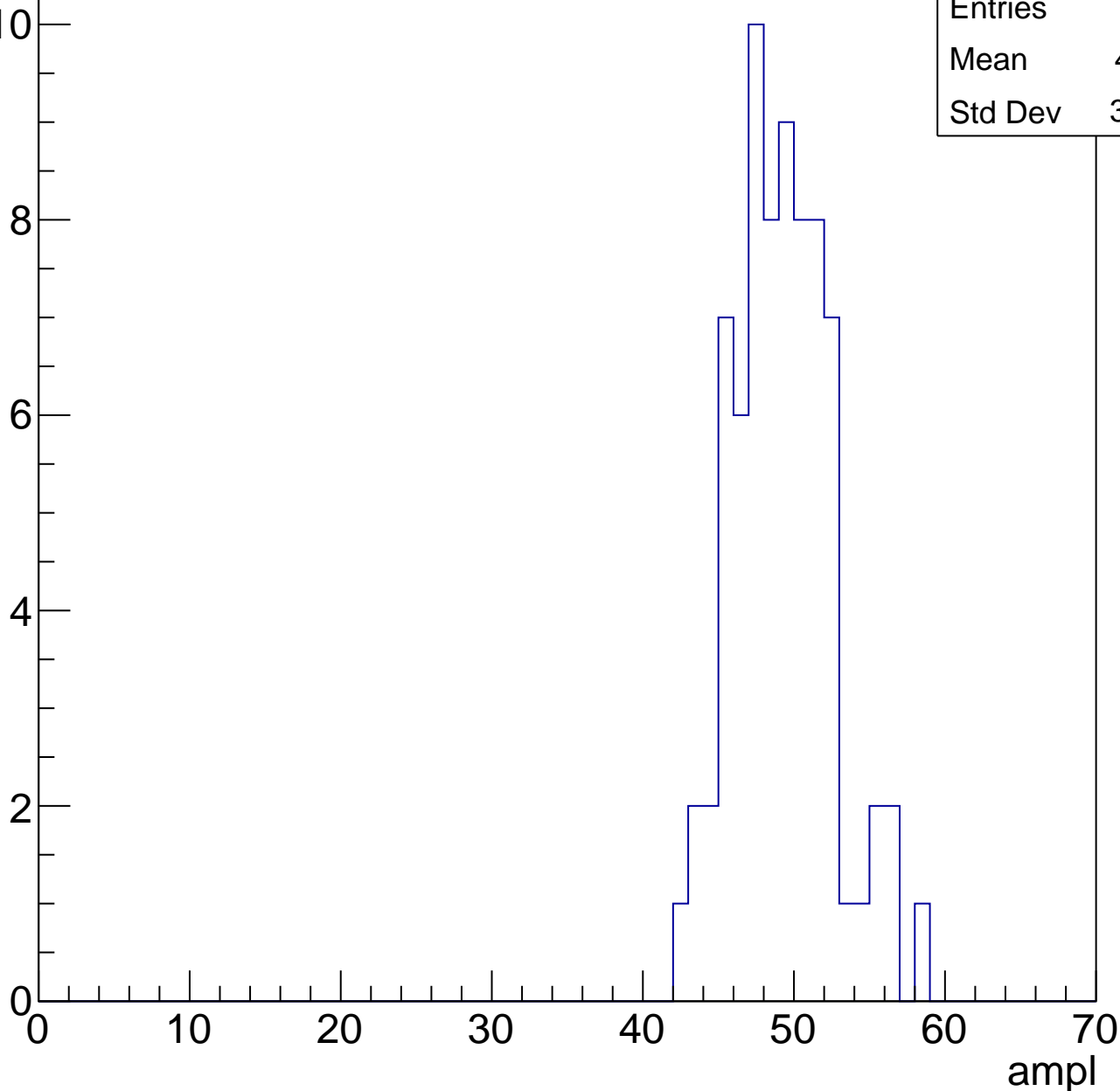


# B1L003S, U22-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	48.81
Std Dev	3.219

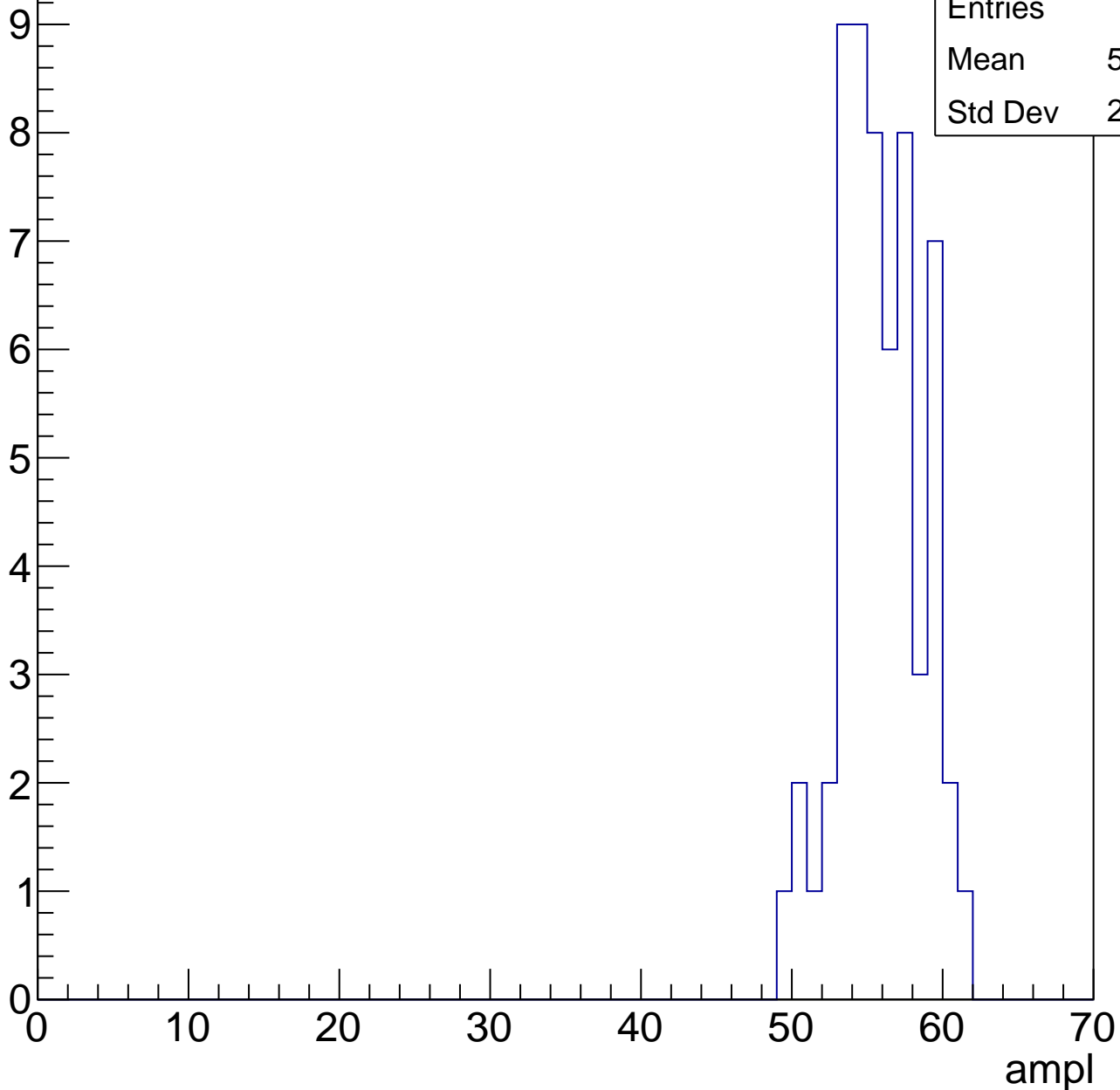


# B1L003S, U22-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

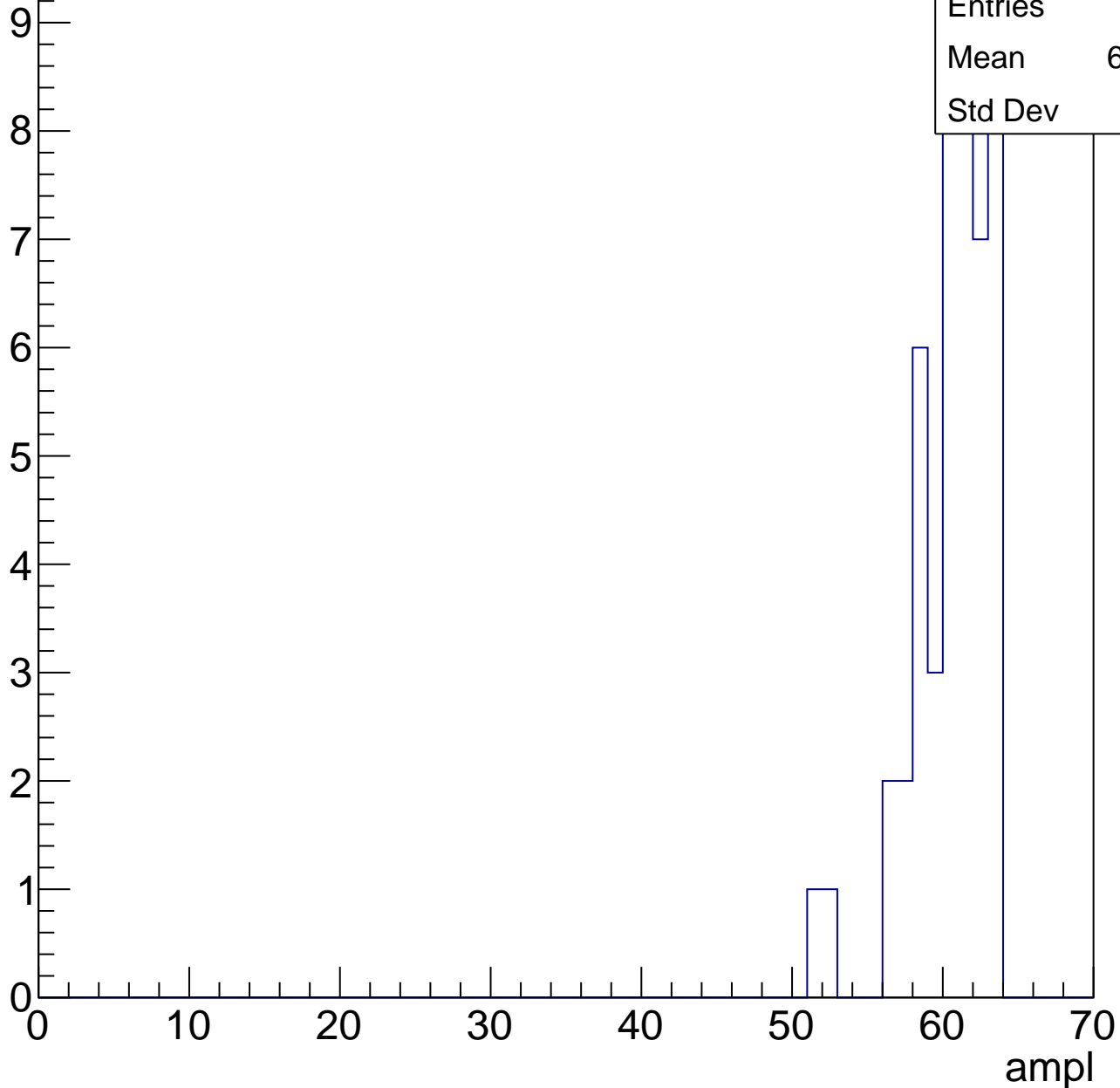
Entries	59
Mean	55.37
Std Dev	2.667



# B1L003S, U22-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

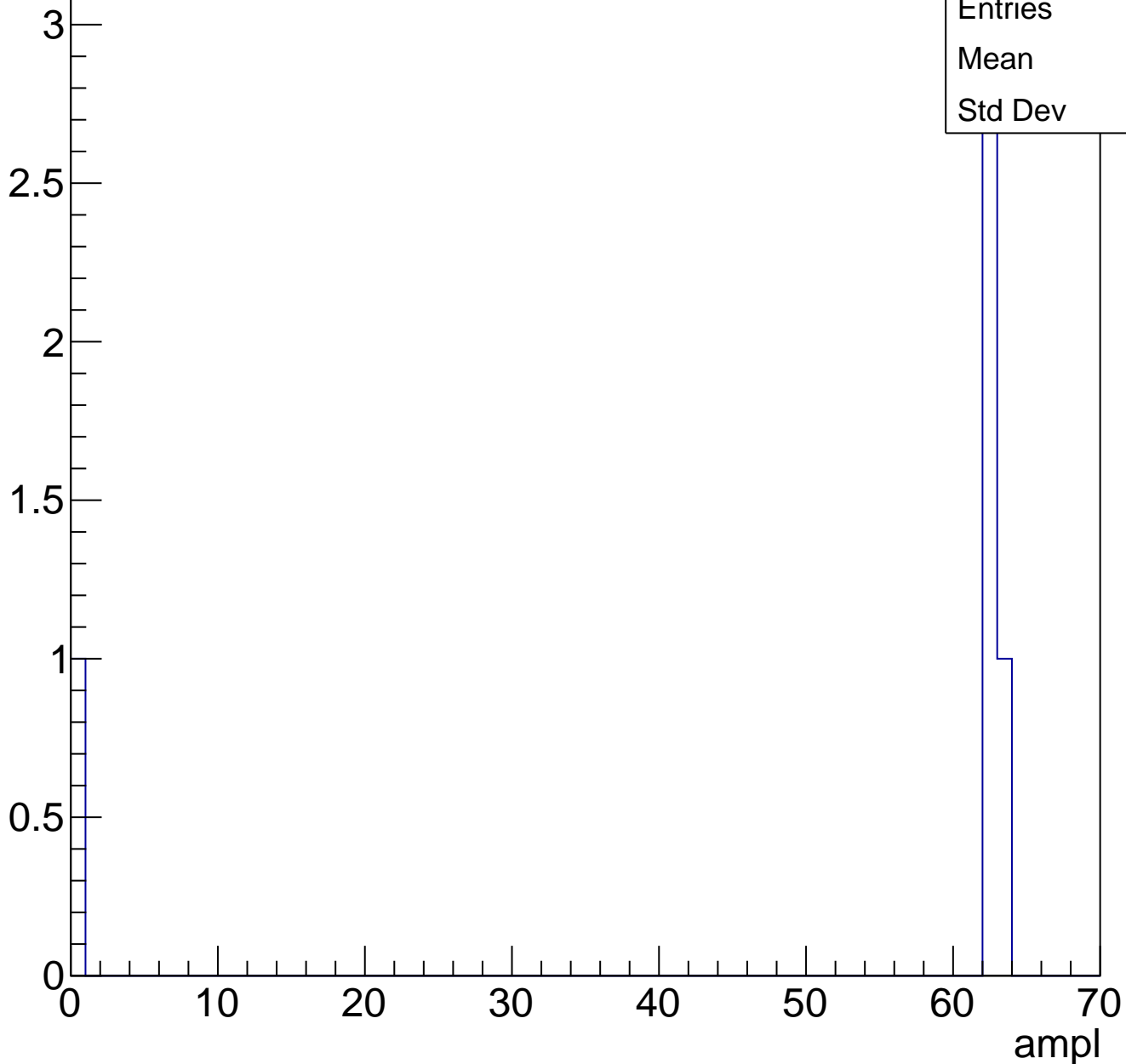
Entry



# B1L003S, U22-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	5
Mean	49.8
Std Dev	24.9



# B1L003S, U22-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch32, adc0

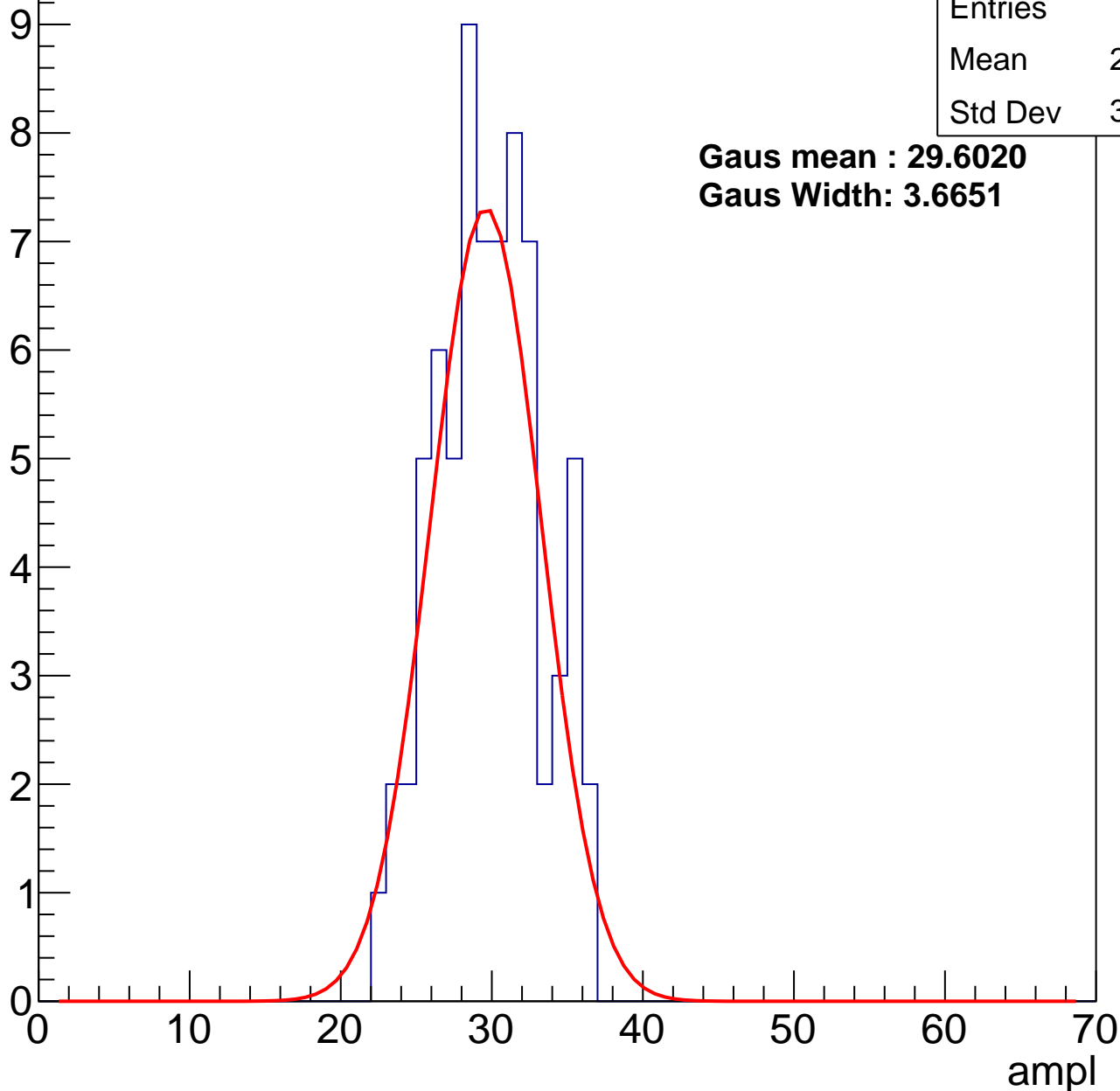
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29.35
Std Dev	3.382

**Gaus mean : 29.6020**

**Gaus Width: 3.6651**



# B1L003S, U22-ch32, adc1

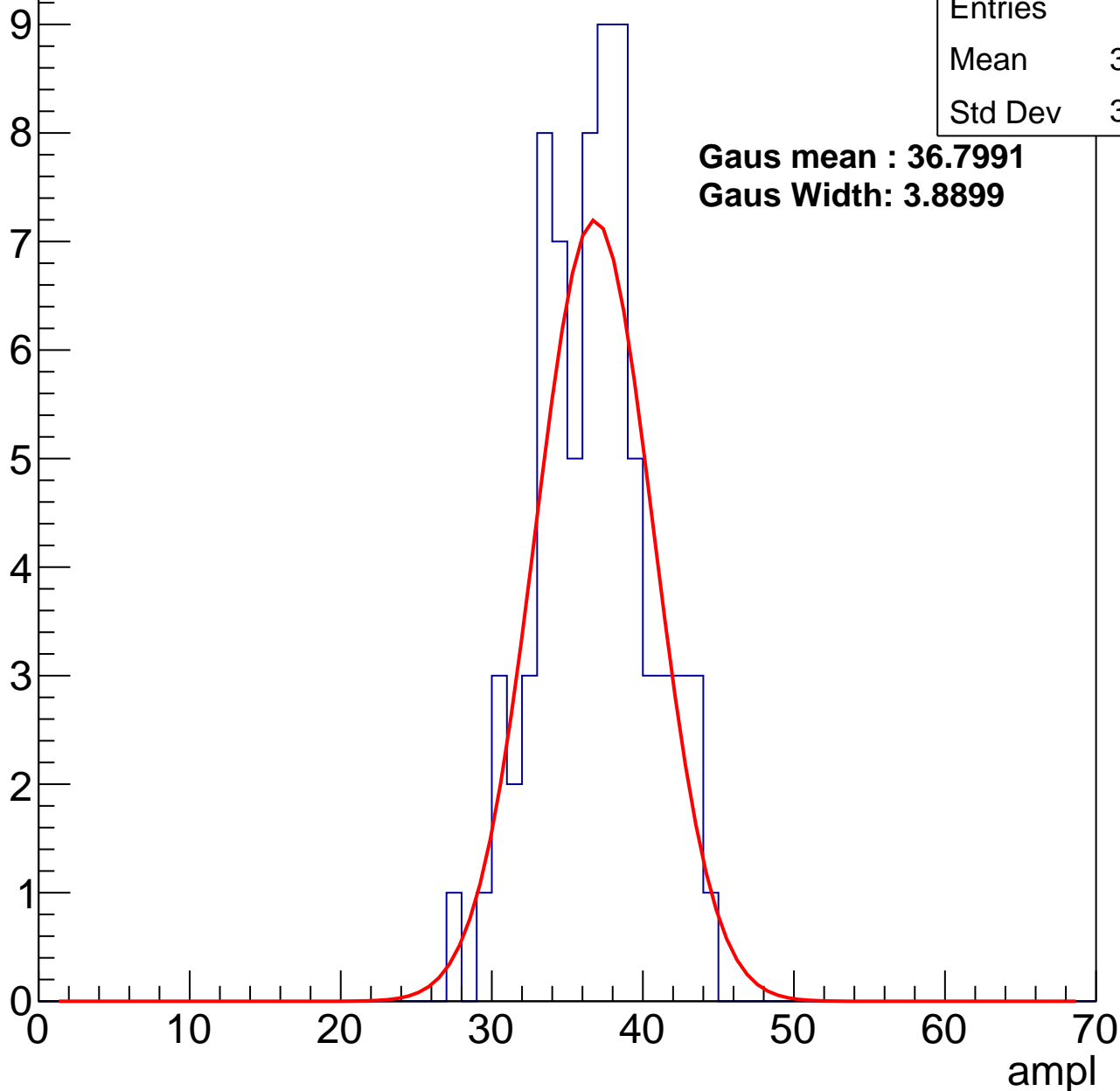
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	36.23
Std Dev	3.619

**Gaus mean : 36.7991**

**Gaus Width: 3.8899**



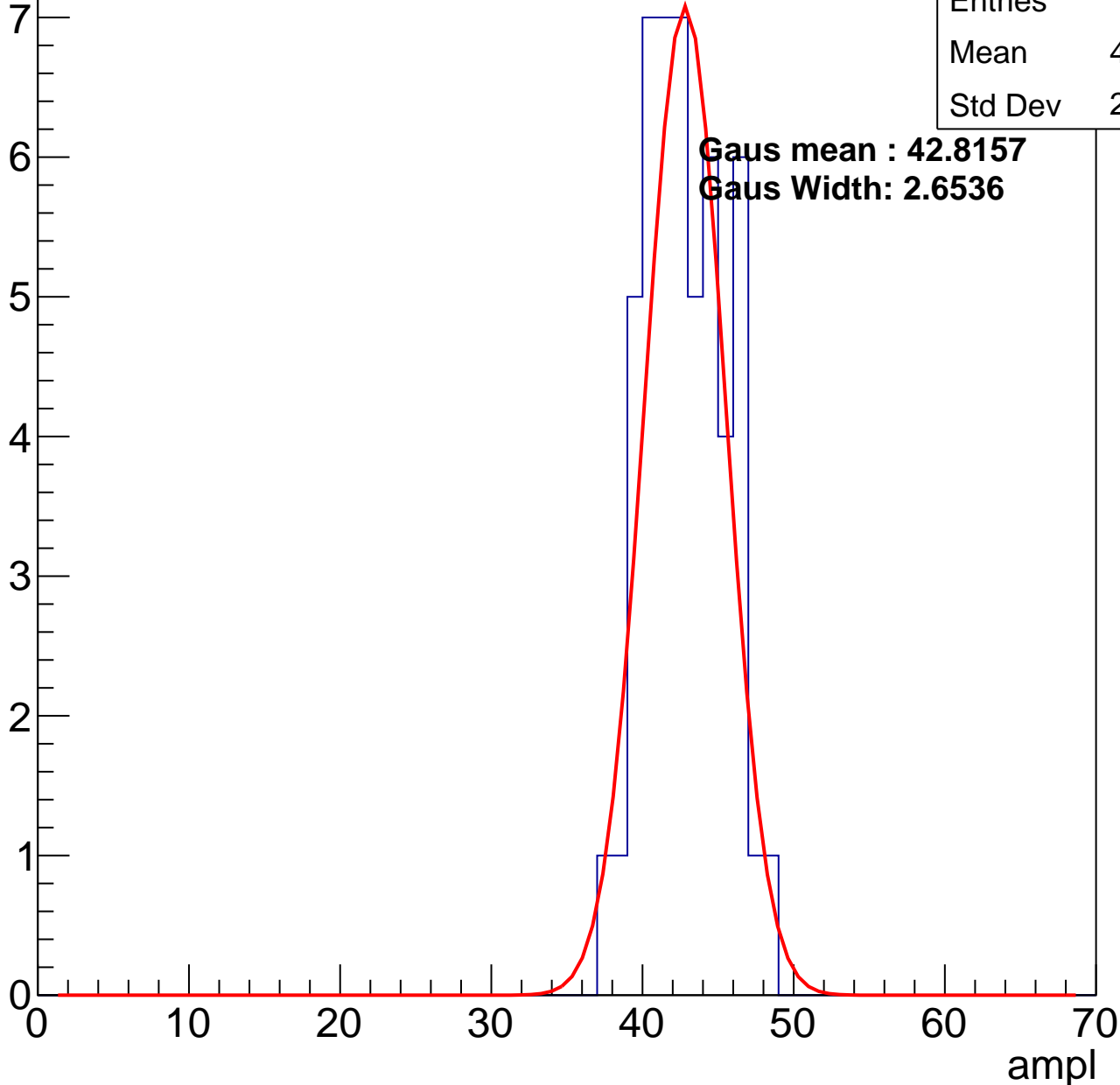
# B1L003S, U22-ch32, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	42.37
Std Dev	2.566

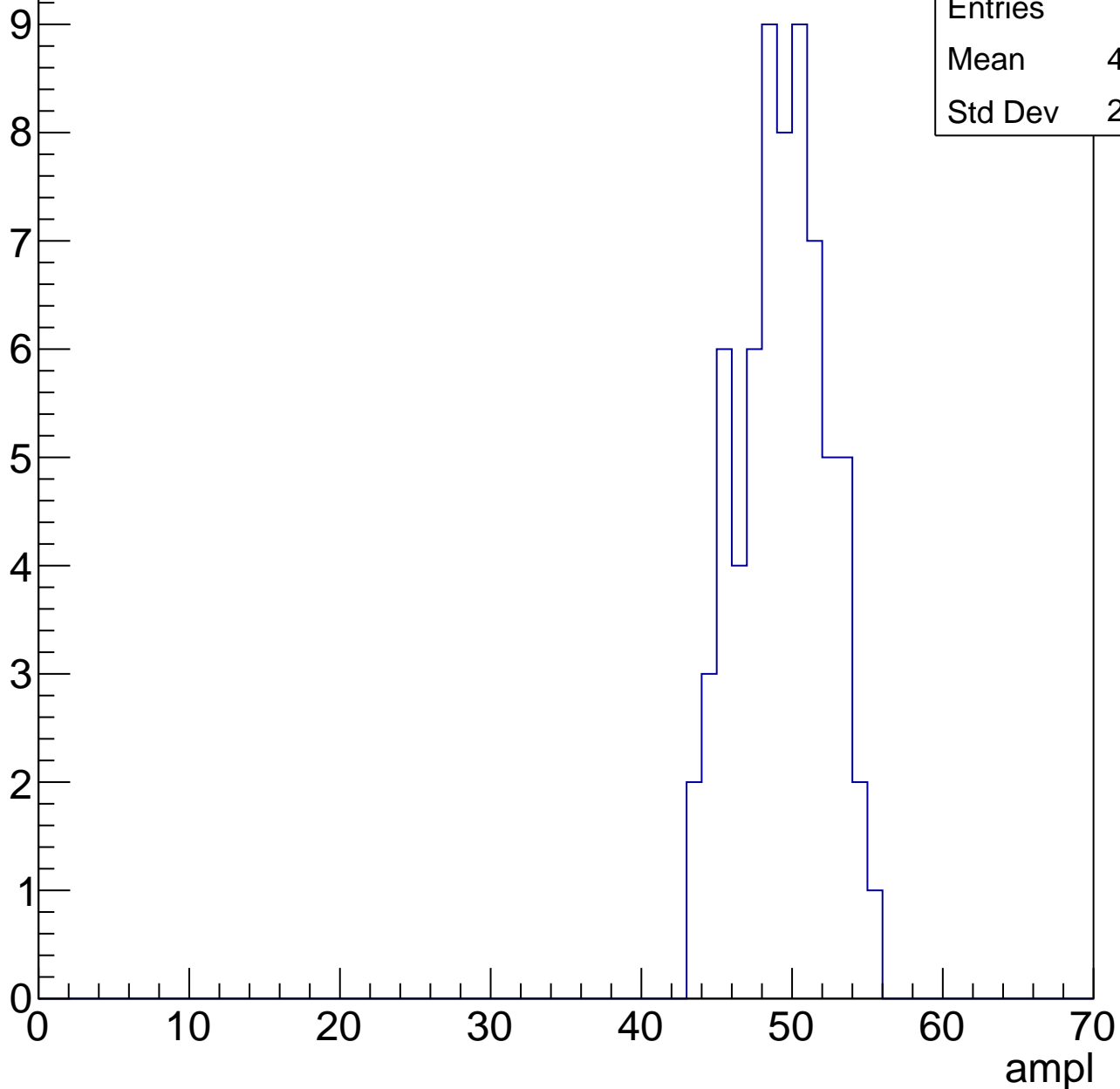
**Gaus mean : 42.8157**  
**Gaus Width: 2.6536**



# B1L003S, U22-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

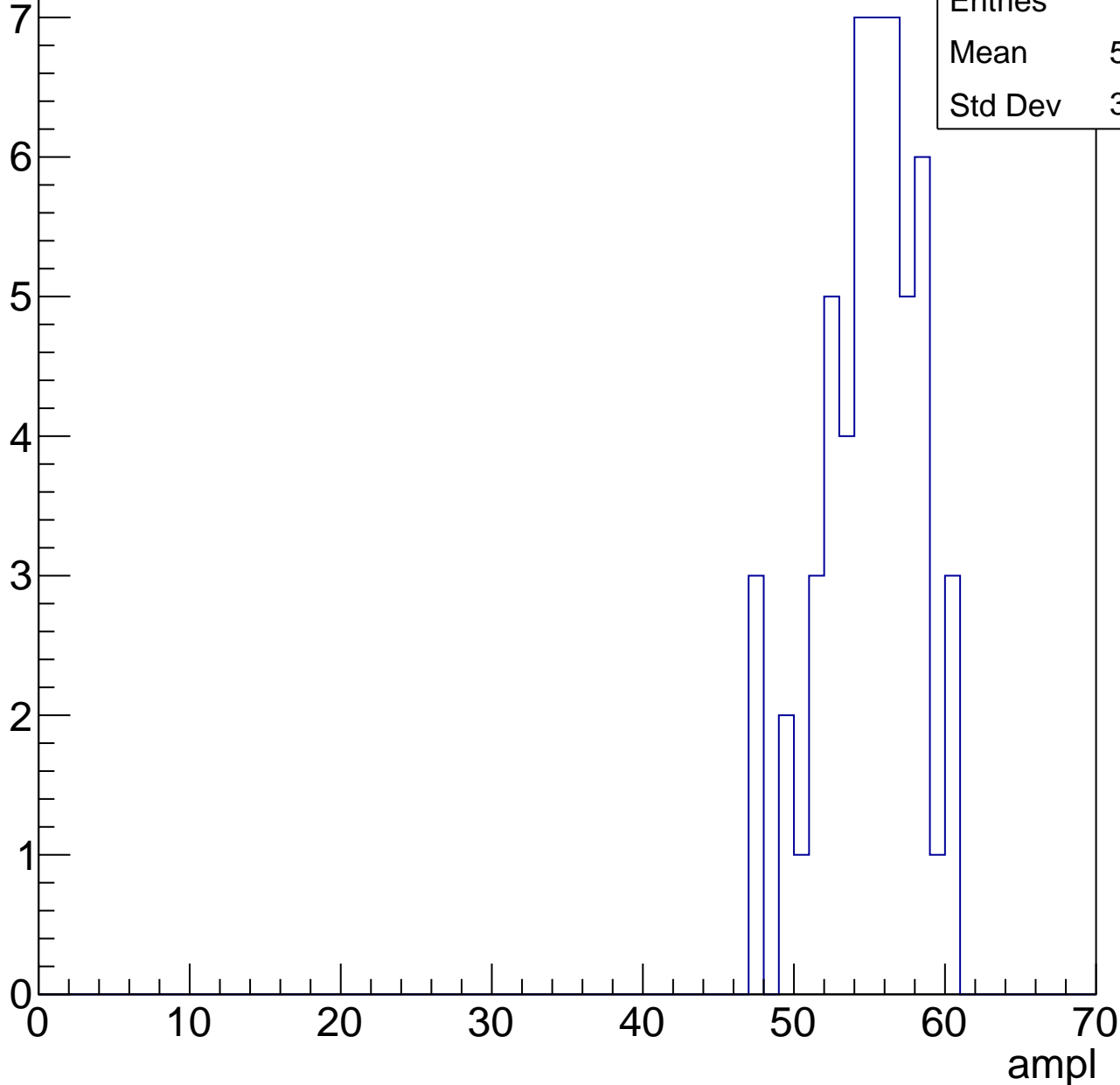
Entry



# B1L003S, U22-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

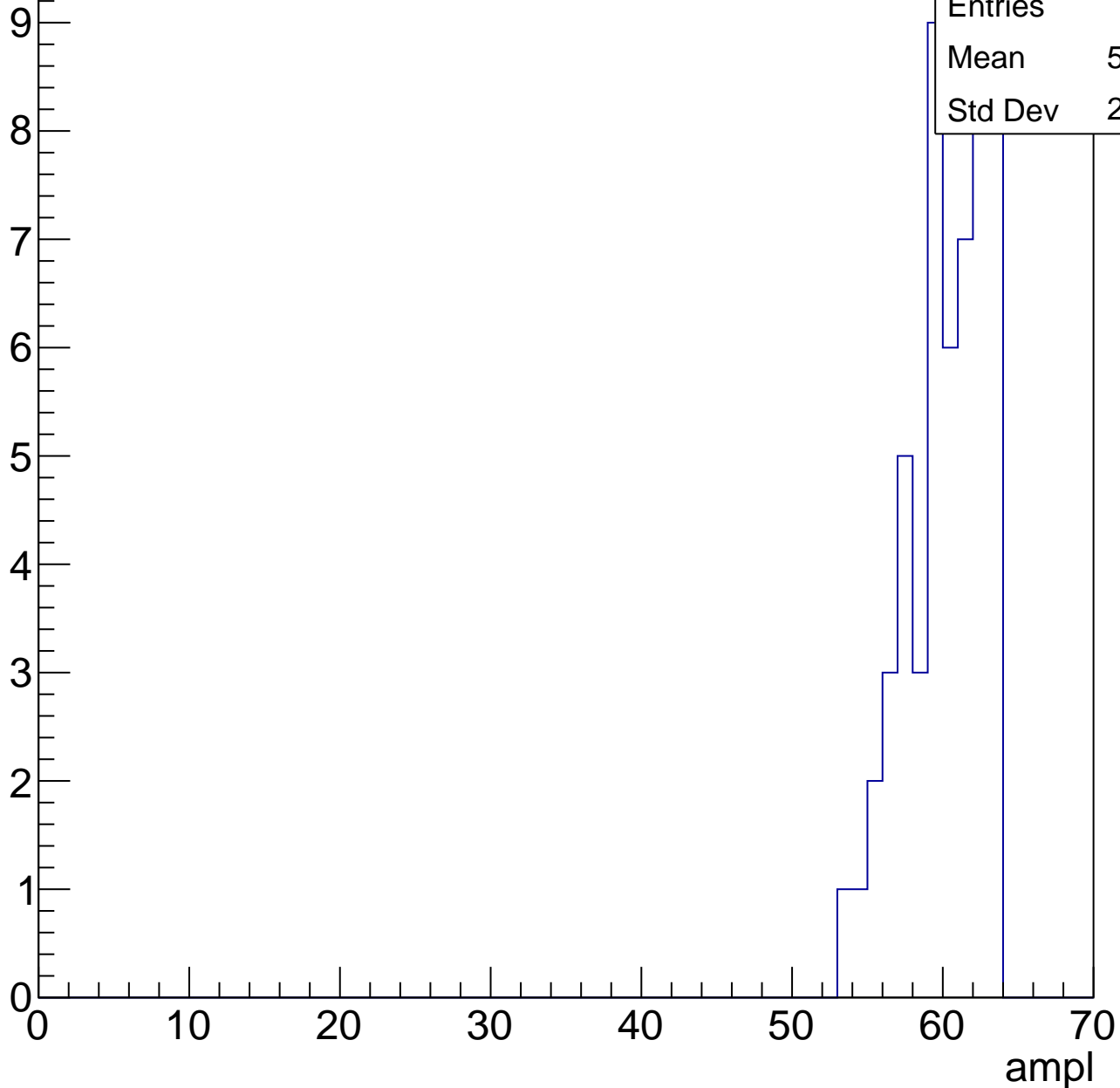


Entries	54
Mean	54.46
Std Dev	3.224

# B1L003S, U22-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

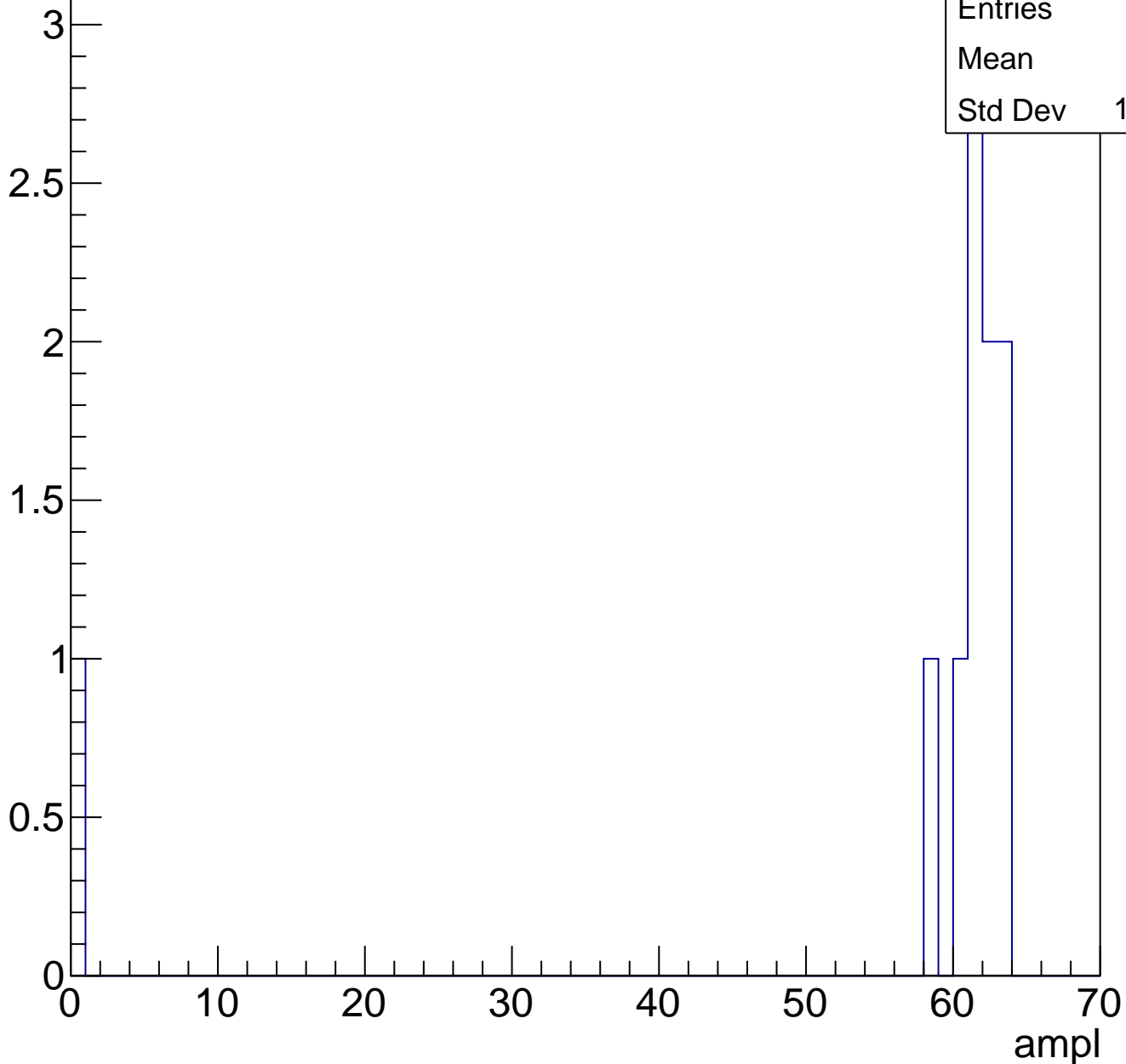
Entry



# B1L003S, U22-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	30.96
Std Dev	3.16

**Gaus mean : 31.3483**

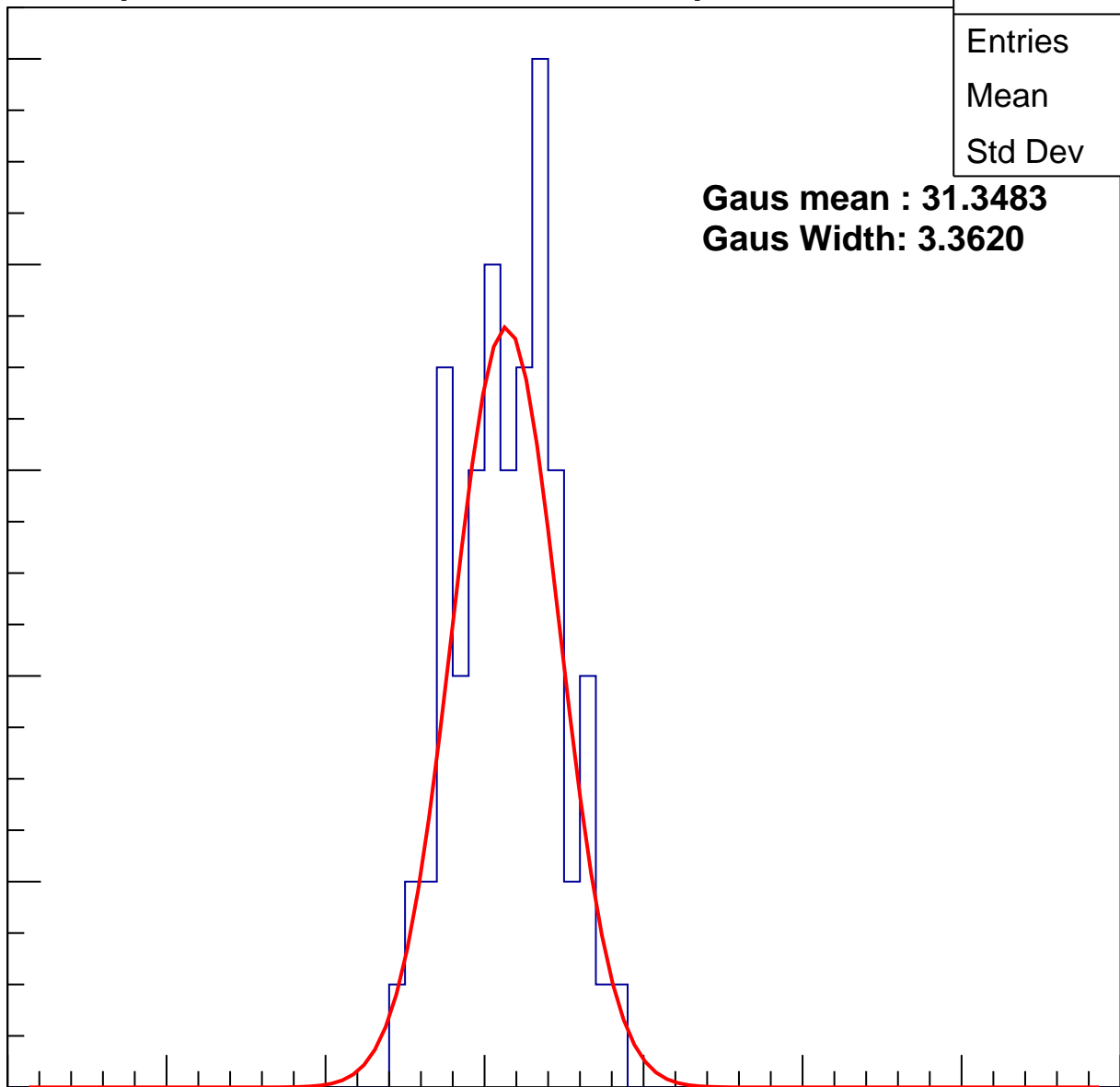
**Gaus Width: 3.3620**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch33, adc1

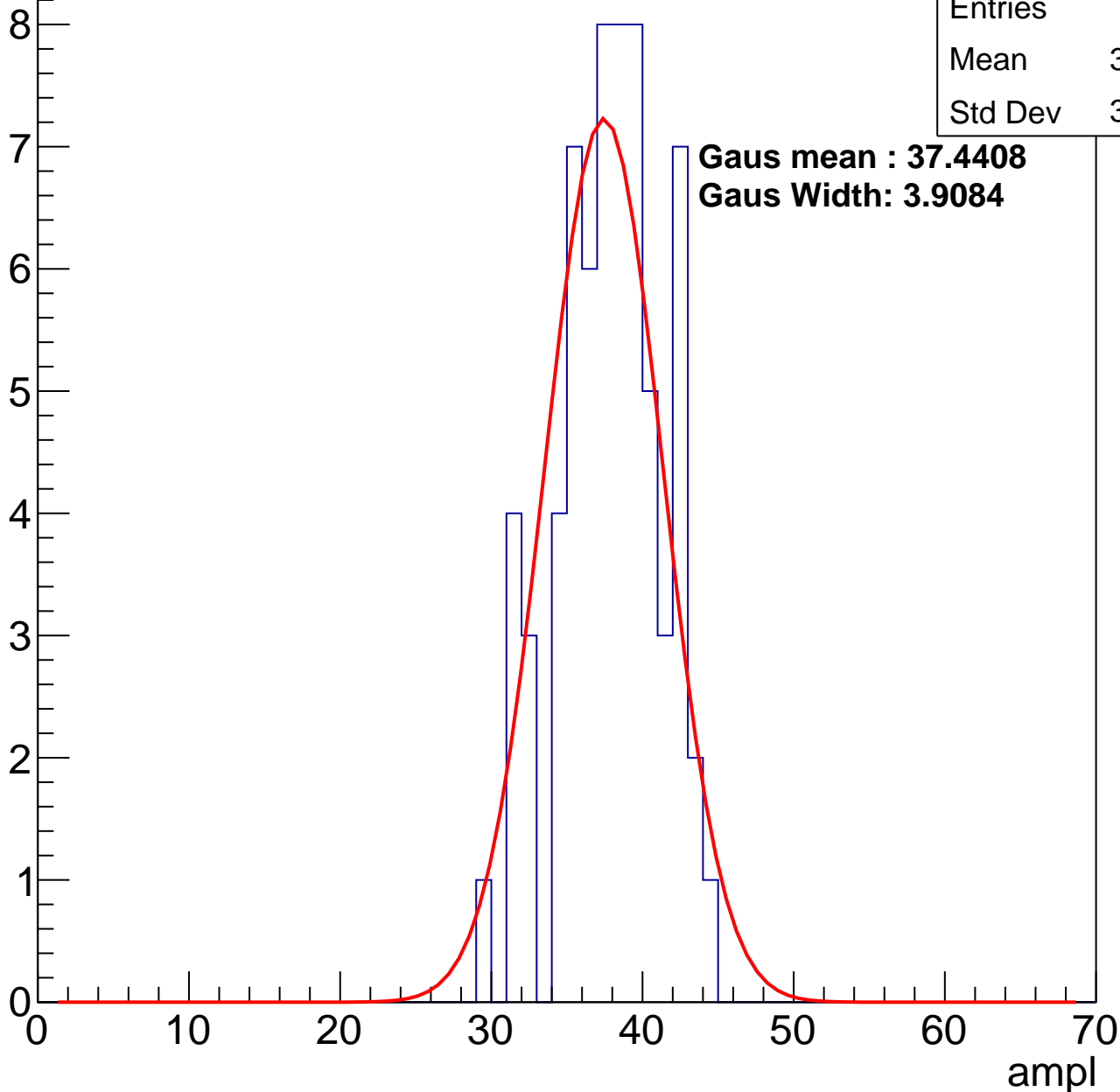
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.39
Std Dev	3.394

**Gaus mean : 37.4408**

**Gaus Width: 3.9084**



# B1L003S, U22-ch33, adc2

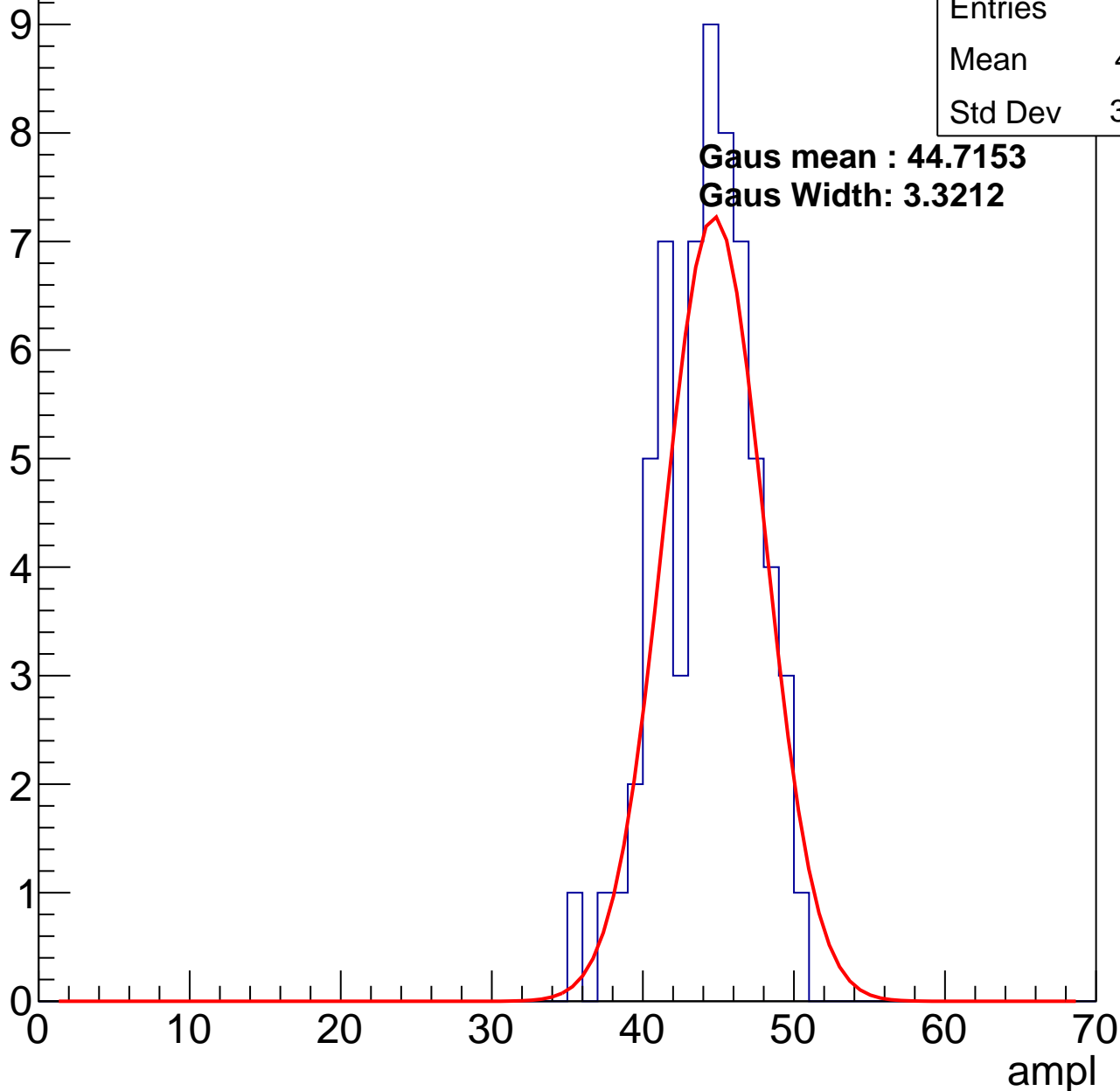
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.81
Std Dev	3.142

**Gaus mean : 44.7153**

**Gaus Width: 3.3212**

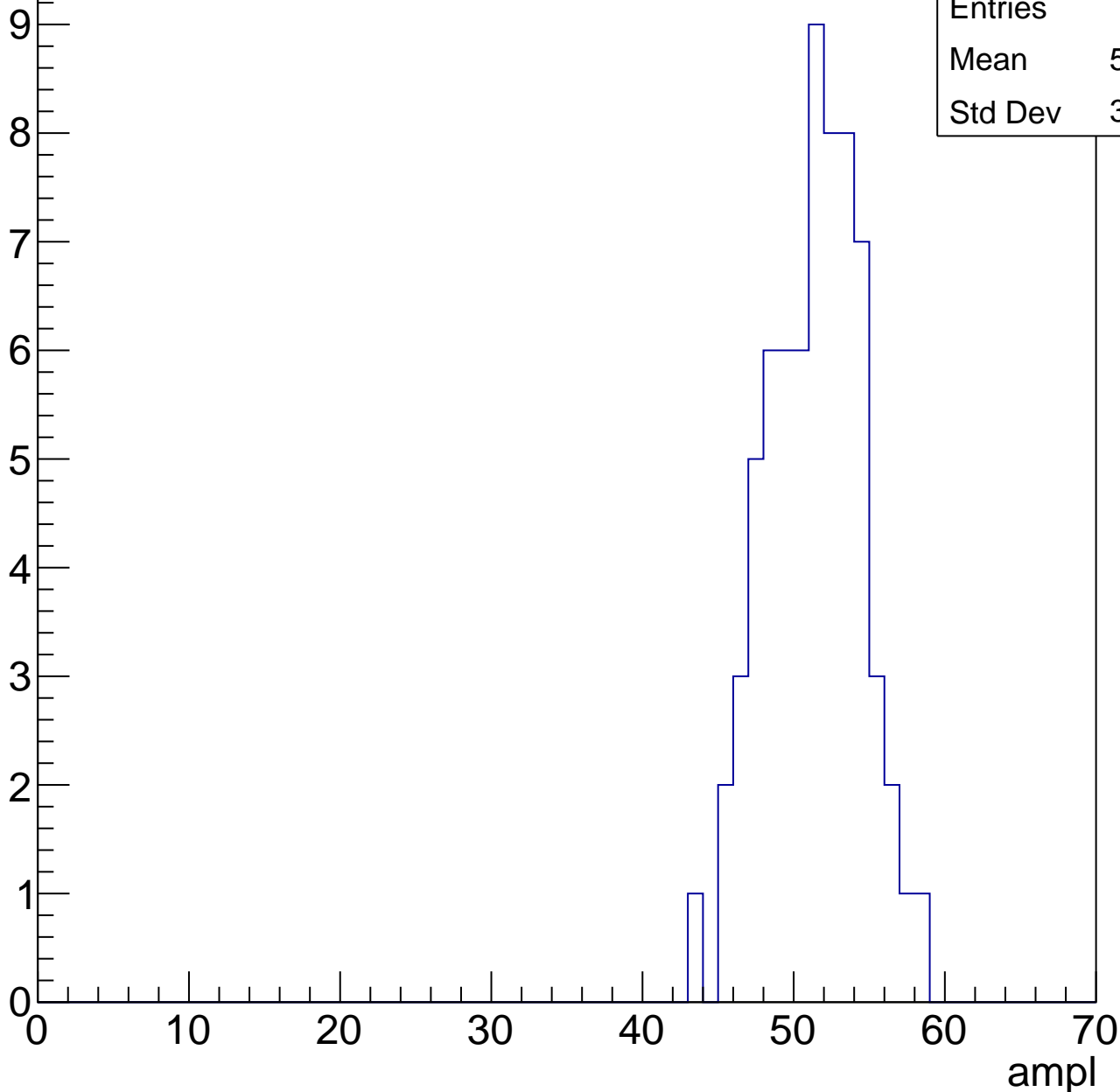


# B1L003S, U22-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.84
Std Dev	3.114

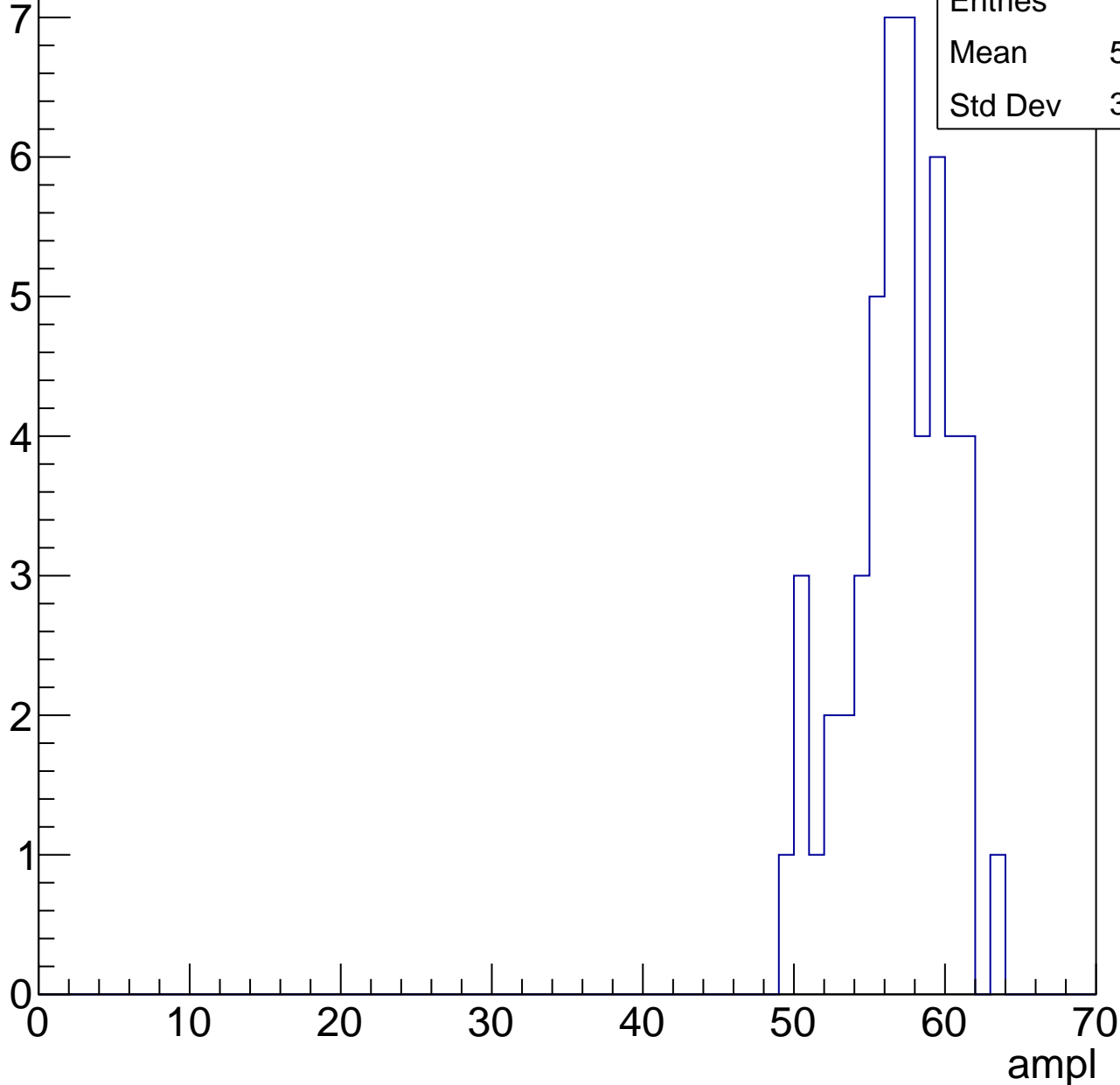


# B1L003S, U22-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	56.42
Std Dev	3.256

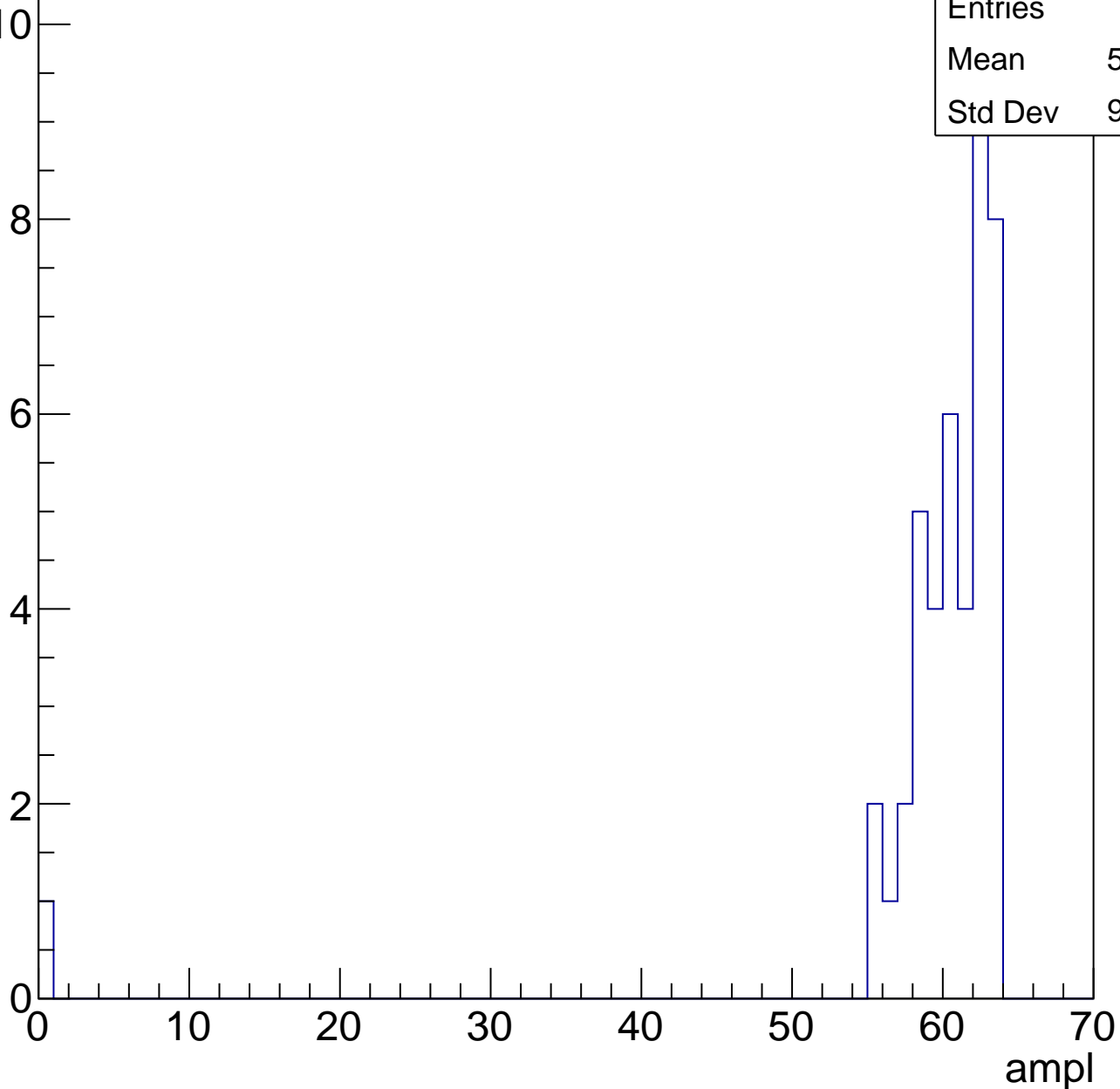


# B1L003S, U22-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	58.93
Std Dev	9.369



# B1L003S, U22-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.9428

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch34, adc0

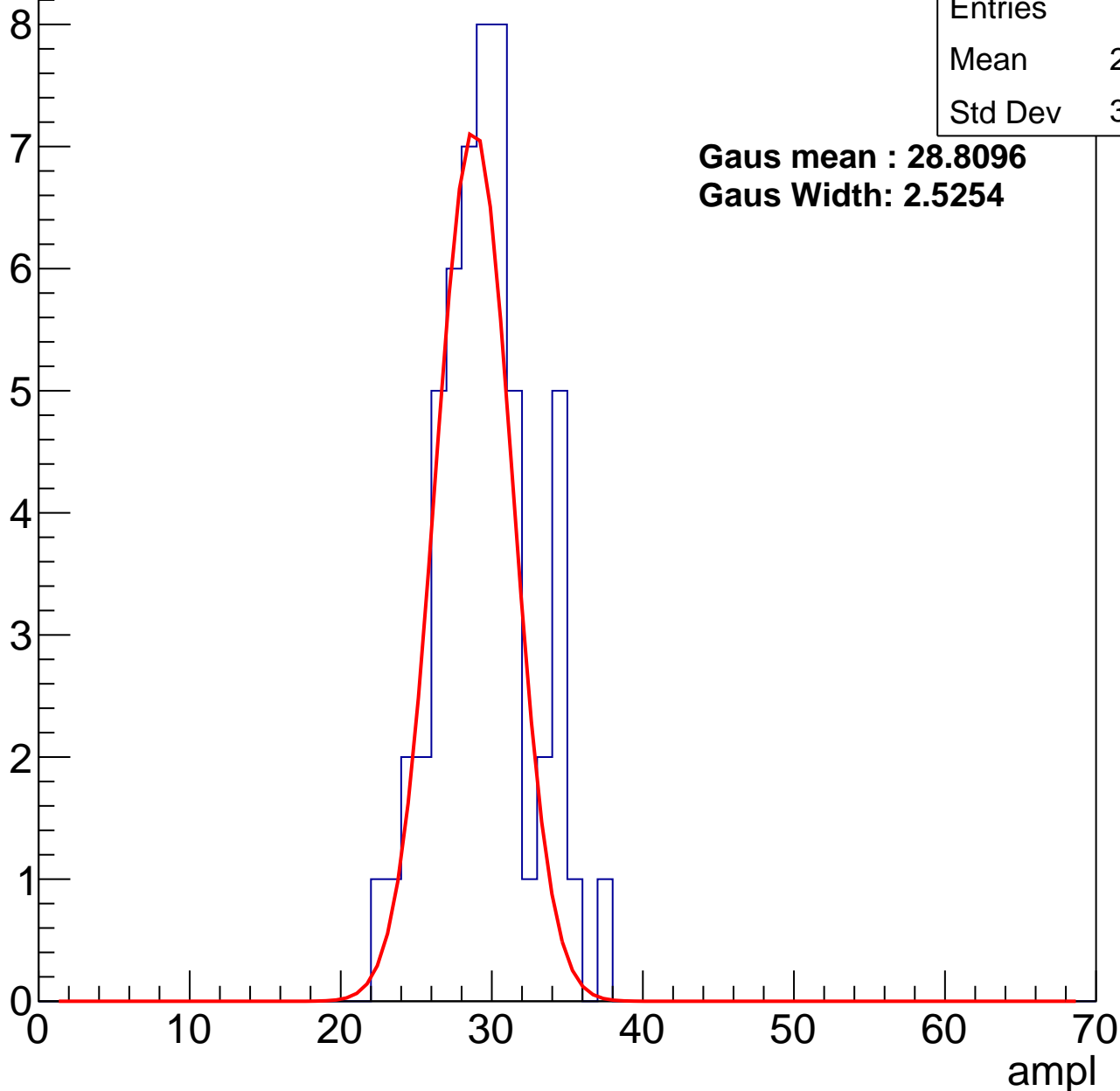
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.05
Std Dev	3.124

**Gaus mean : 28.8096**

**Gaus Width: 2.5254**

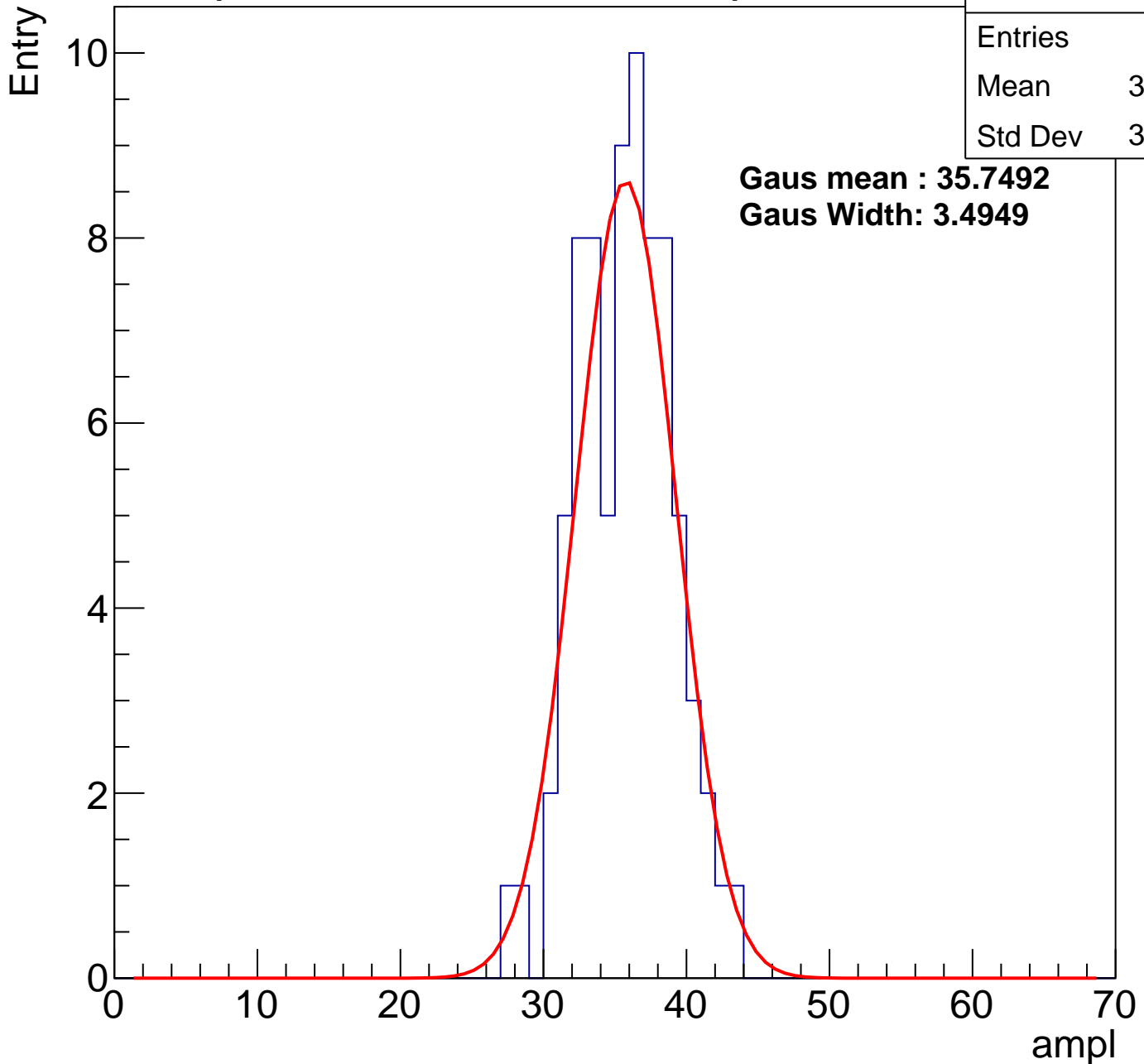


# B1L003S, U22-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	77
Mean	35.29
Std Dev	3.223

**Gaus mean : 35.7492**  
**Gaus Width: 3.4949**



# B1L003S, U22-ch34, adc2

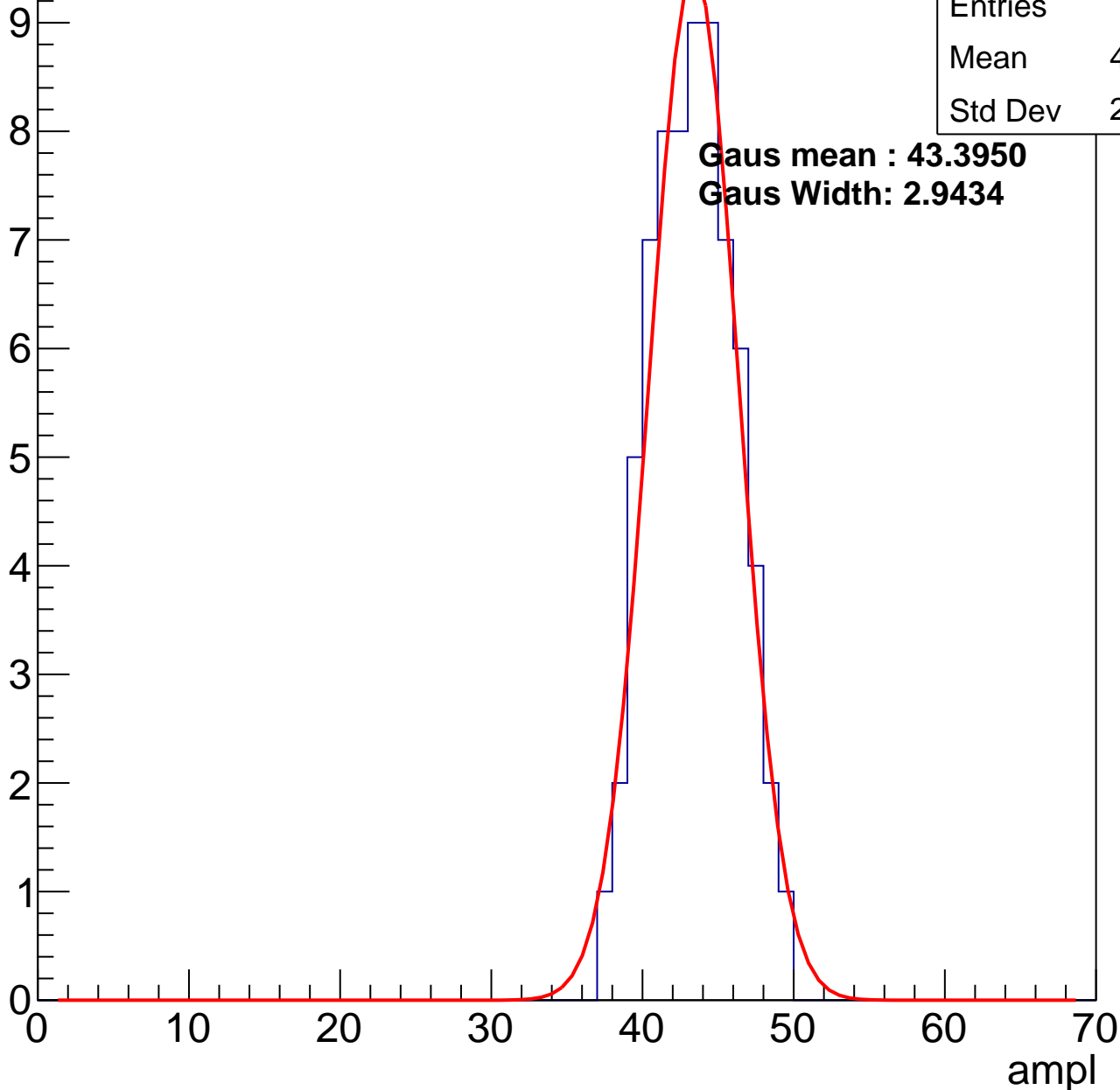
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.88
Std Dev	2.716

**Gaus mean : 43.3950**

**Gaus Width: 2.9434**

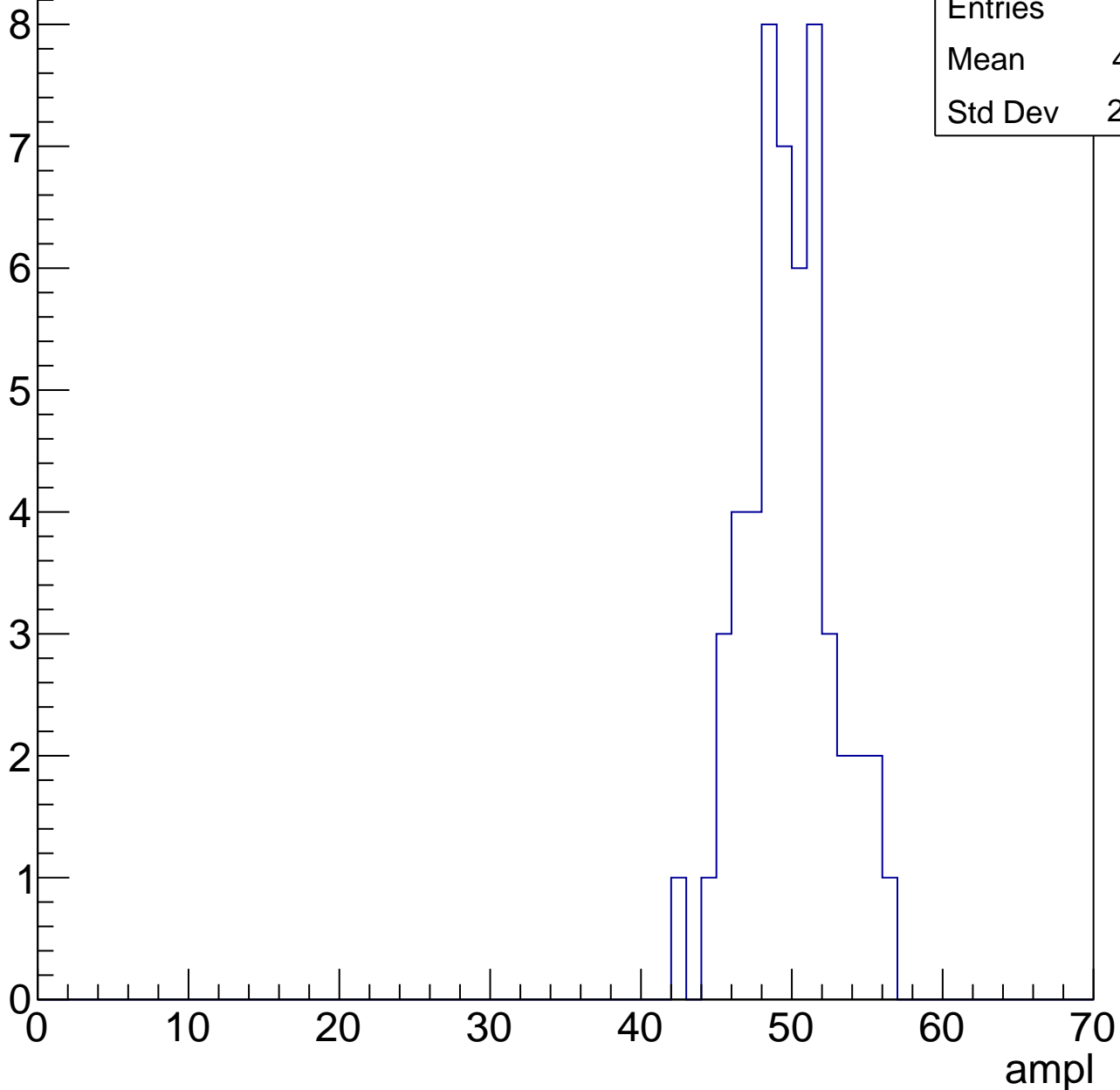


# B1L003S, U22-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	49.31
Std Dev	2.926

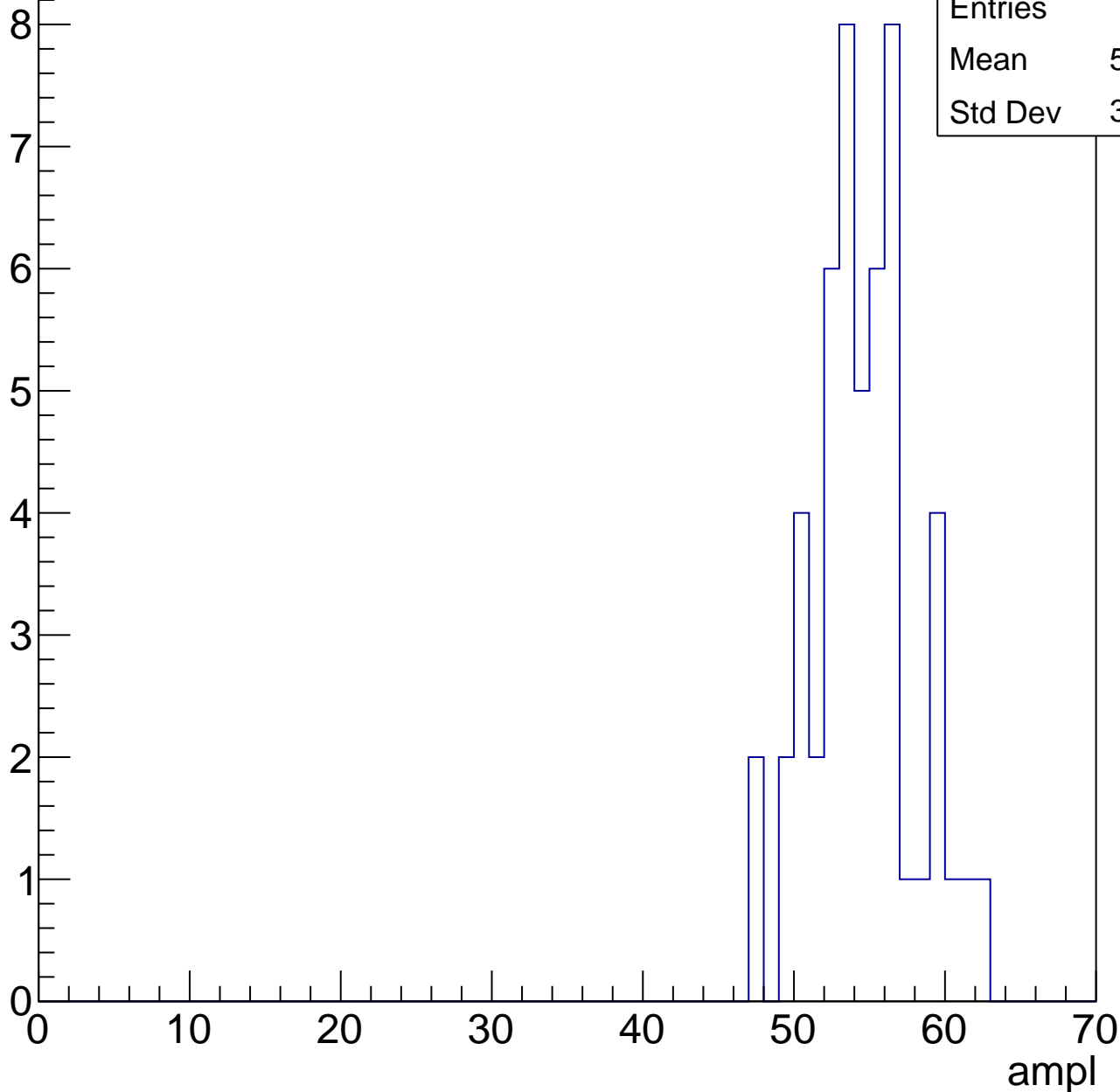


# B1L003S, U22-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

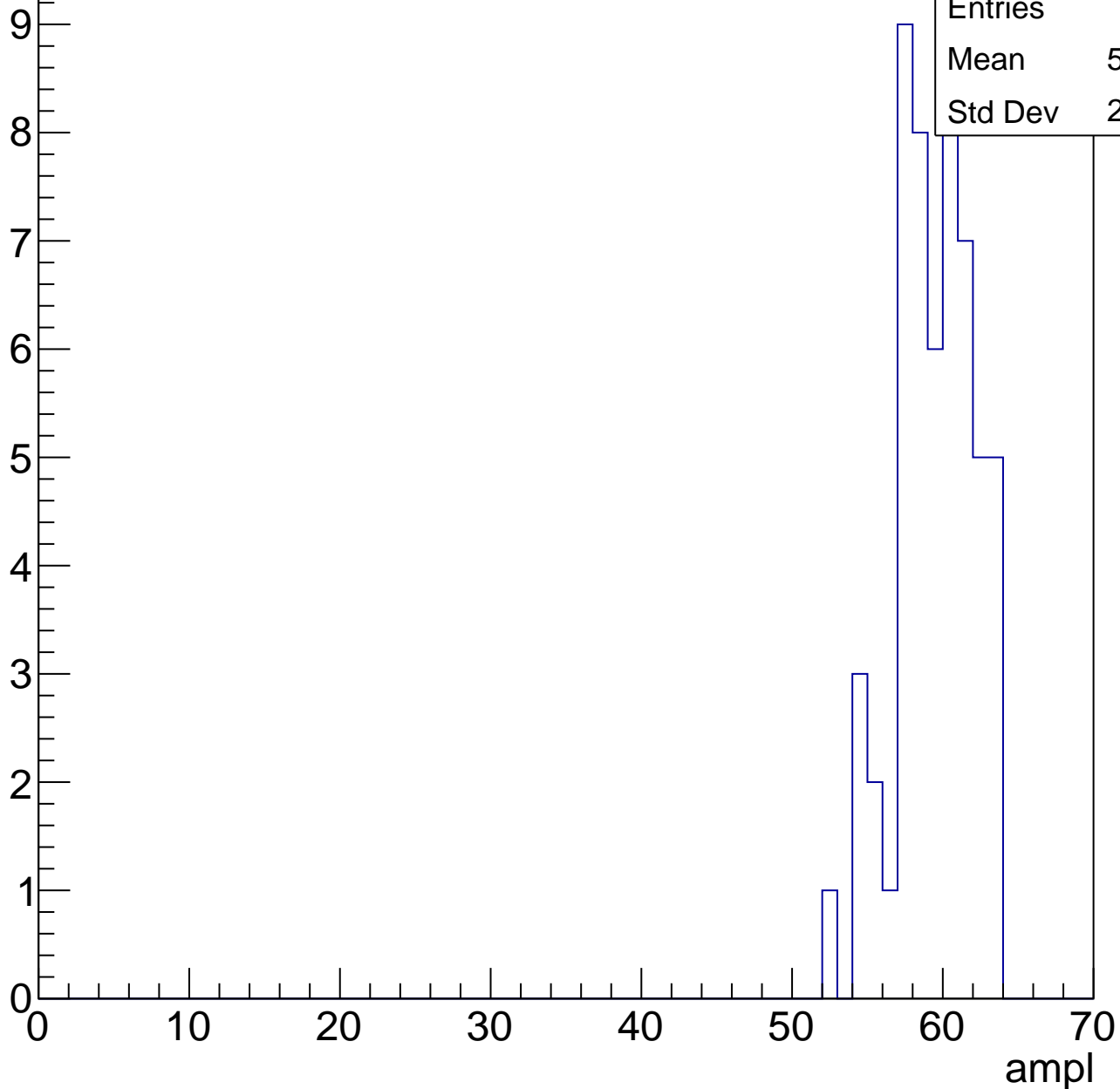
Entries	52
Mean	54.08
Std Dev	3.322



# B1L003S, U22-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

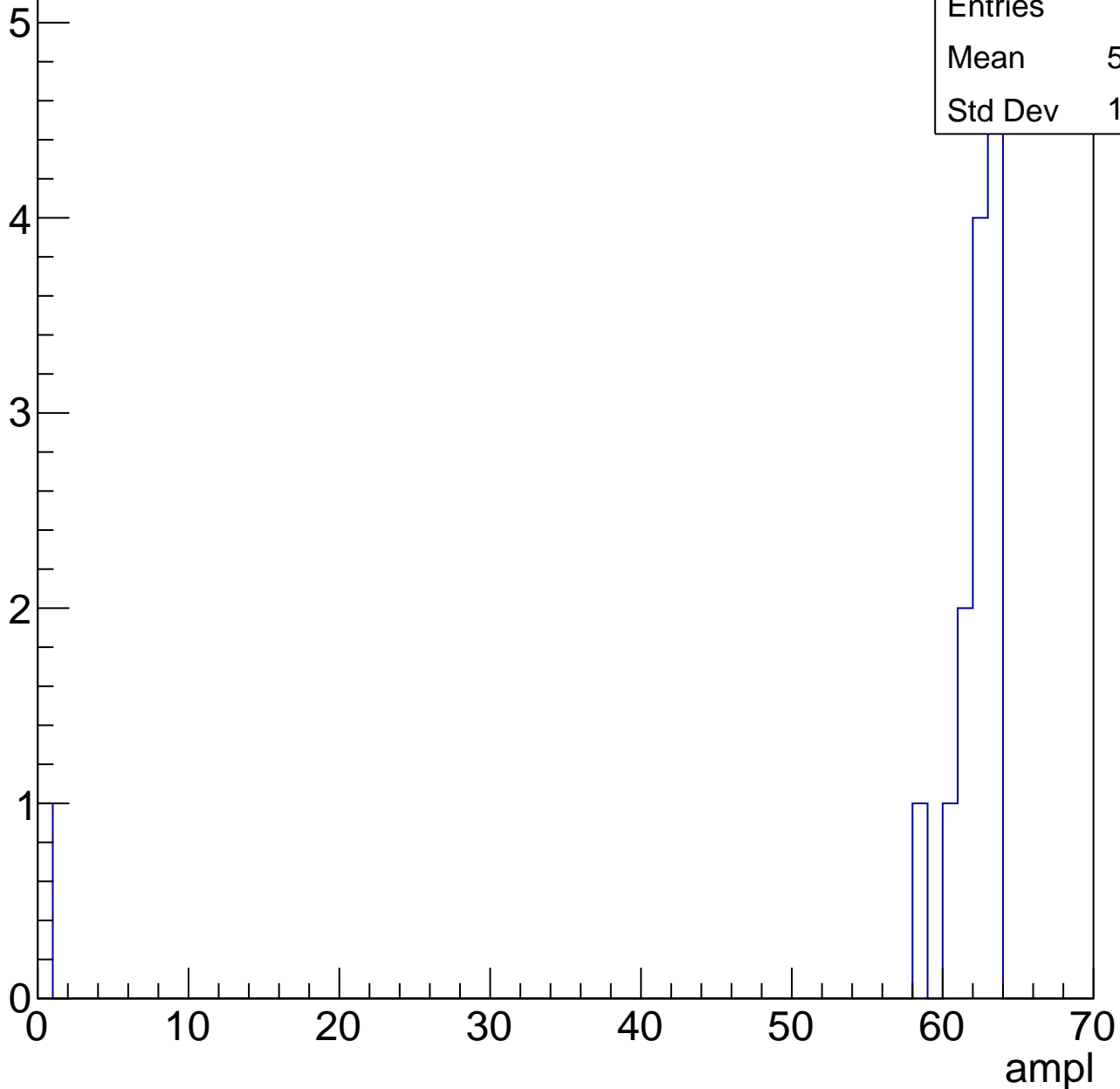


# B1L003S, U22-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	14
Mean	57.36
Std Dev	15.97





# B1L003S, U22-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch35, adc0

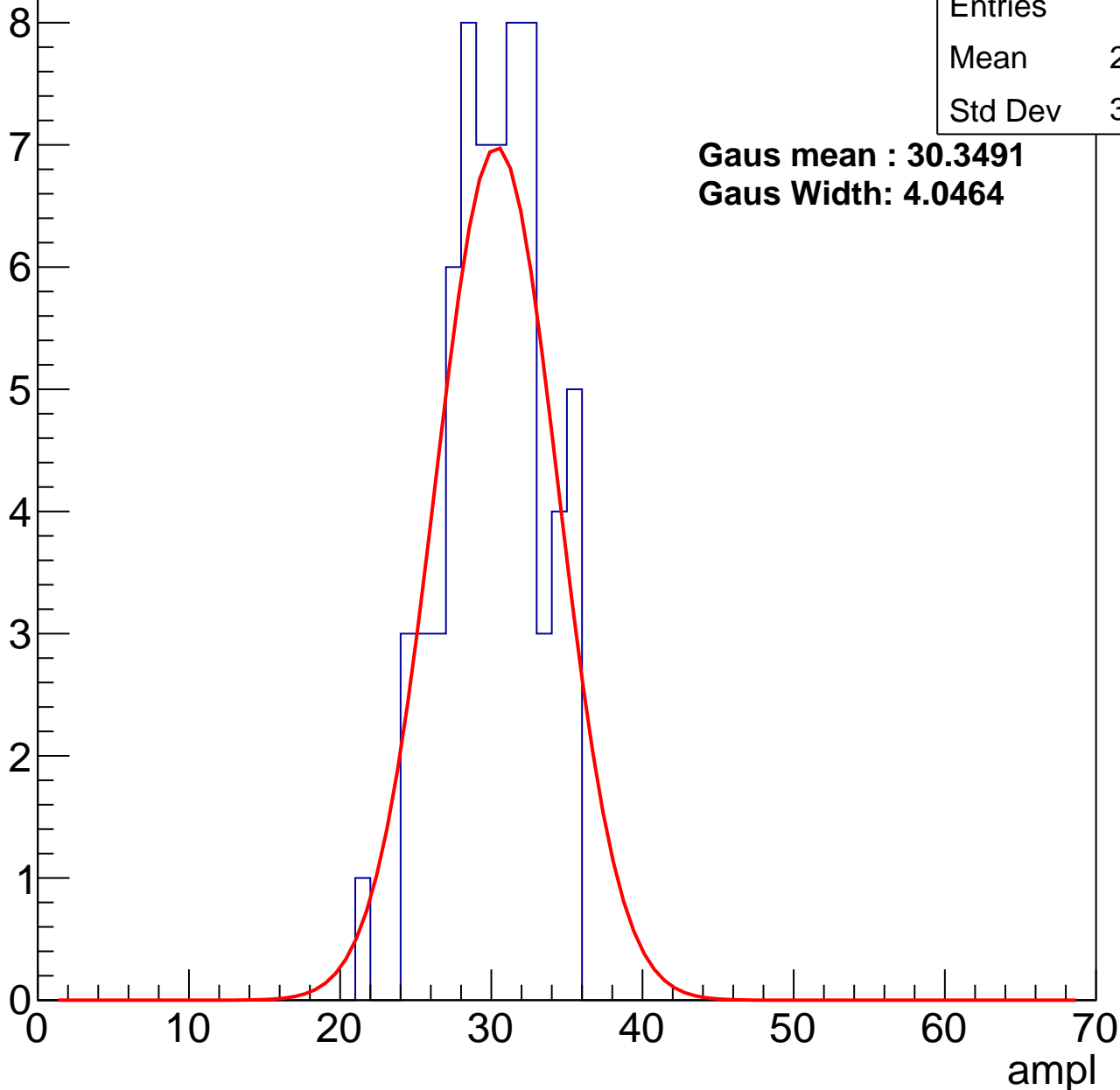
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.68
Std Dev	3.149

**Gaus mean : 30.3491**

**Gaus Width: 4.0464**



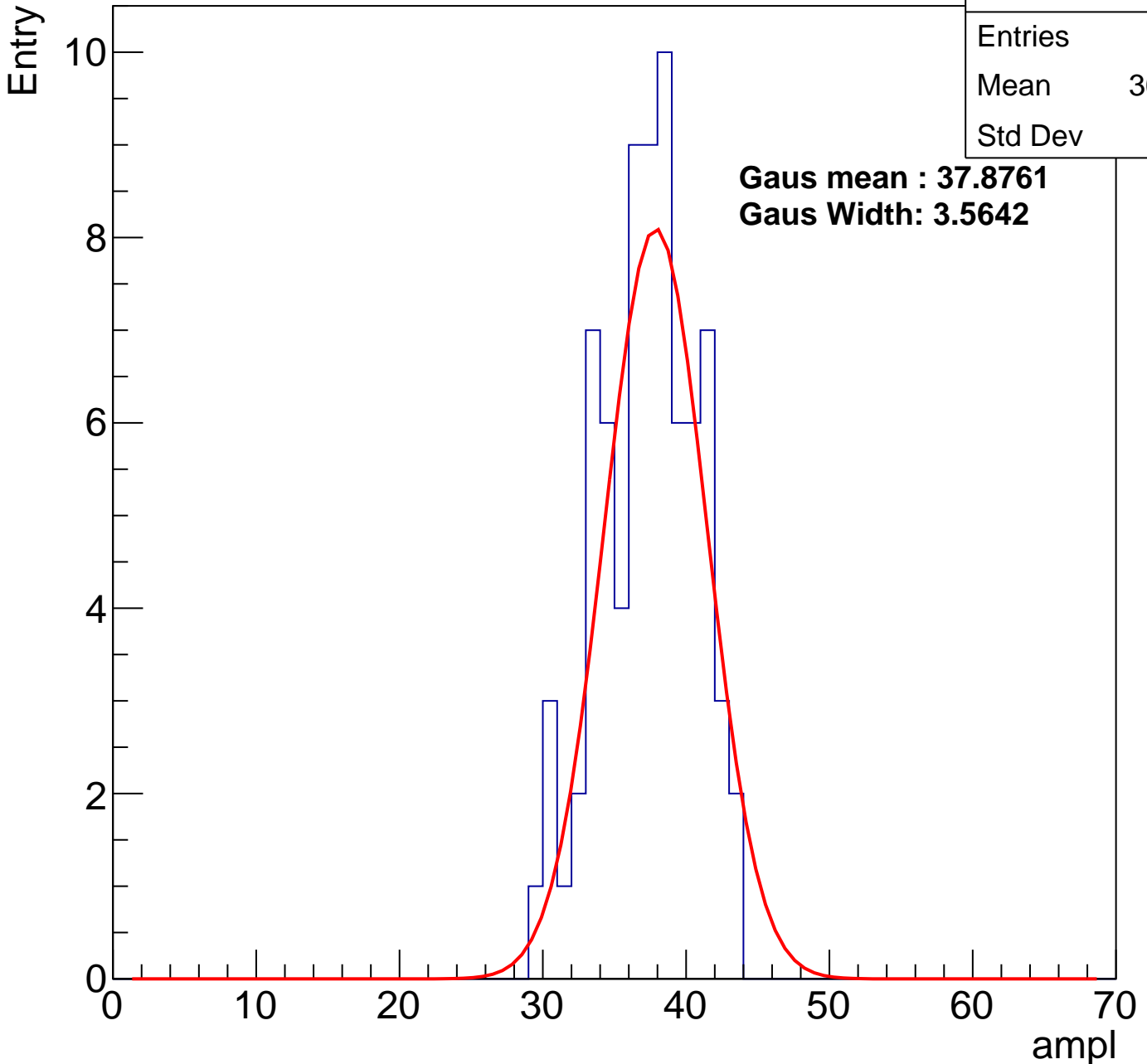
# B1L003S, U22-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	76
Mean	36.83
Std Dev	3.31

**Gaus mean : 37.8761**

**Gaus Width: 3.5642**



# B1L003S, U22-ch35, adc2

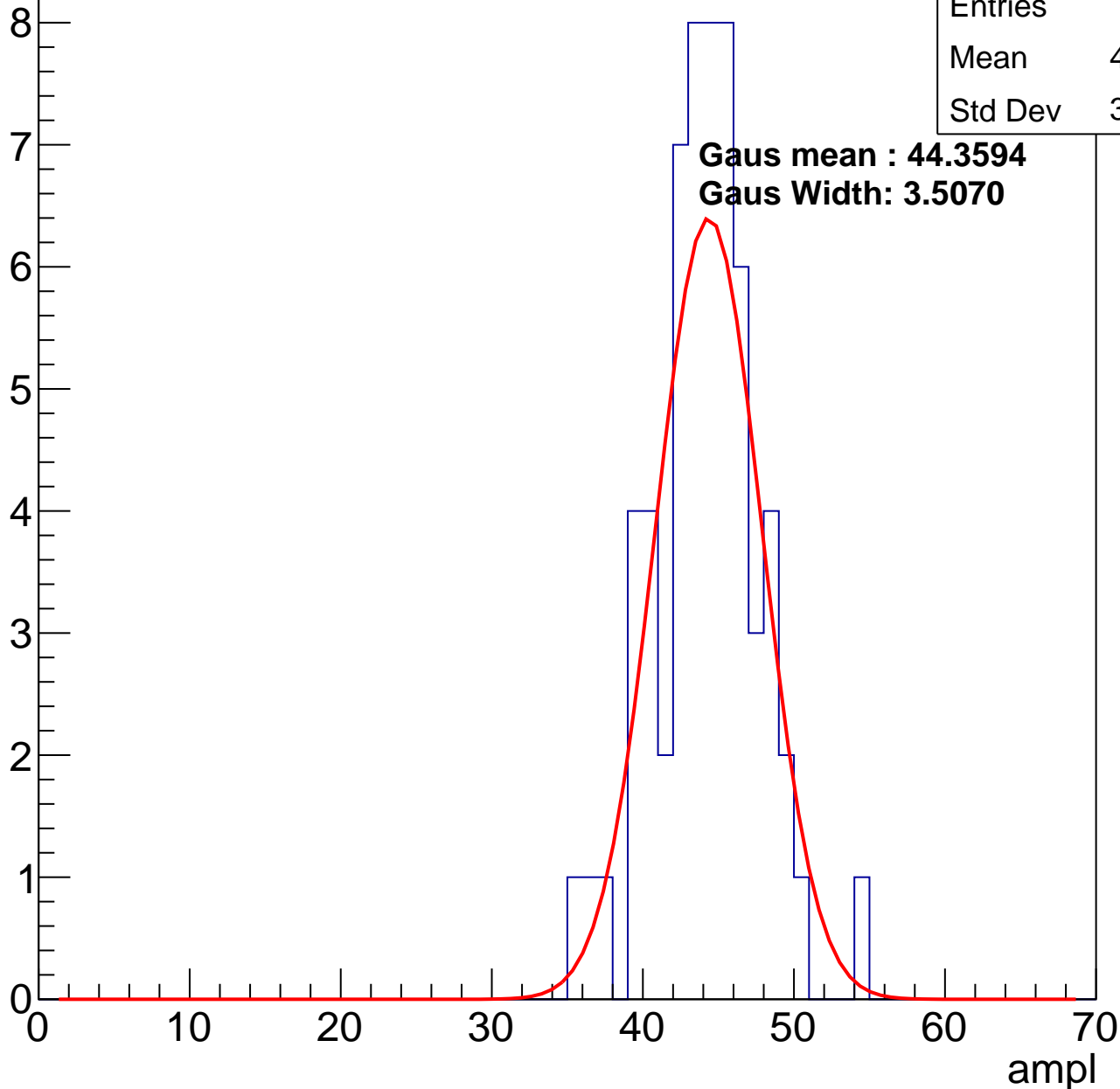
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.72
Std Dev	3.427

**Gaus mean : 44.3594**

**Gaus Width: 3.5070**

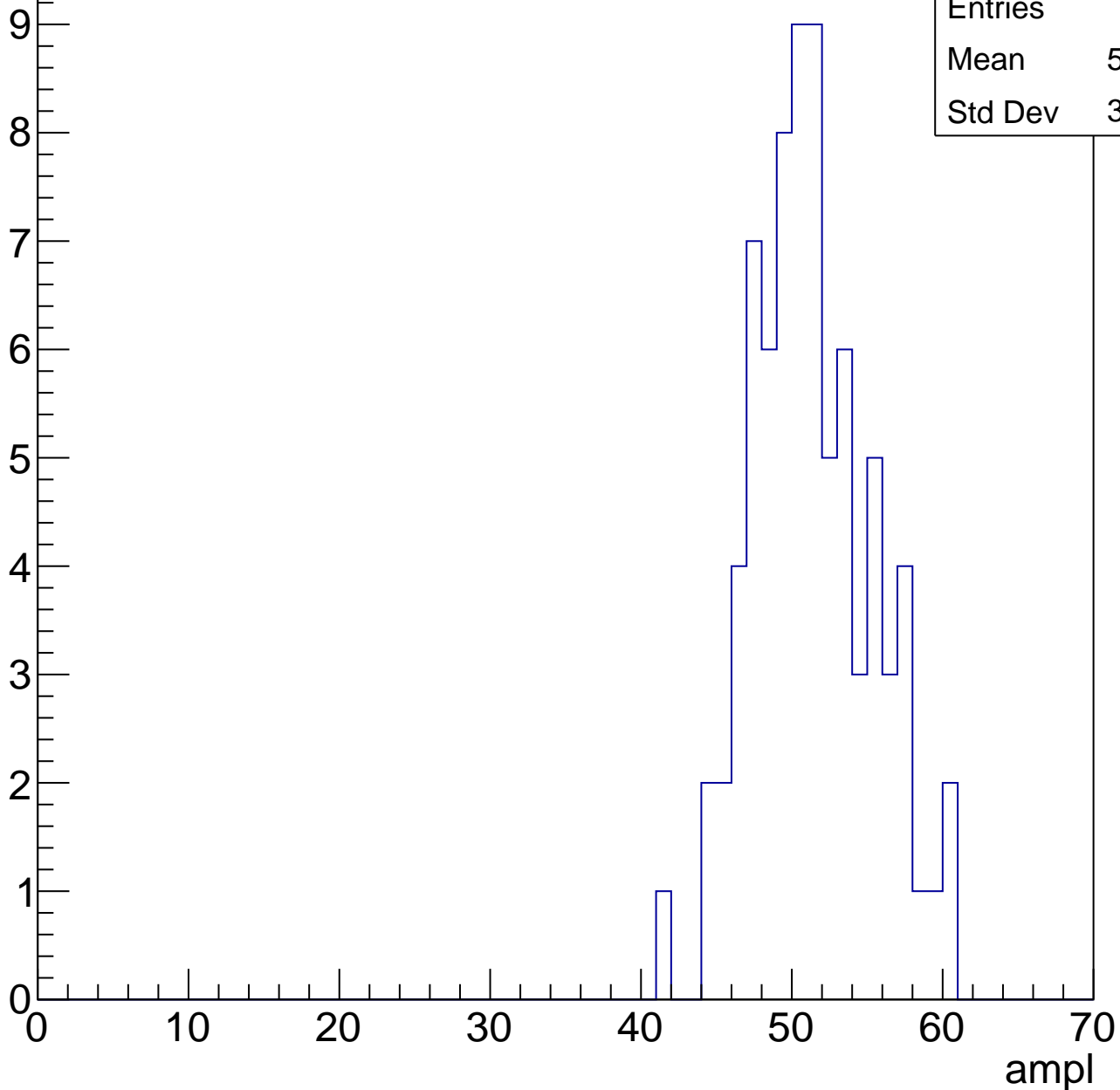


# B1L003S, U22-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	50.88
Std Dev	3.932

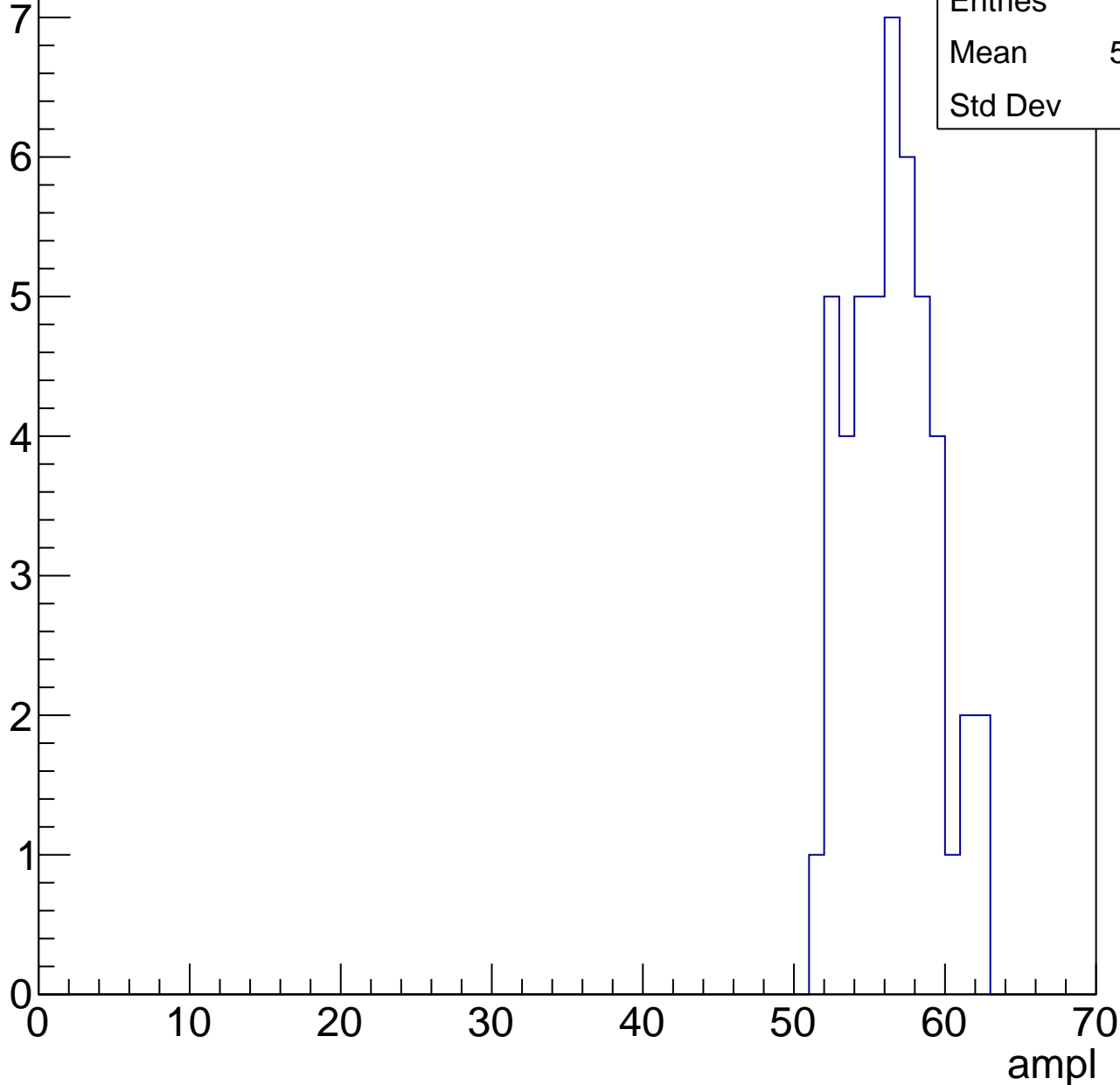


# B1L003S, U22-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	56.04
Std Dev	2.79



# B1L003S, U22-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	40
Mean	59.17
Std Dev	9.674

Entry

10

8

6

4

2

0

0

10

20

30

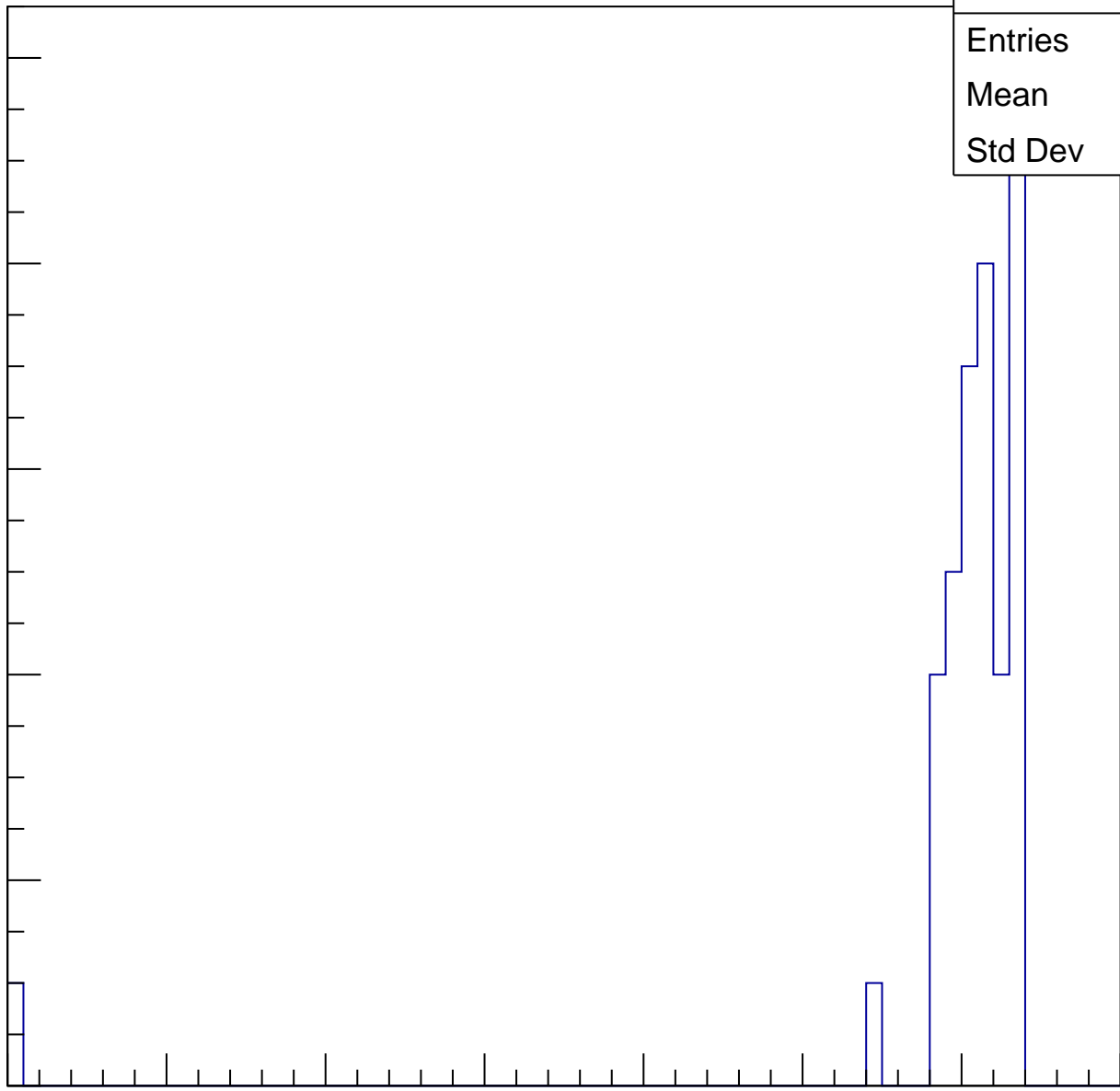
40

50

60

70

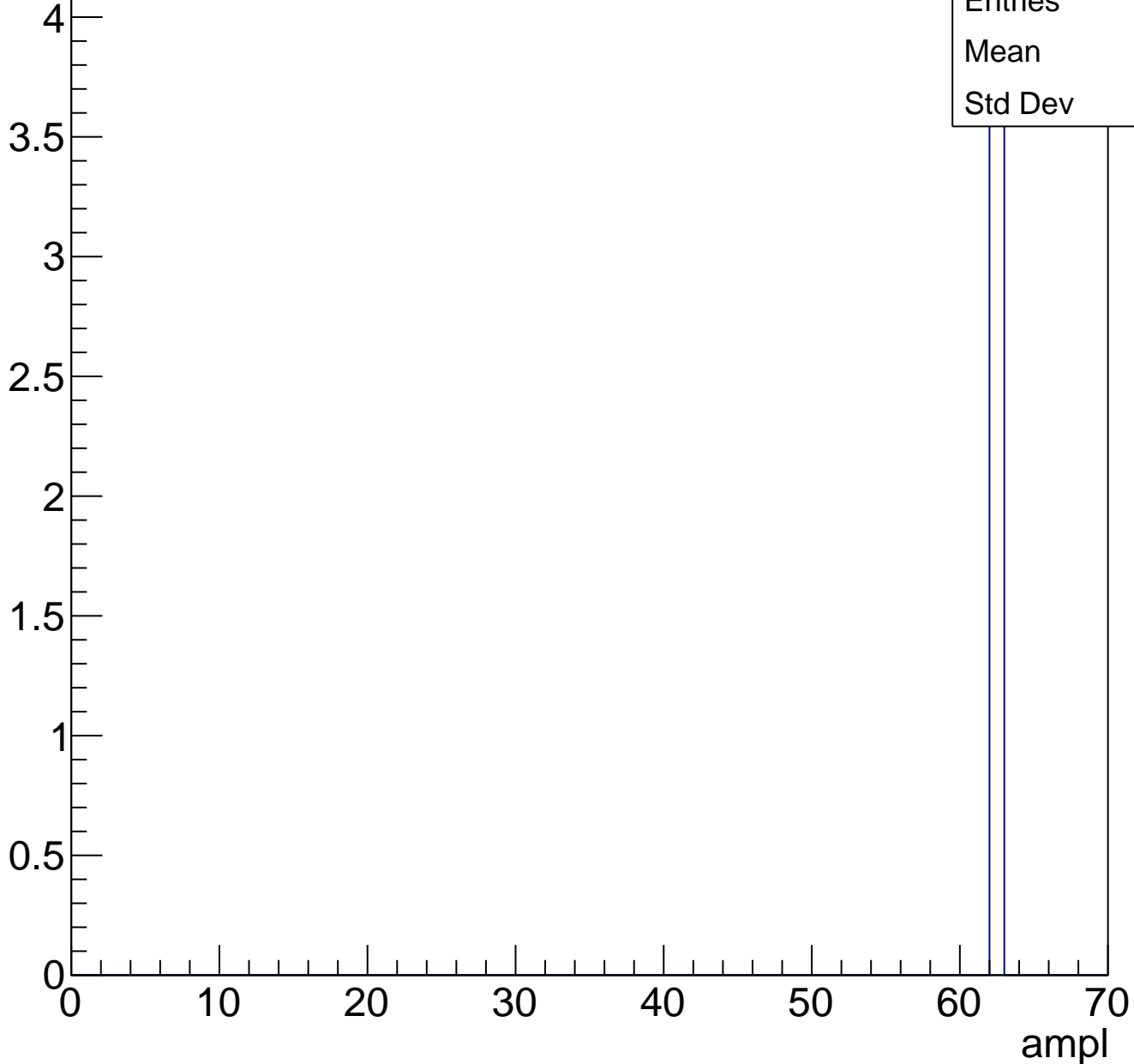
ampl



# B1L003S, U22-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	29.71
Std Dev	4.904

**Gaus mean : 30.6642**

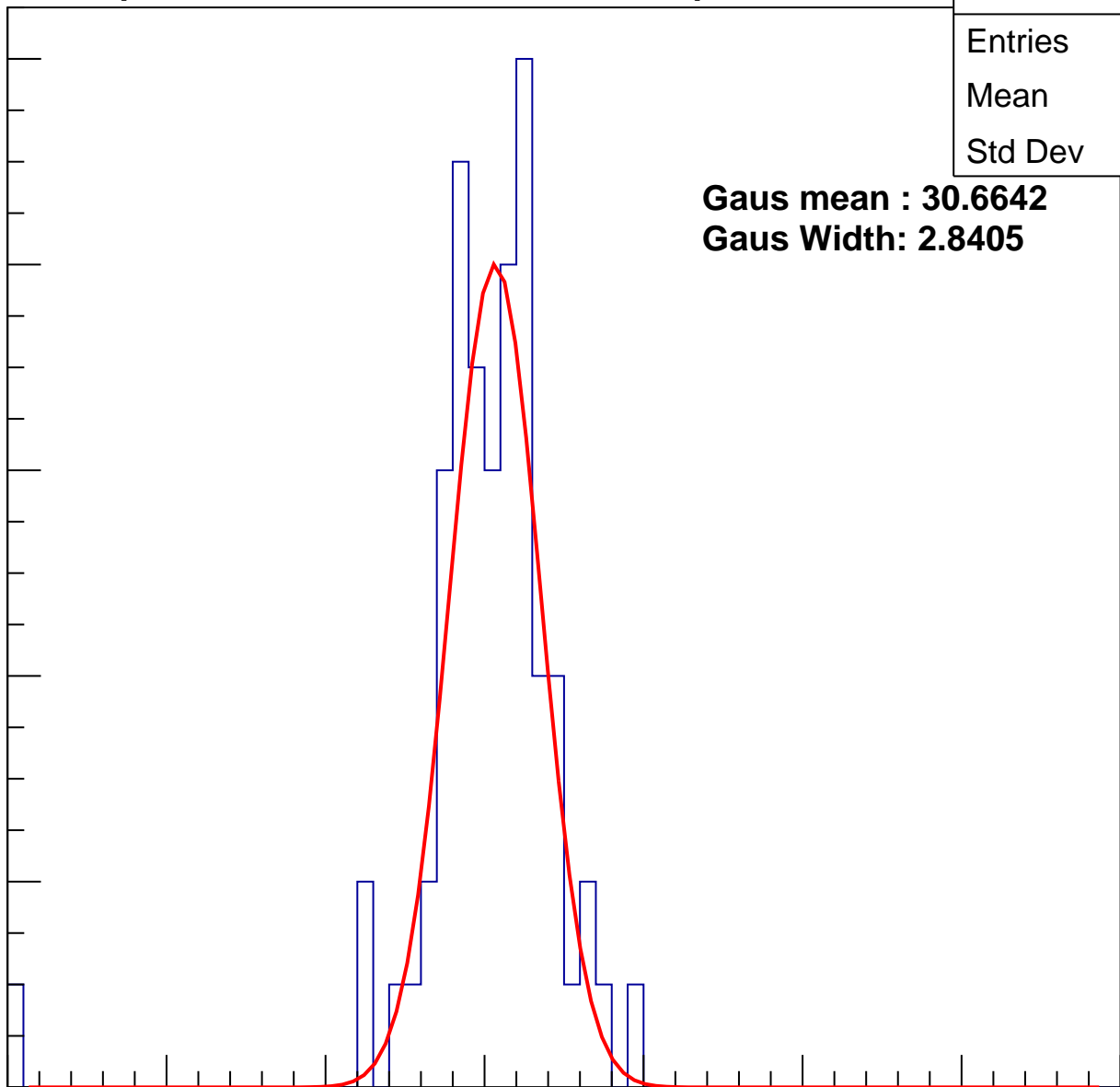
**Gaus Width: 2.8405**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch36, adc1

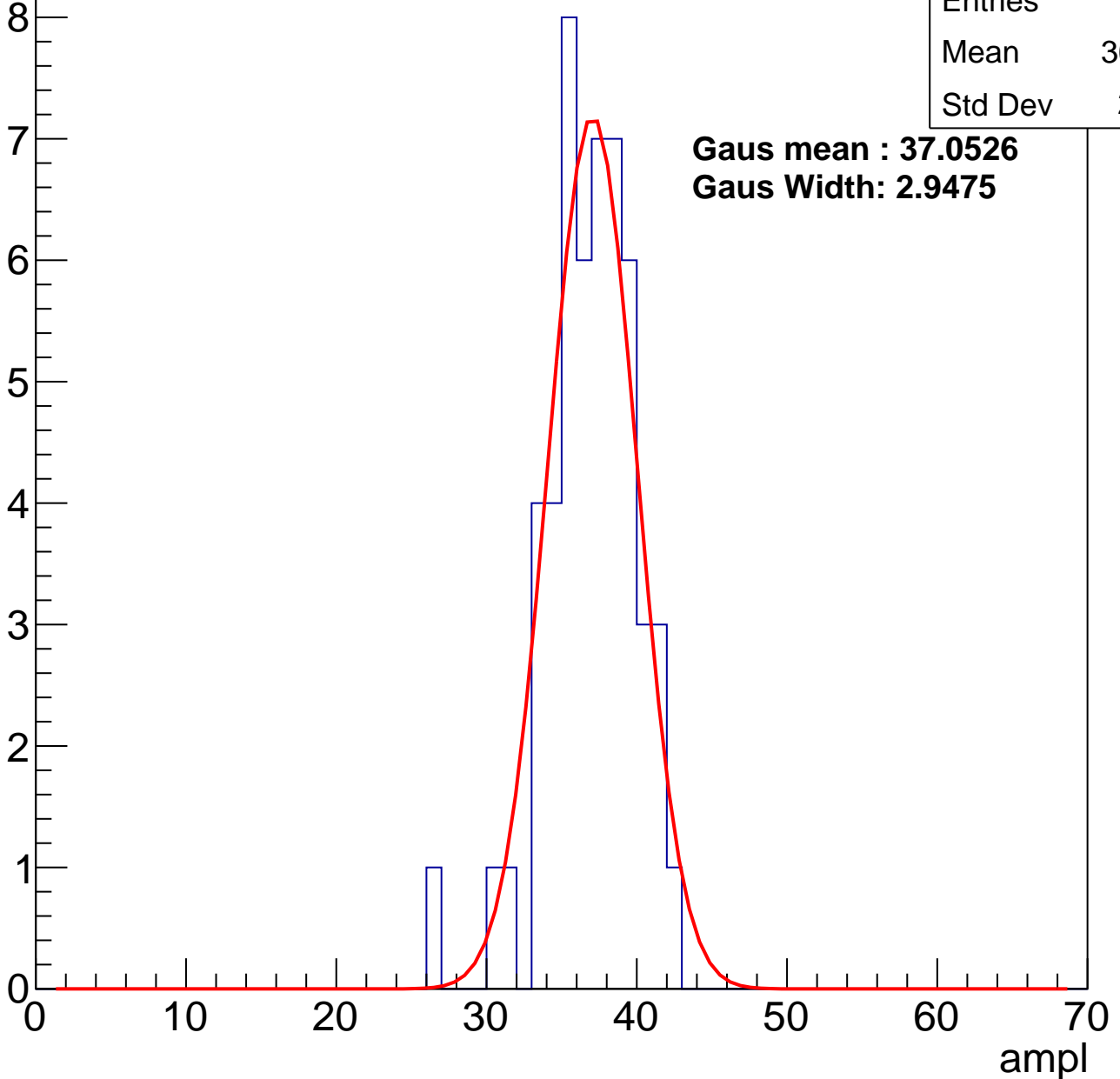
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	36.44
Std Dev	2.97

**Gaus mean : 37.0526**

**Gaus Width: 2.9475**



# B1L003S, U22-ch36, adc2

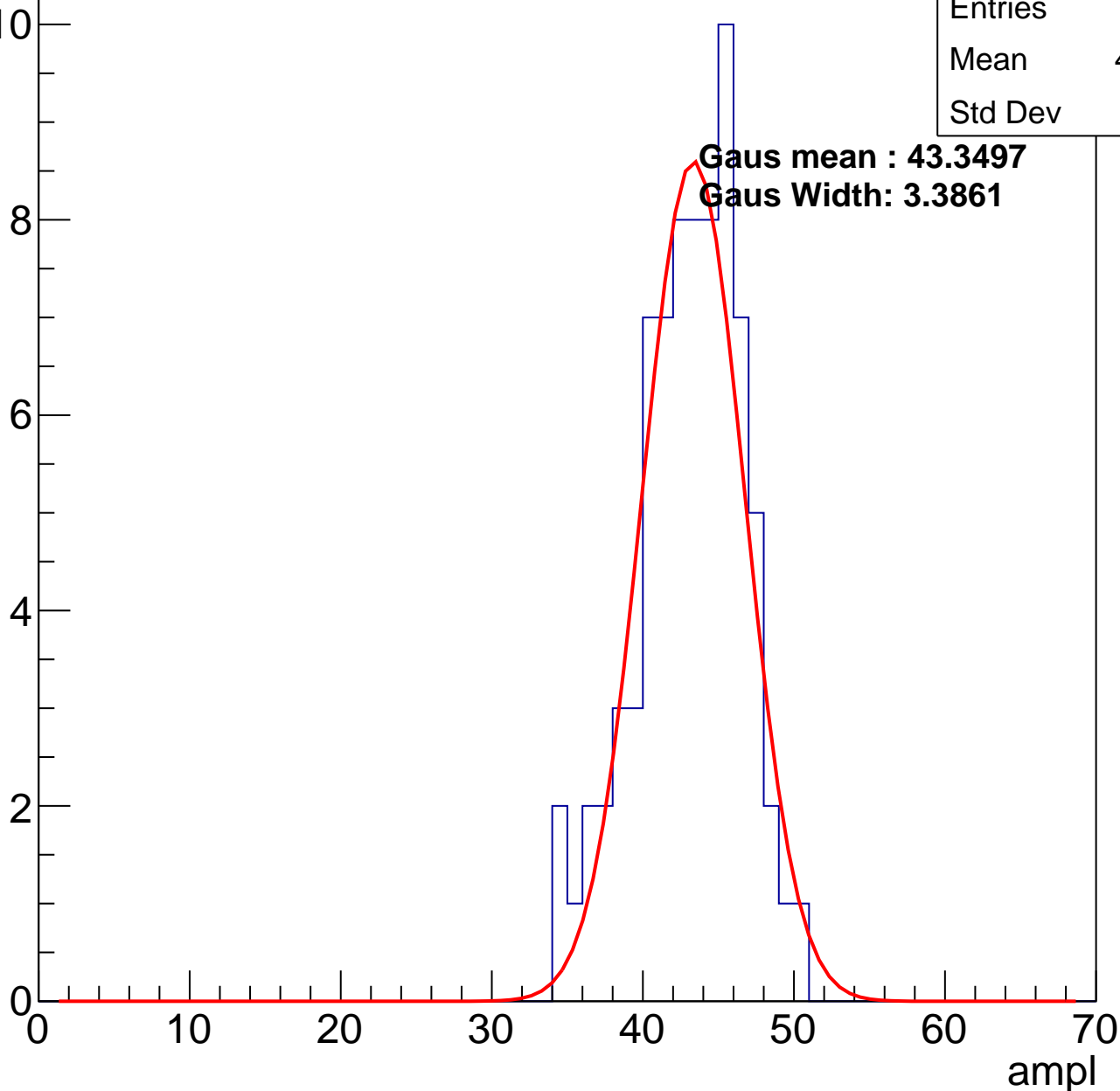
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	42.61
Std Dev	3.48

**Gaus mean : 43.3497**

**Gaus Width: 3.3861**

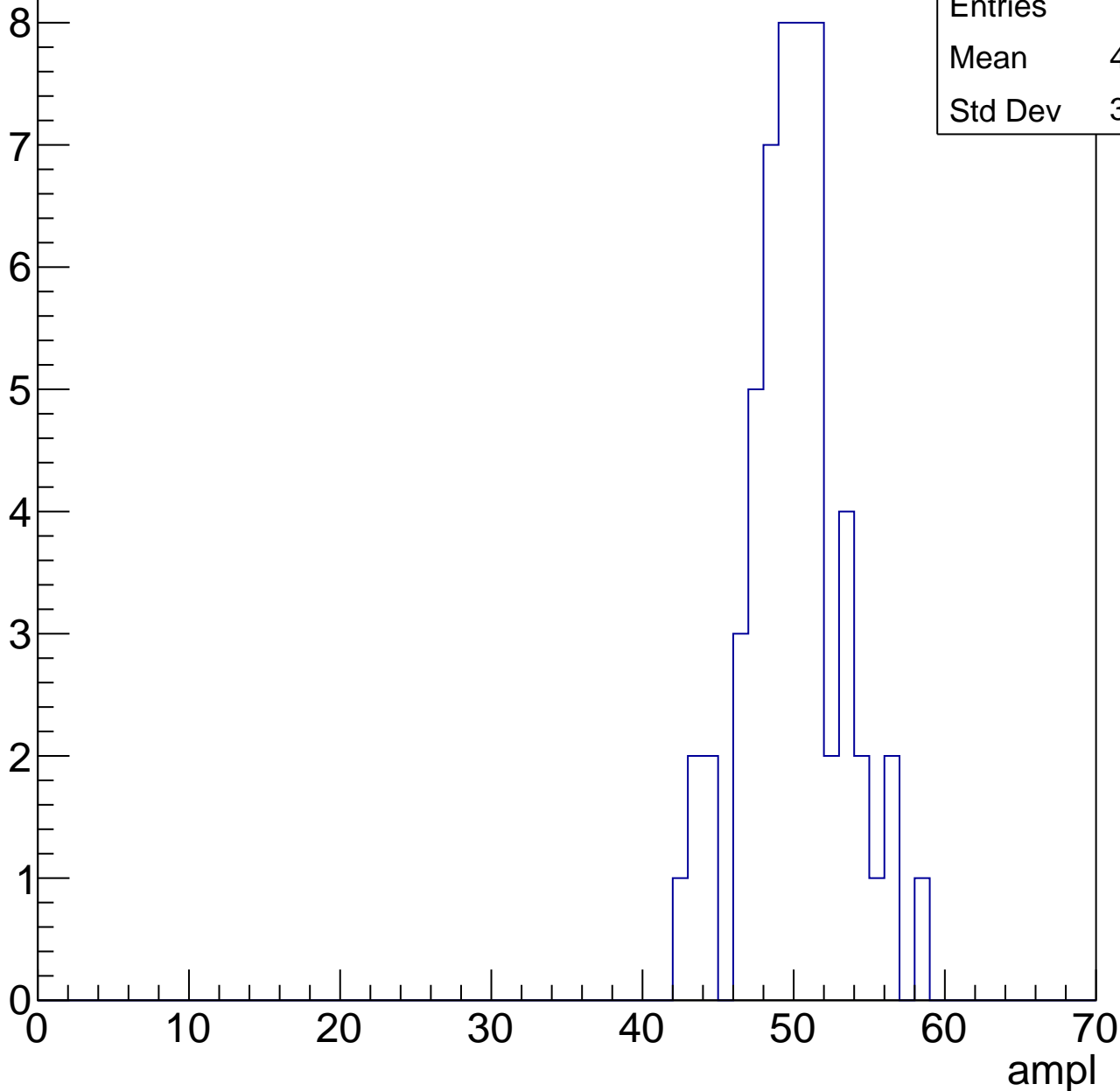


# B1L003S, U22-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

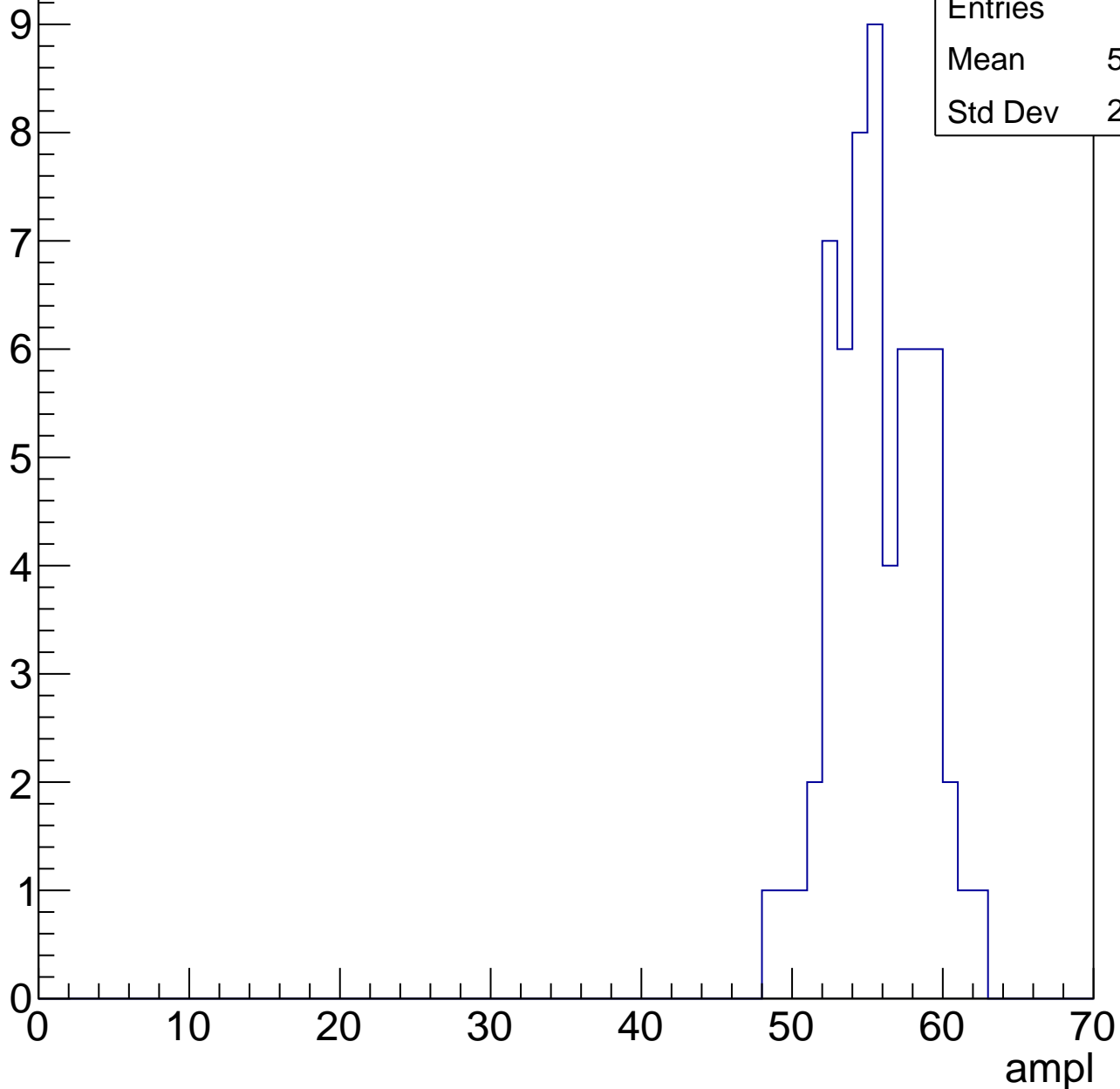
Entries	56
Mean	49.54
Std Dev	3.262



# B1L003S, U22-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

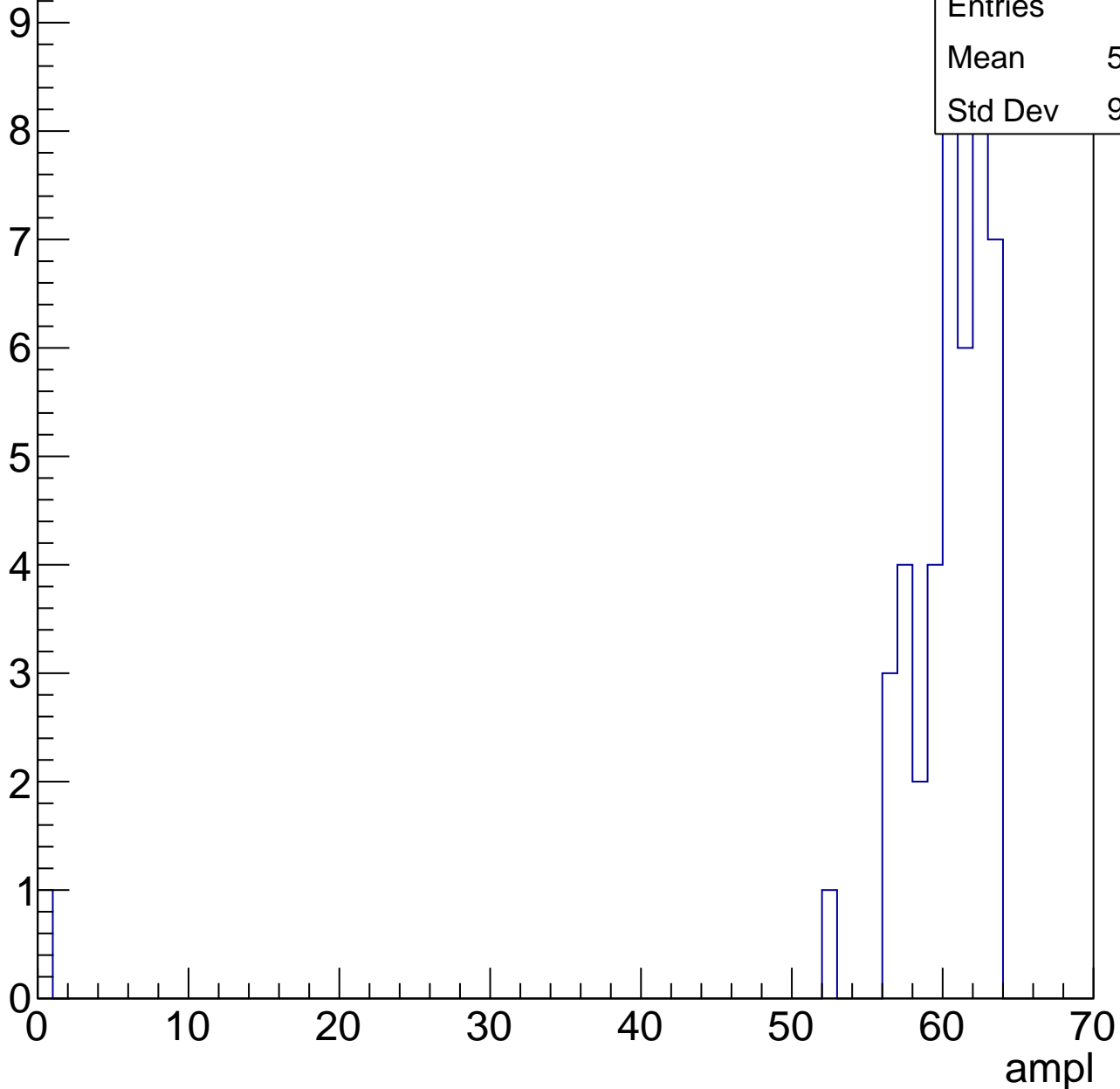


# B1L003S, U22-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

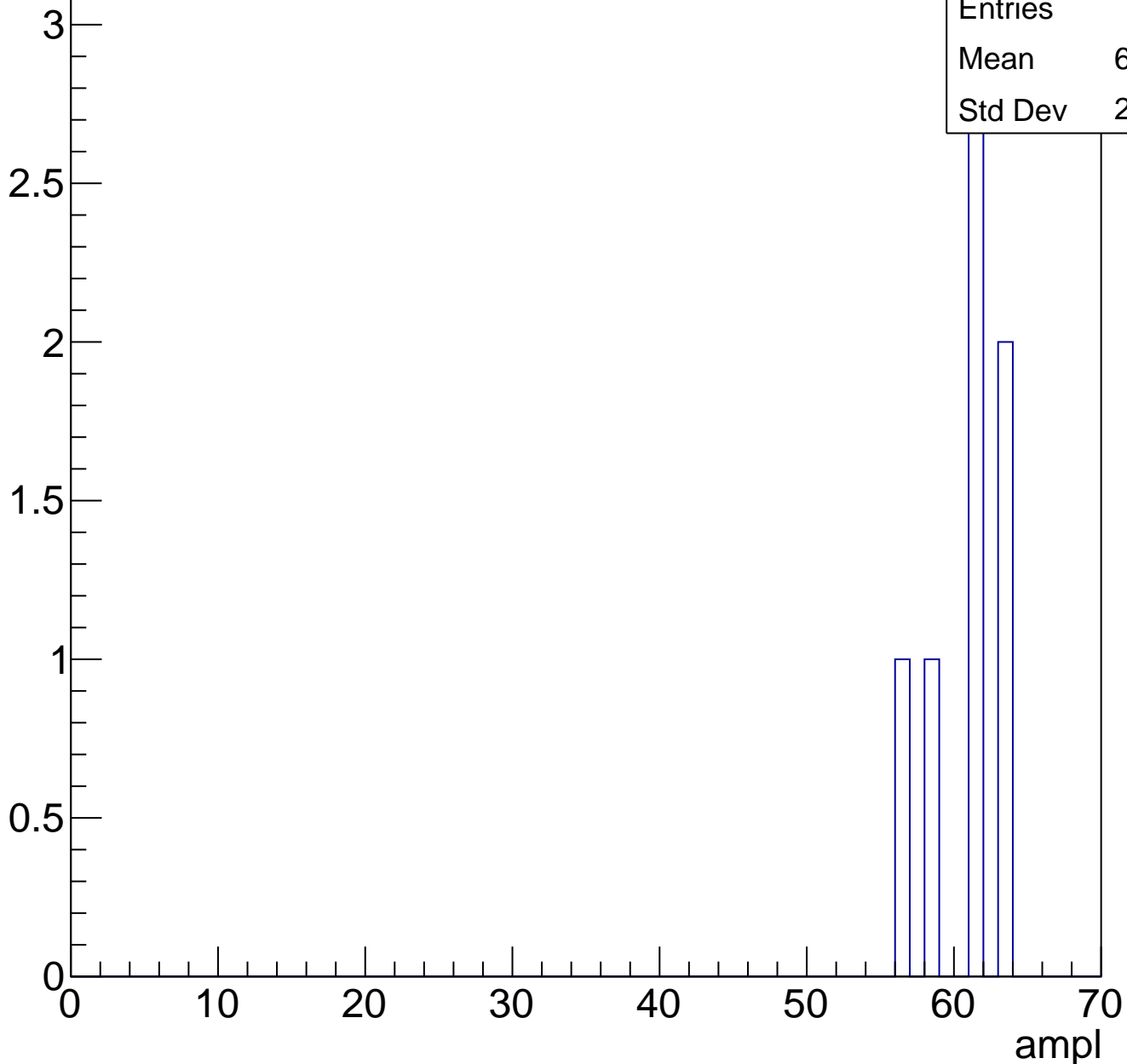
Entries	45
Mean	58.78
Std Dev	9.187



# B1L003S, U22-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L003S, U22-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	63
Mean	30.13
Std Dev	2.963

**Gaus mean : 30.7040**

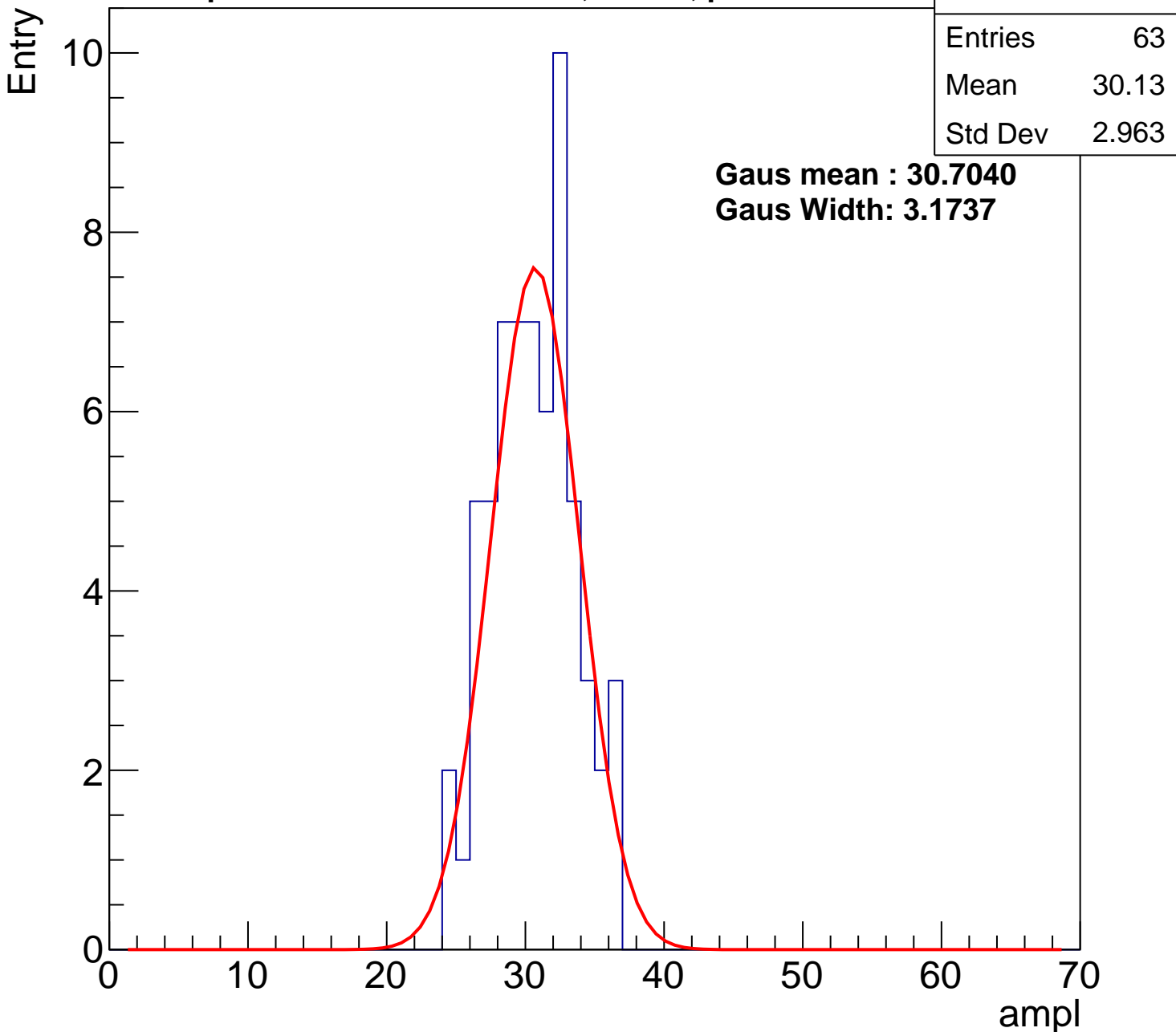
**Gaus Width: 3.1737**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch37, adc1

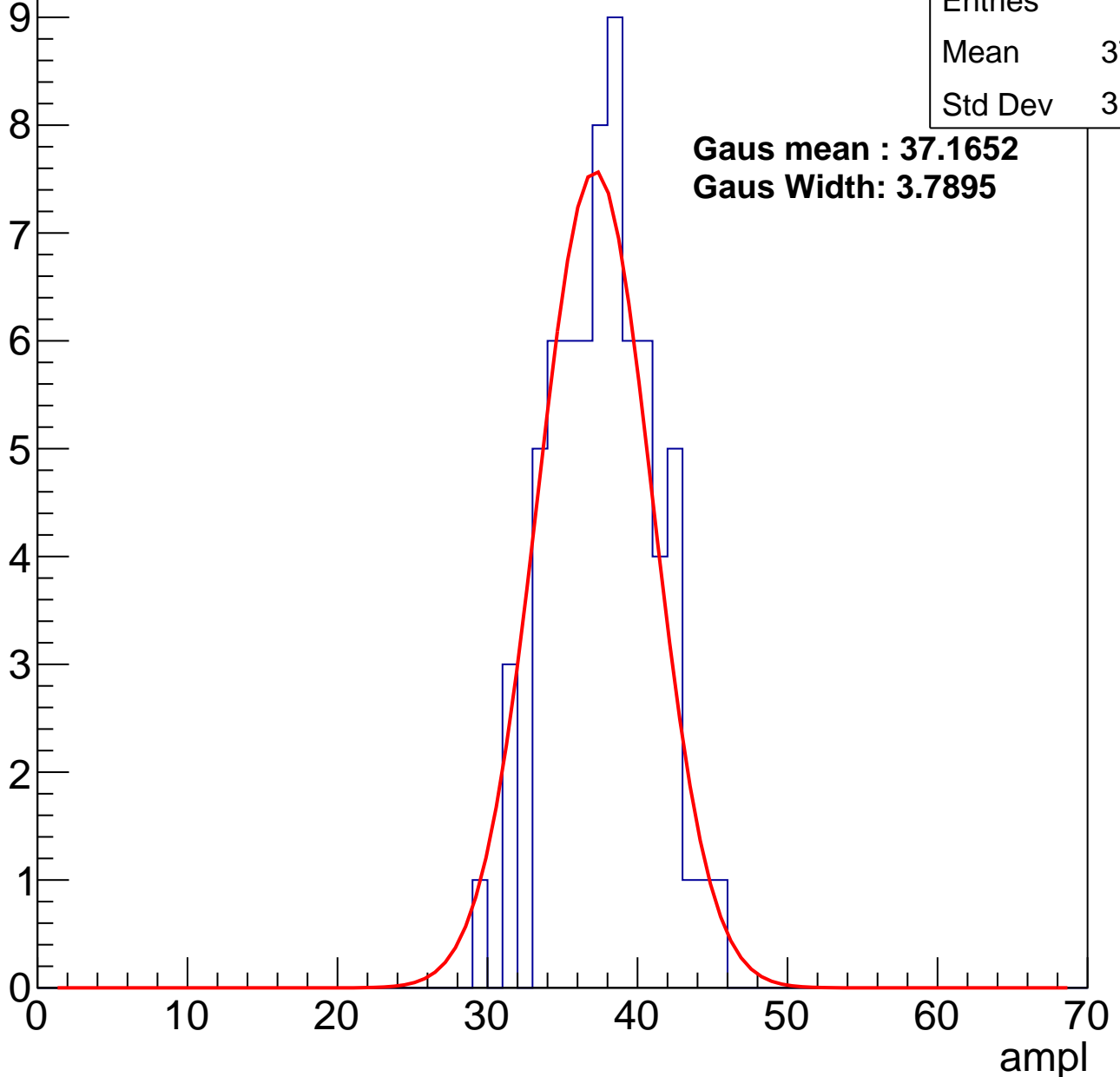
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.28
Std Dev	3.334

**Gaus mean : 37.1652**

**Gaus Width: 3.7895**



# B1L003S, U22-ch37, adc2

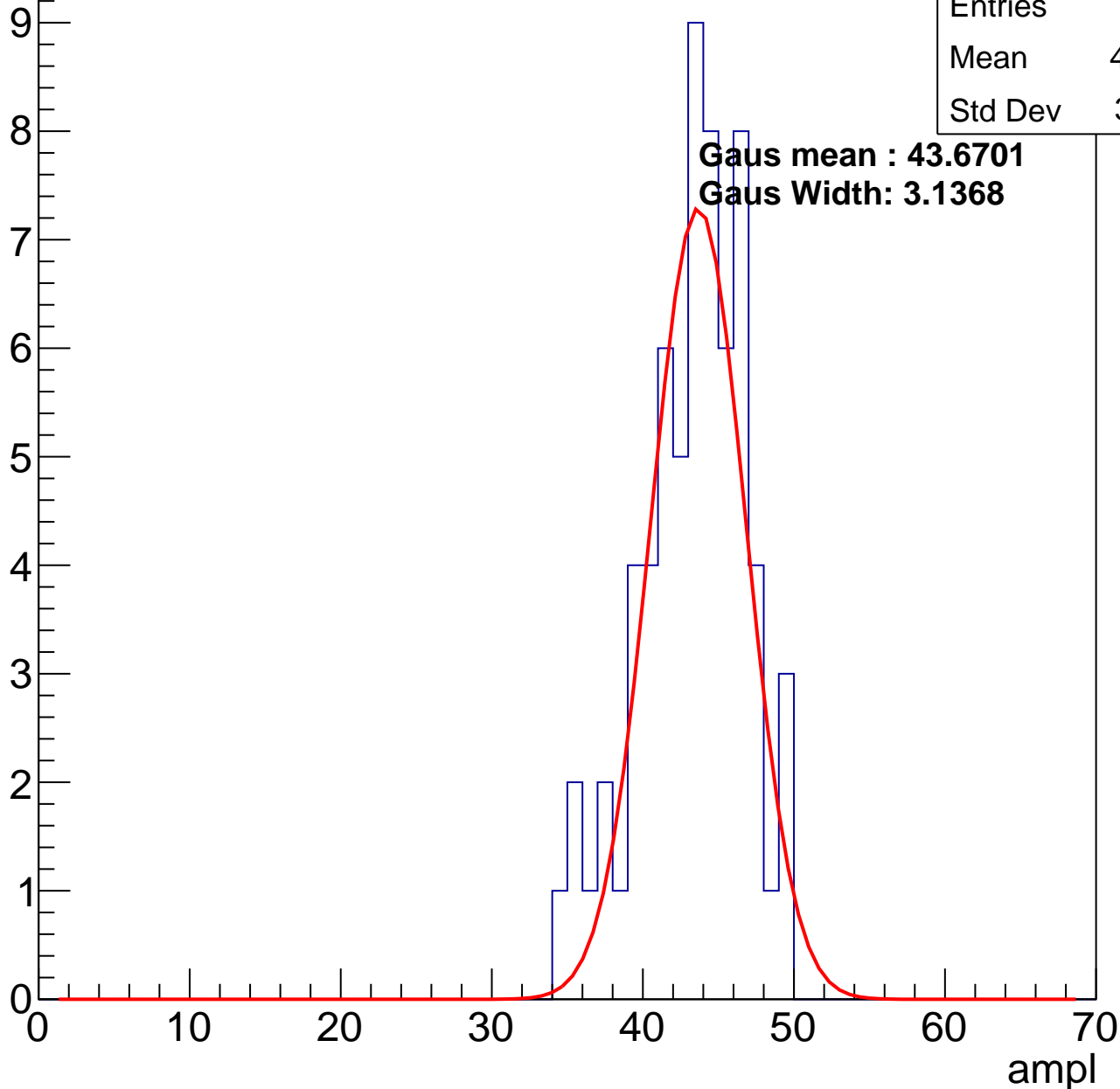
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.83
Std Dev	3.471

**Gaus mean : 43.6701**

**Gaus Width: 3.1368**



# B1L003S, U22-ch37, adc3

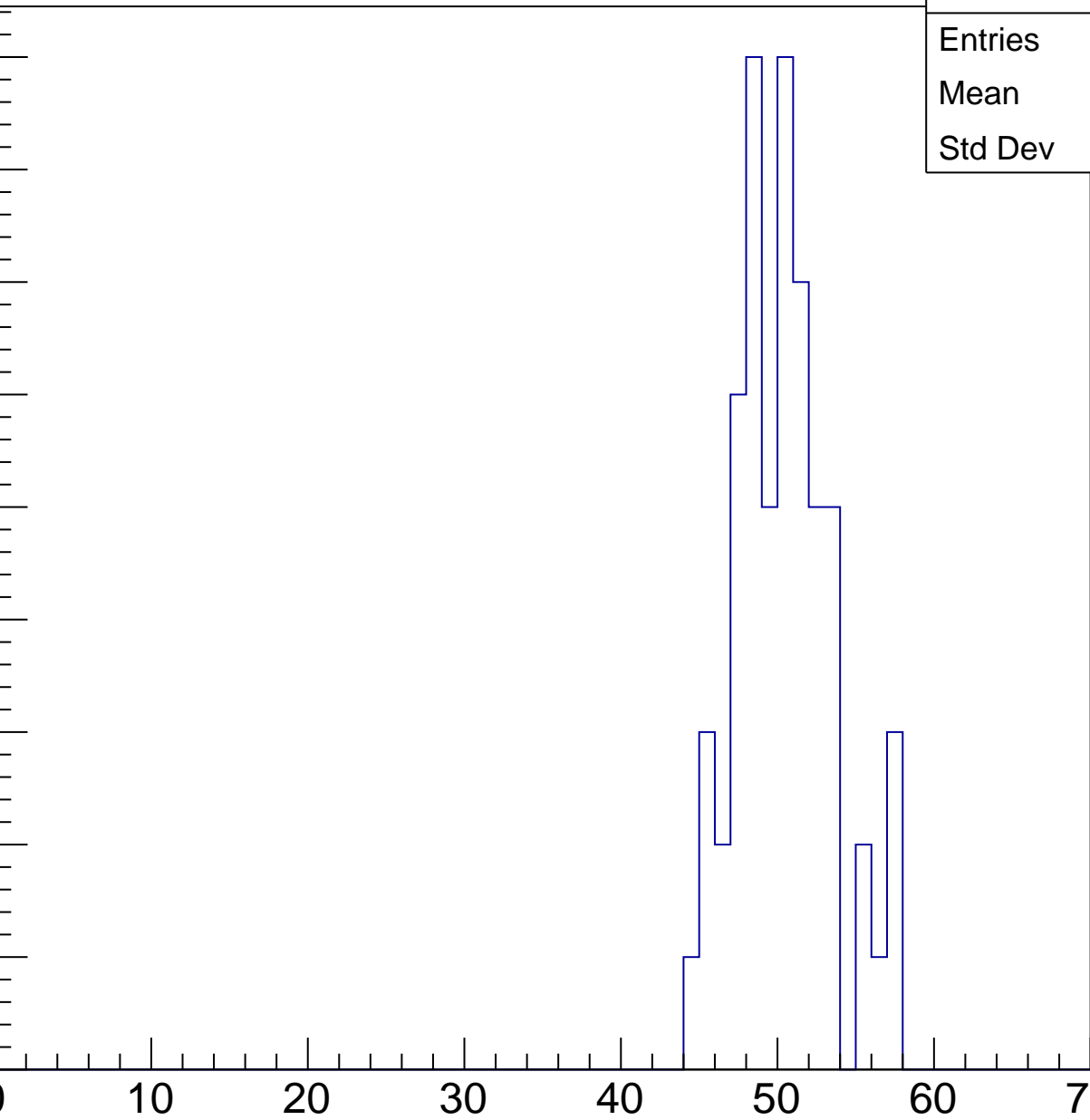
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	49.98
Std Dev	3.06

ampl

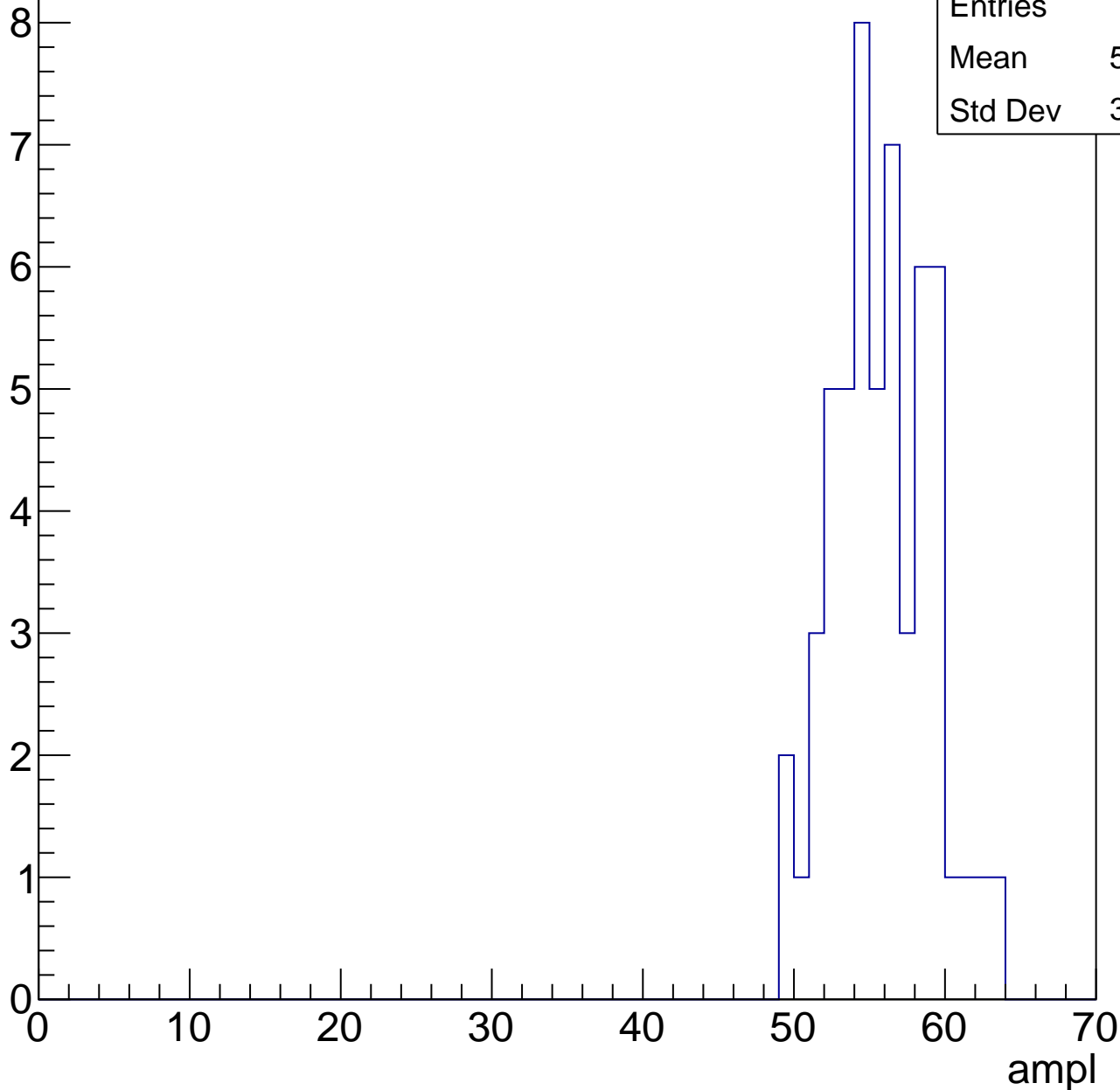


# B1L003S, U22-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

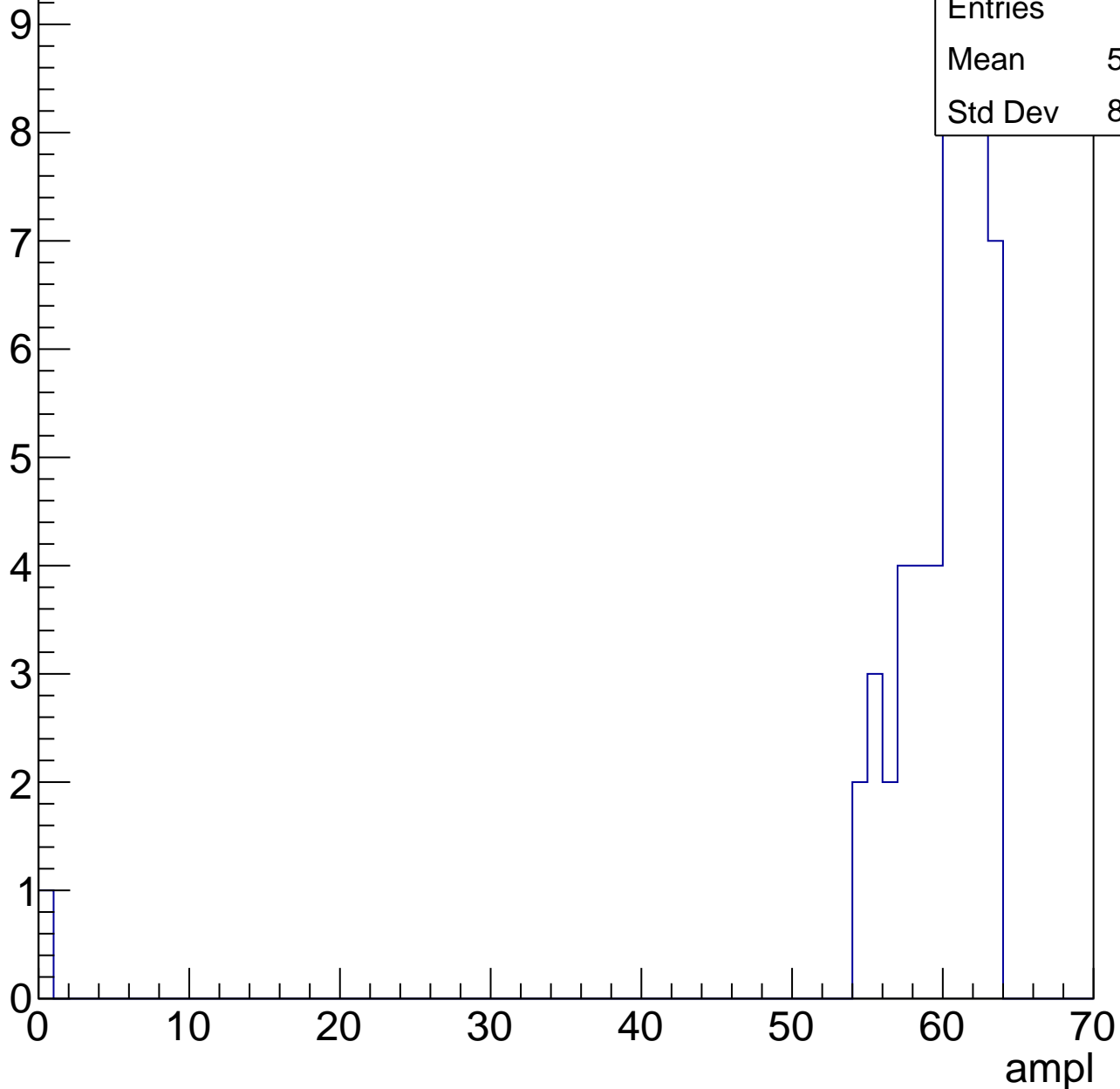
Entries	55
Mean	55.35
Std Dev	3.175



# B1L003S, U22-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch38, adc0

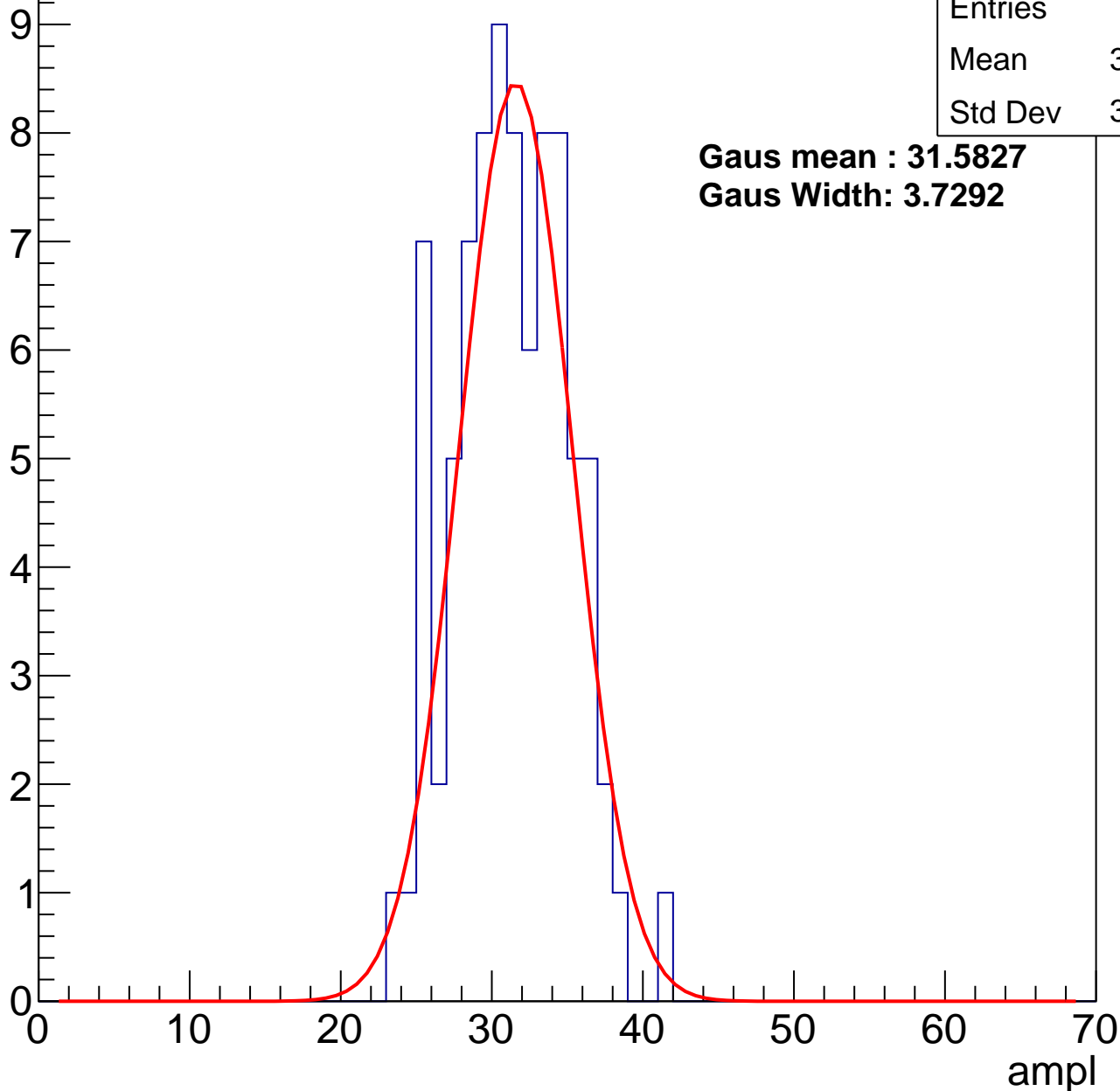
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	30.85
Std Dev	3.673

**Gaus mean : 31.5827**

**Gaus Width: 3.7292**



# B1L003S, U22-ch38, adc1

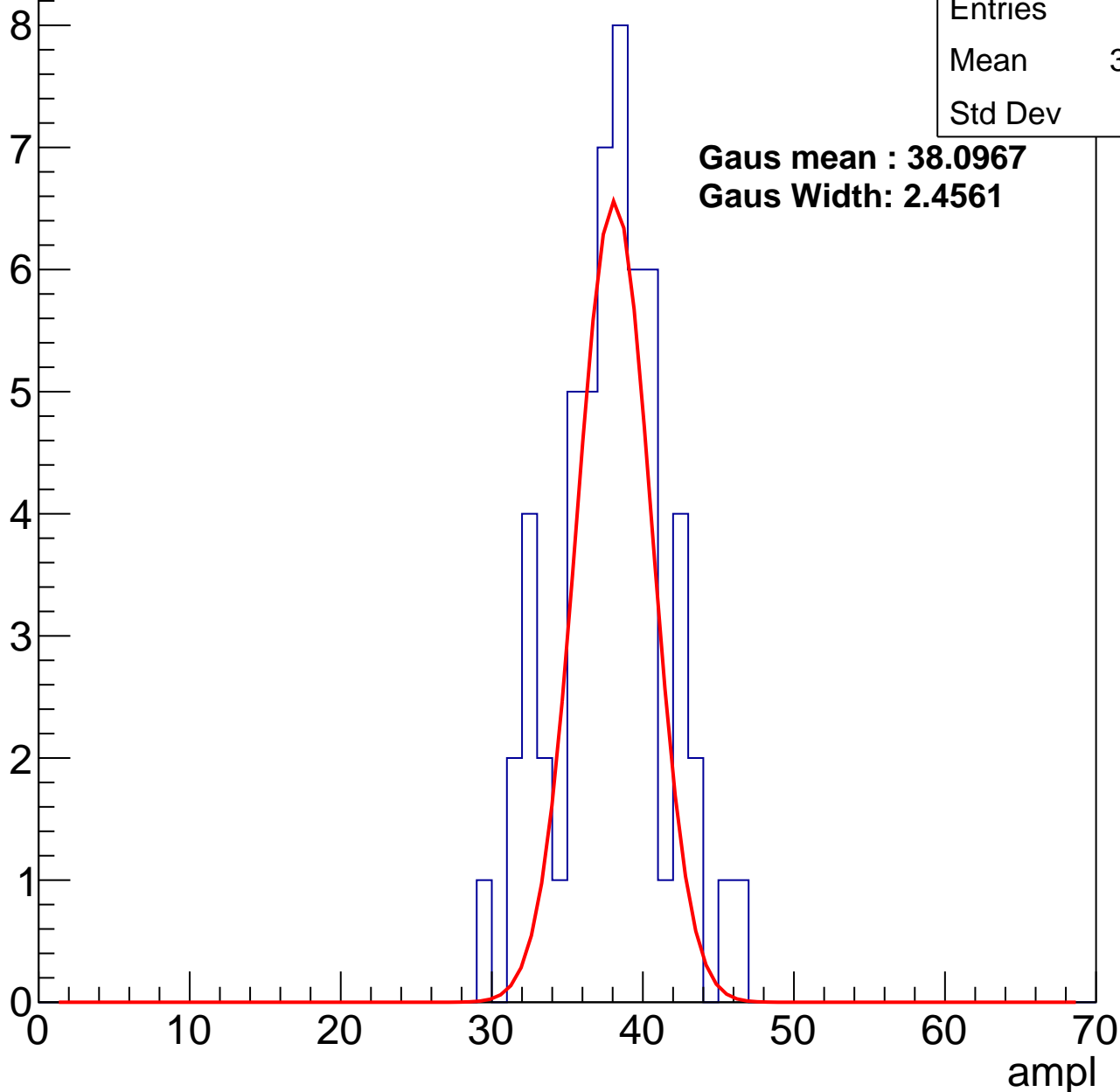
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	37.45
Std Dev	3.56

**Gaus mean : 38.0967**

**Gaus Width: 2.4561**



# B1L003S, U22-ch38, adc2

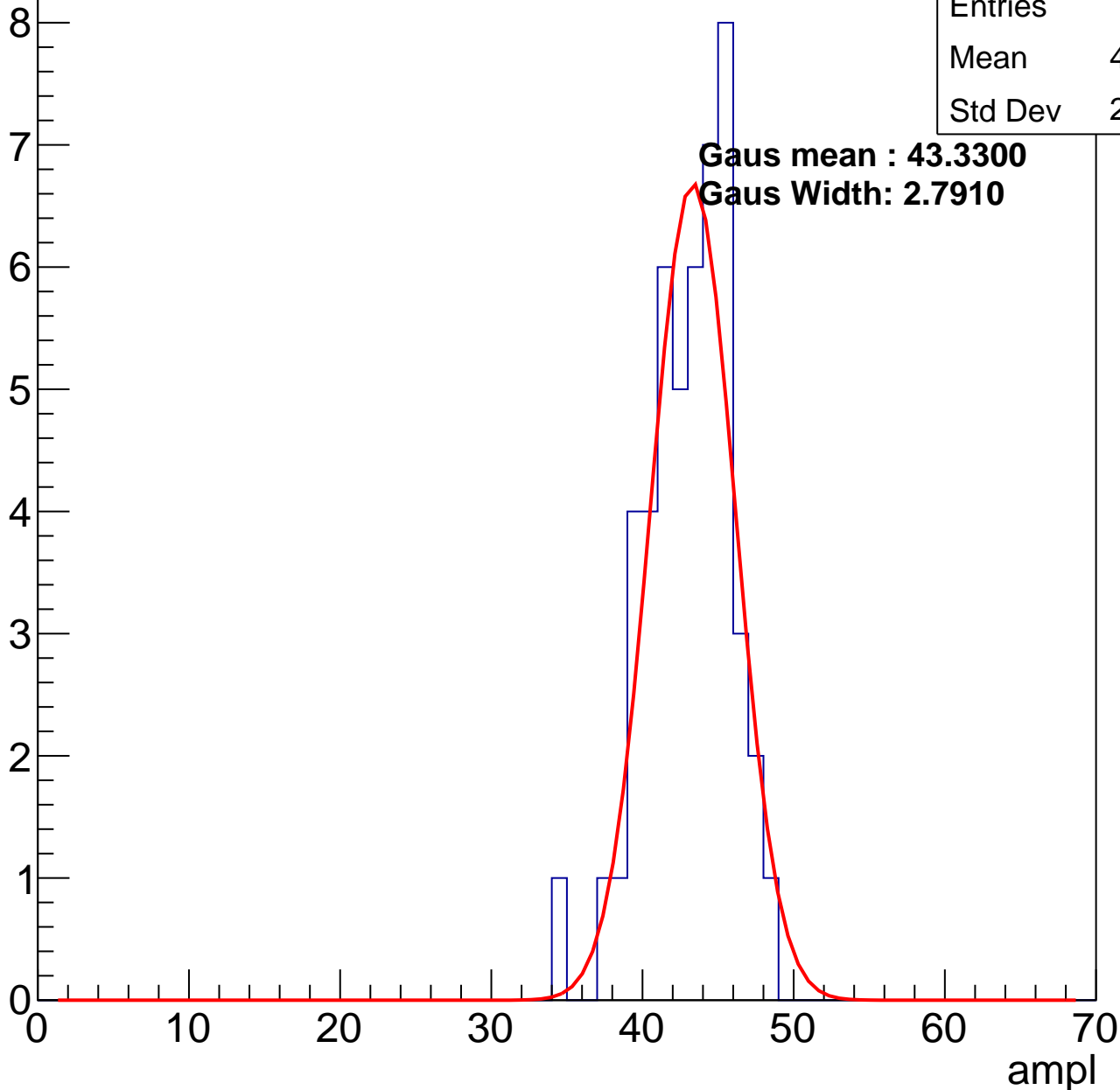
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	42.59
Std Dev	2.806

**Gaus mean : 43.3300**

**Gaus Width: 2.7910**



# B1L003S, U22-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

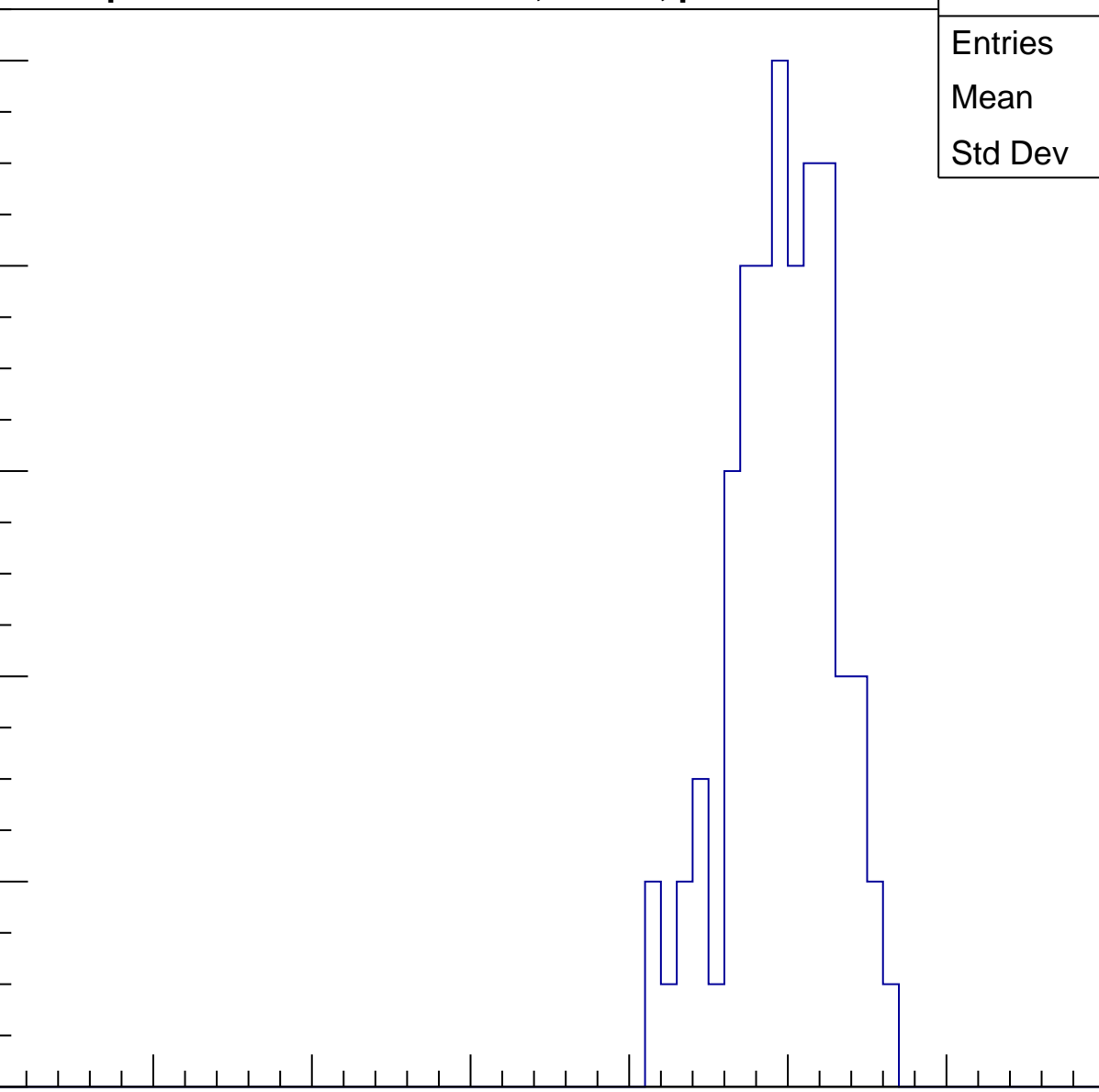
Entries	78
Mean	49.15
Std Dev	3.286

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

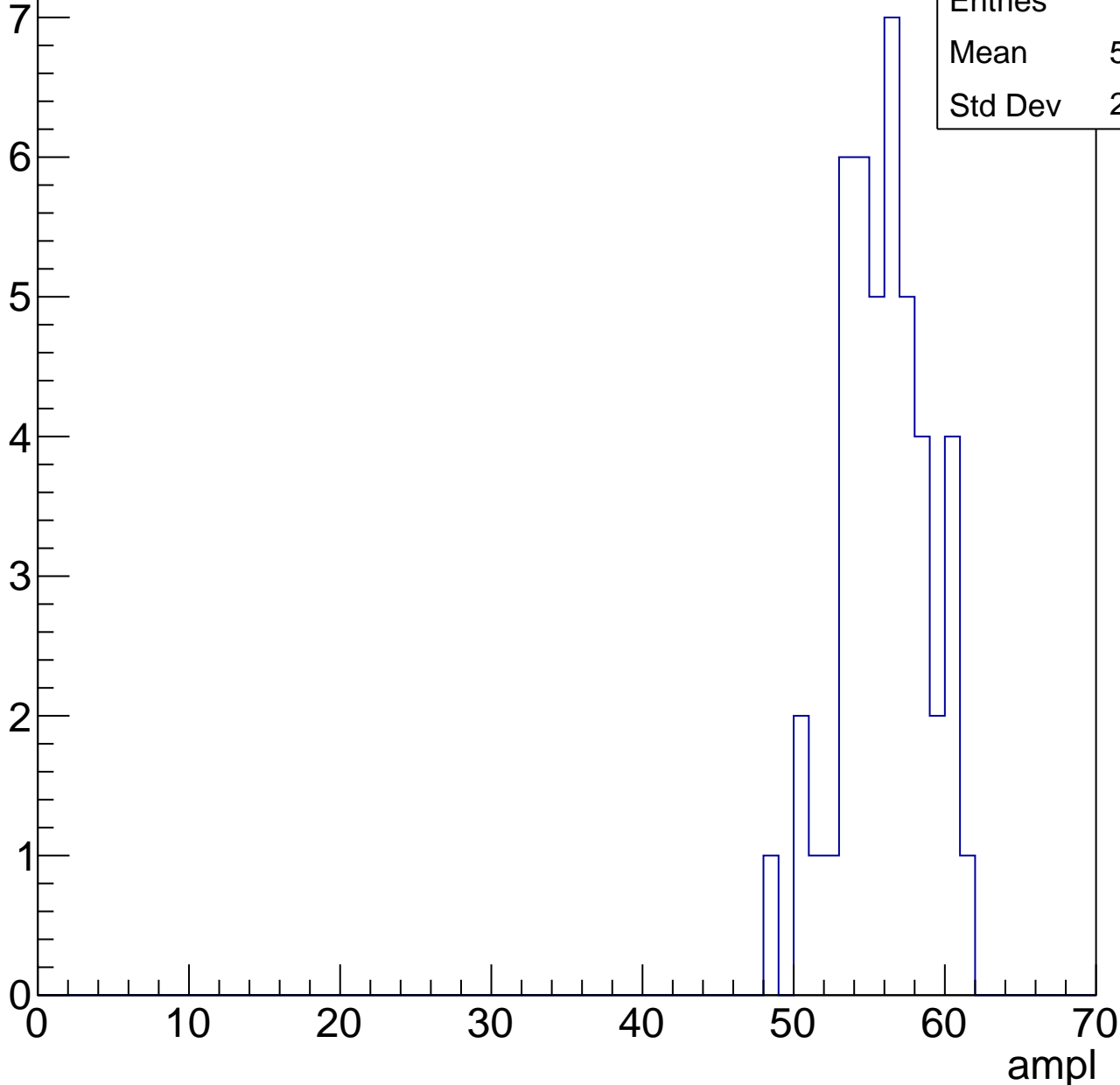


# B1L003S, U22-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

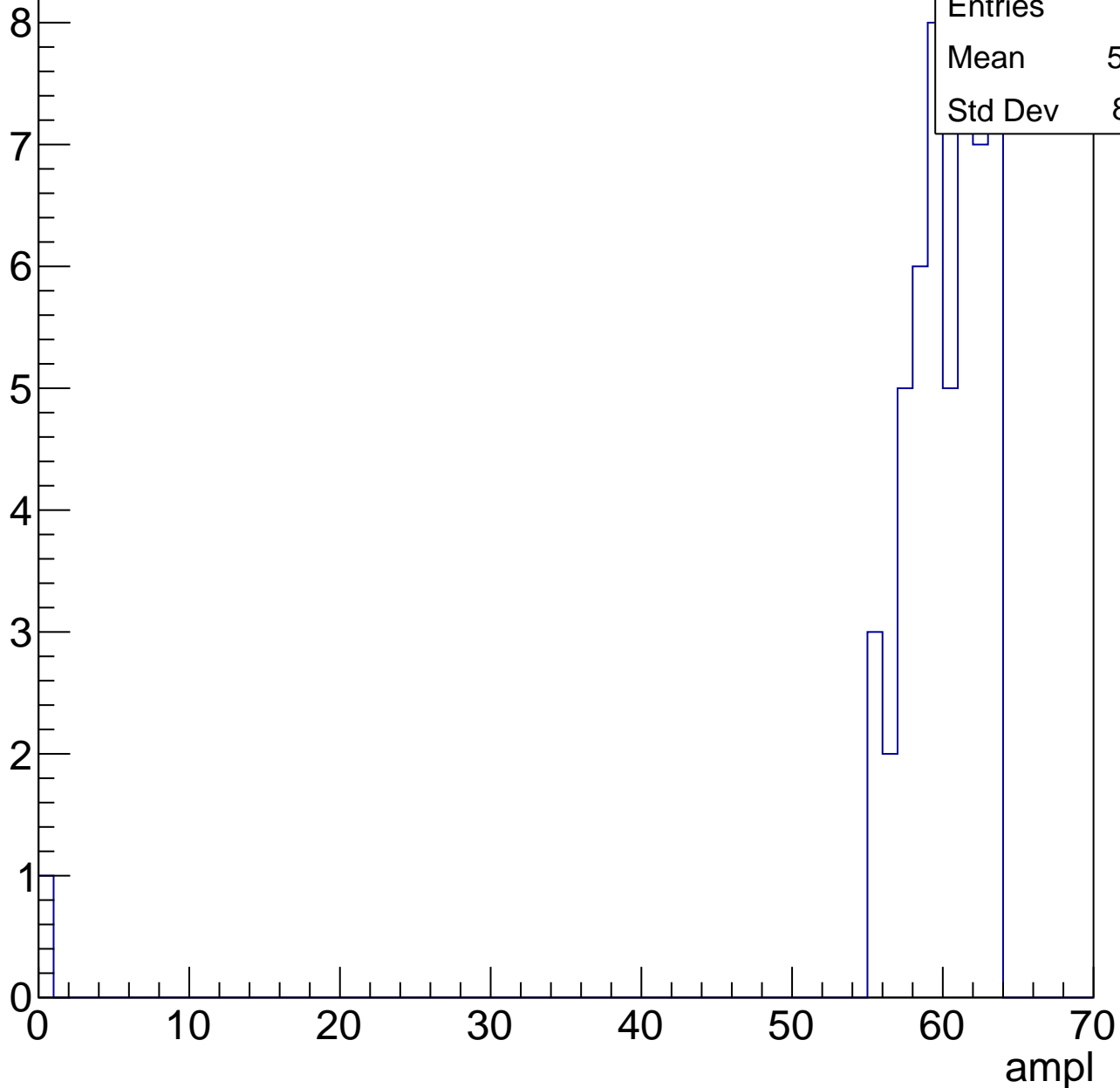
Entries	45
Mean	55.47
Std Dev	2.888



# B1L003S, U22-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch39, adc0

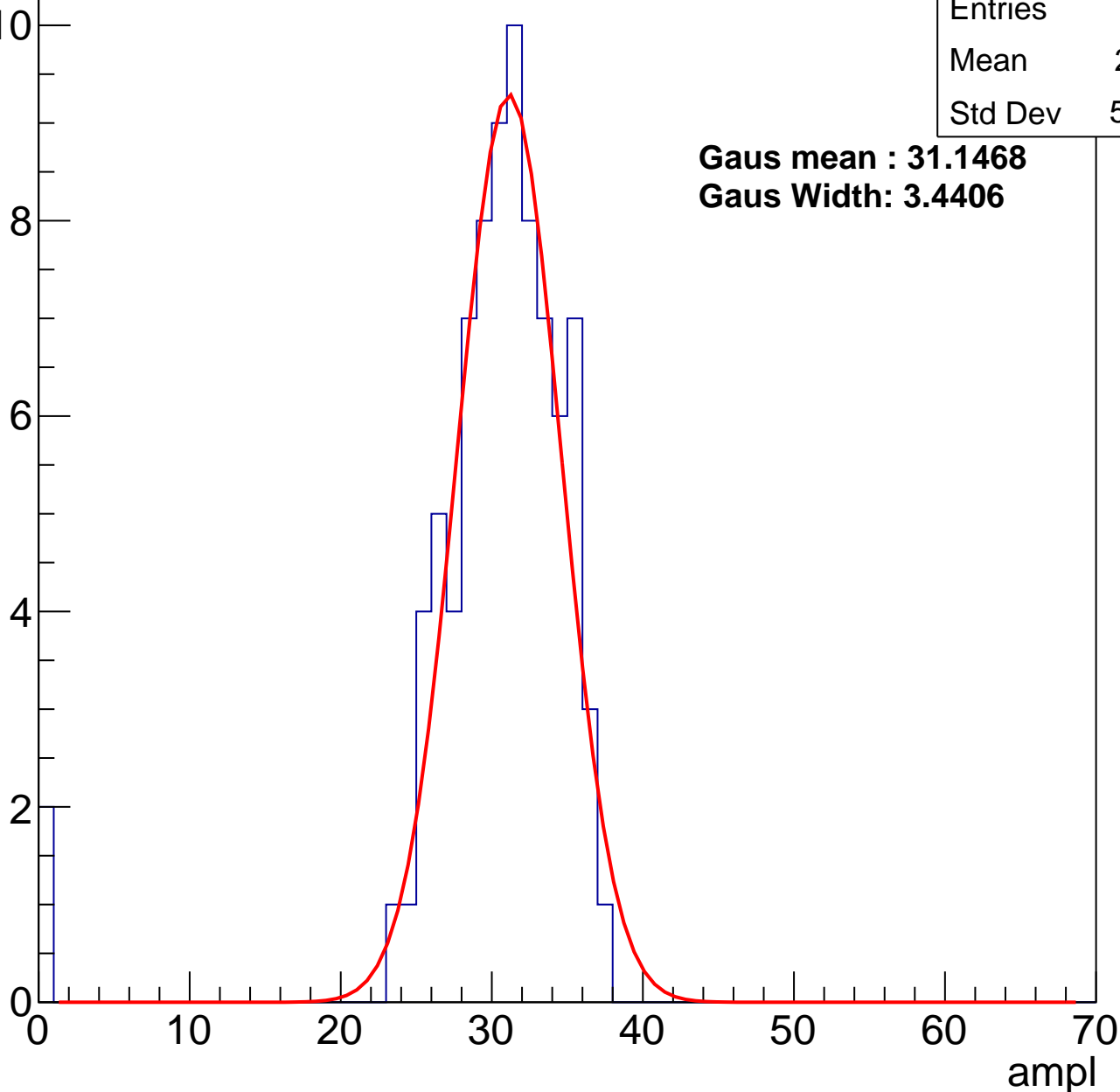
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	29.81
Std Dev	5.668

**Gaus mean : 31.1468**

**Gaus Width: 3.4406**



# B1L003S, U22-ch39, adc1

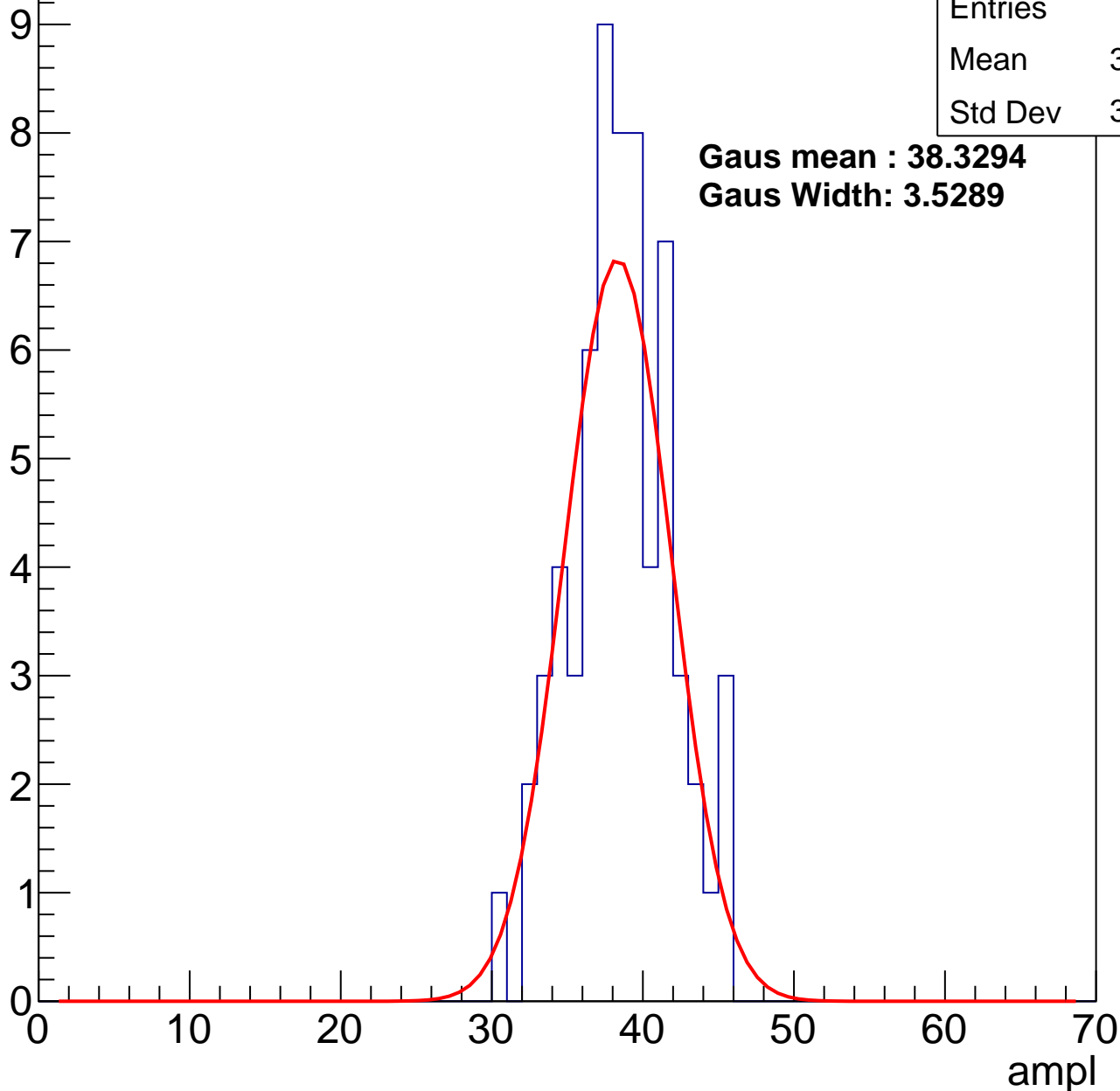
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	38.08
Std Dev	3.313

**Gaus mean : 38.3294**

**Gaus Width: 3.5289**

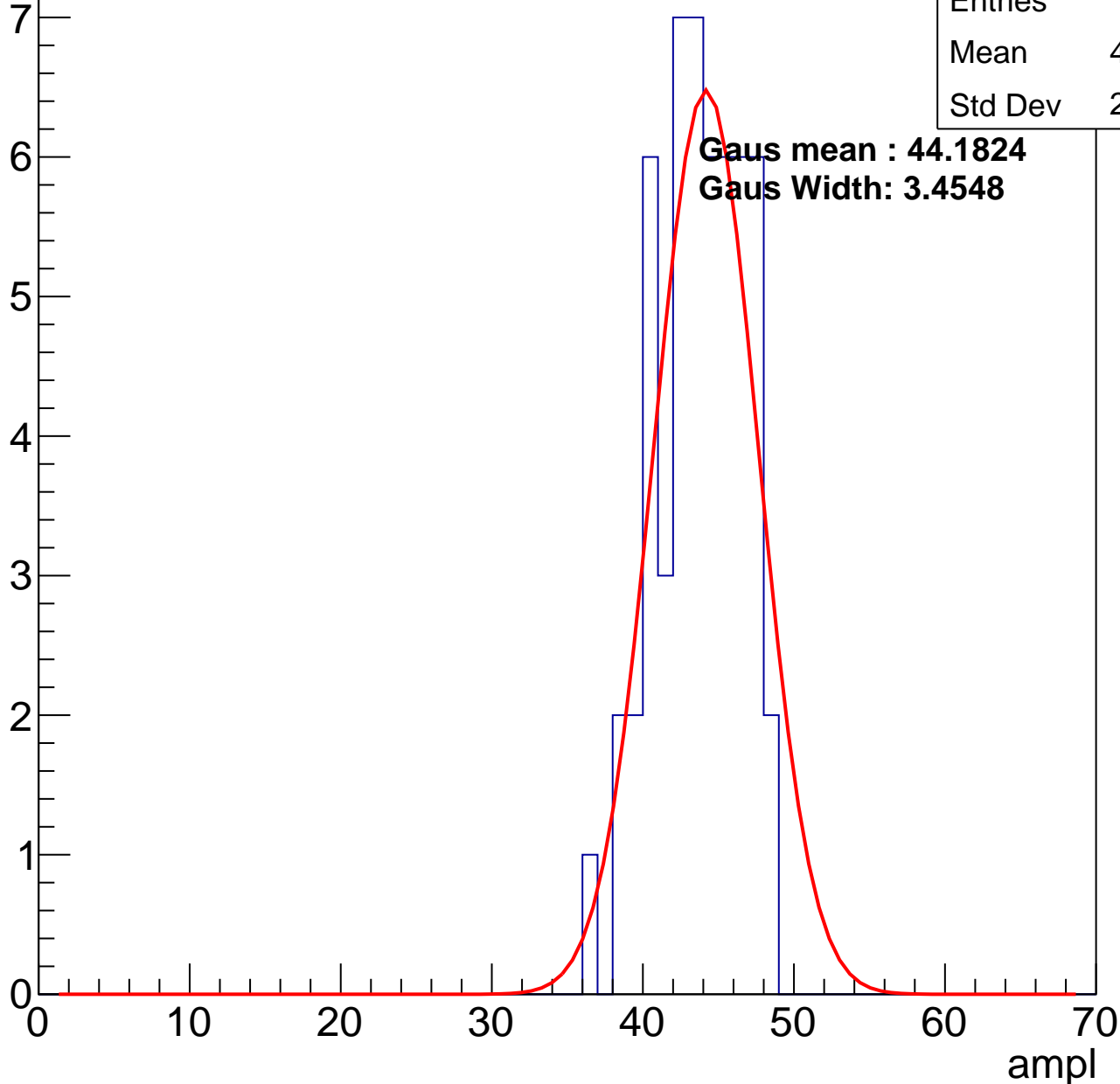


# B1L003S, U22-ch39, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	43.26
Std Dev	2.823

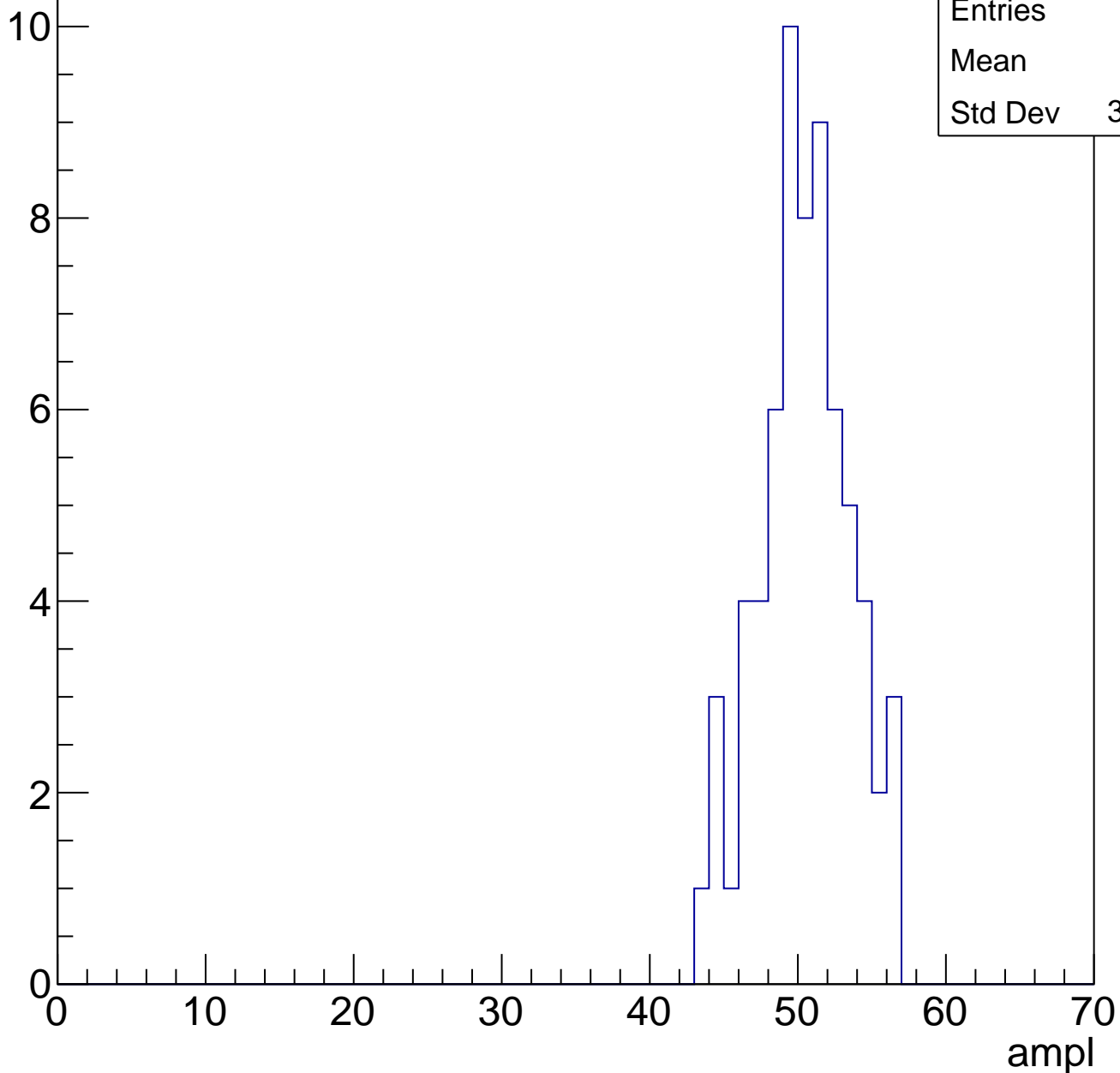


# B1L003S, U22-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	66
Mean	50
Std Dev	3.055

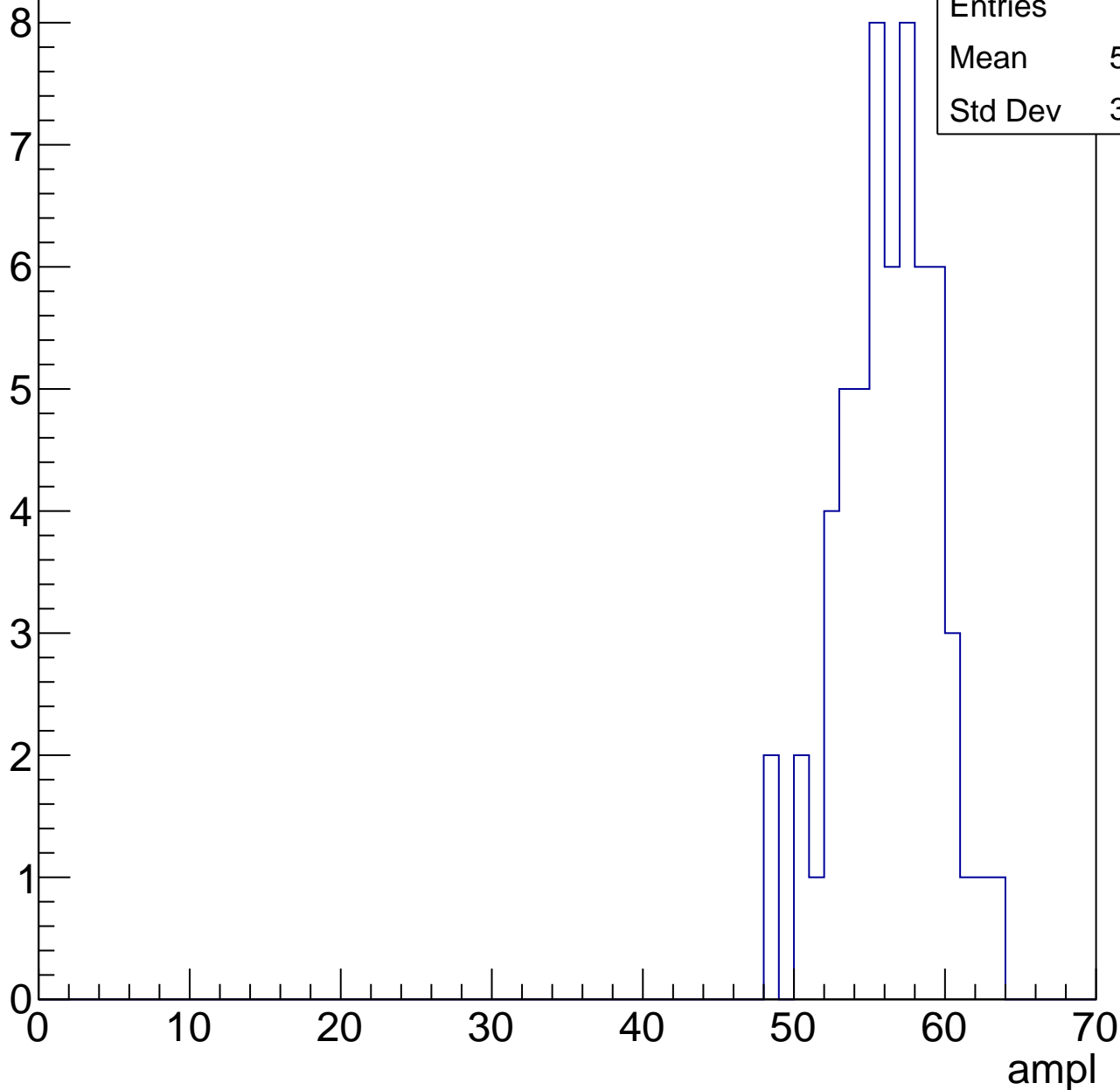
Entry



# B1L003S, U22-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



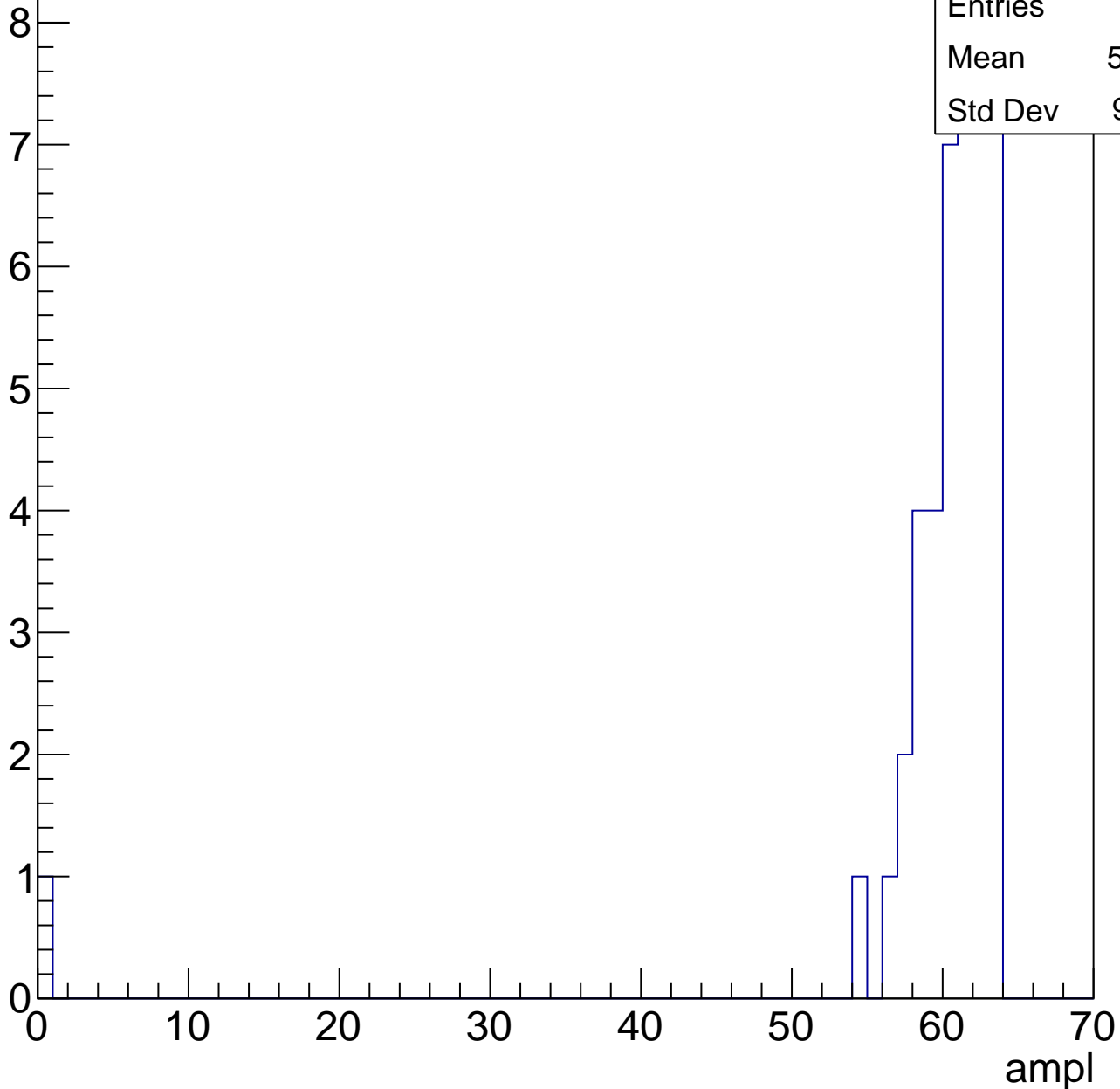
Entries	59
Mean	55.76
Std Dev	3.196

# B1L003S, U22-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.09
Std Dev	9.251



# B1L003S, U22-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch39, adc7

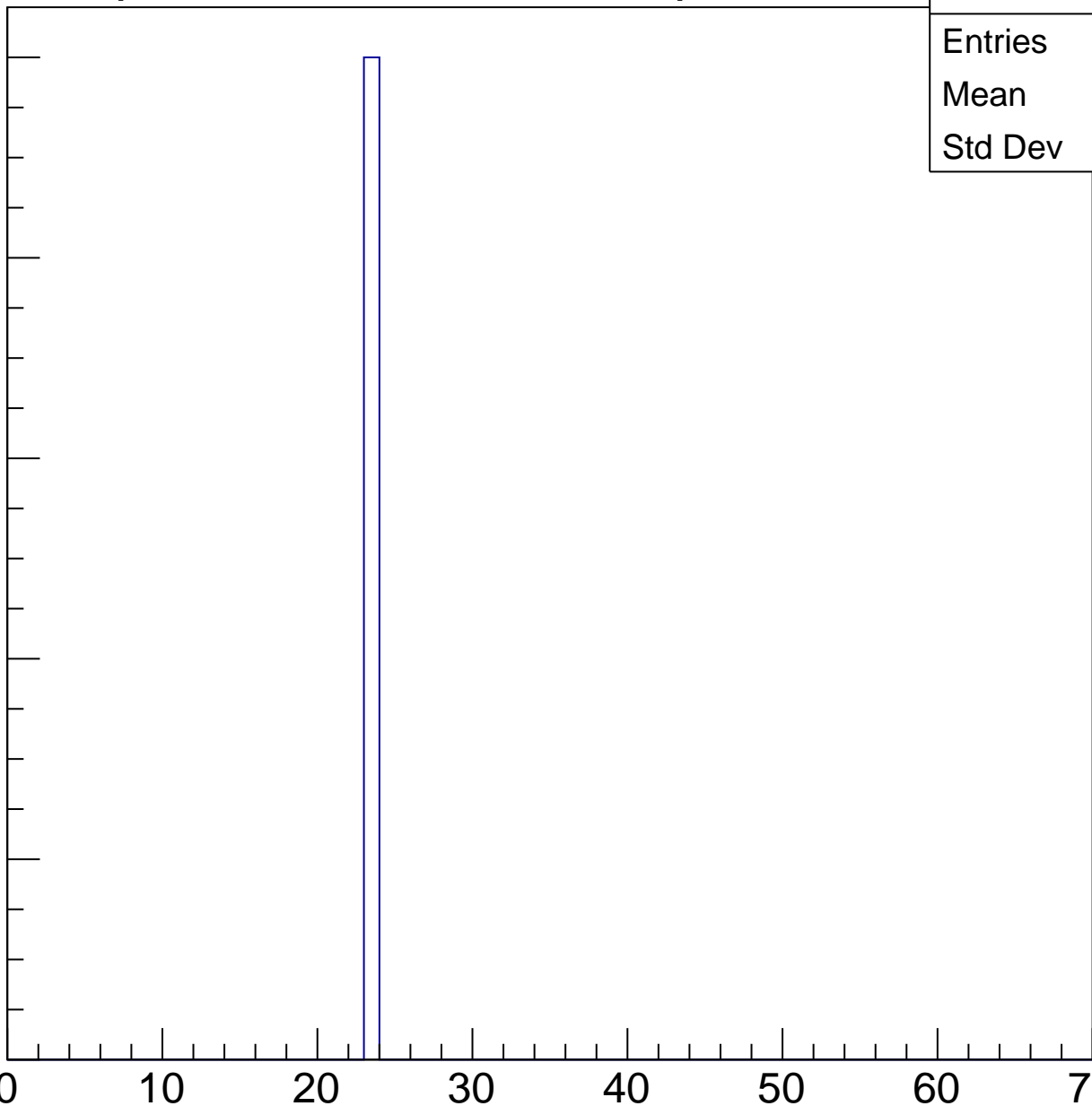
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl



# B1L003S, U22-ch40, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	68
Mean	28.37
Std Dev	3.535

**Gaus mean : 28.7863**

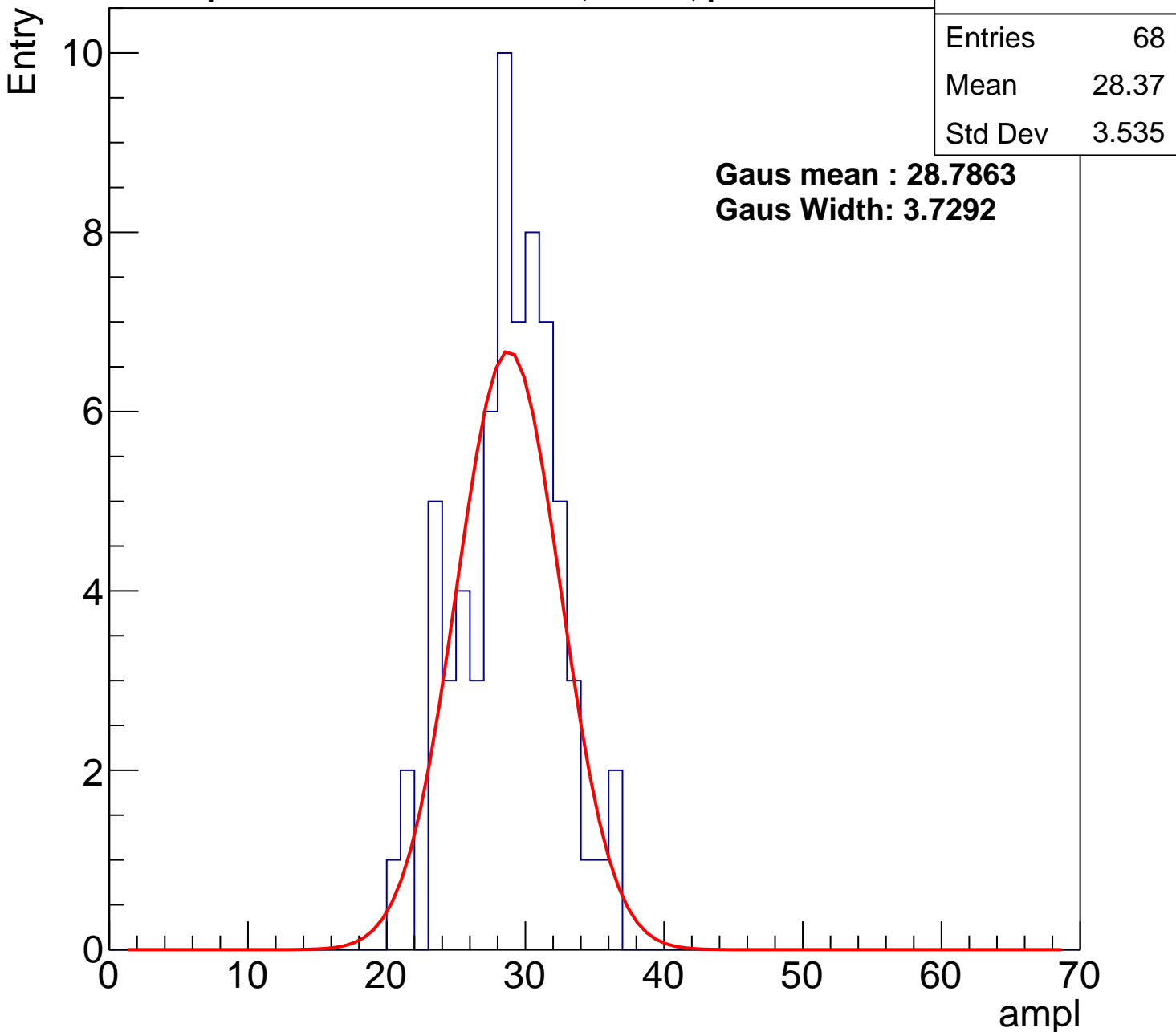
**Gaus Width: 3.7292**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch40, adc1

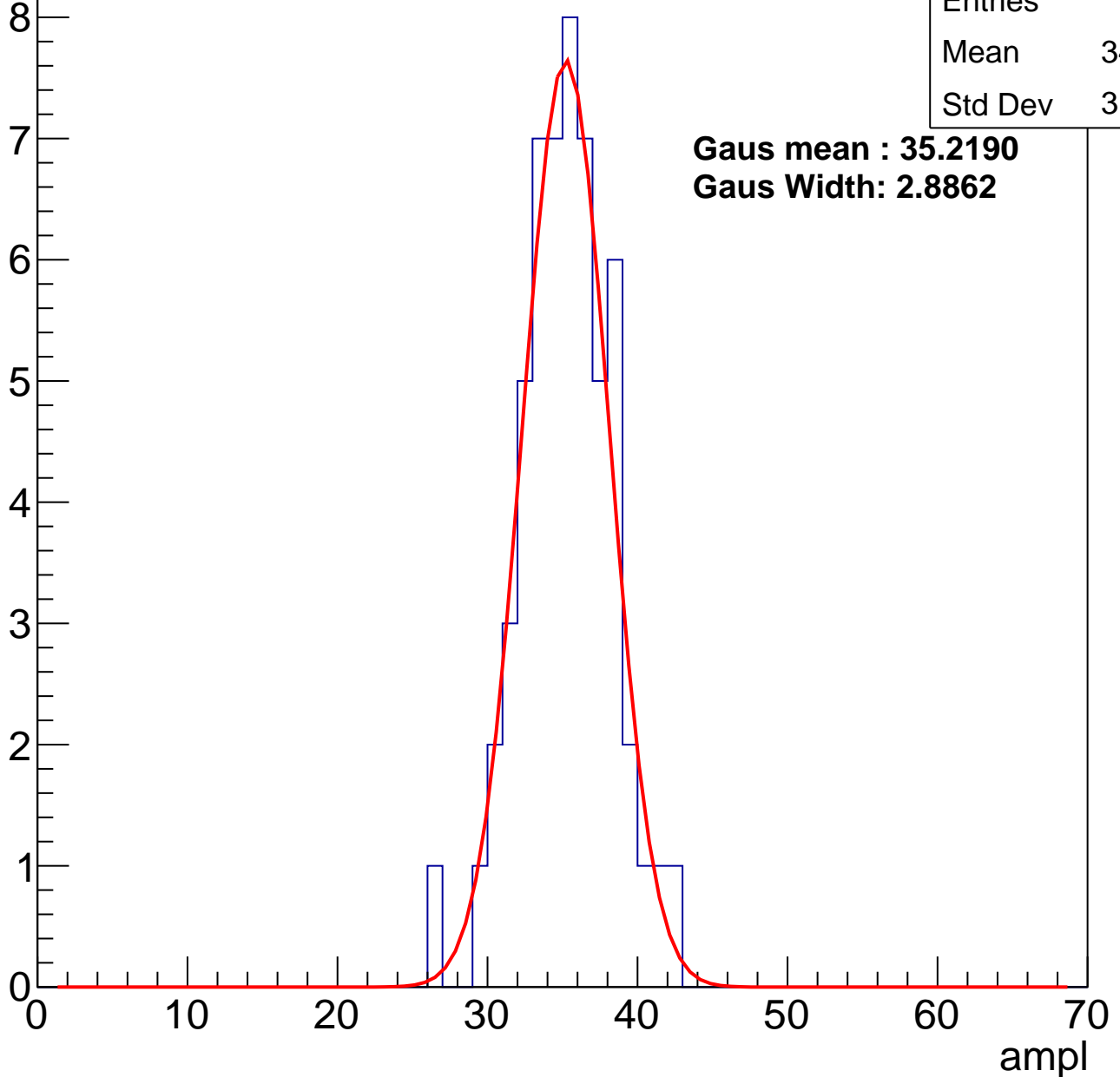
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	34.79
Std Dev	3.007

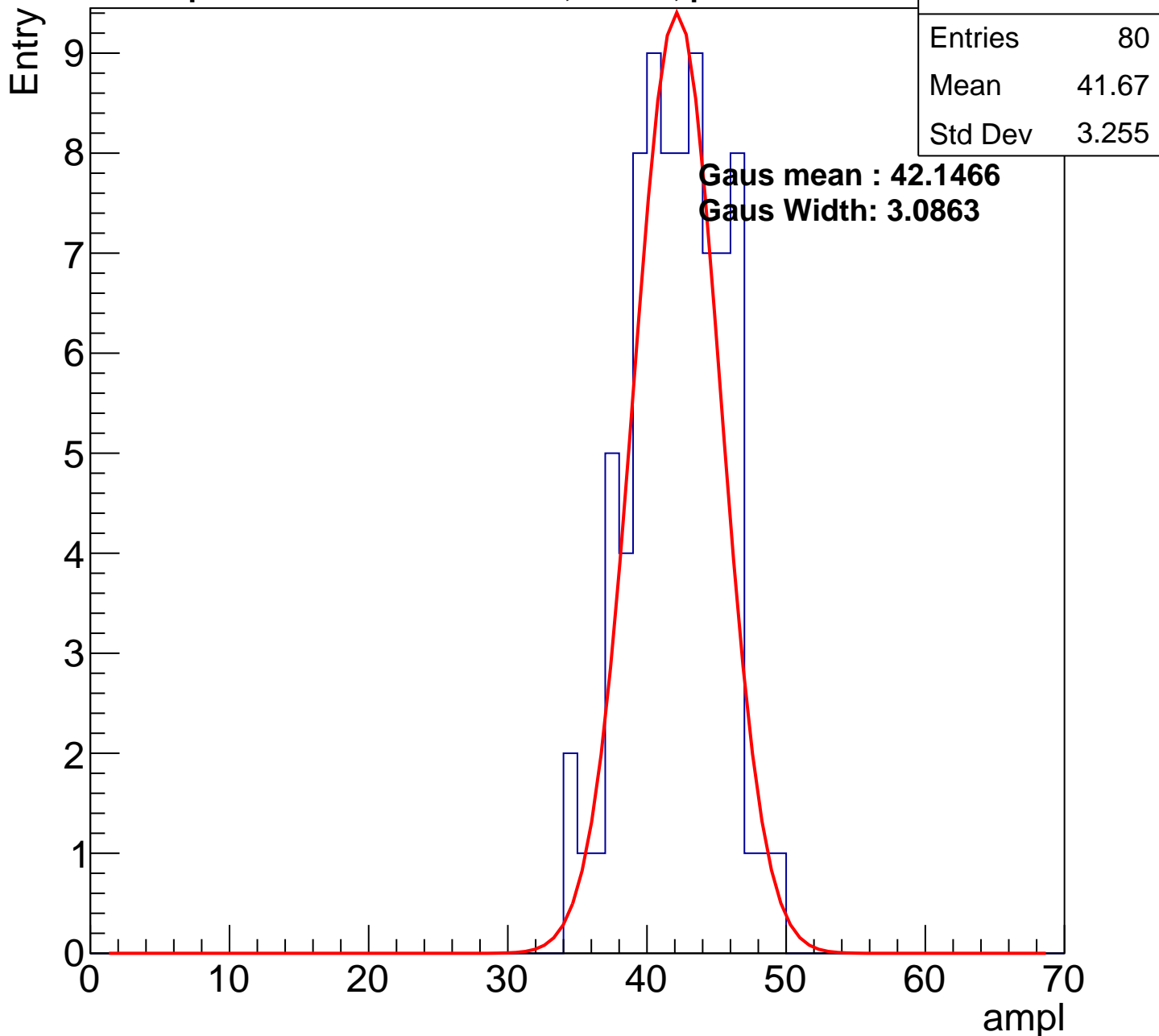
**Gaus mean : 35.2190**

**Gaus Width: 2.8862**



# B1L003S, U22-ch40, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

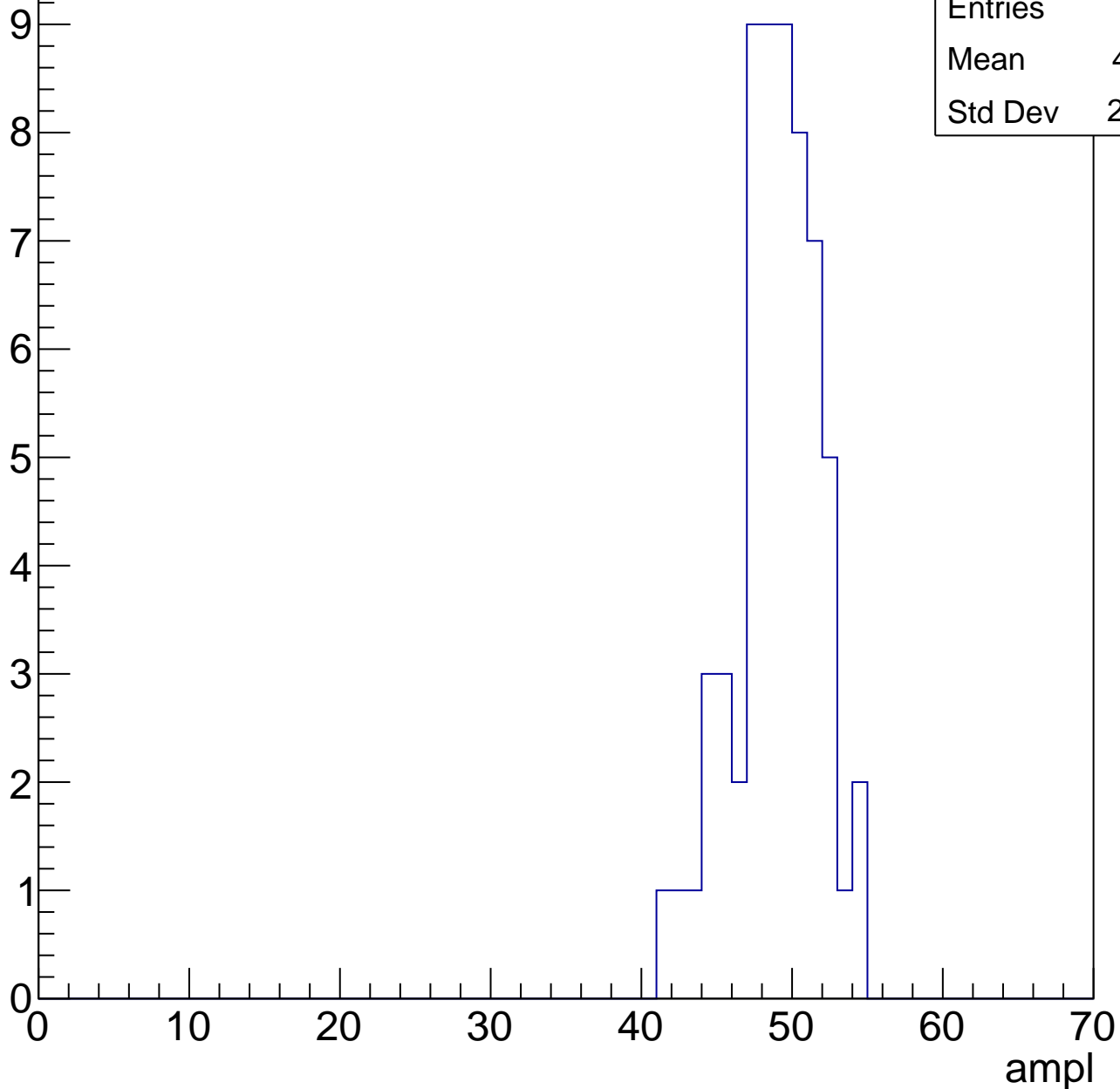


# B1L003S, U22-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.51
Std Dev	2.768

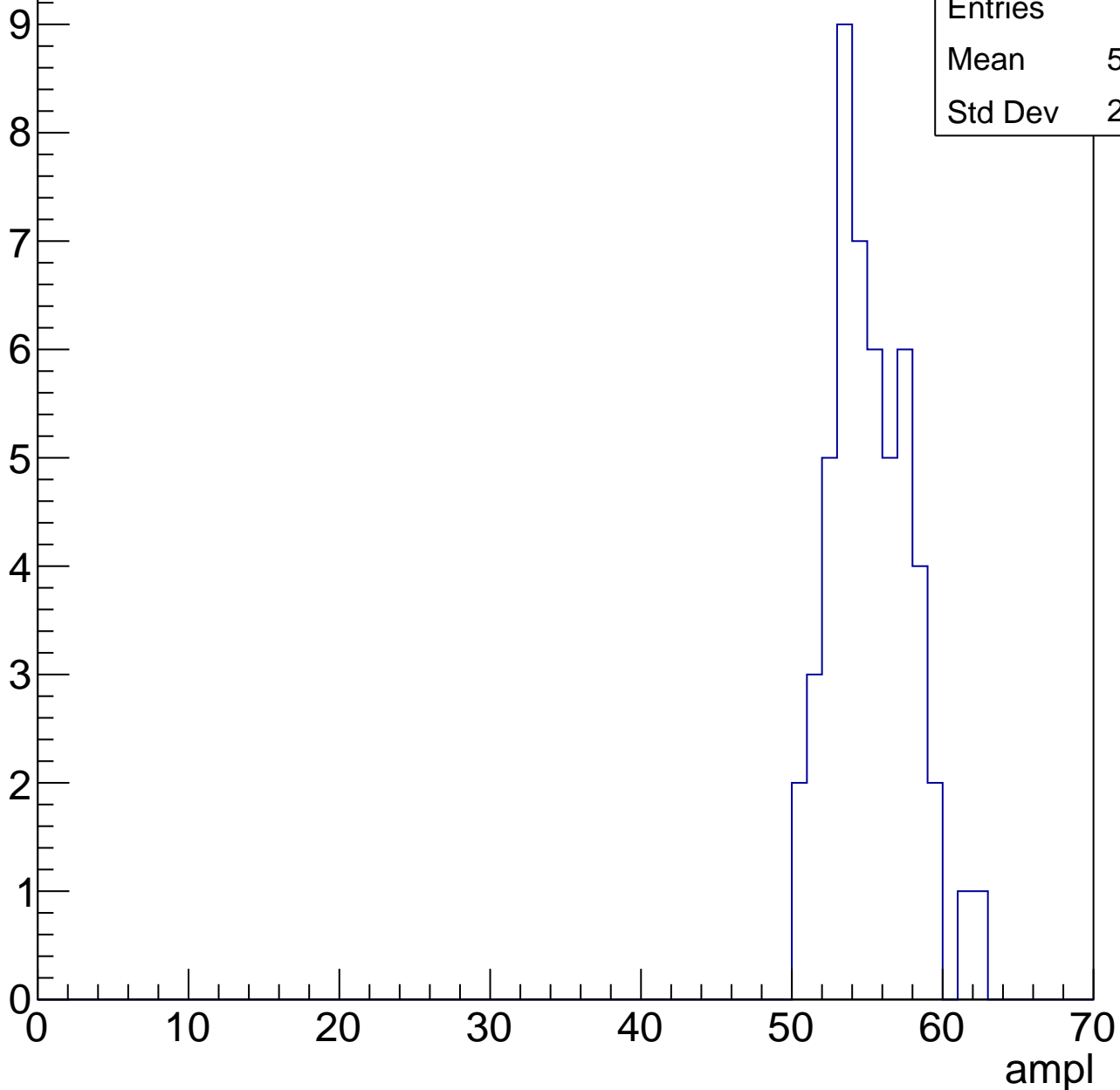


# B1L003S, U22-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

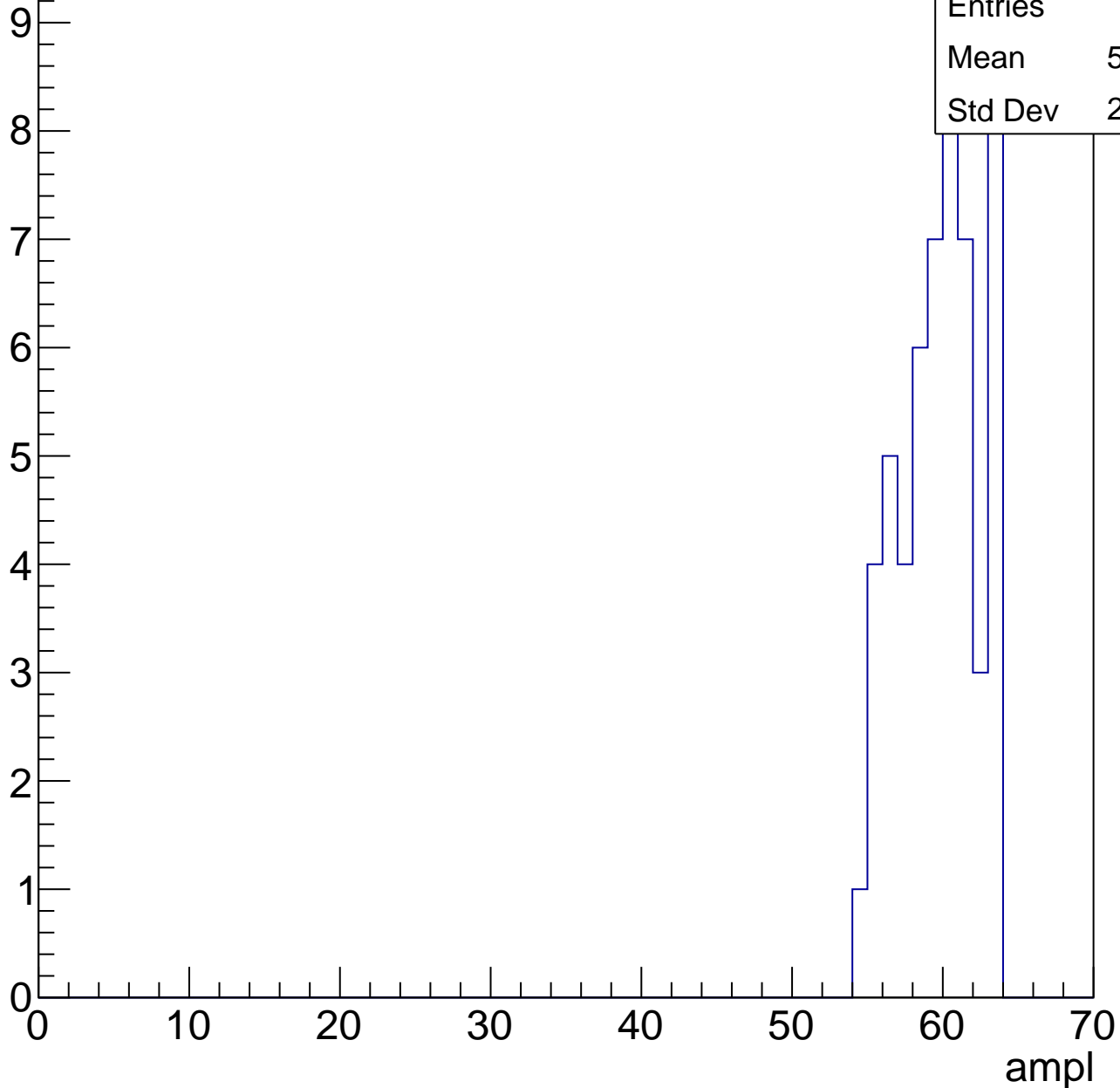
Entries	51
Mean	54.76
Std Dev	2.676



# B1L003S, U22-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

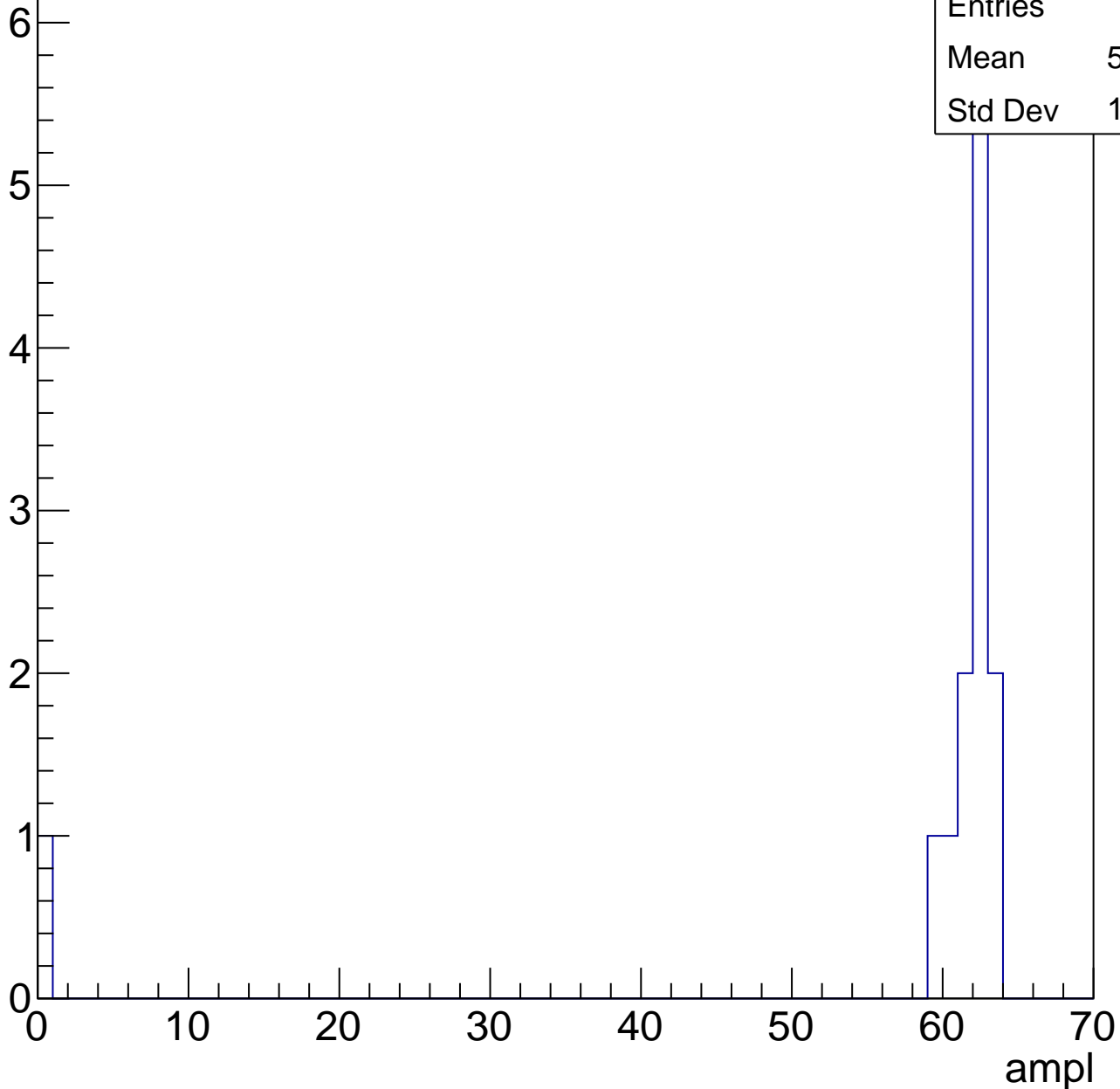


# B1L003S, U22-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	13
Mean	56.85
Std Dev	16.44





# B1L003S, U22-ch40, adc7

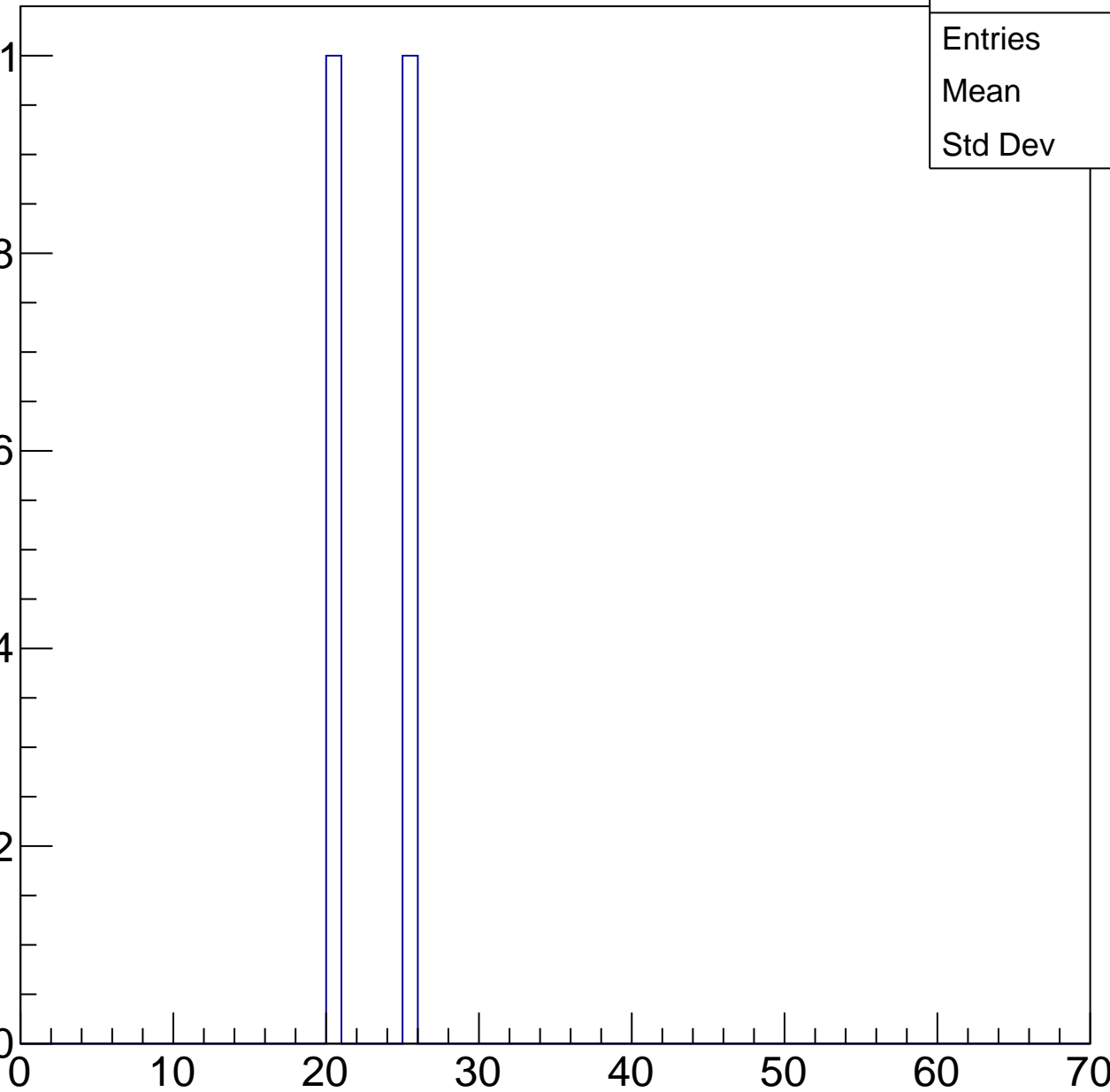
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	22.5
Std Dev	2.5

ampl



# B1L003S, U22-ch41, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	30.08
Std Dev	4.733

**Gaus mean : 30.7059**

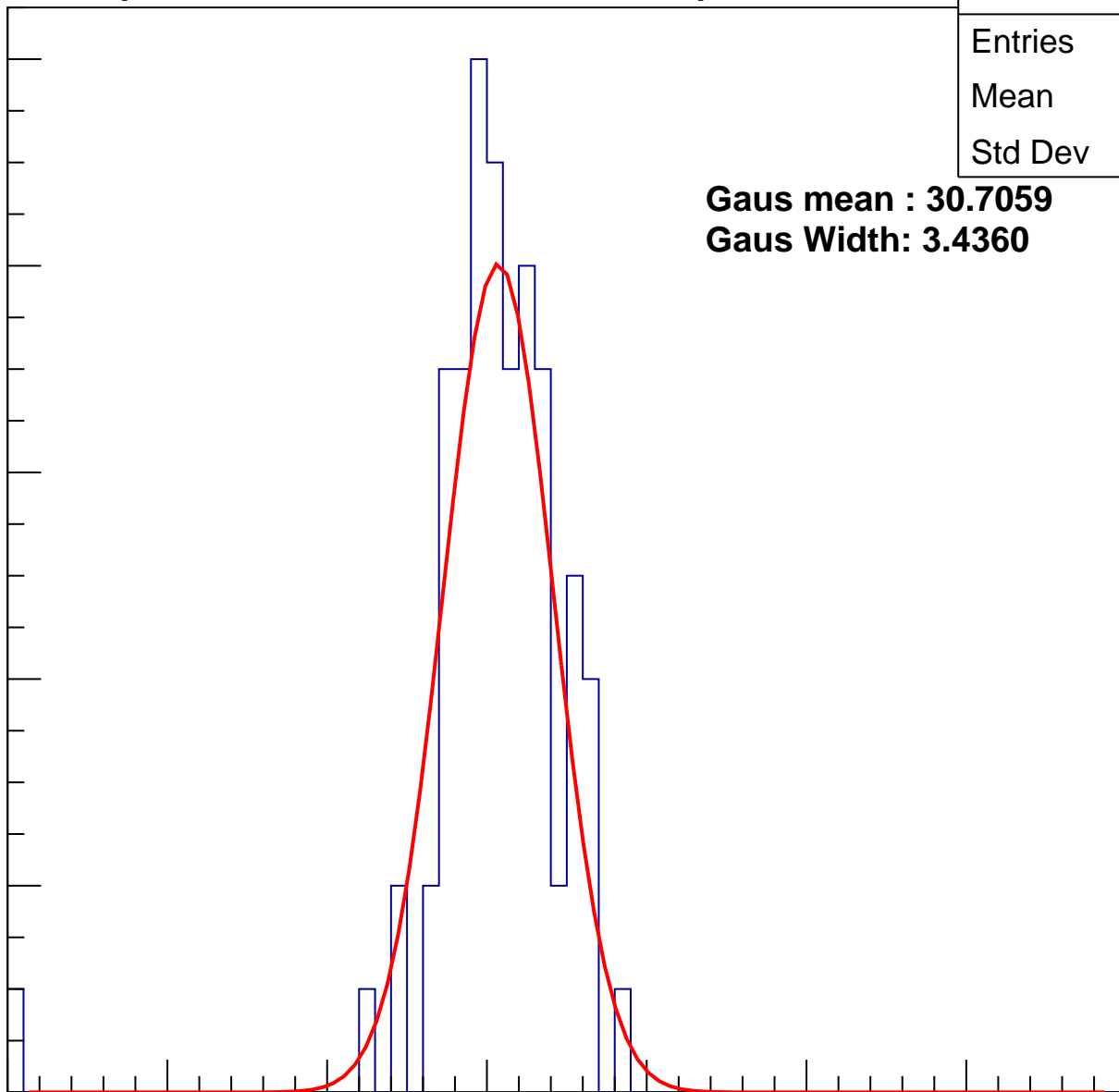
**Gaus Width: 3.4360**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U22-ch41, adc1

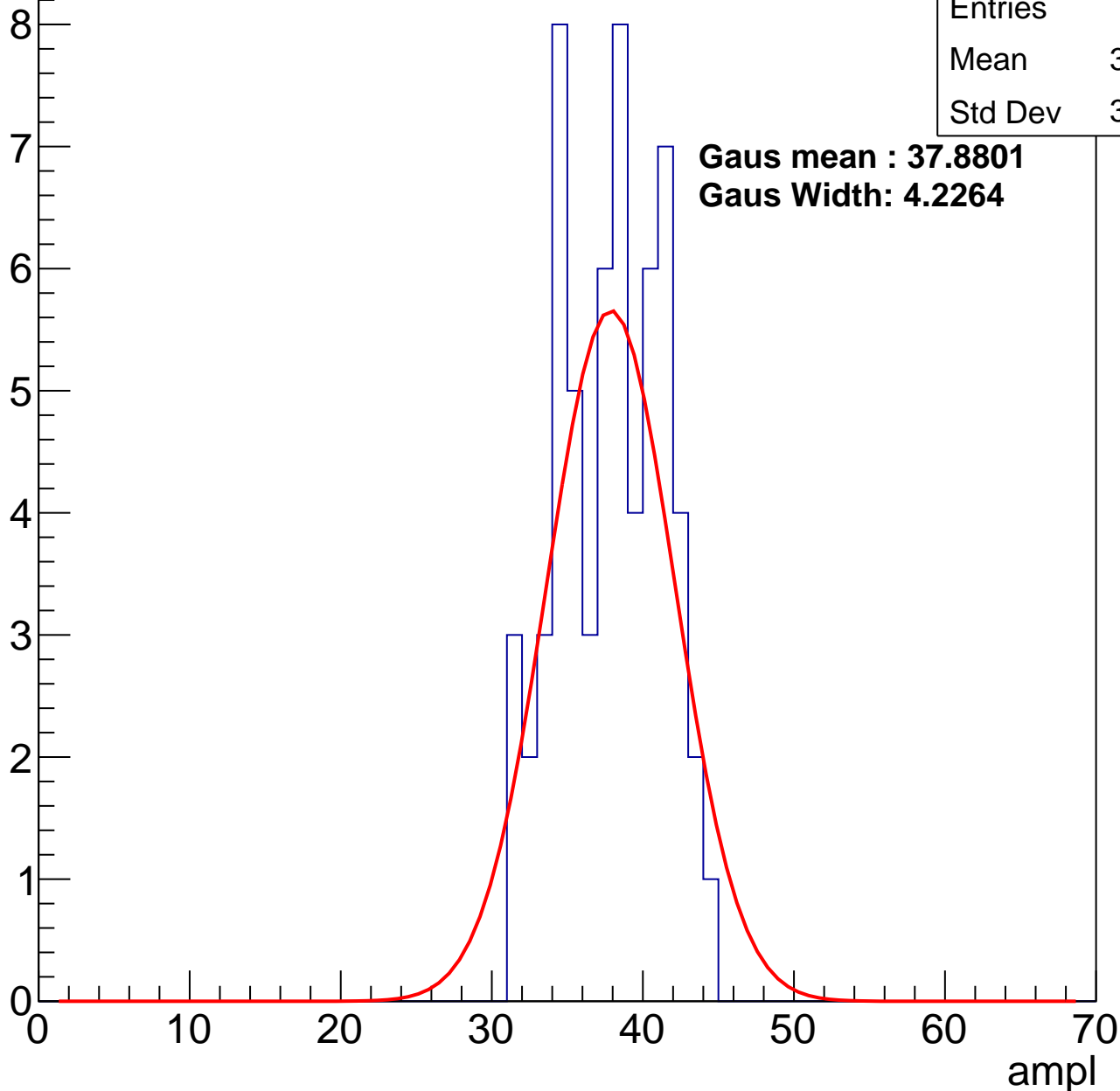
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.39
Std Dev	3.367

**Gaus mean : 37.8801**

**Gaus Width: 4.2264**

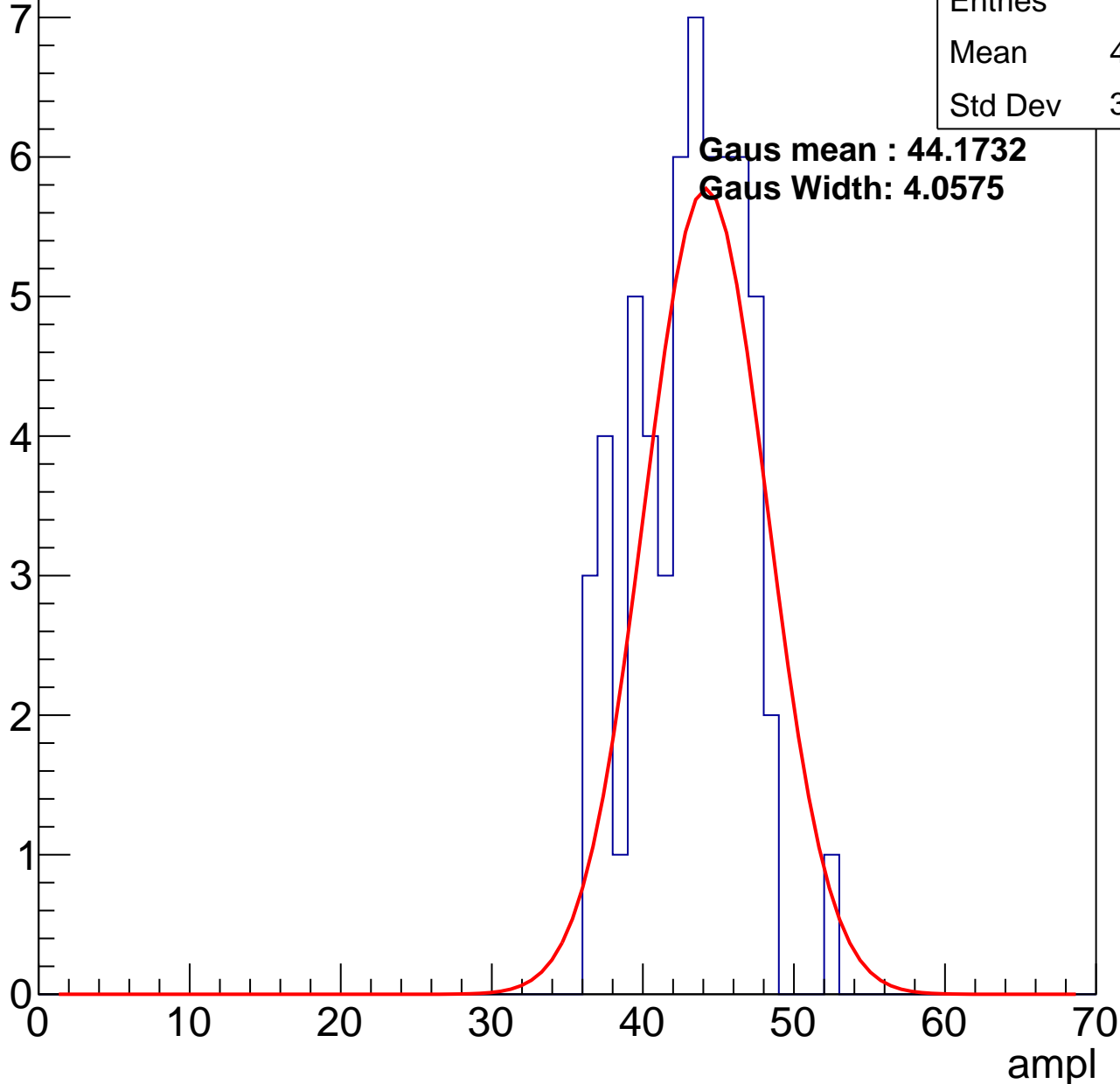


# B1L003S, U22-ch41, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	42.68
Std Dev	3.539

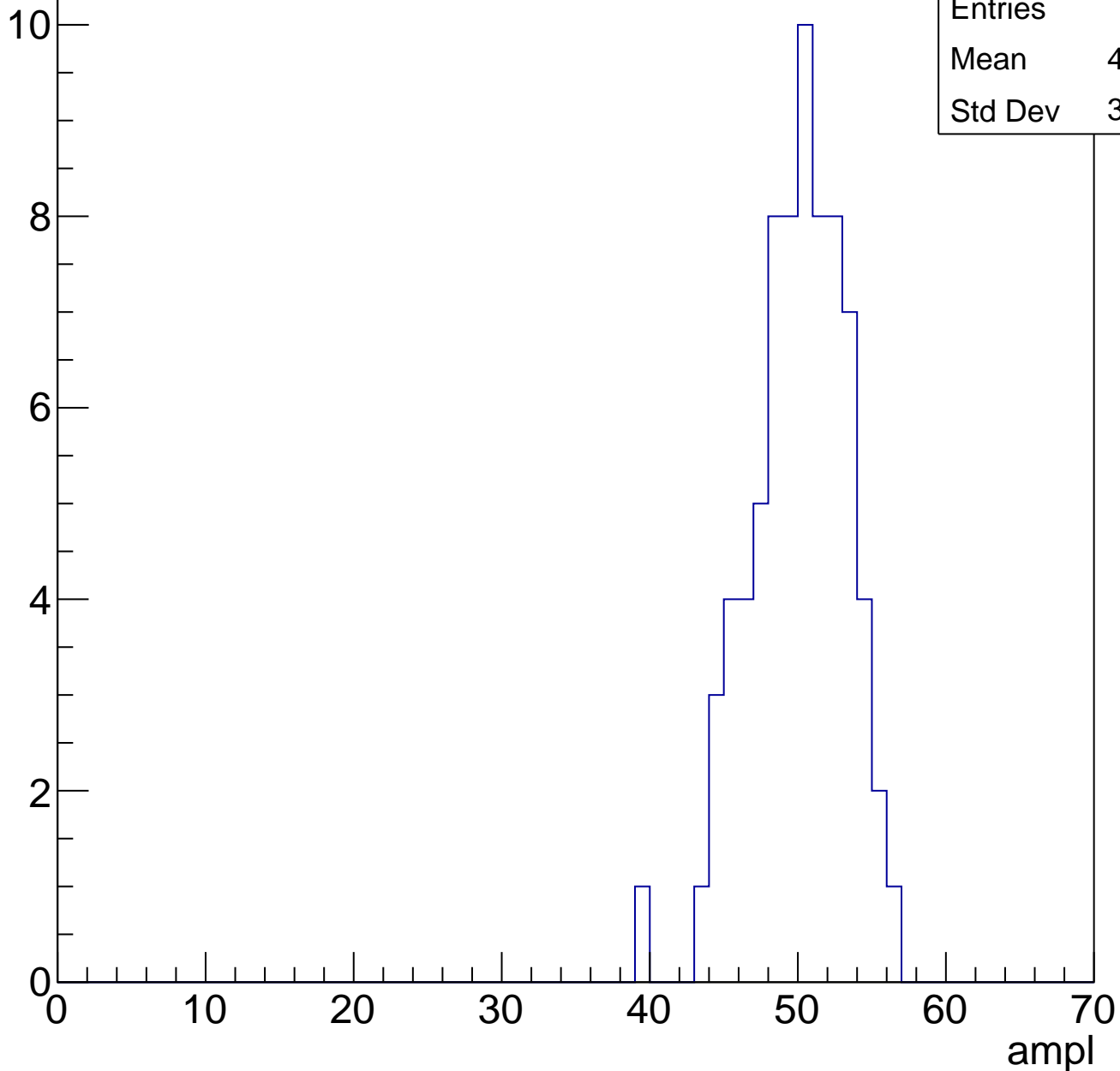


# B1L003S, U22-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.54
Std Dev	3.214

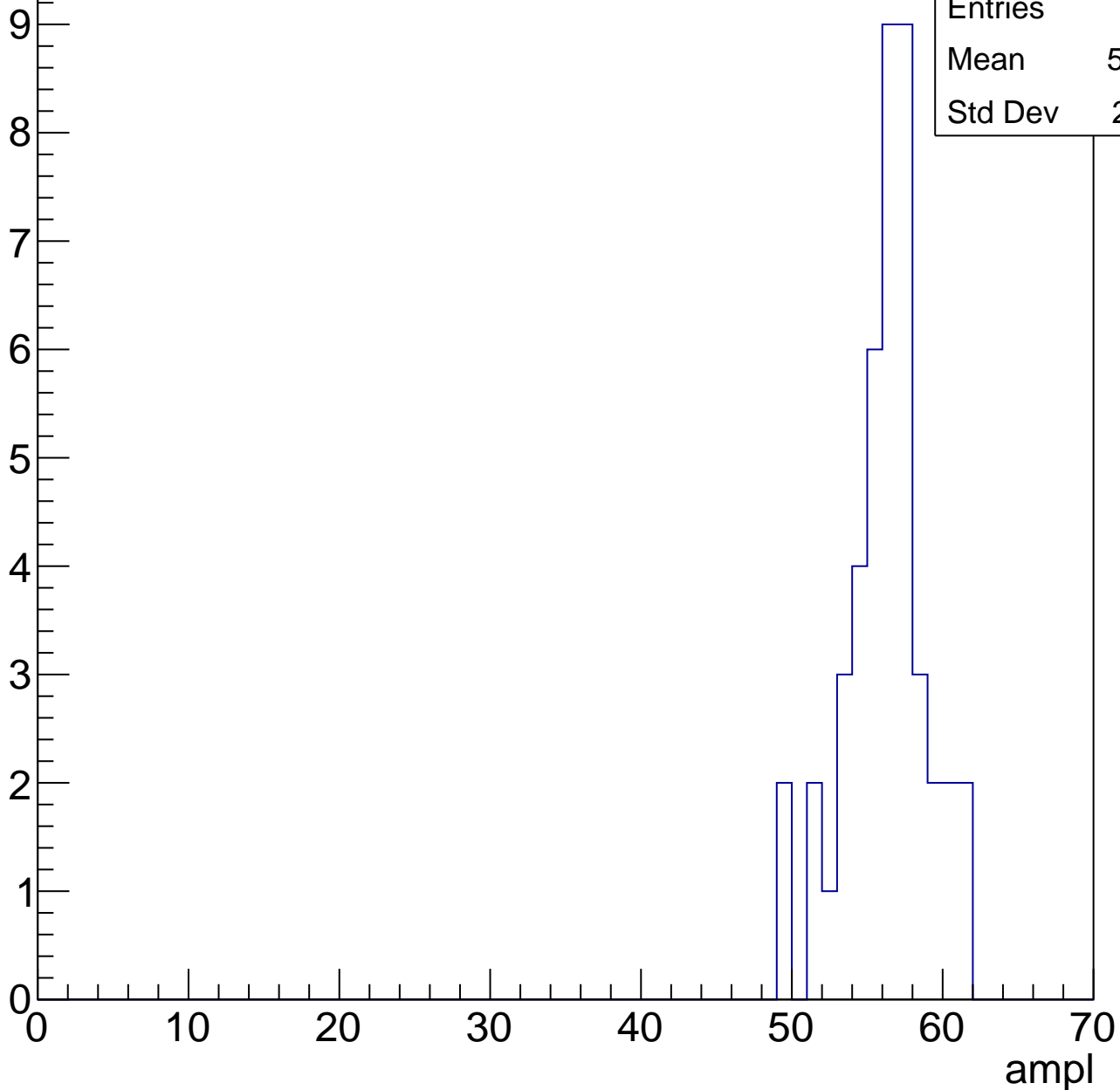


# B1L003S, U22-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

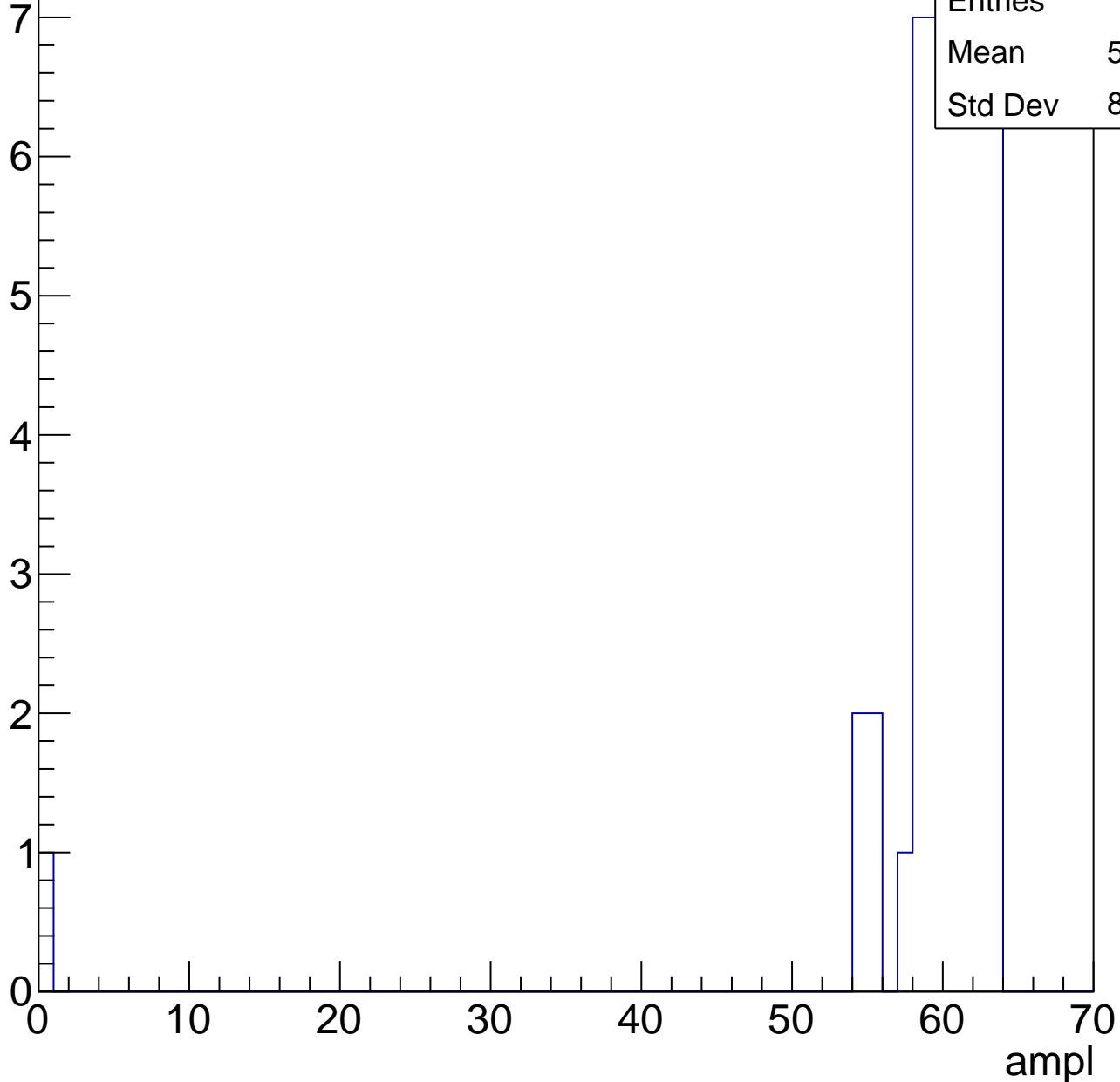
Entries	45
Mean	55.73
Std Dev	2.711



# B1L003S, U22-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

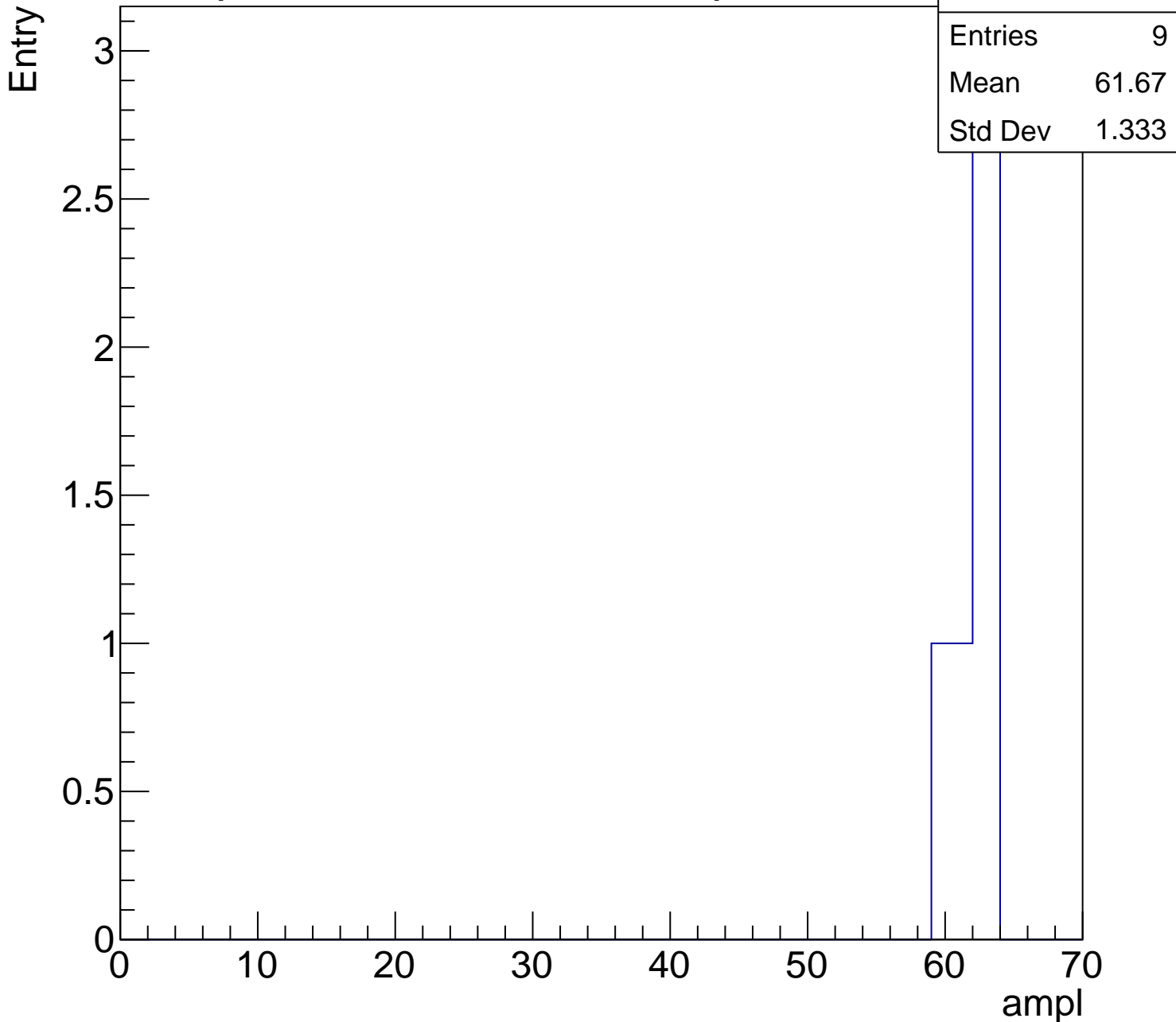
9

Mean

61.67

Std Dev

1.333





# B1L003S, U22-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch42, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	28.63
Std Dev	4.783

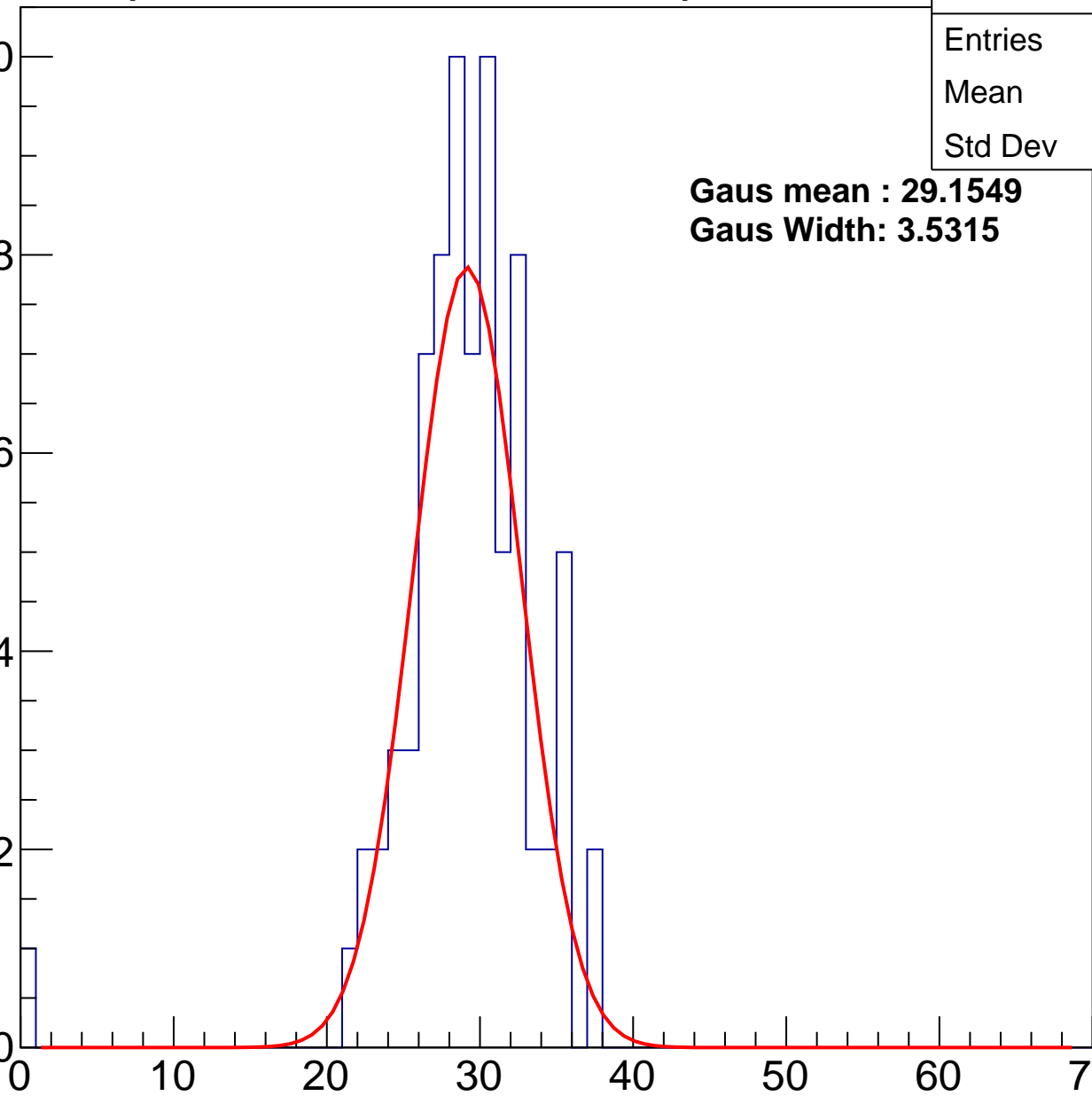
**Gaus mean : 29.1549**

**Gaus Width: 3.5315**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch42, adc1

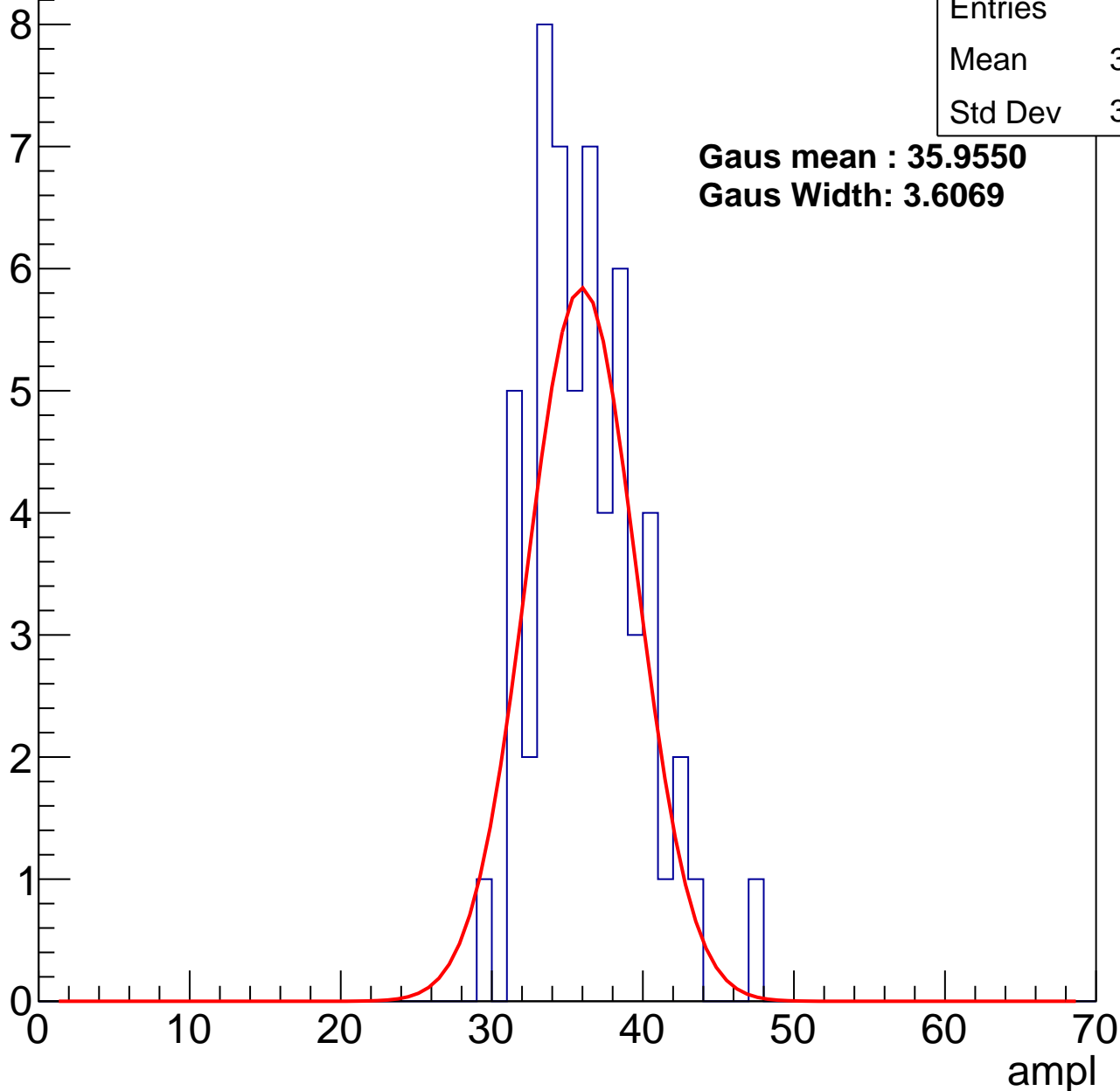
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.88
Std Dev	3.495

**Gaus mean : 35.9550**

**Gaus Width: 3.6069**



# B1L003S, U22-ch42, adc2

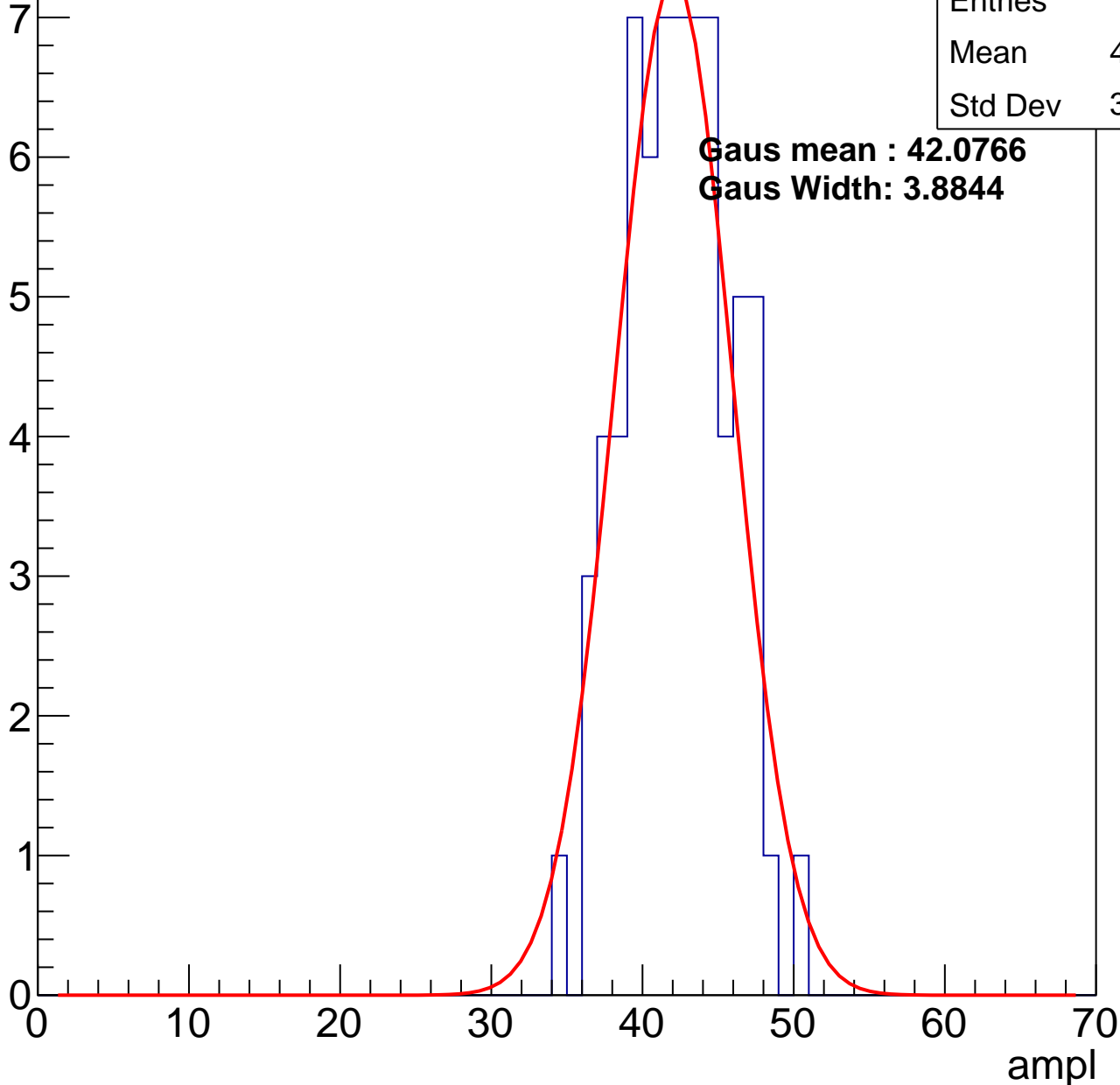
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.86
Std Dev	3.415

**Gaus mean : 42.0766**

**Gaus Width: 3.8844**

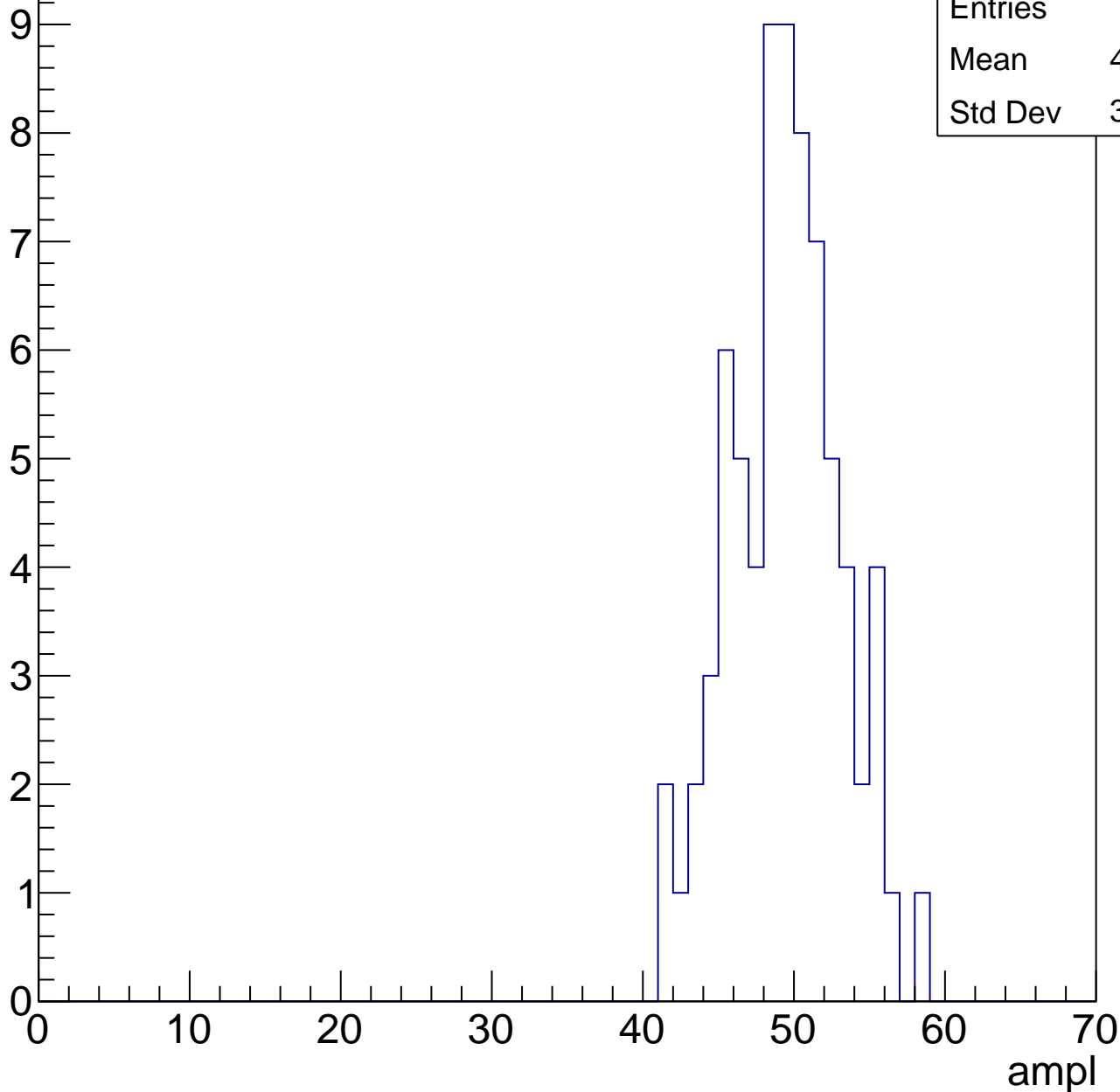


# B1L003S, U22-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	48.96
Std Dev	3.624

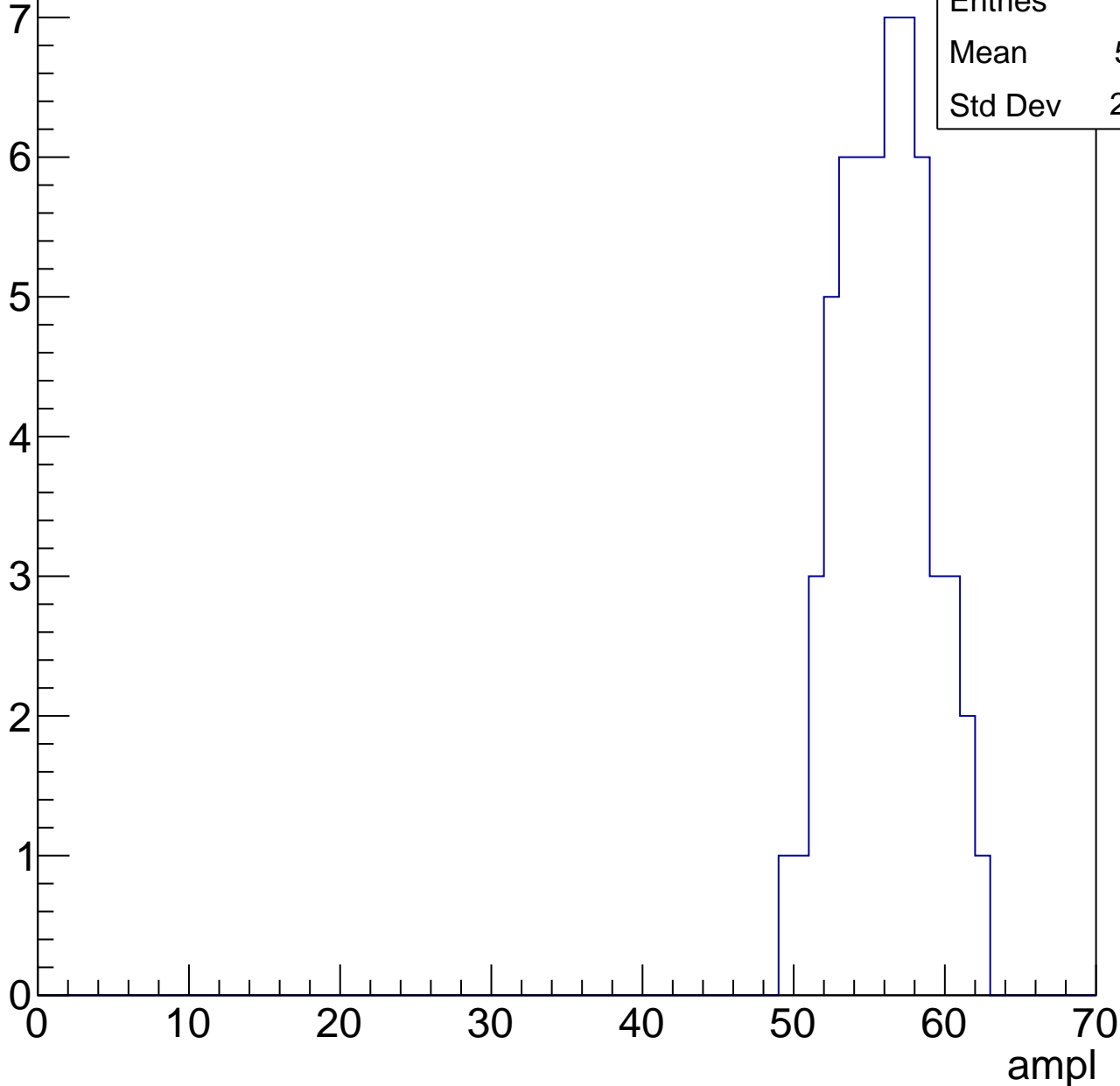


# B1L003S, U22-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	55.51
Std Dev	2.968

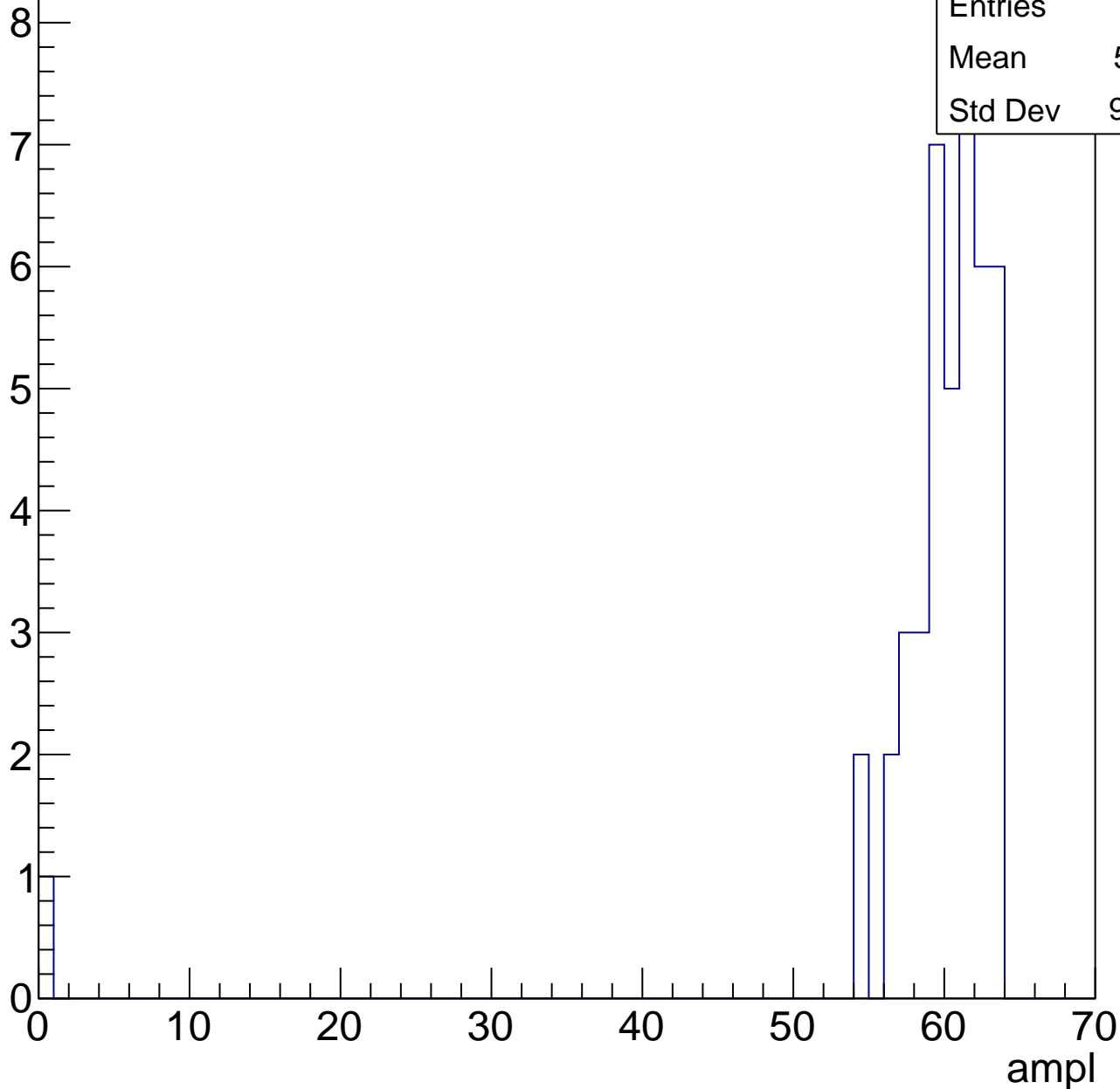


# B1L003S, U22-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

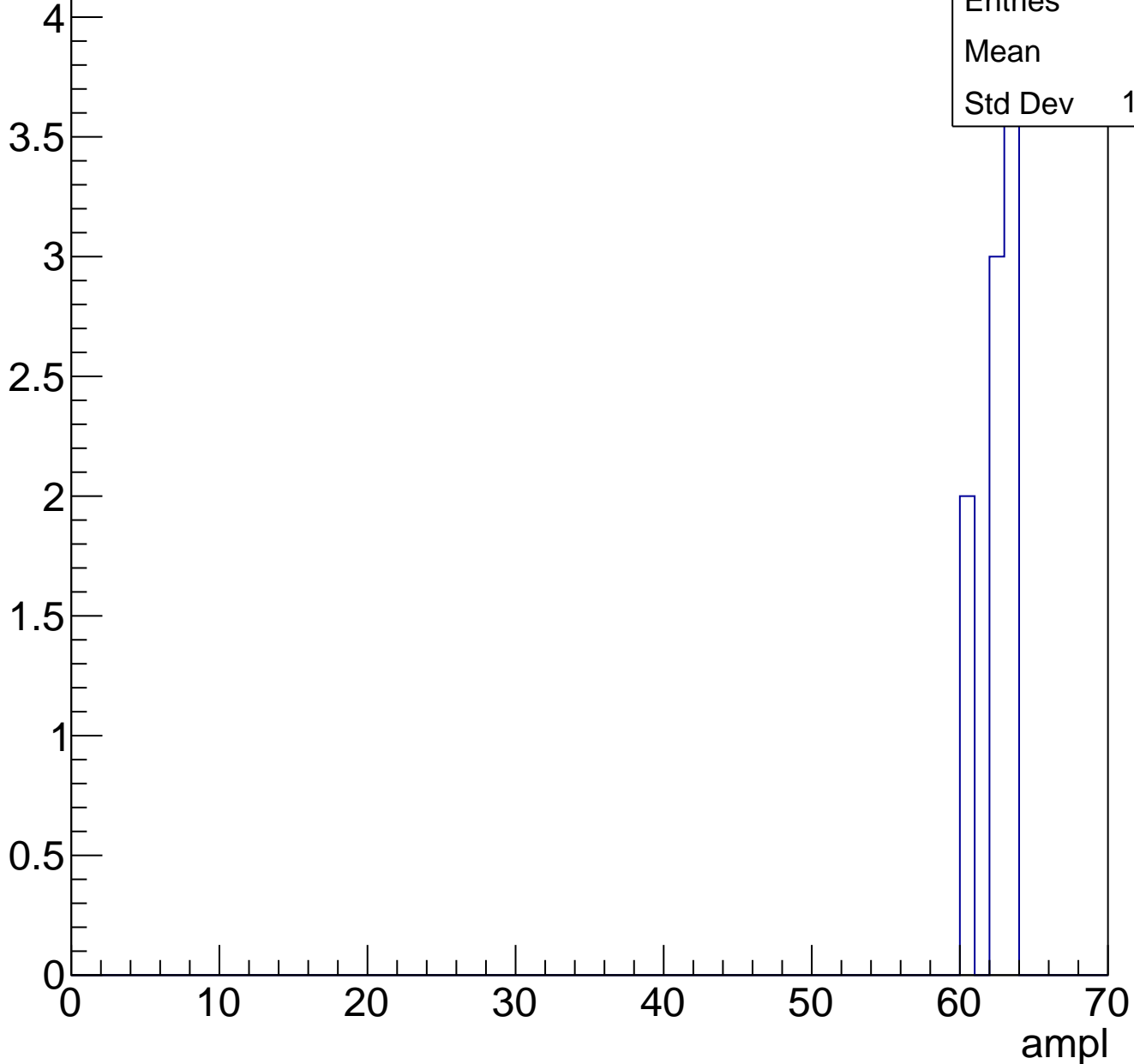
Entries	43
Mean	58.51
Std Dev	9.327



# B1L003S, U22-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch43, adc0

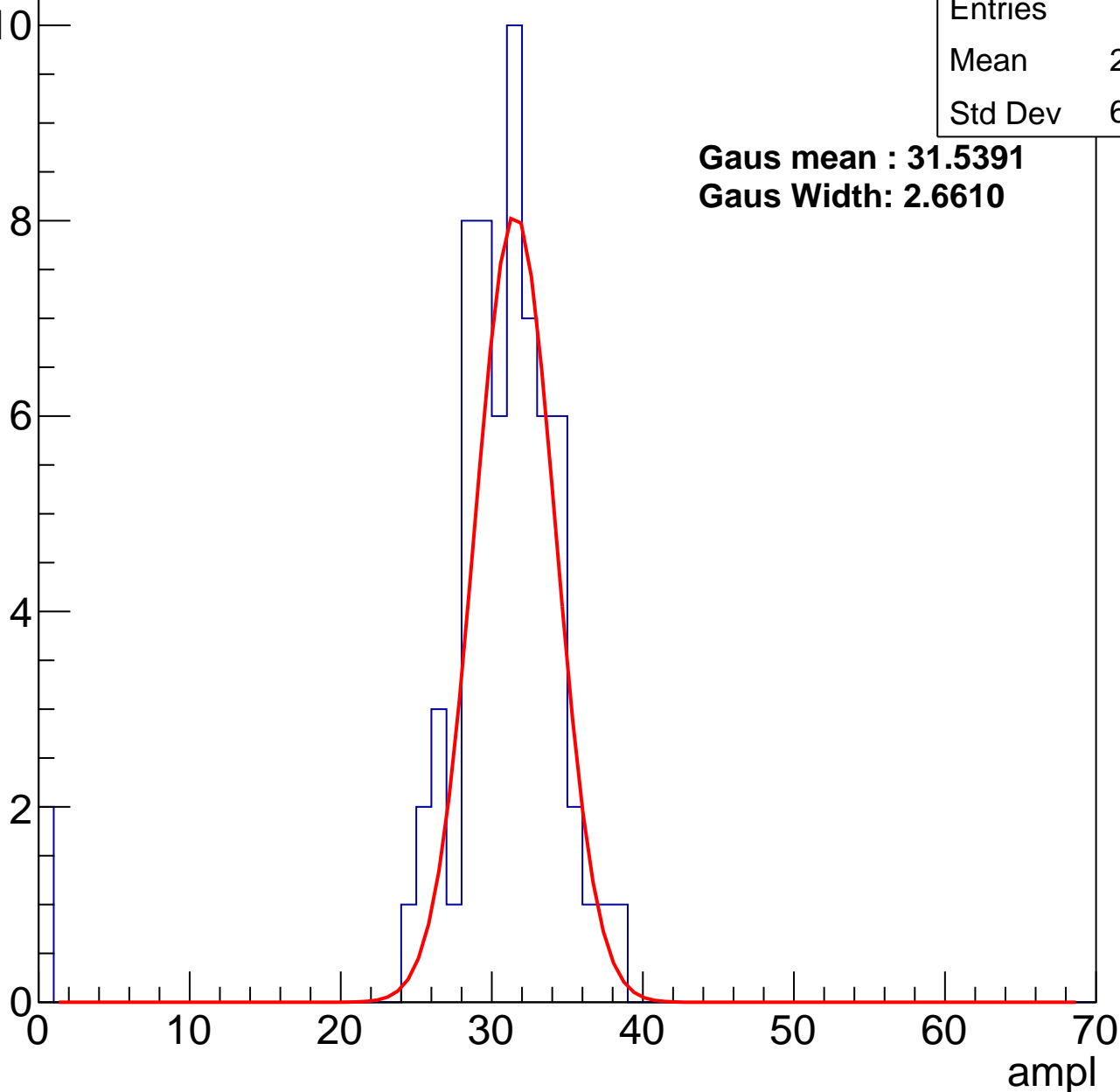
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.72
Std Dev	6.032

**Gaus mean : 31.5391**

**Gaus Width: 2.6610**



# B1L003S, U22-ch43, adc1

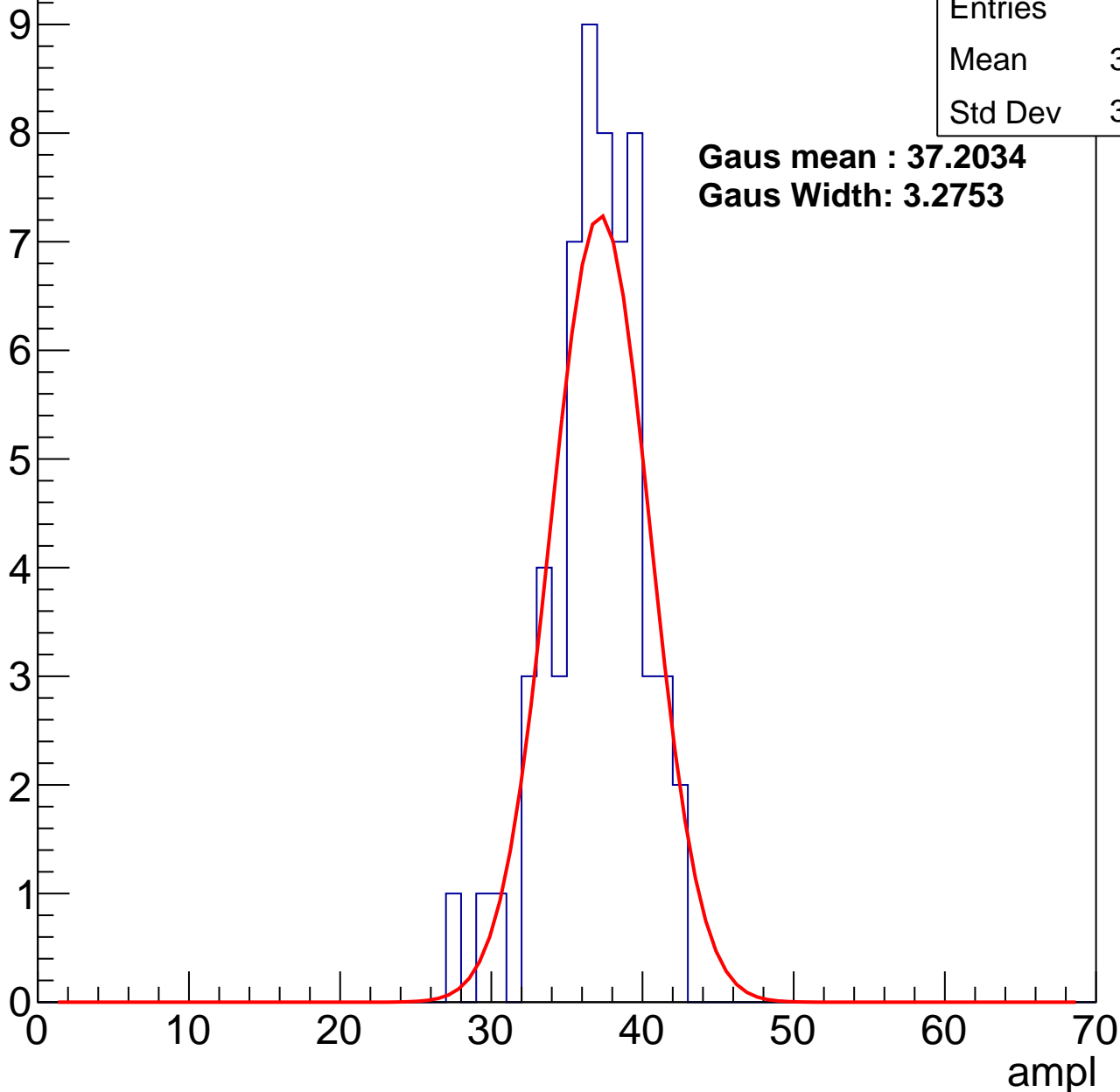
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.43
Std Dev	3.057

**Gaus mean : 37.2034**

**Gaus Width: 3.2753**



# B1L003S, U22-ch43, adc2

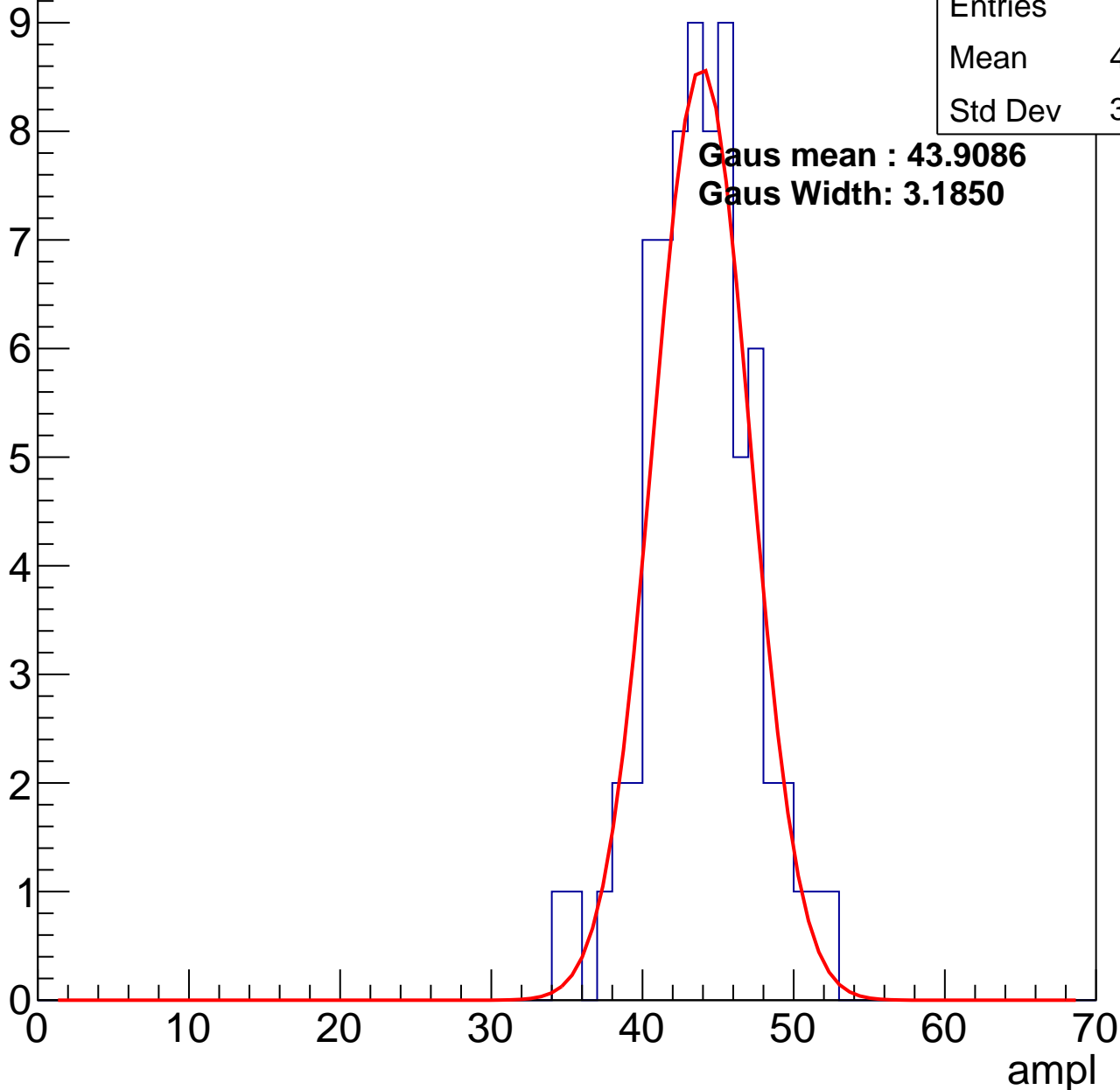
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.37
Std Dev	3.418

**Gaus mean : 43.9086**

**Gaus Width: 3.1850**

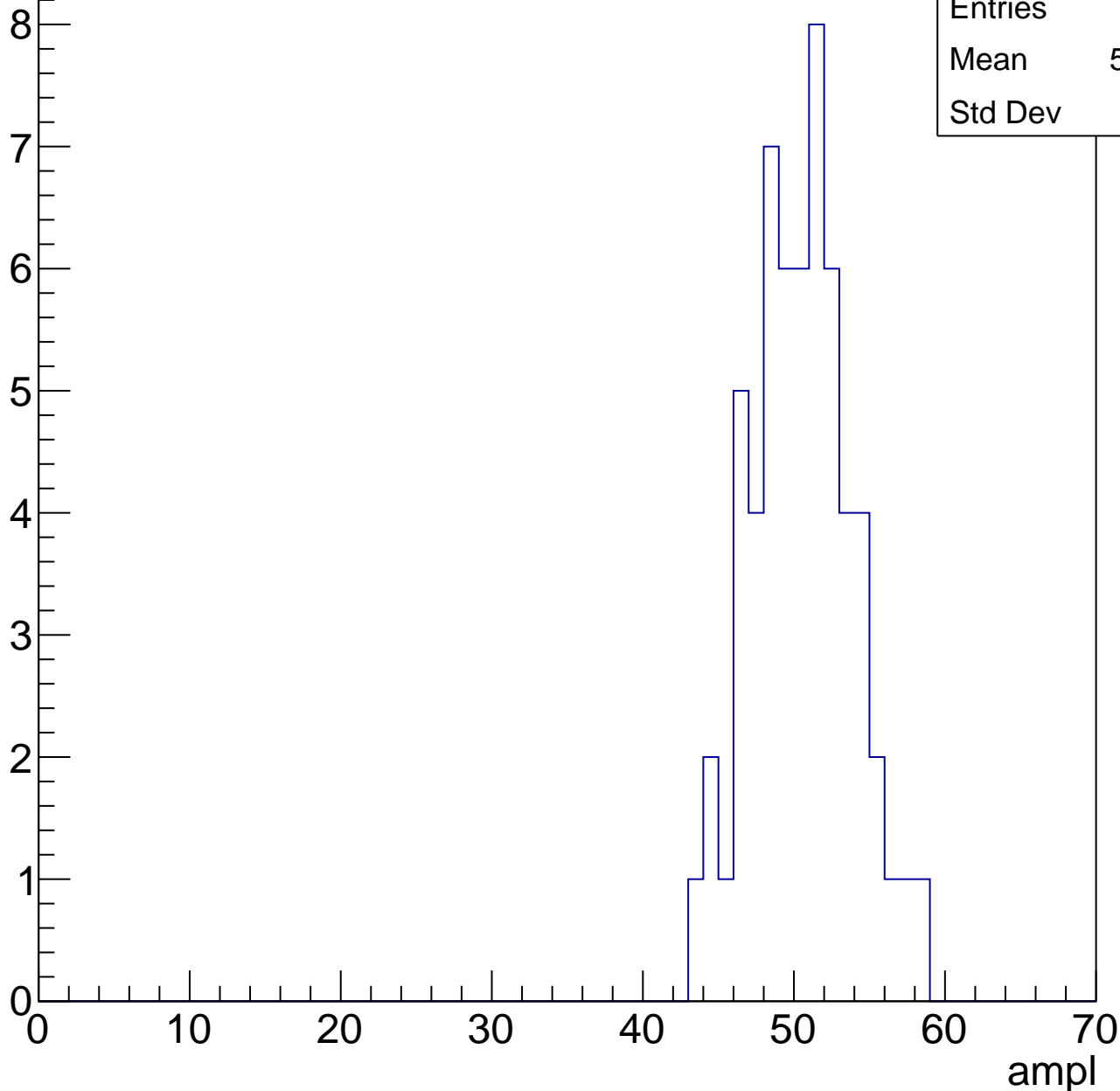


# B1L003S, U22-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	50.05
Std Dev	3.26

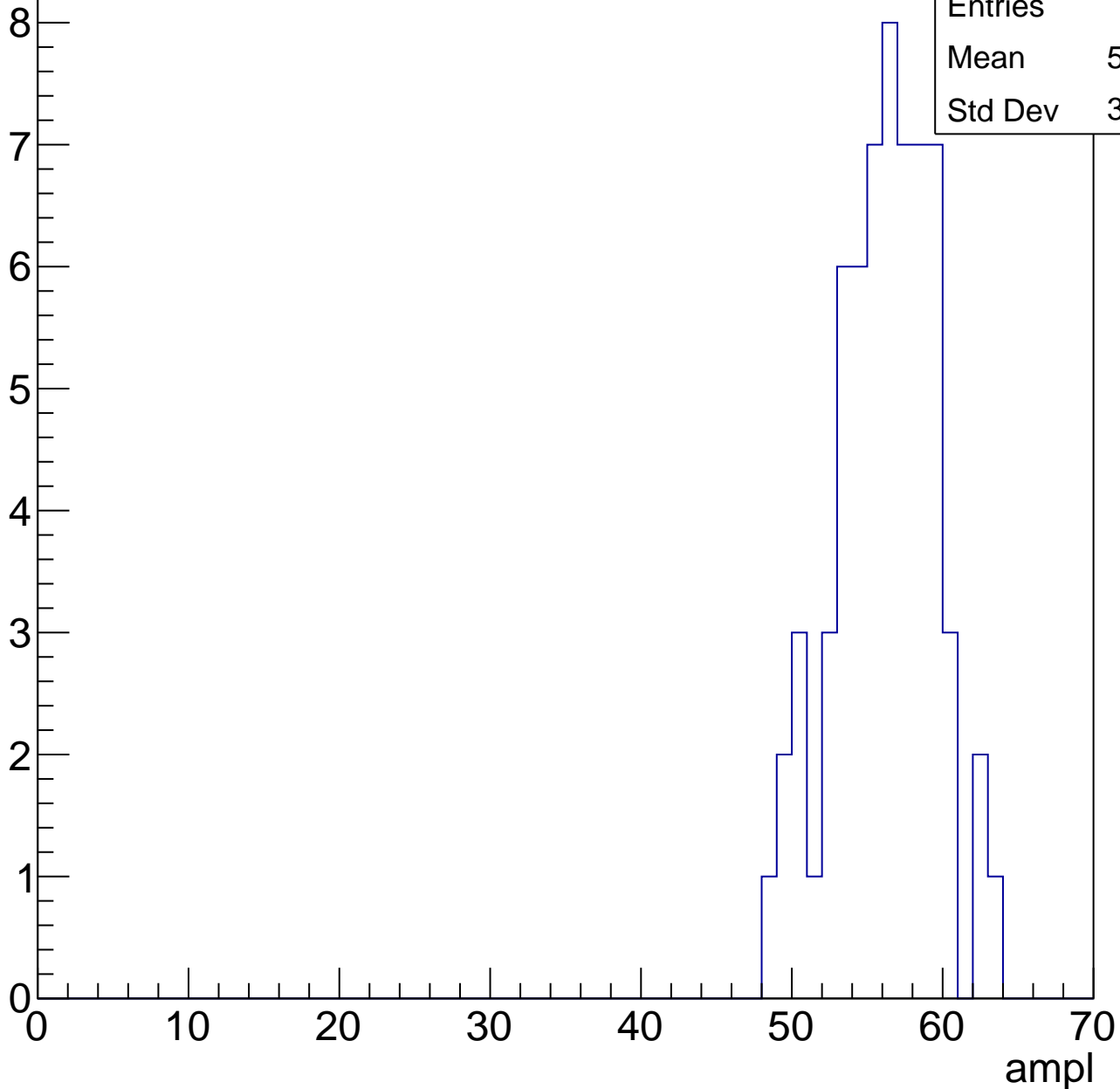


# B1L003S, U22-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

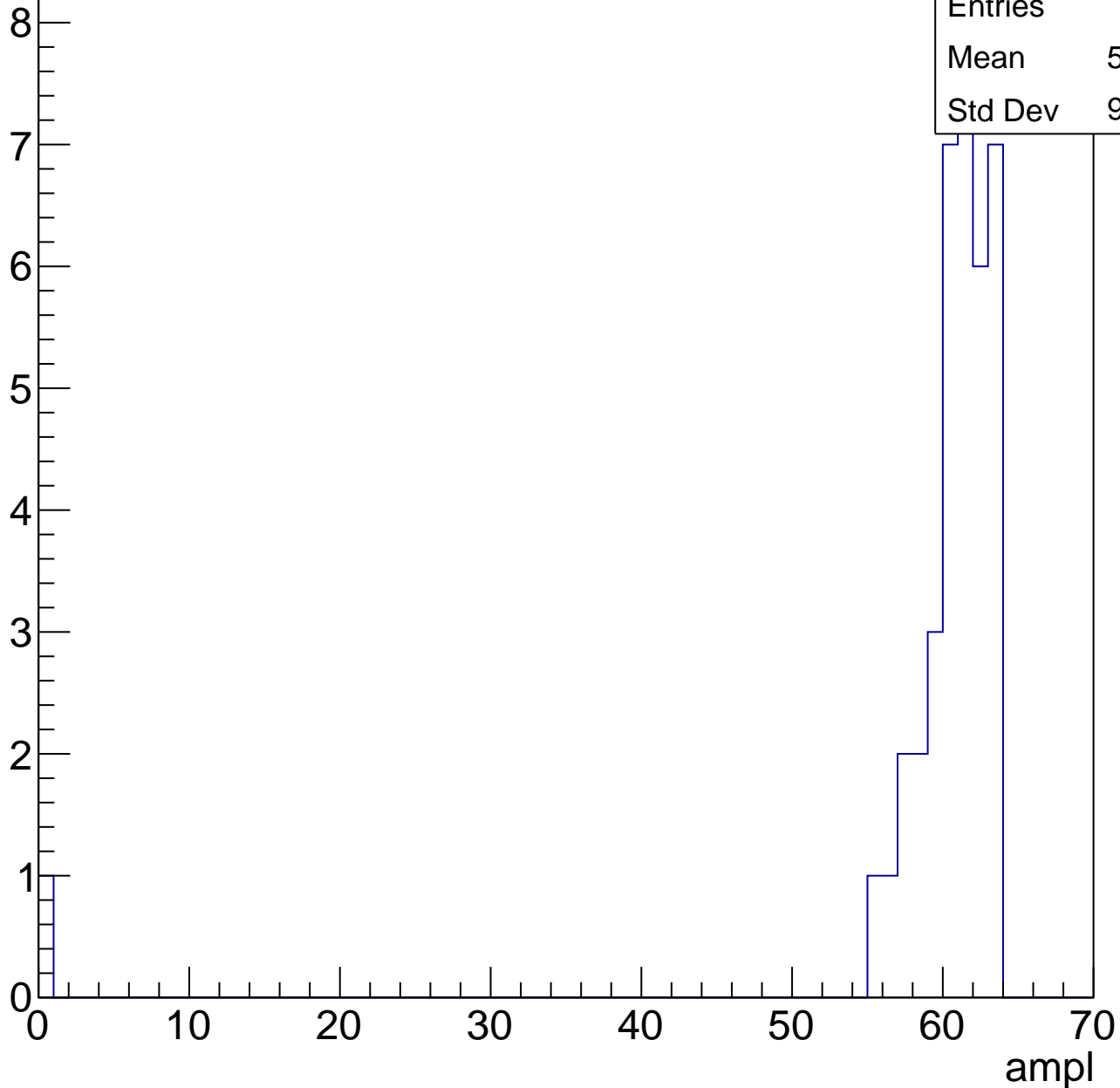
Entries	64
Mean	55.67
Std Dev	3.279



# B1L003S, U22-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

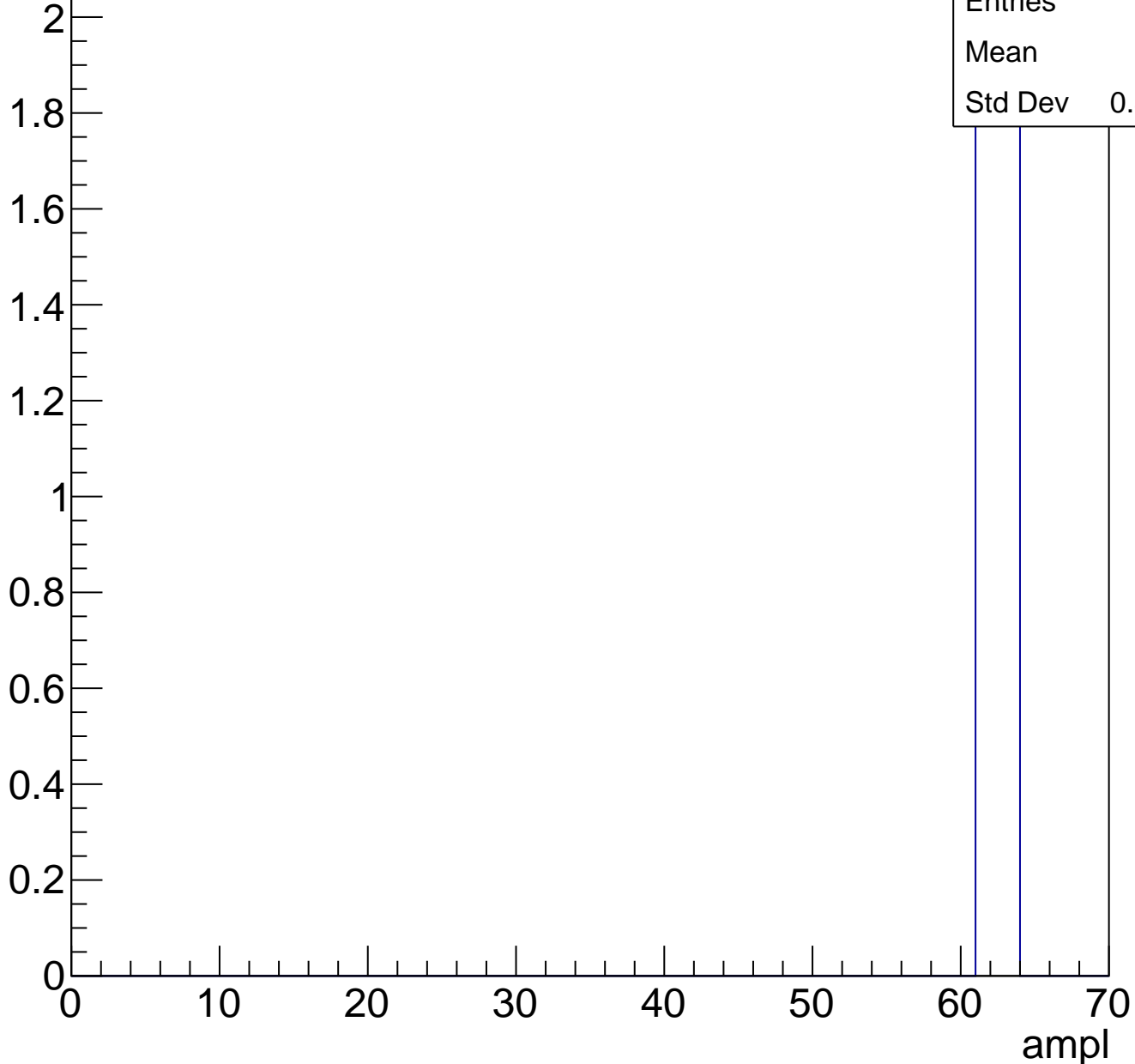
Entry



# B1L003S, U22-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch44, adc0

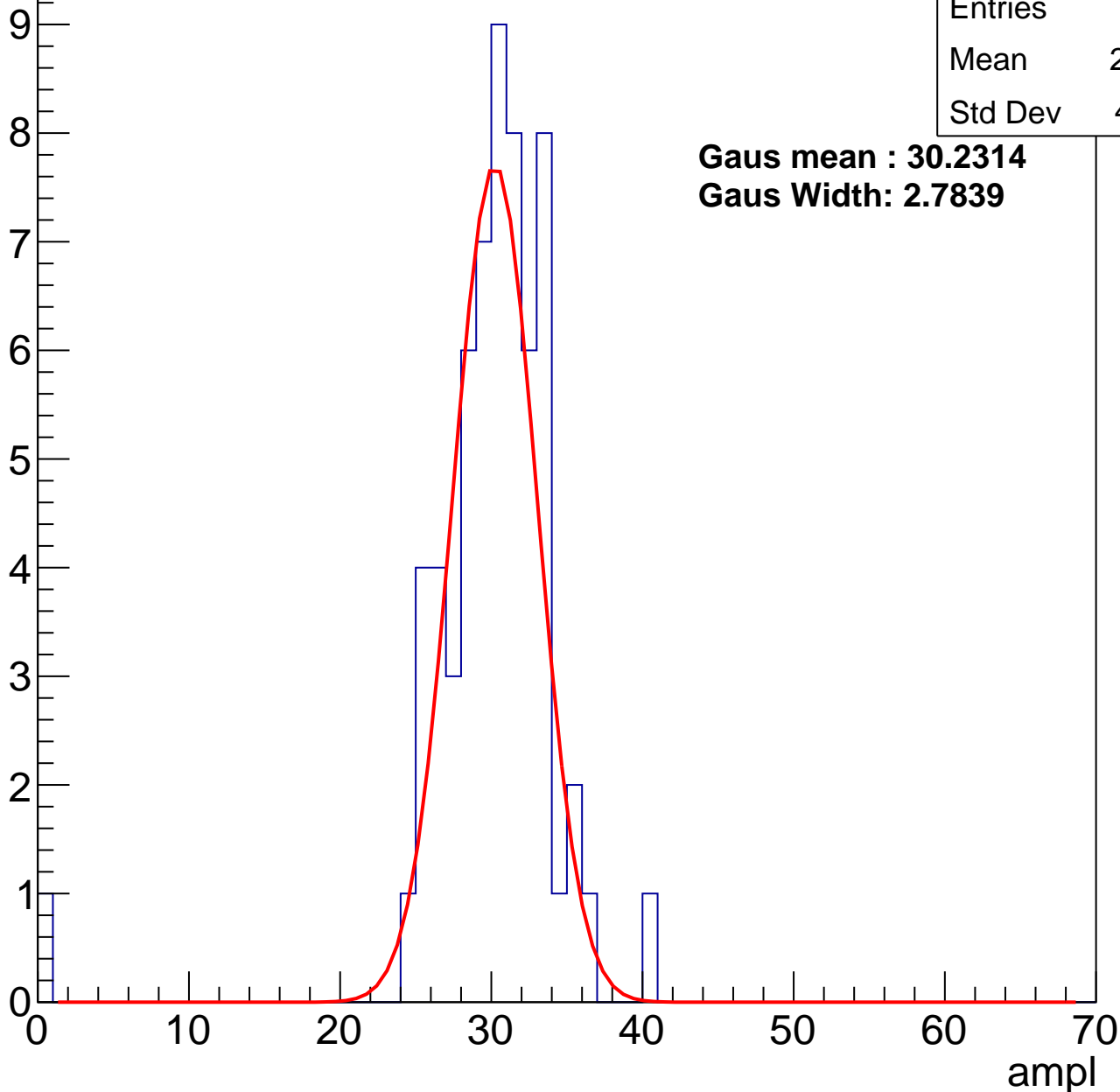
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.58
Std Dev	4.841

**Gaus mean : 30.2314**

**Gaus Width: 2.7839**



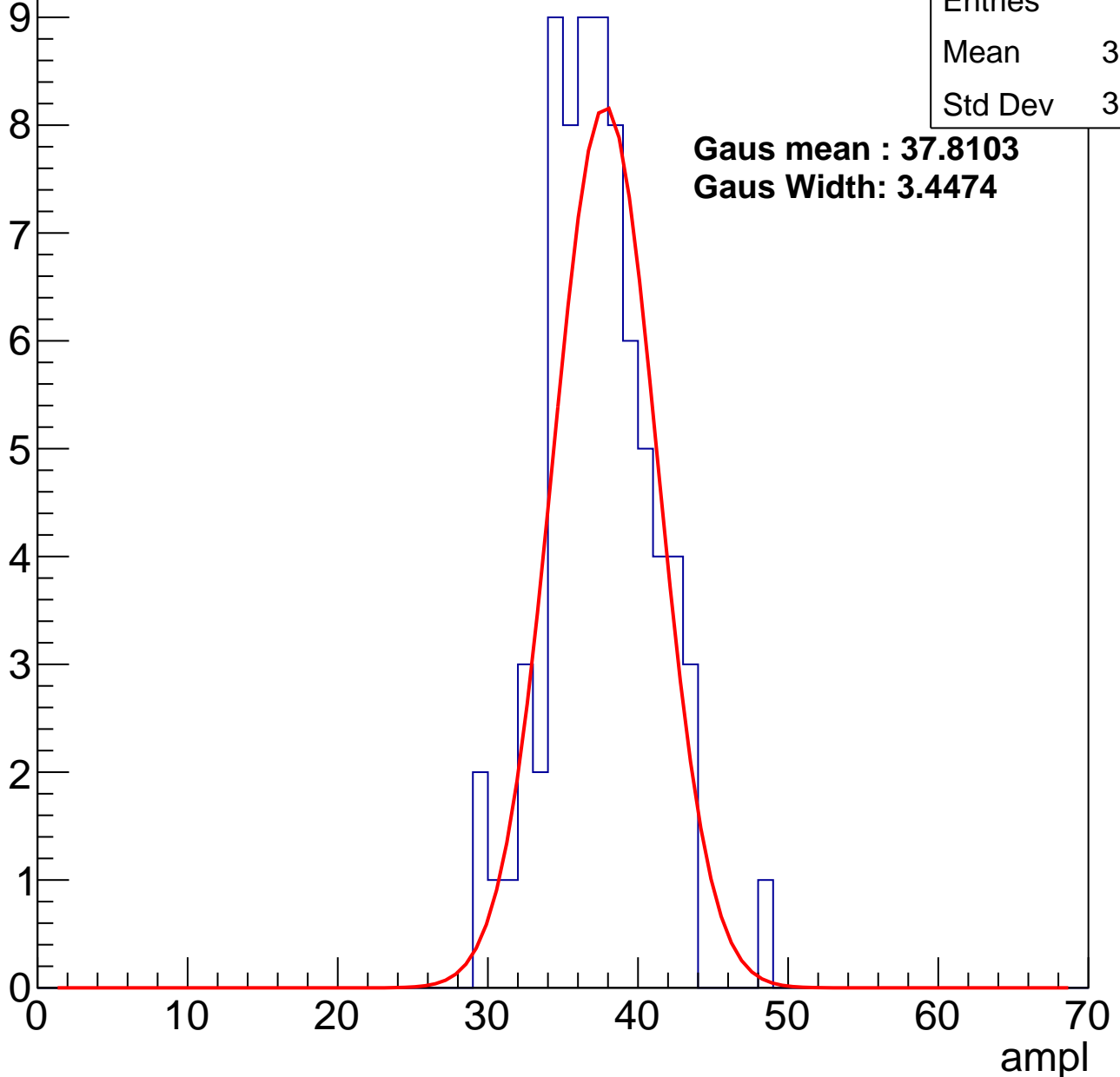
# B1L003S, U22-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.95
Std Dev	3.487

**Gaus mean : 37.8103**  
**Gaus Width: 3.4474**



# B1L003S, U22-ch44, adc2

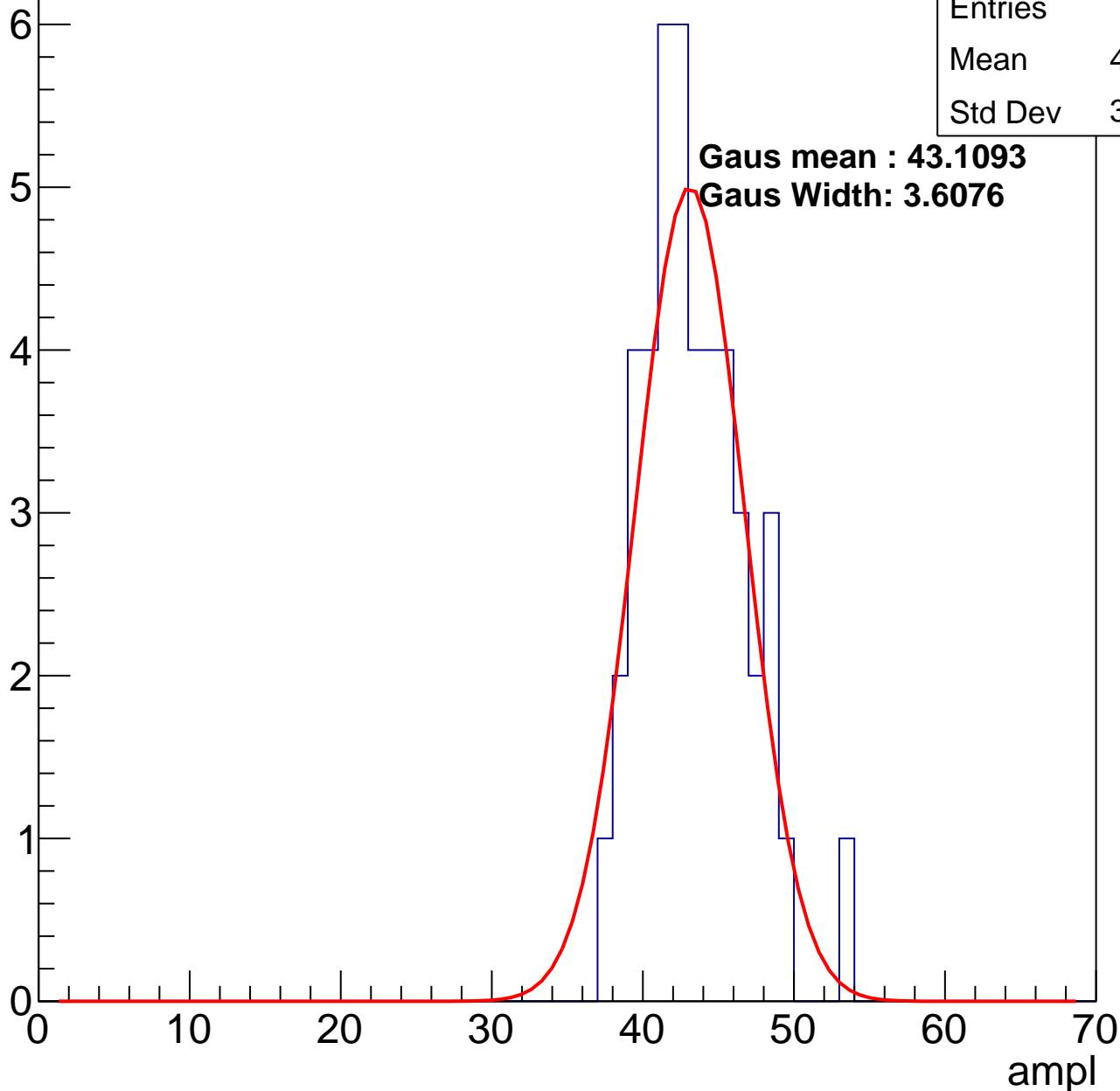
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	42.96
Std Dev	3.353

**Gaus mean : 43.1093**

**Gaus Width: 3.6076**

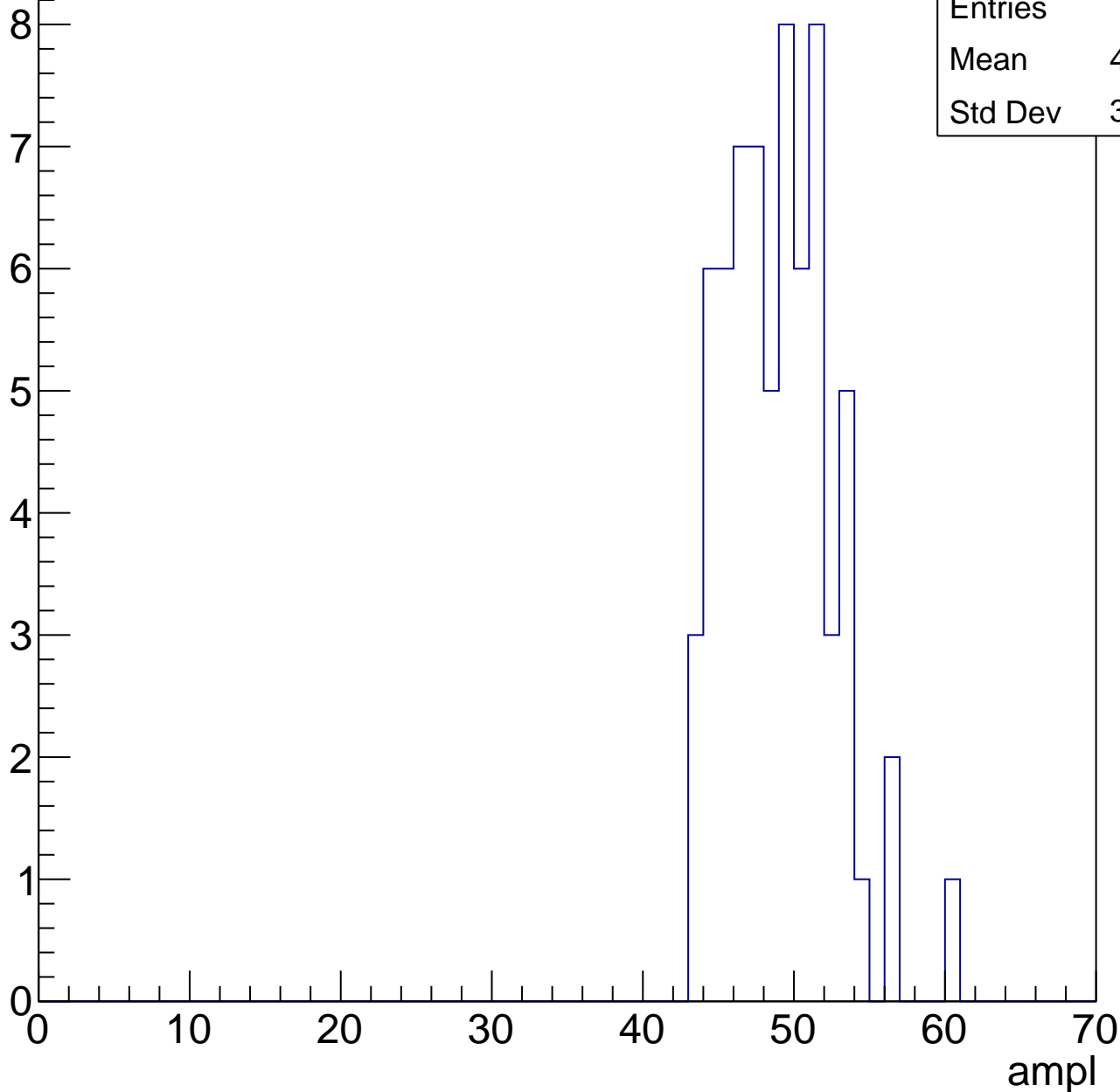


# B1L003S, U22-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	48.54
Std Dev	3.483

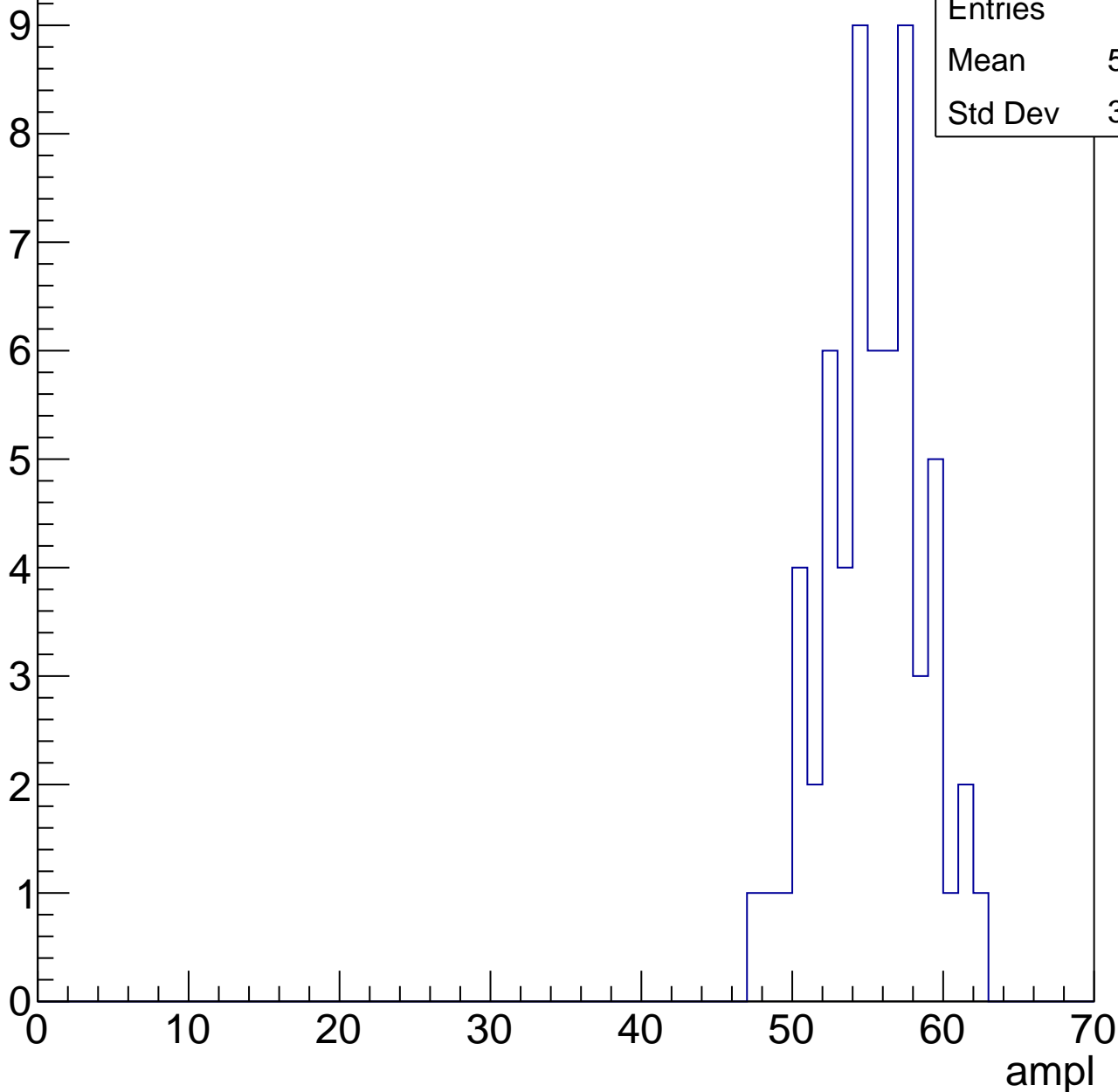


# B1L003S, U22-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	54.89
Std Dev	3.275

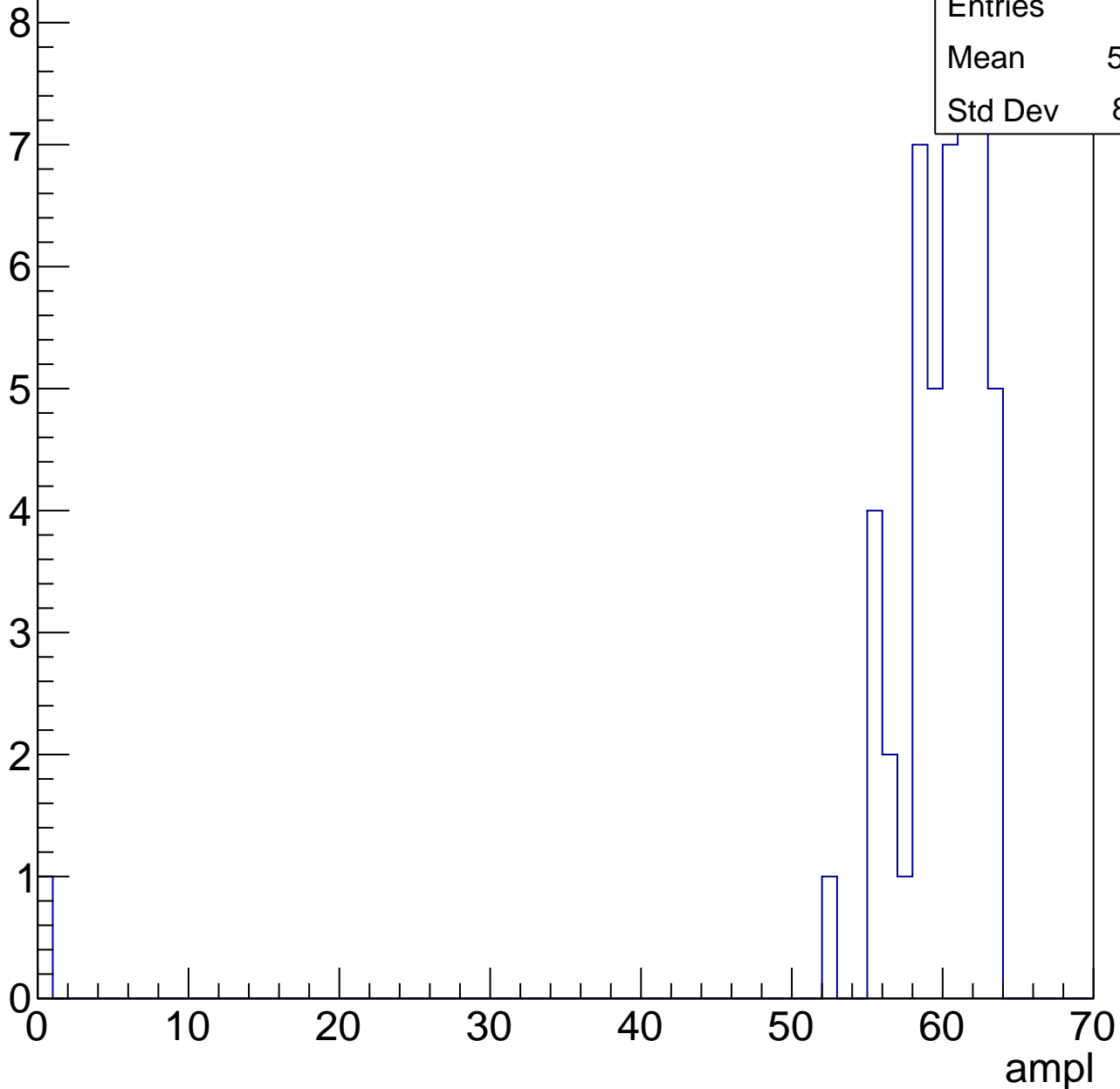


# B1L003S, U22-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.39
Std Dev	8.801

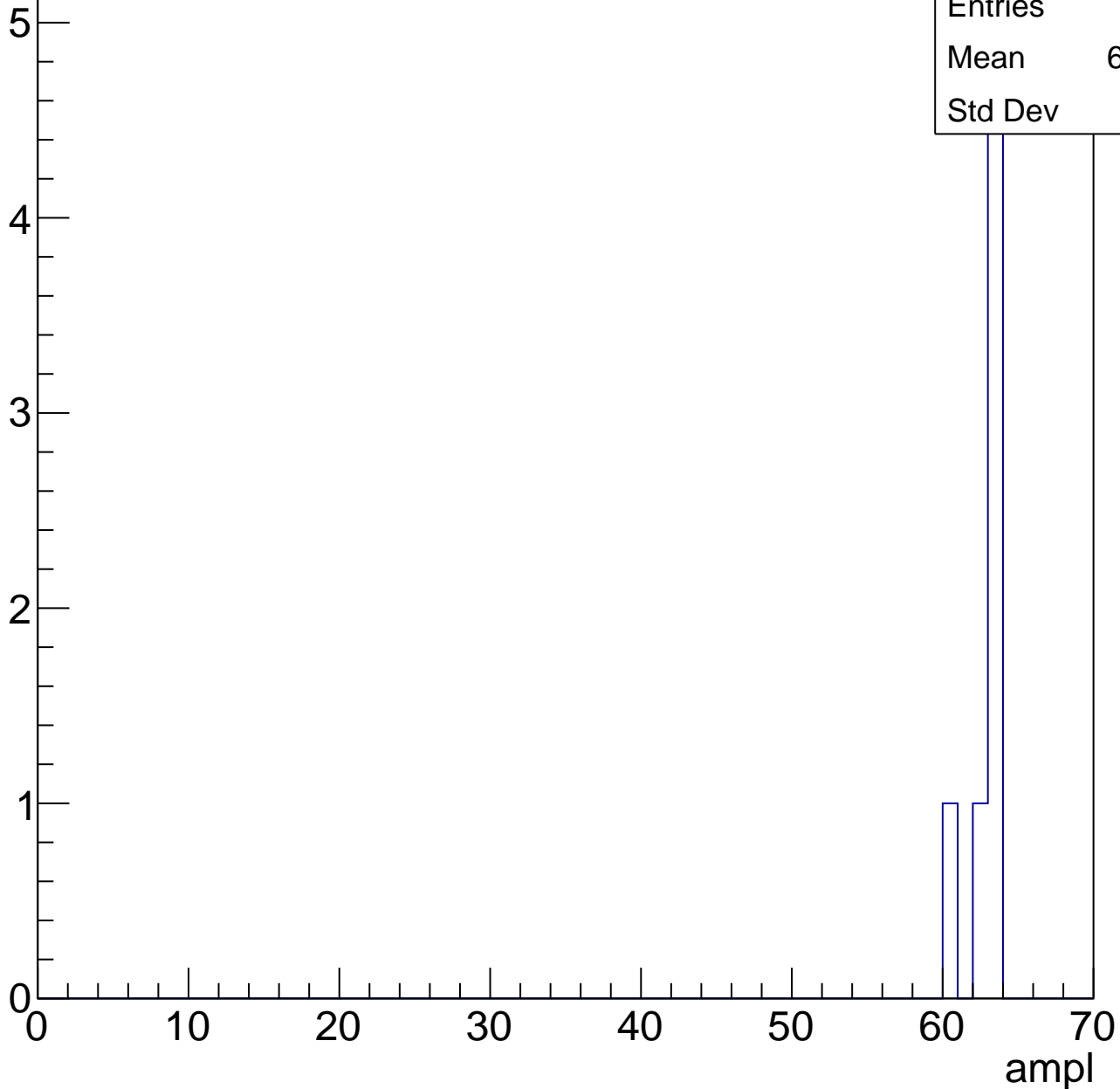


# B1L003S, U22-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	7
Mean	62.43
Std Dev	1.05





# B1L003S, U22-ch44, adc7

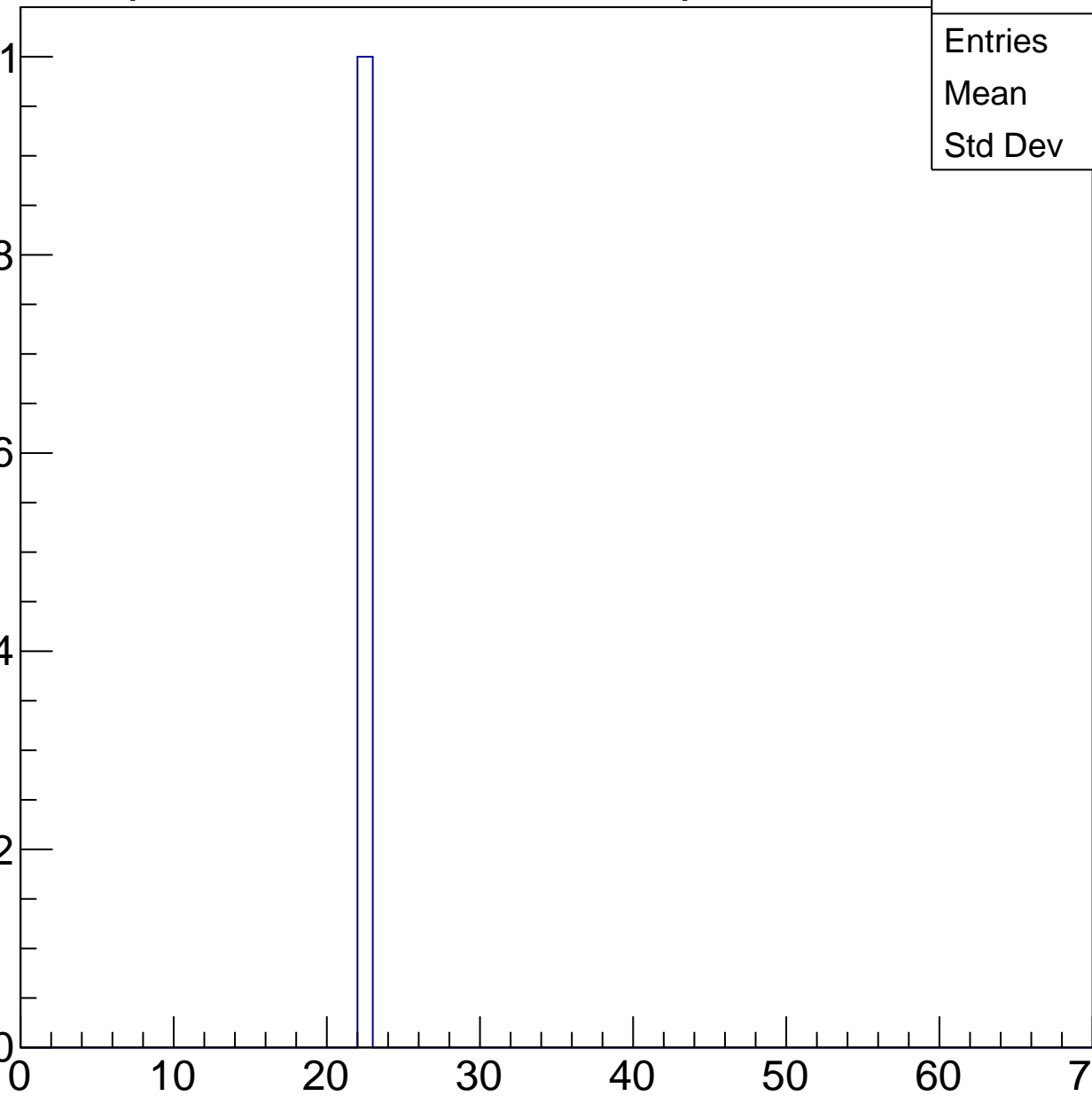
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl



# B1L003S, U22-ch45, adc0

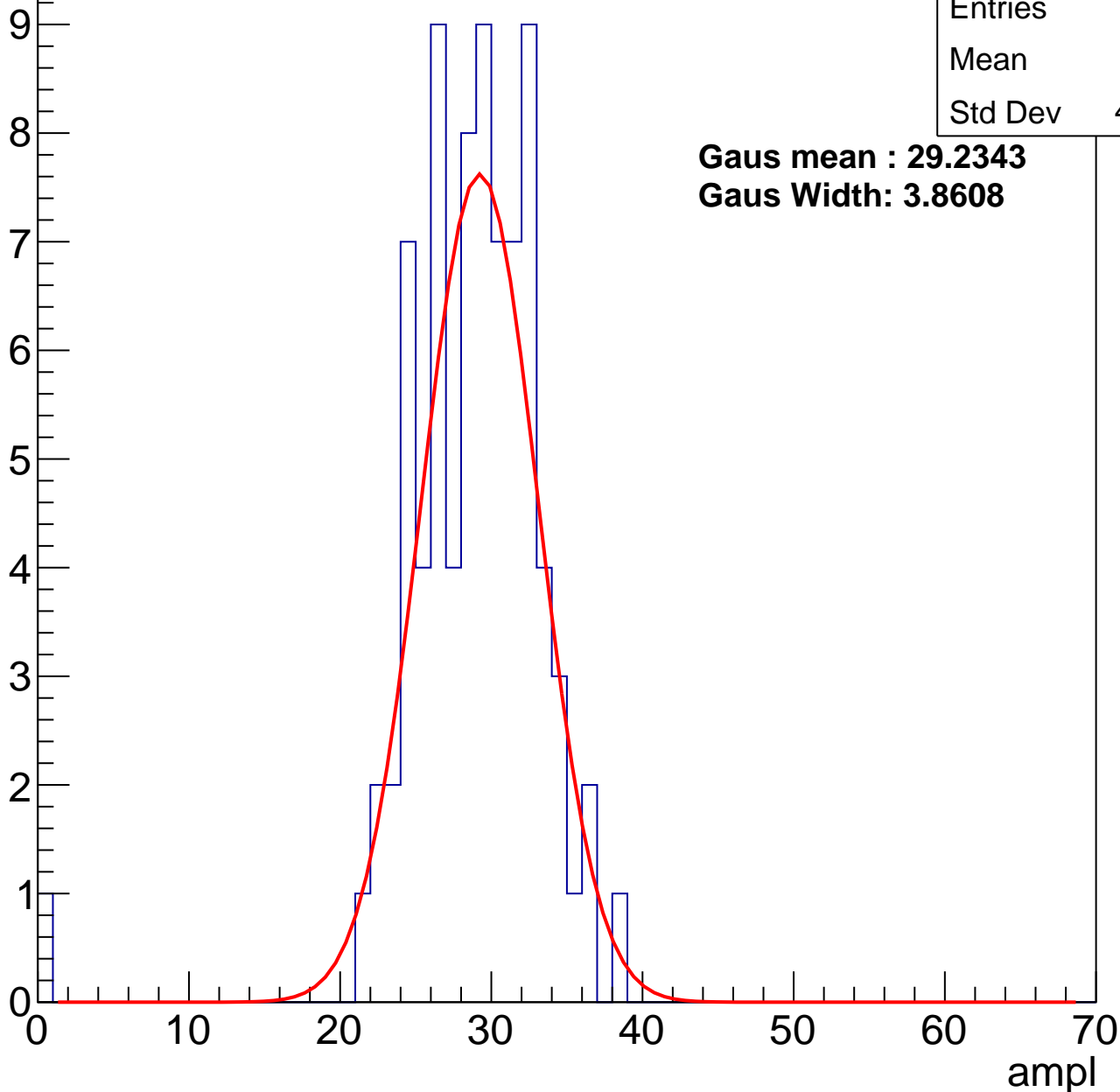
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	28.4
Std Dev	4.791

**Gaus mean : 29.2343**

**Gaus Width: 3.8608**



# B1L003S, U22-ch45, adc1

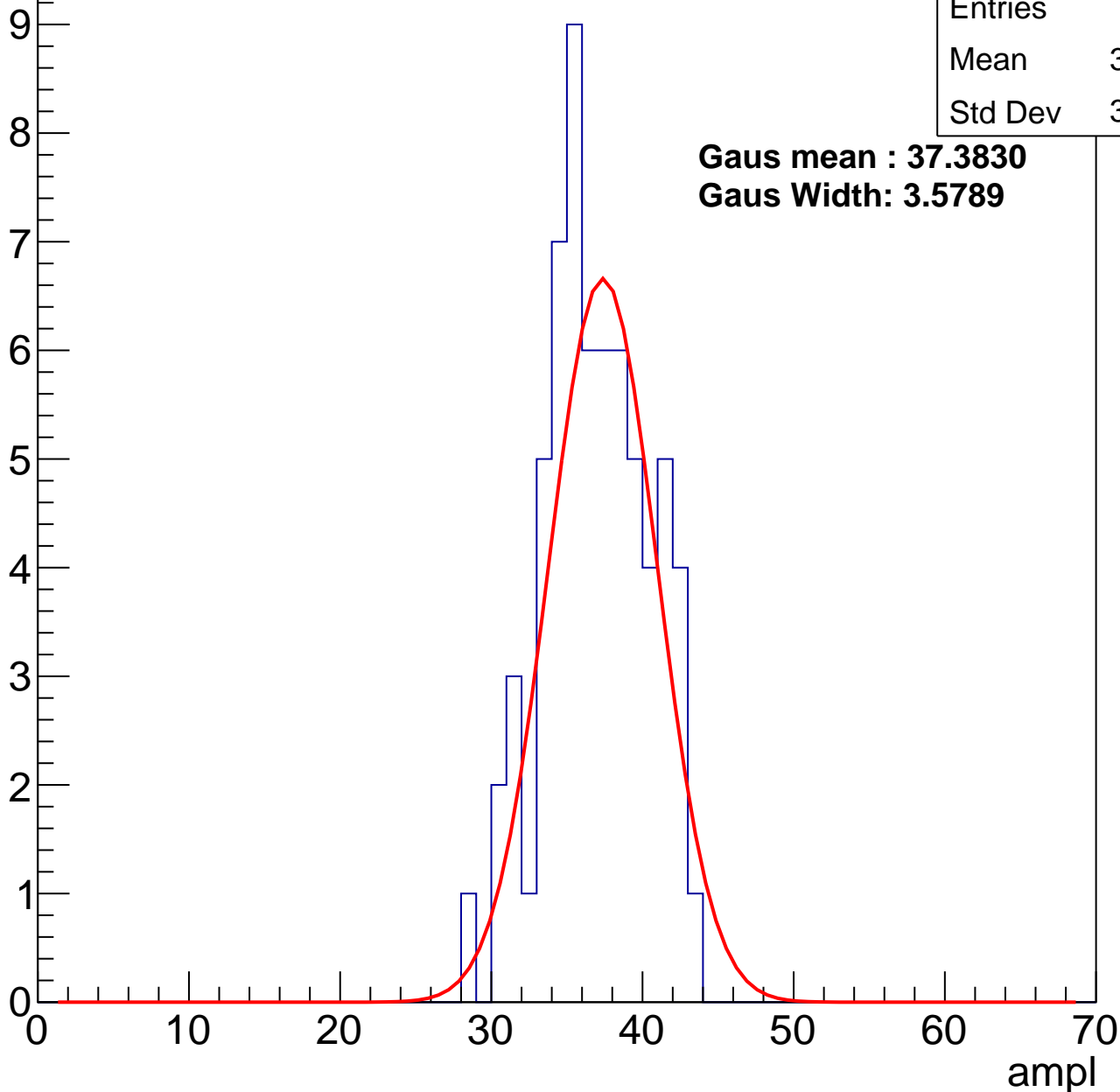
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.43
Std Dev	3.406

**Gaus mean : 37.3830**

**Gaus Width: 3.5789**



# B1L003S, U22-ch45, adc2

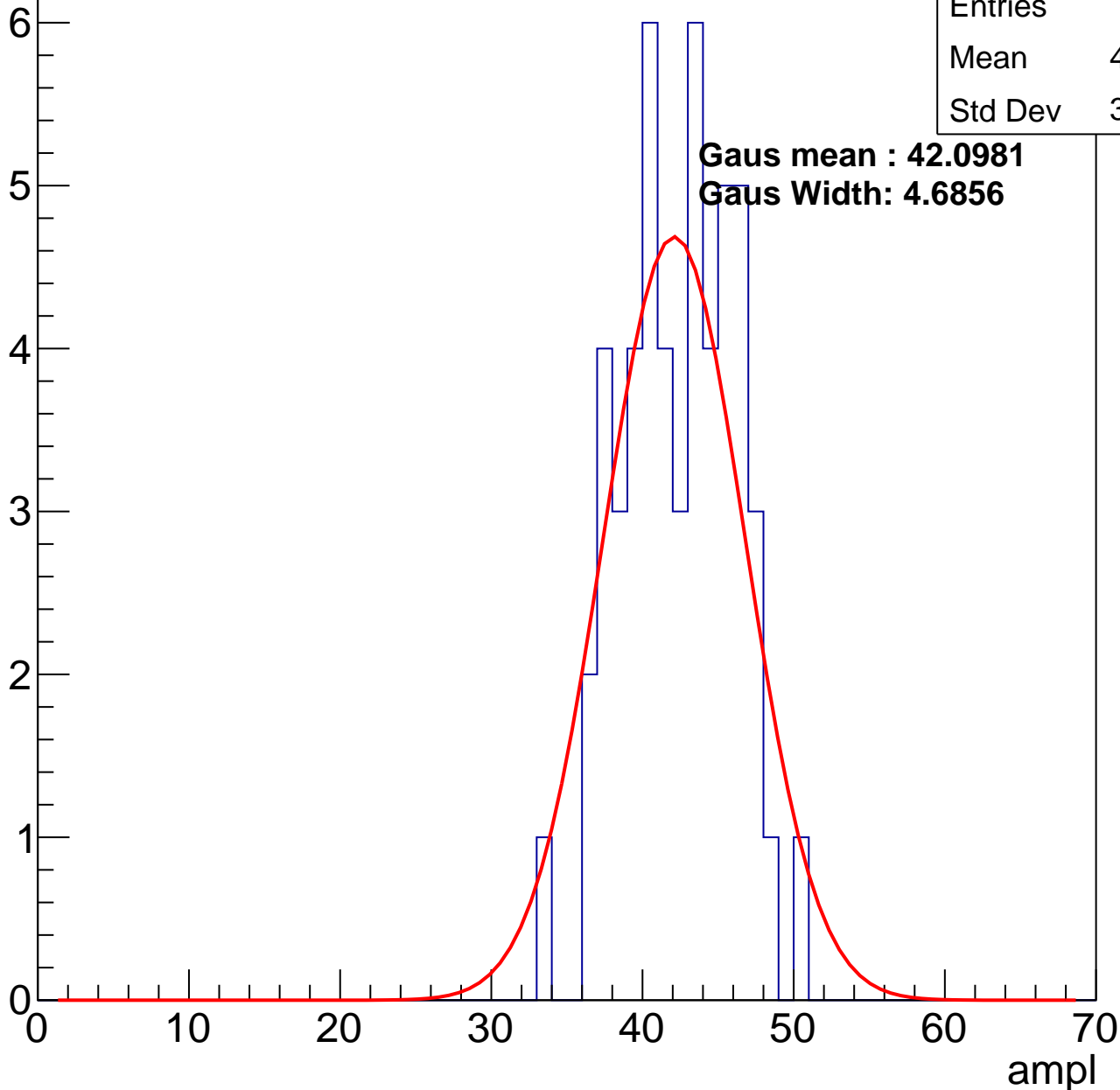
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	41.94
Std Dev	3.634

**Gaus mean : 42.0981**

**Gaus Width: 4.6856**

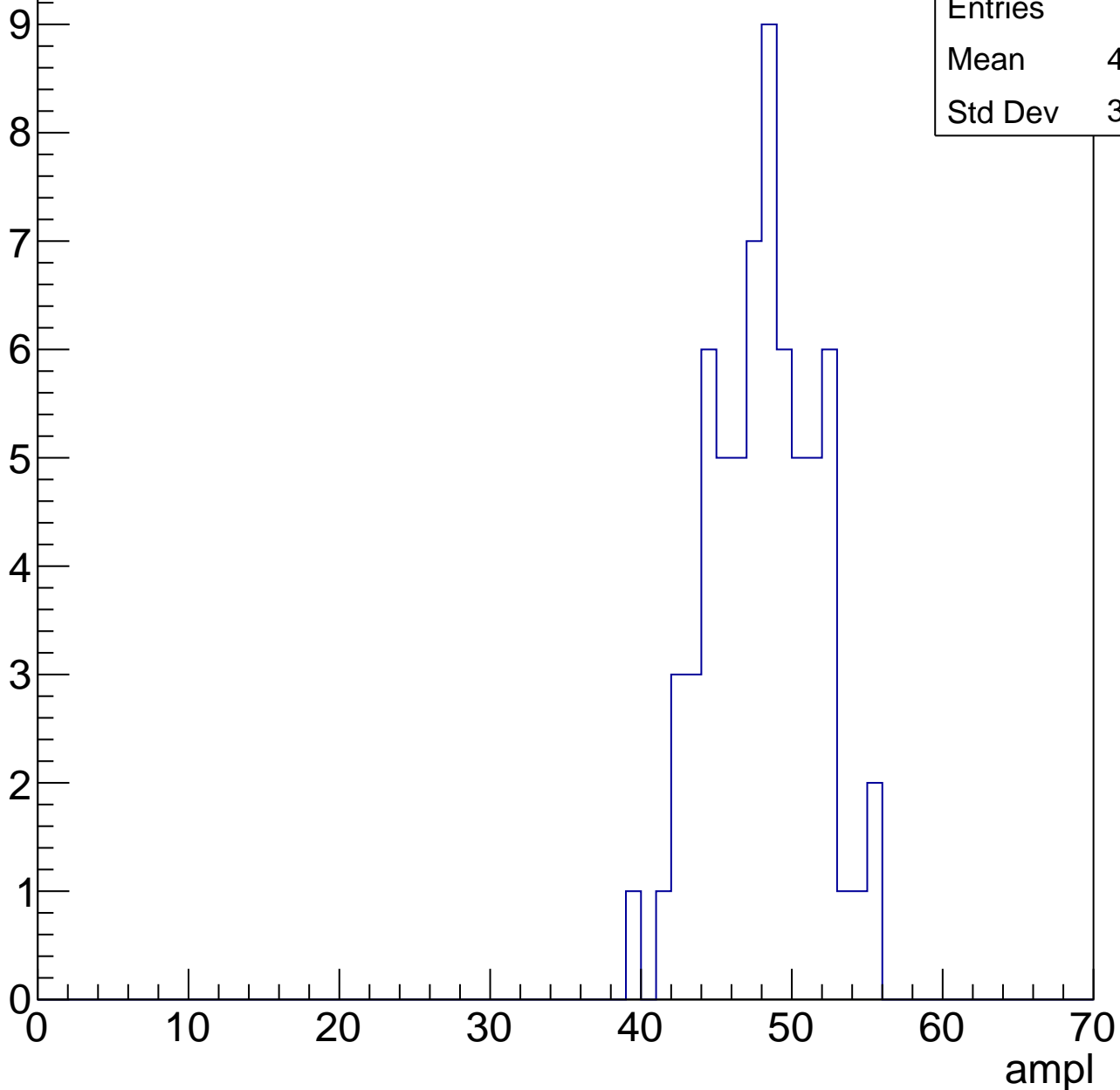


# B1L003S, U22-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

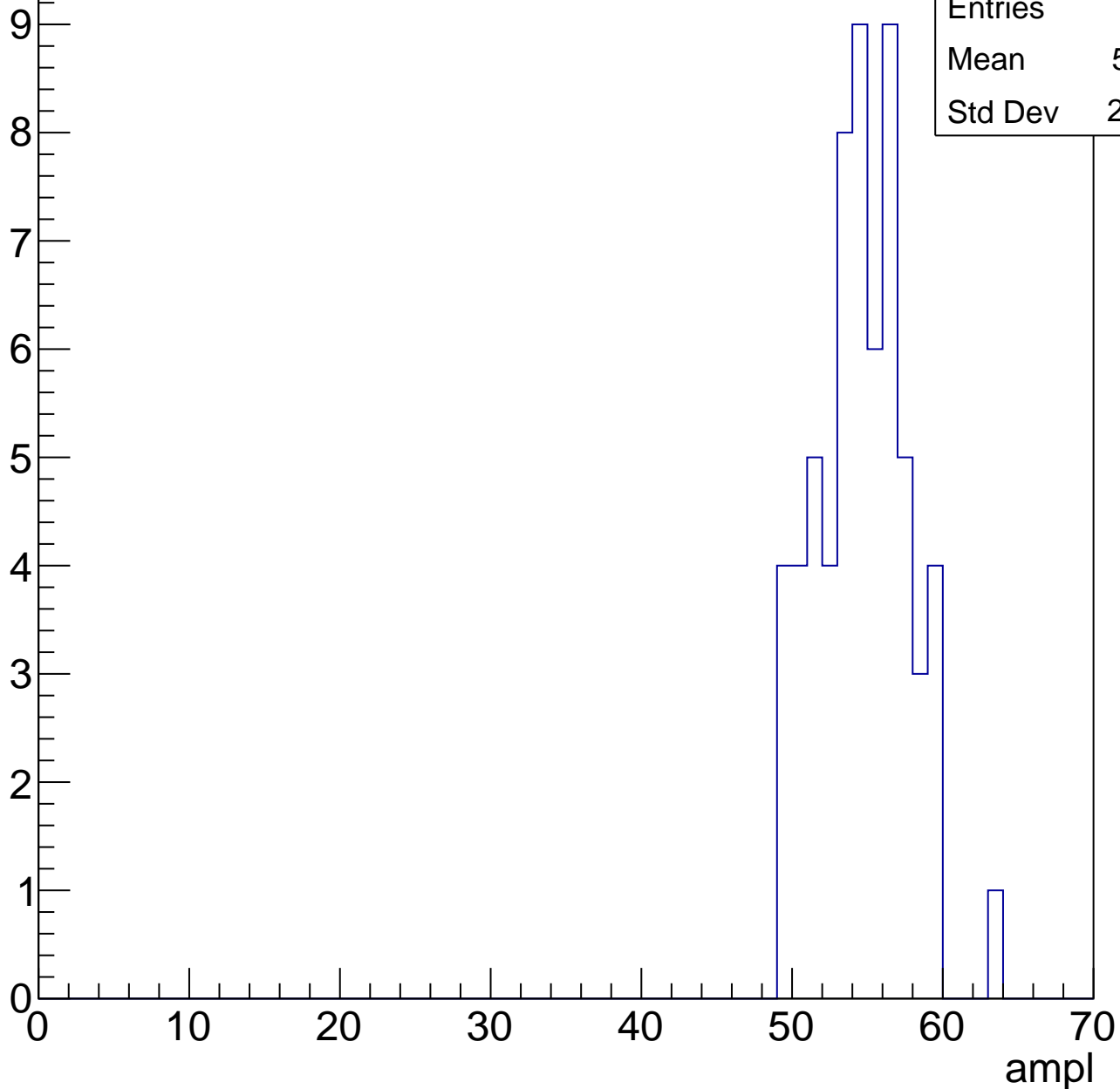
Entries	66
Mean	47.62
Std Dev	3.476



# B1L003S, U22-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



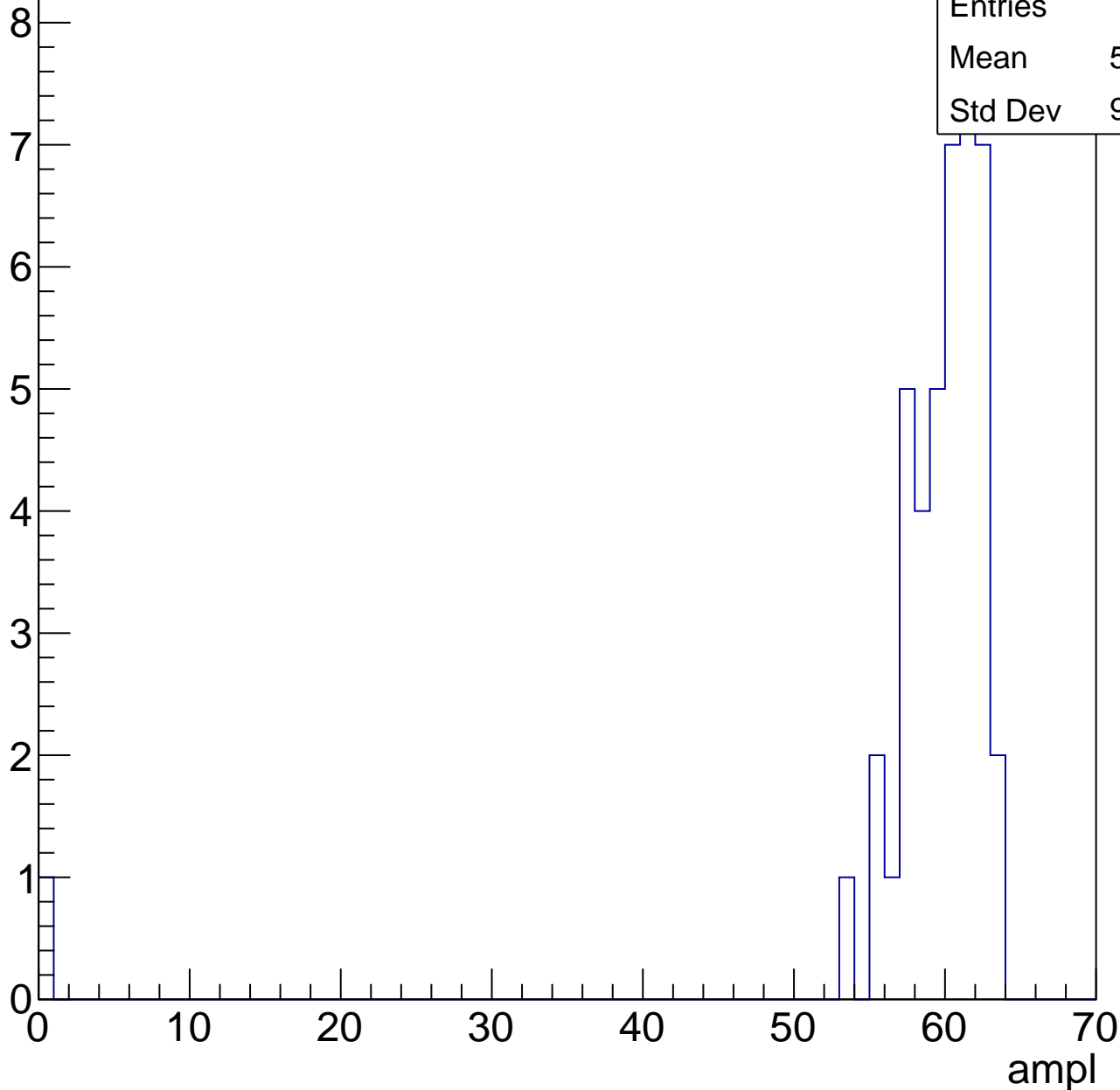
Entries	62
Mean	54.21
Std Dev	2.968

# B1L003S, U22-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	58.12
Std Dev	9.254

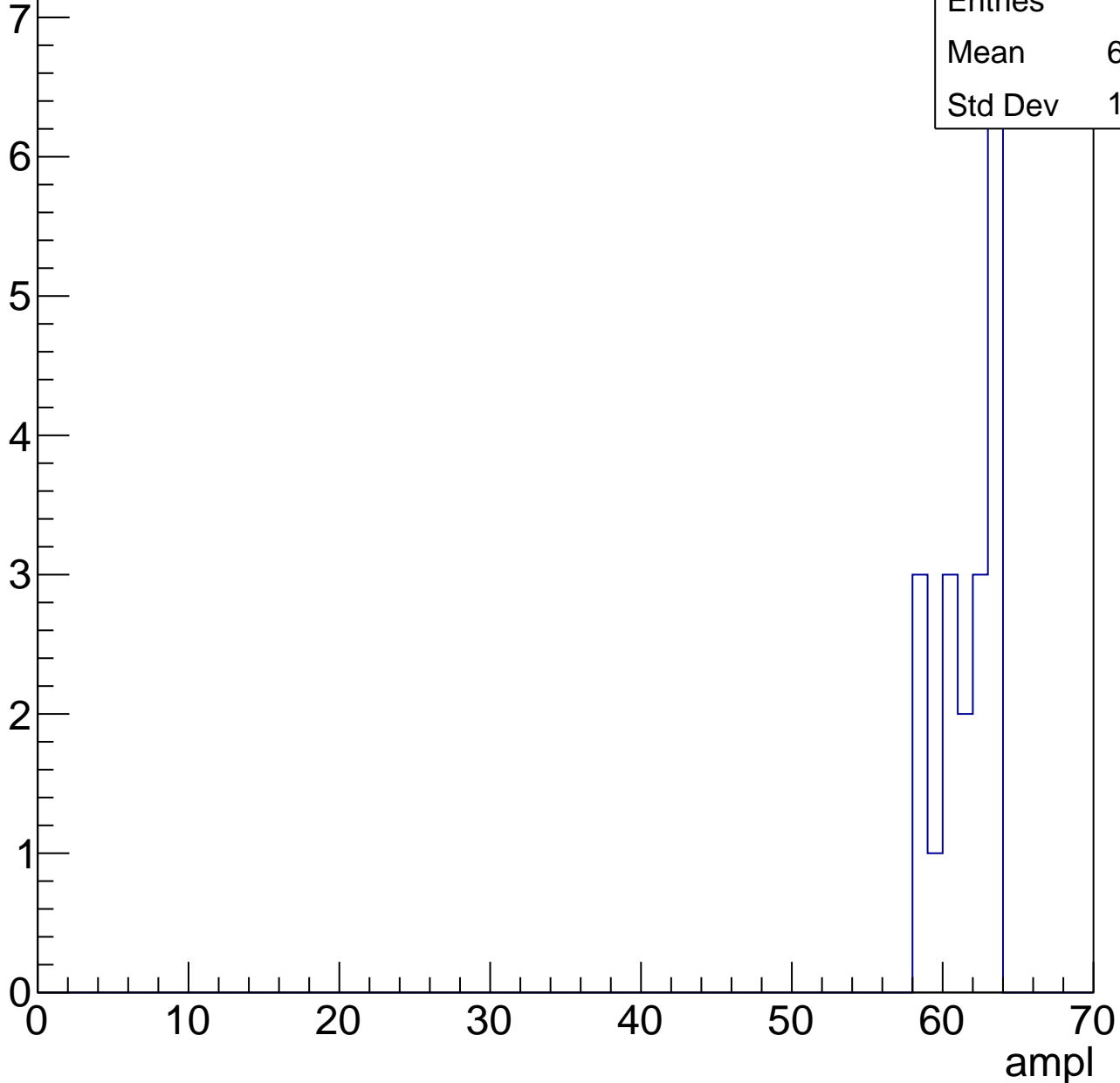


# B1L003S, U22-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	19
Mean	61.16
Std Dev	1.843





# B1L003S, U22-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch46, adc0

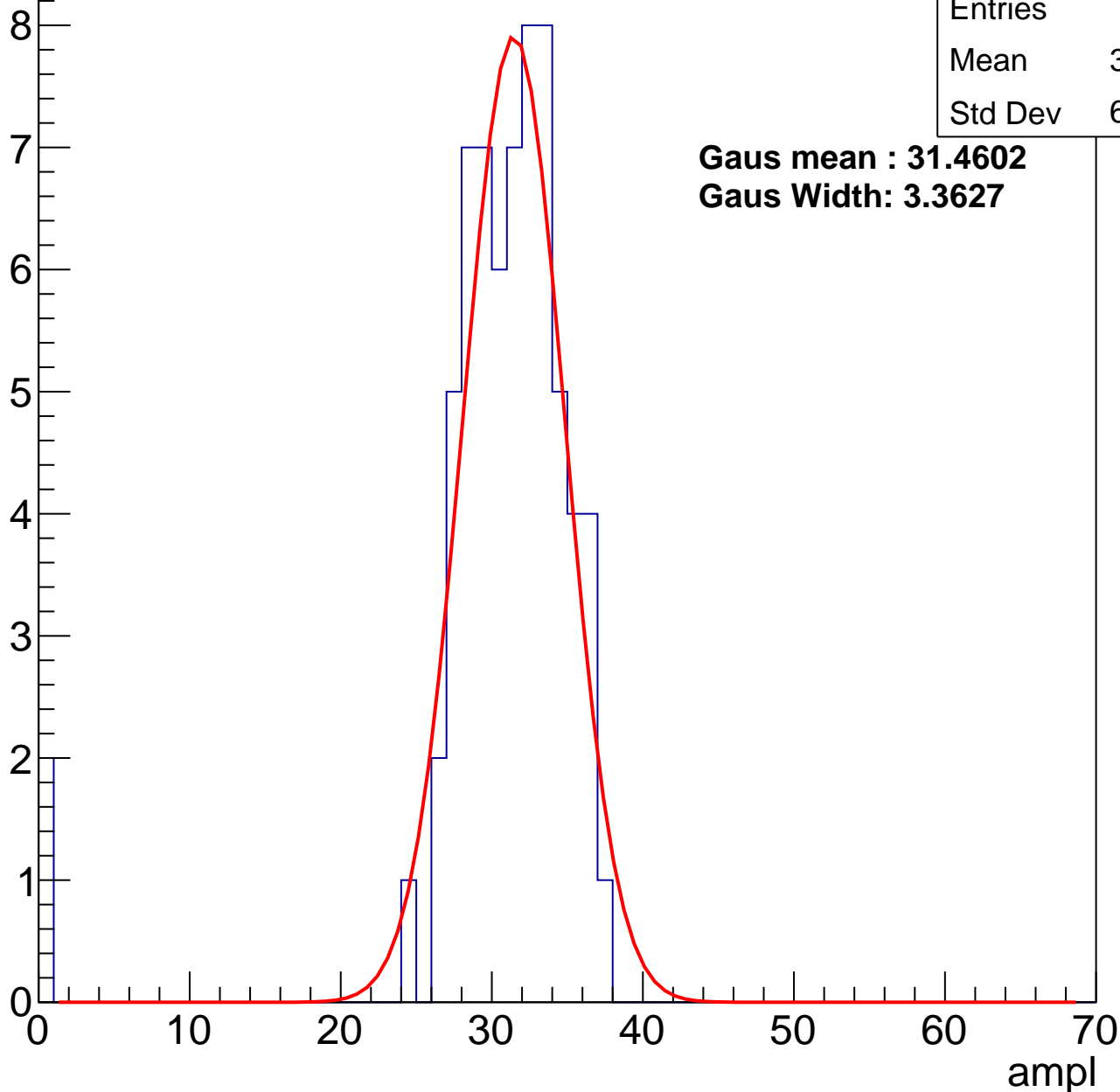
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	30.12
Std Dev	6.024

**Gaus mean : 31.4602**

**Gaus Width: 3.3627**



# B1L003S, U22-ch46, adc1

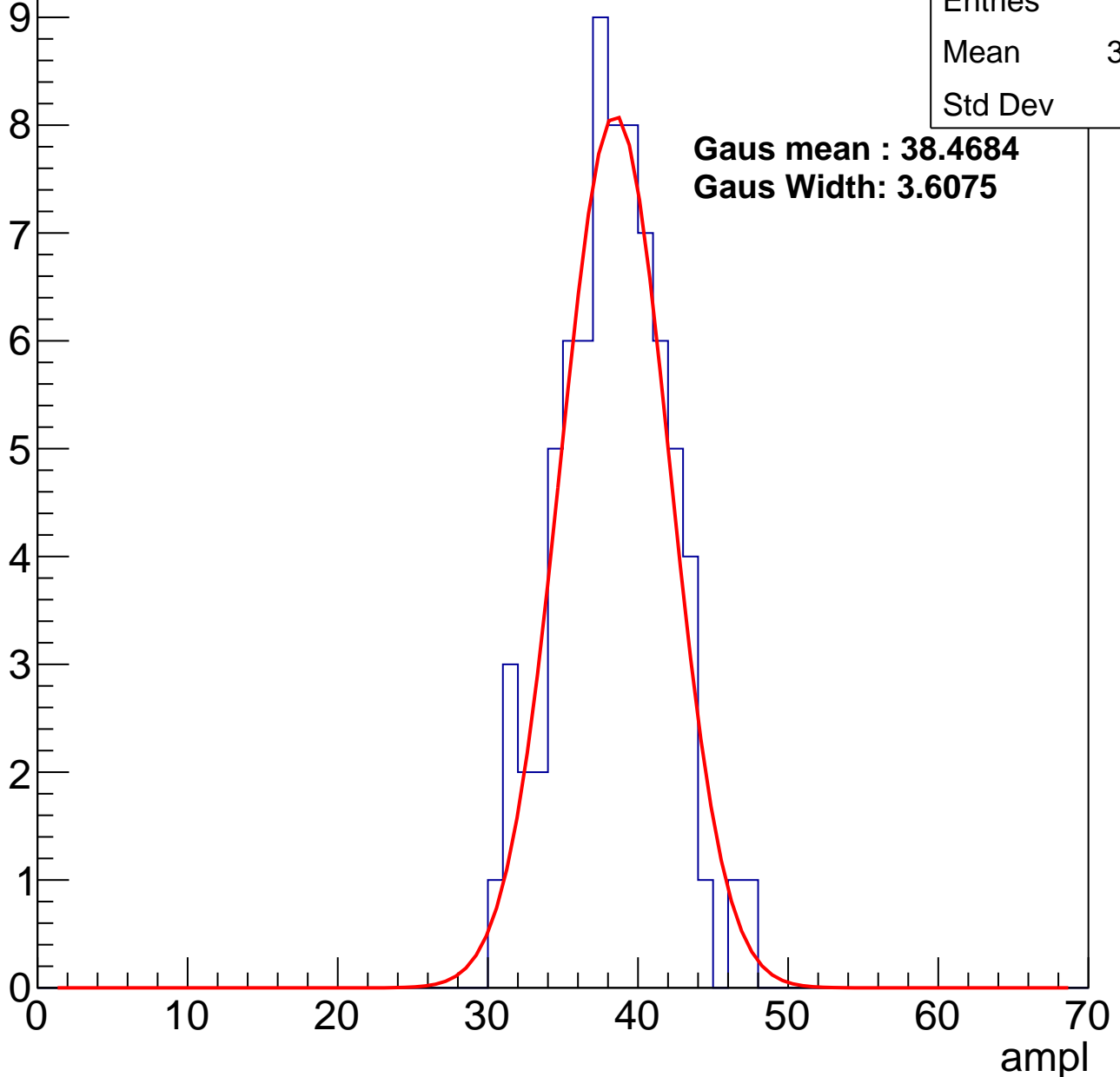
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	37.91
Std Dev	3.56

**Gaus mean : 38.4684**

**Gaus Width: 3.6075**



# B1L003S, U22-ch46, adc2

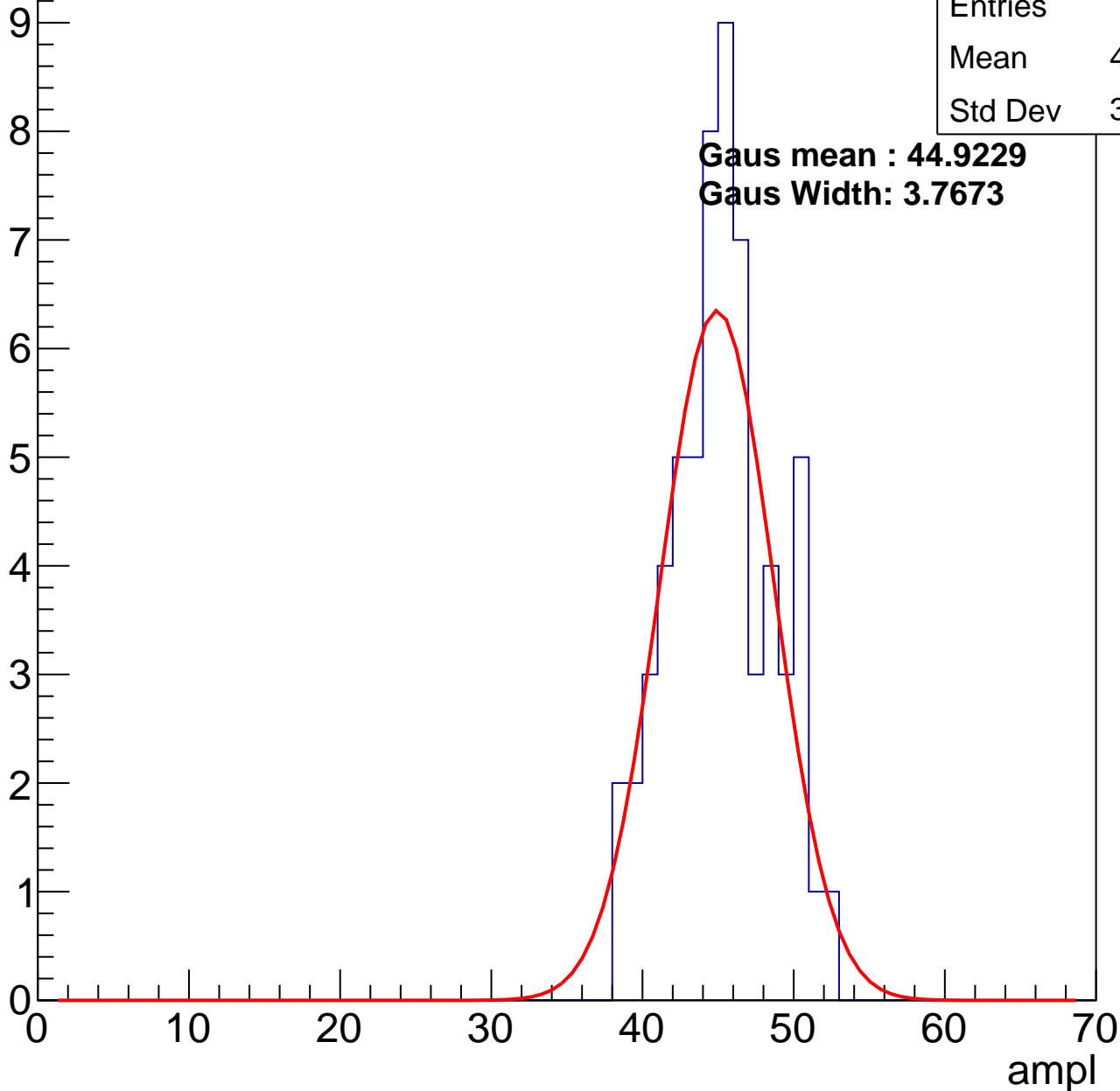
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	44.76
Std Dev	3.339

**Gaus mean : 44.9229**

**Gaus Width: 3.7673**

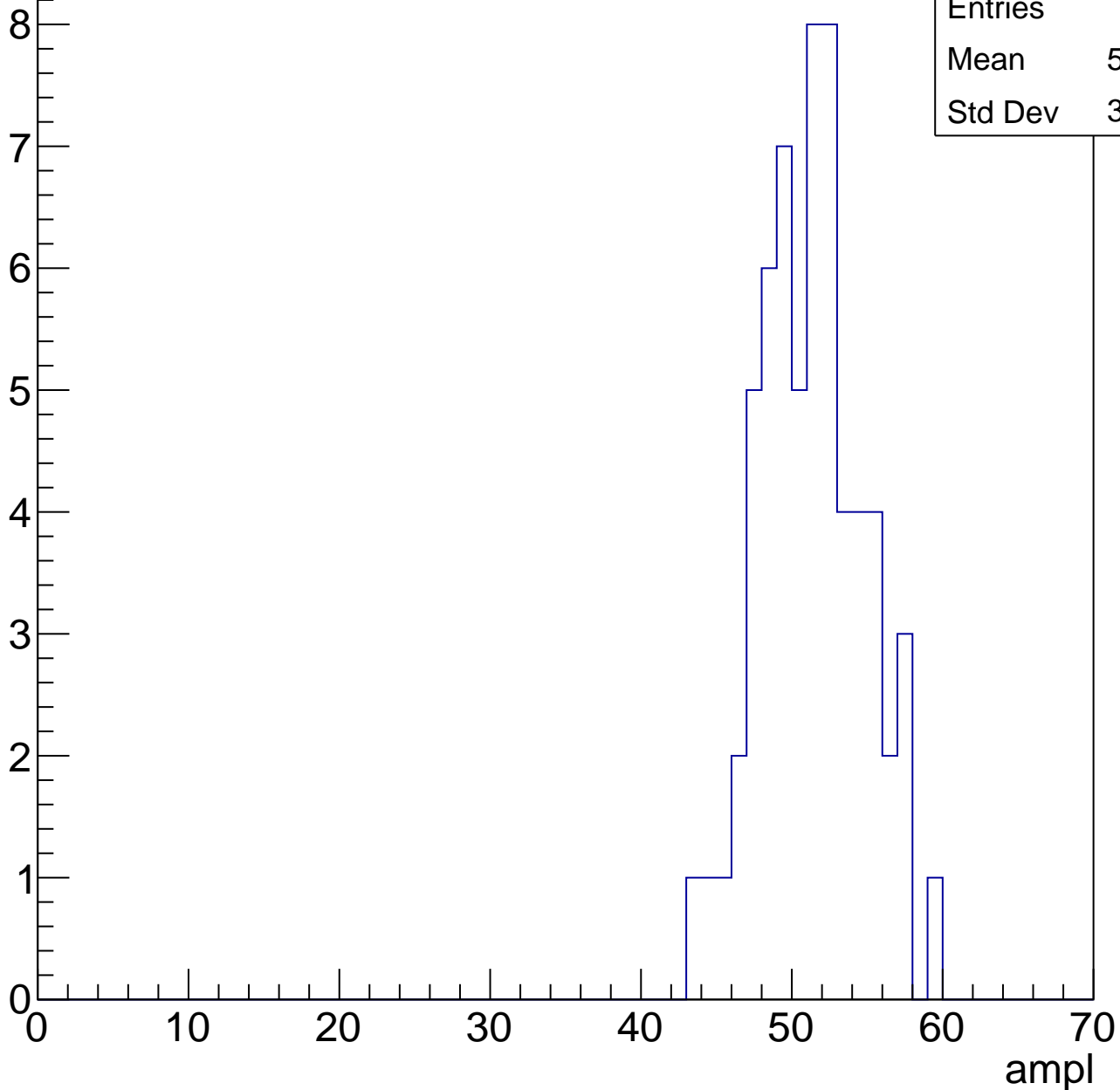


# B1L003S, U22-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

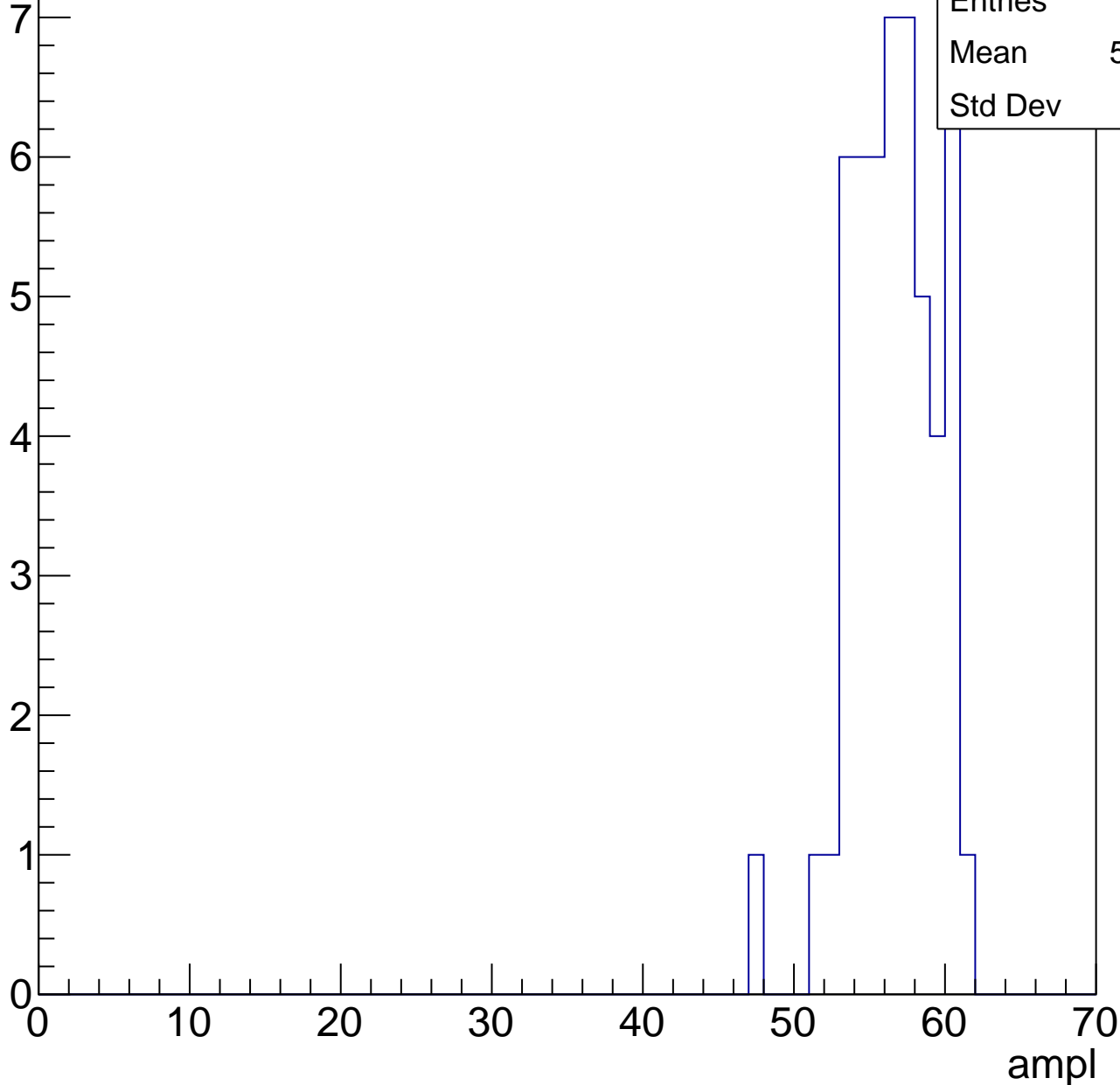
Entries	62
Mean	50.87
Std Dev	3.386



# B1L003S, U22-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

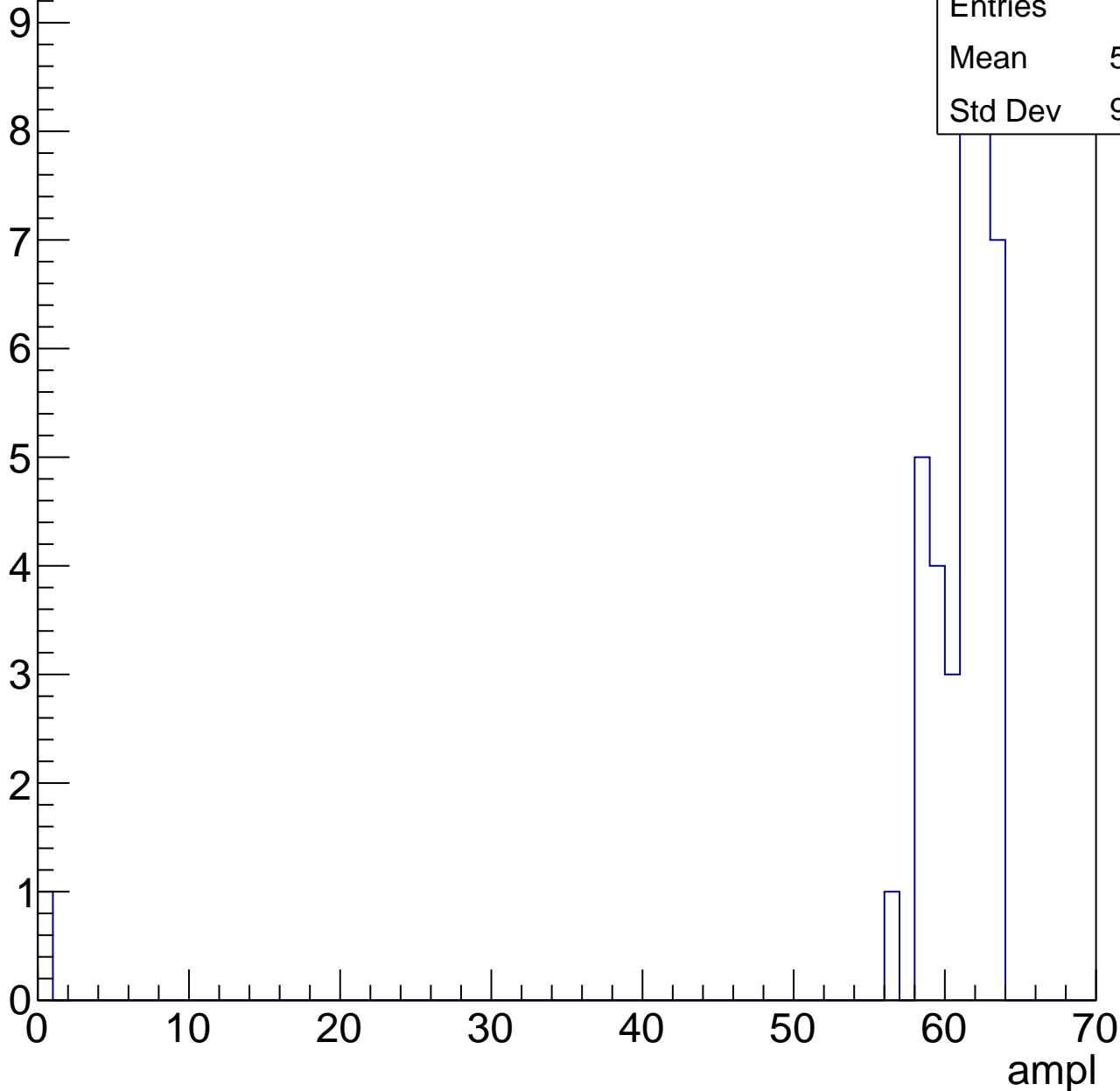


# B1L003S, U22-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	59.23
Std Dev	9.773



# B1L003S, U22-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62

Std Dev

1.549

Entries	5
Mean	62
Std Dev	1.549



# B1L003S, U22-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch47, adc0

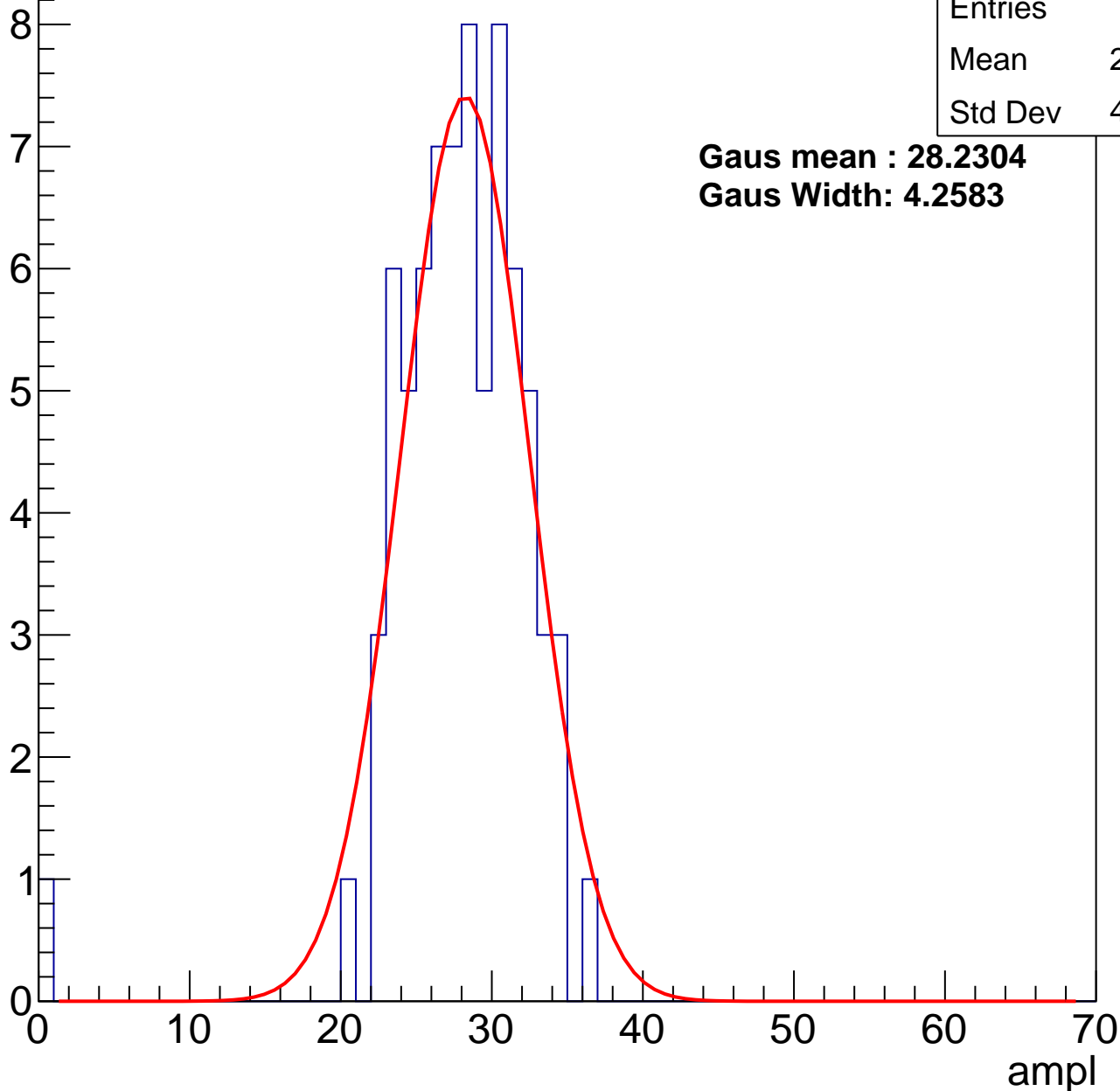
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	27.43
Std Dev	4.716

**Gaus mean : 28.2304**

**Gaus Width: 4.2583**



# B1L003S, U22-ch47, adc1

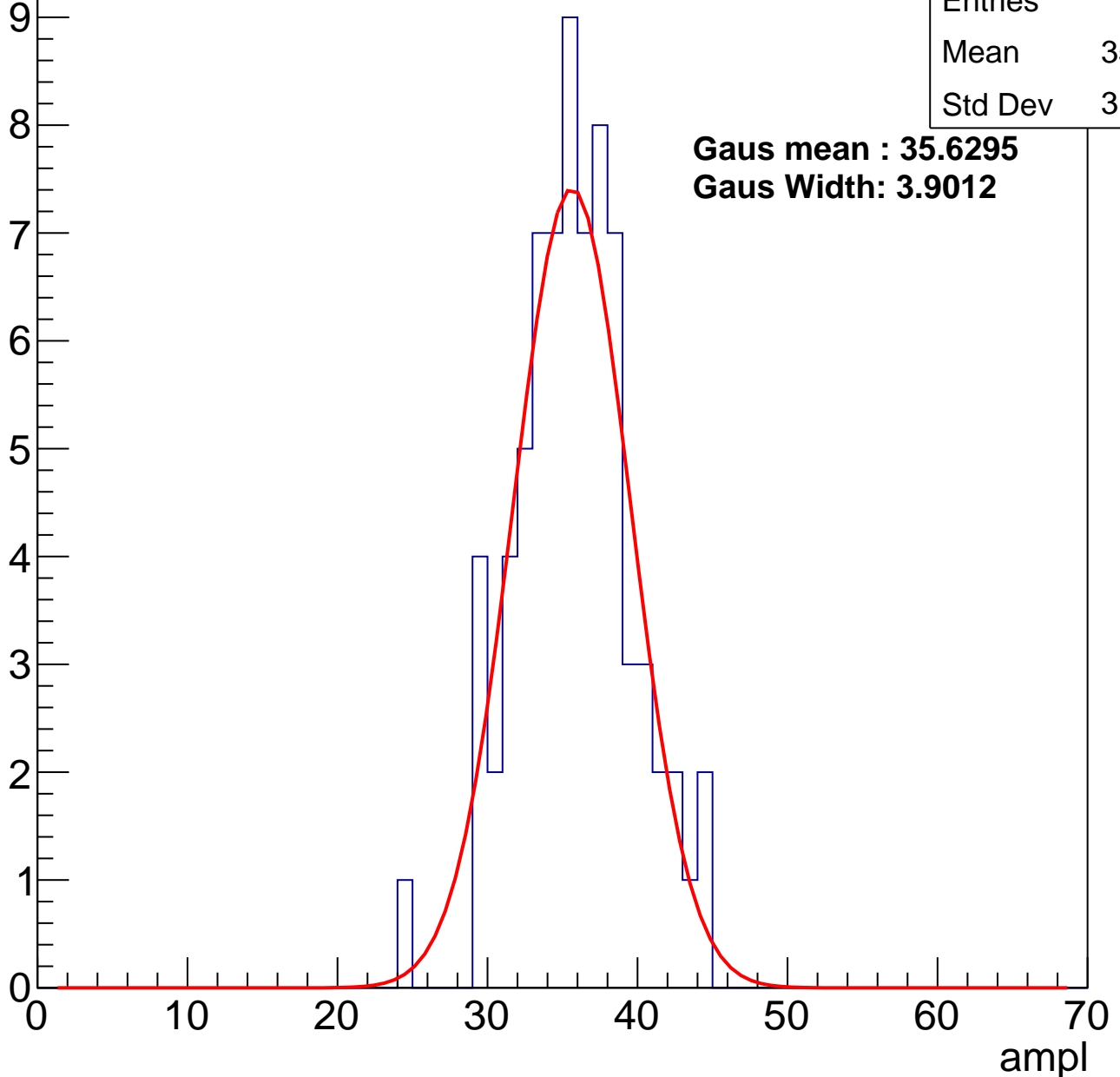
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	35.35
Std Dev	3.804

**Gaus mean : 35.6295**

**Gaus Width: 3.9012**



# B1L003S, U22-ch47, adc2

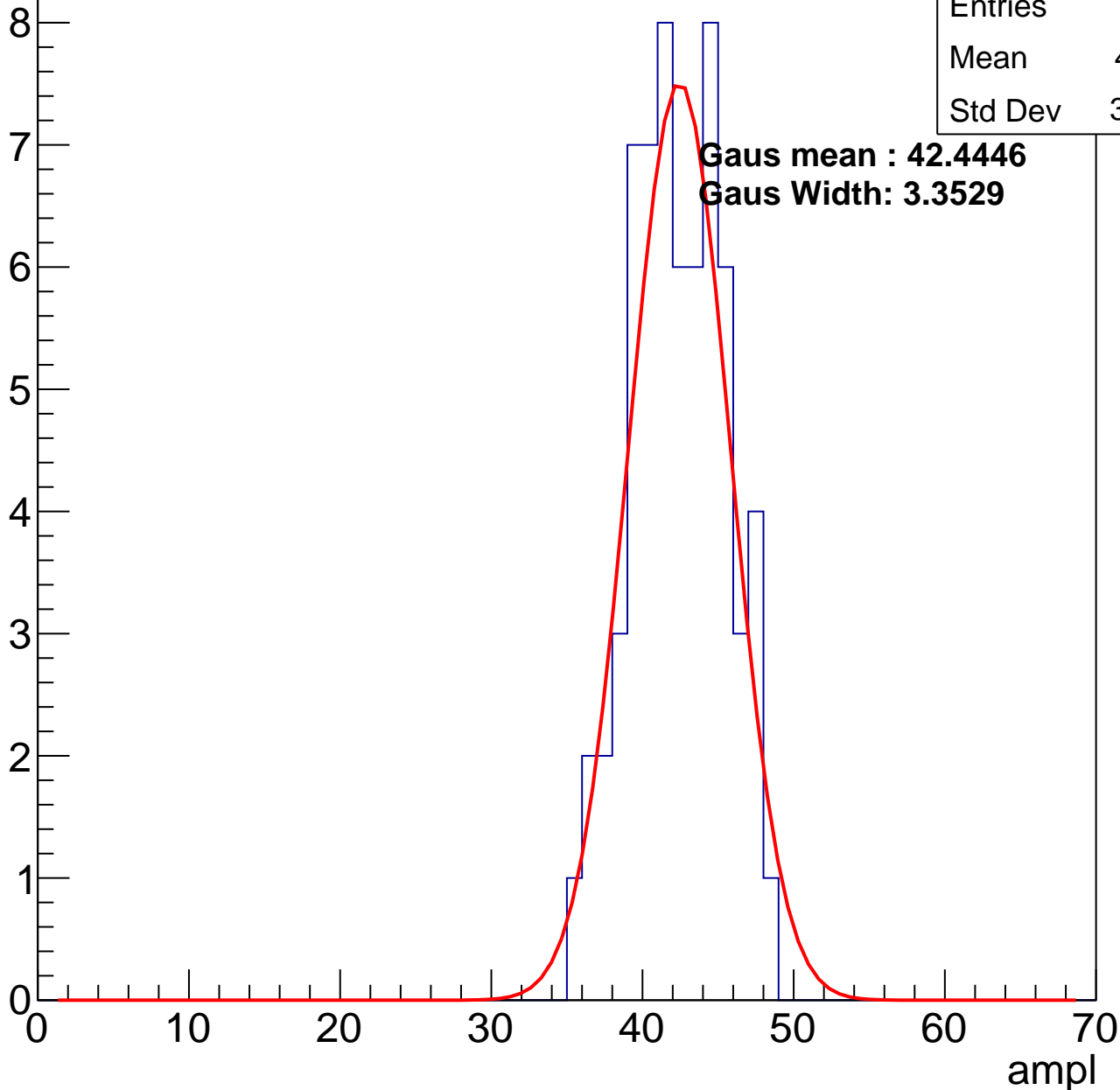
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.91
Std Dev	3.045

**Gaus mean : 42.4446**

**Gaus Width: 3.3529**

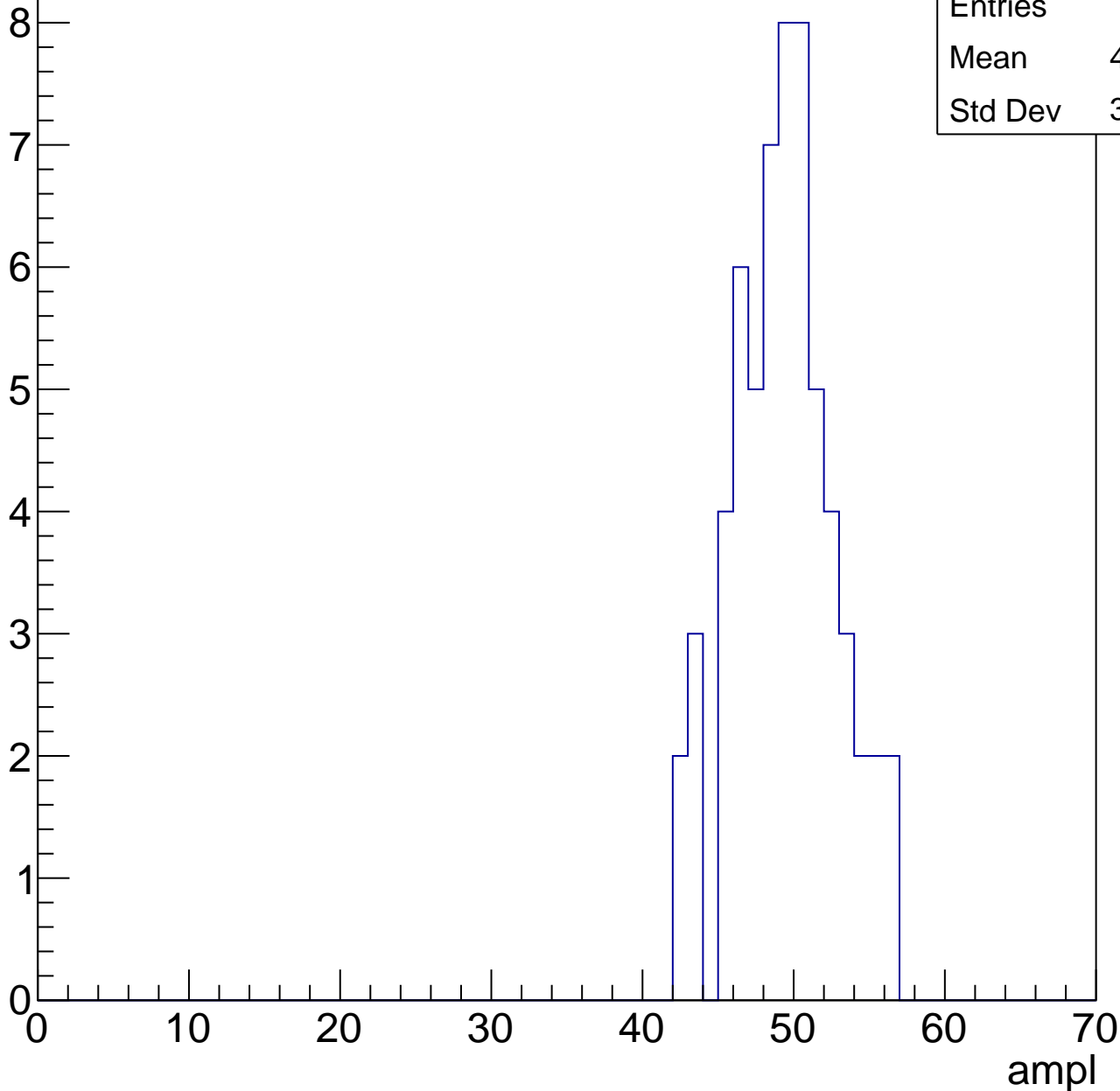


# B1L003S, U22-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	48.92
Std Dev	3.345

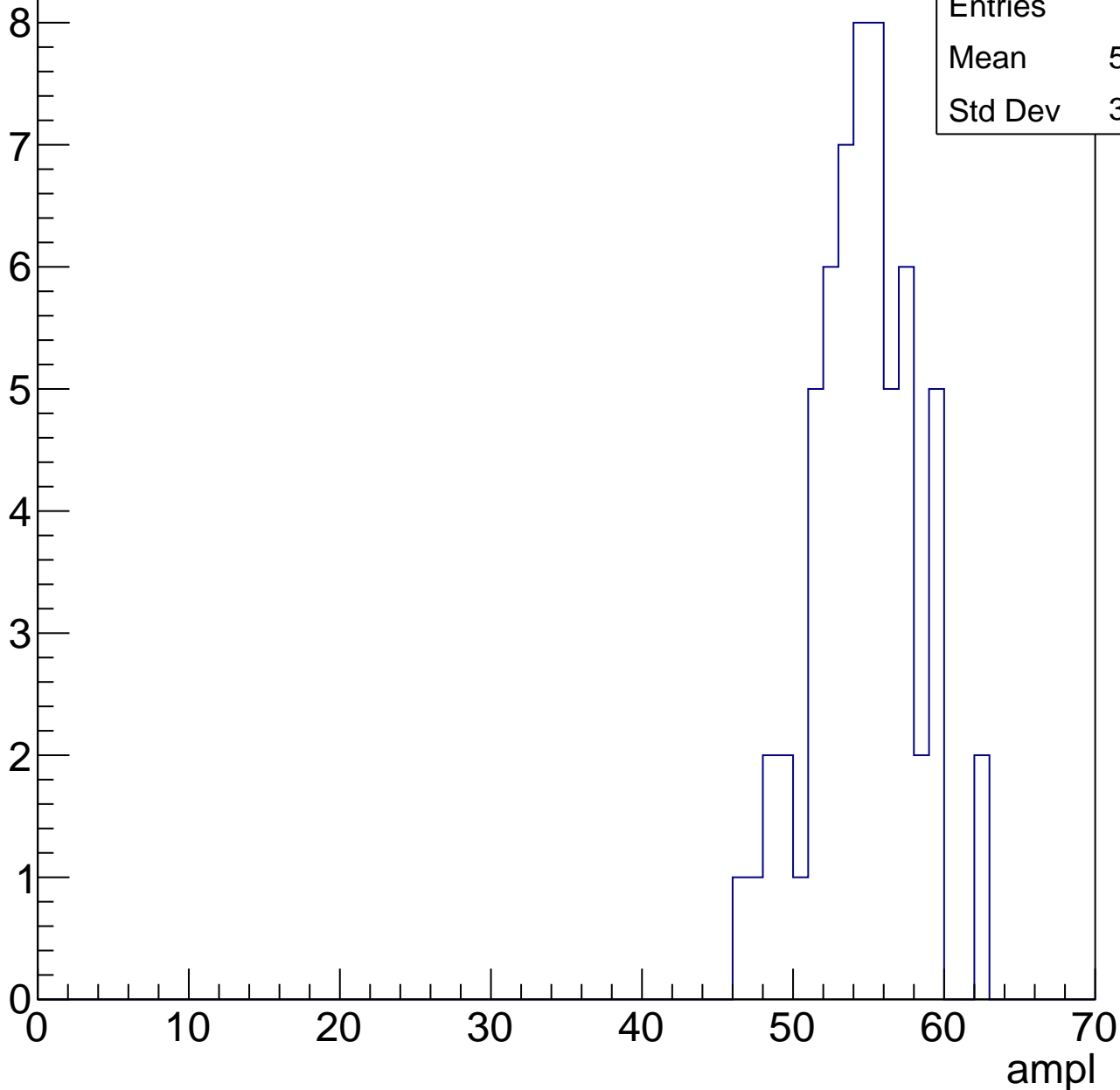


# B1L003S, U22-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	54.16
Std Dev	3.369

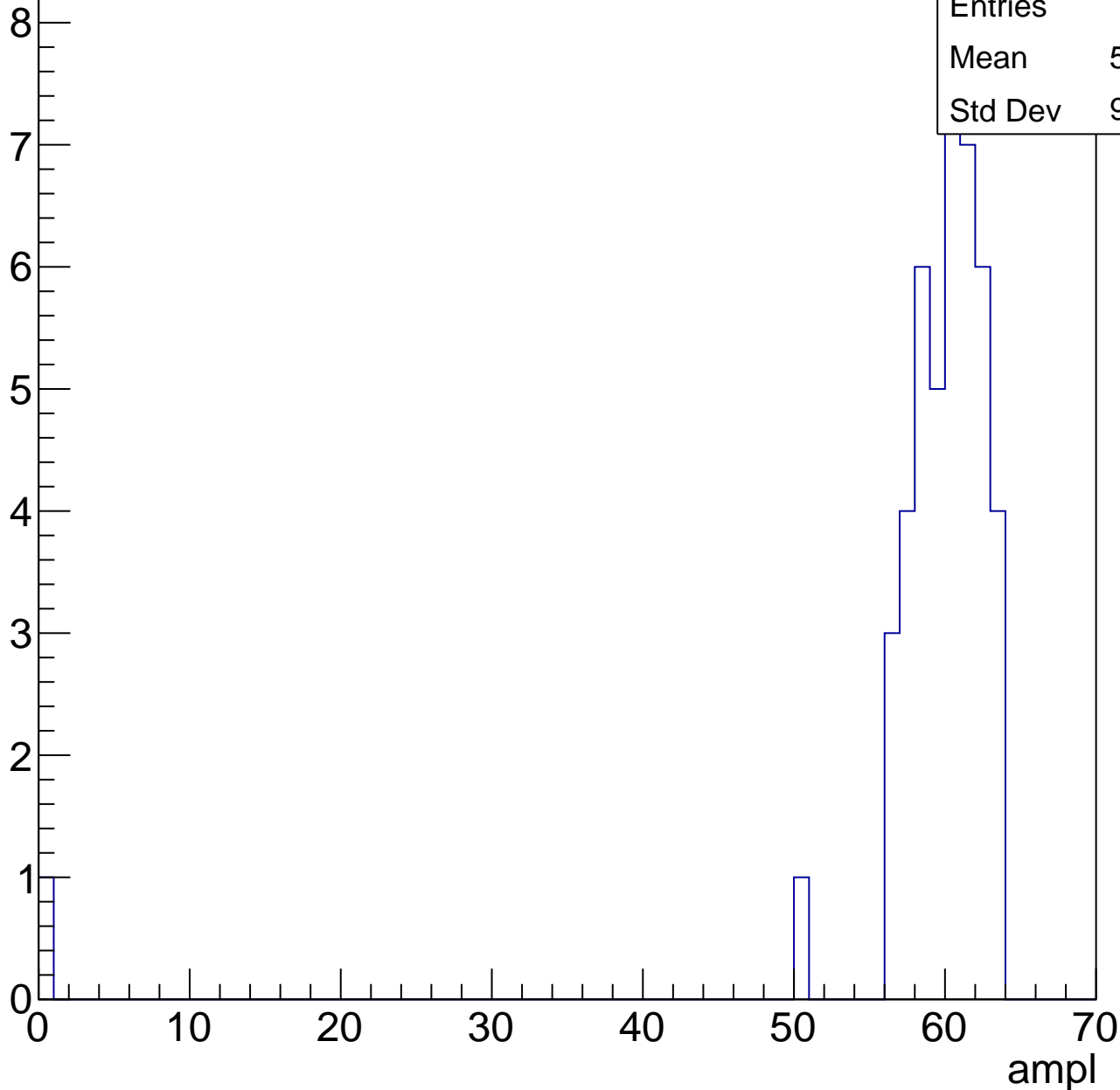


# B1L003S, U22-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.22
Std Dev	9.114

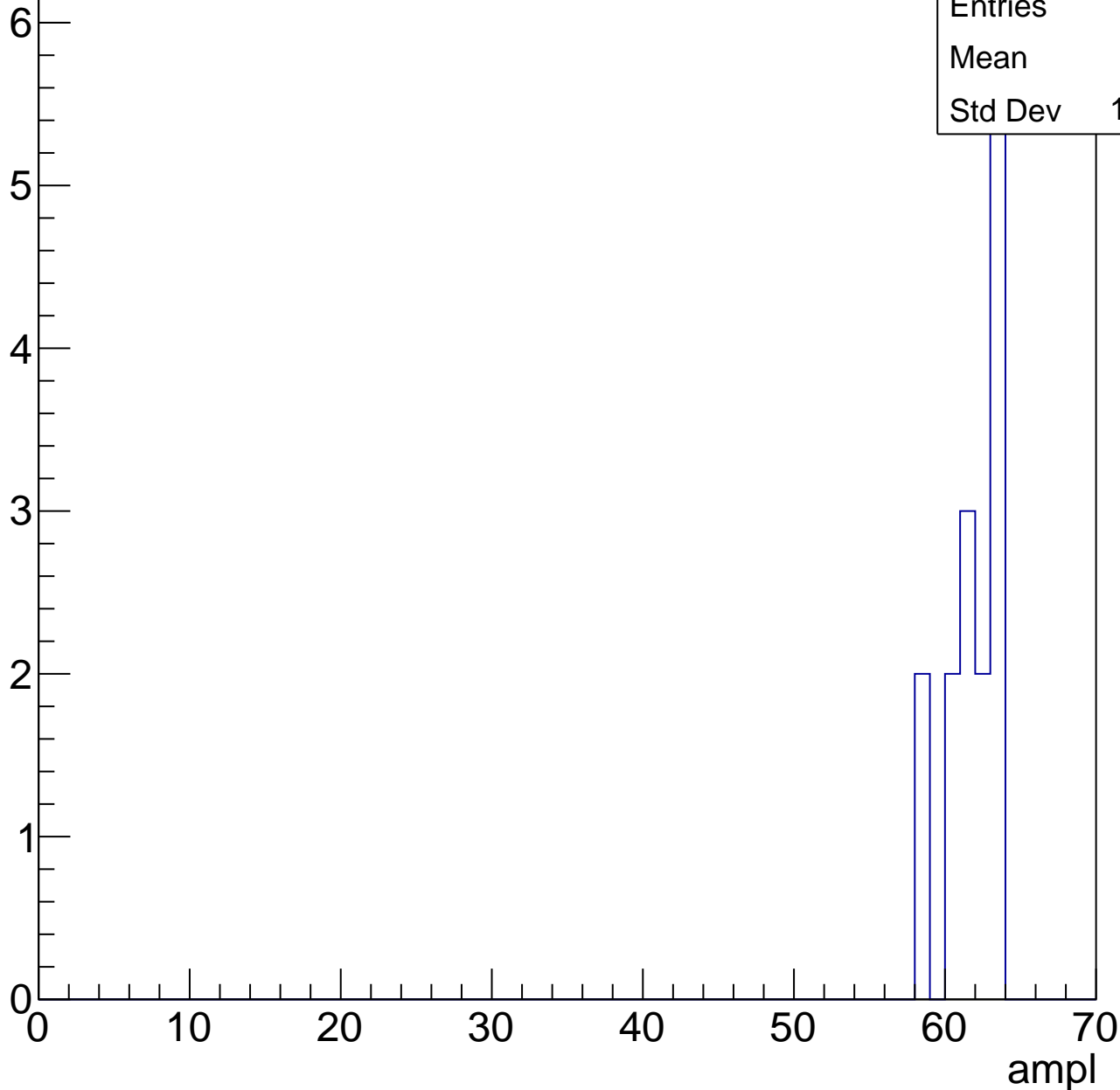


# B1L003S, U22-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.4
Std Dev	1.705





# B1L003S, U22-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch48, adc0

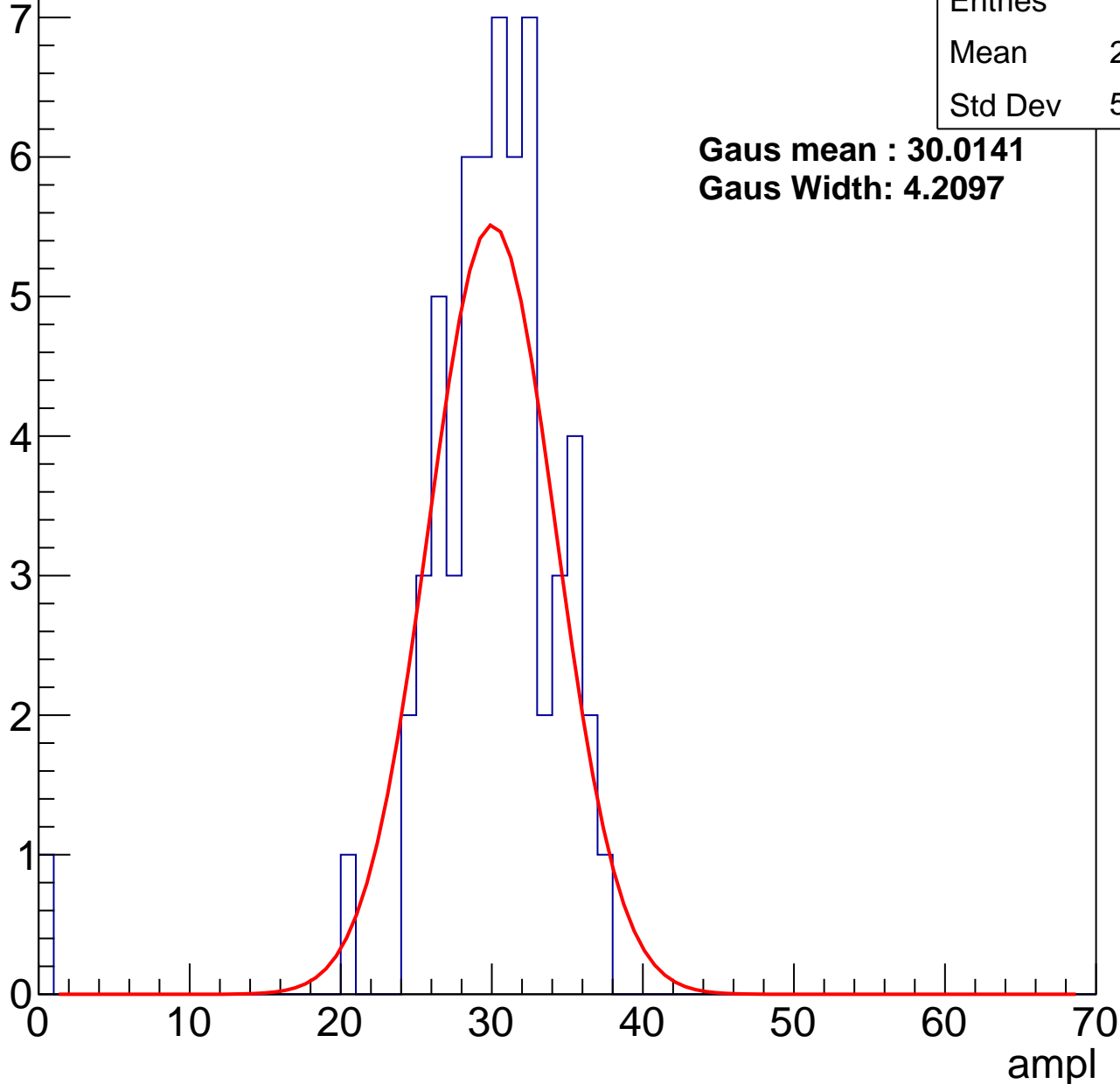
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.37
Std Dev	5.178

**Gaus mean : 30.0141**

**Gaus Width: 4.2097**



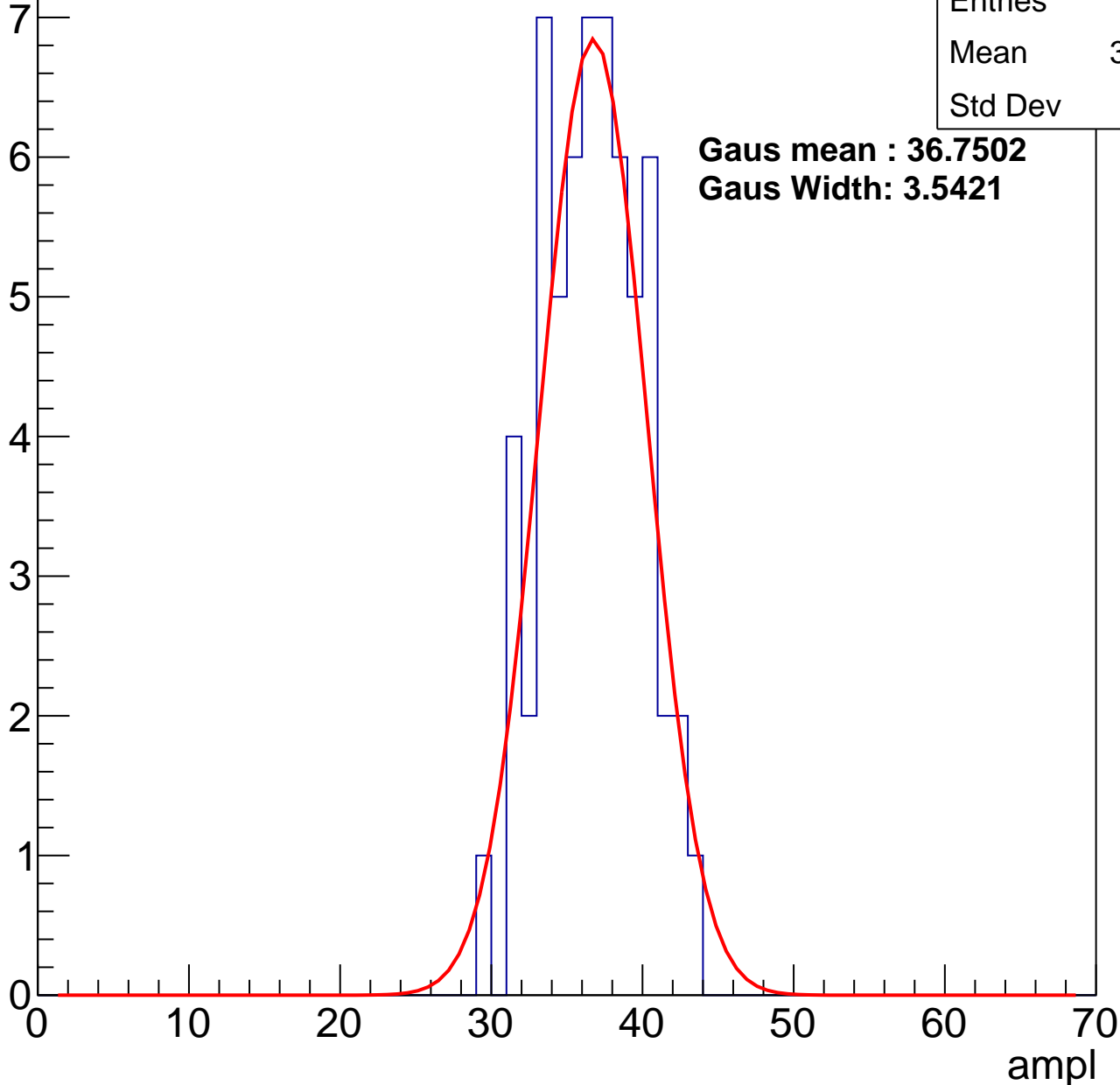
# B1L003S, U22-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.25
Std Dev	3.16

**Gaus mean : 36.7502**  
**Gaus Width: 3.5421**



# B1L003S, U22-ch48, adc2

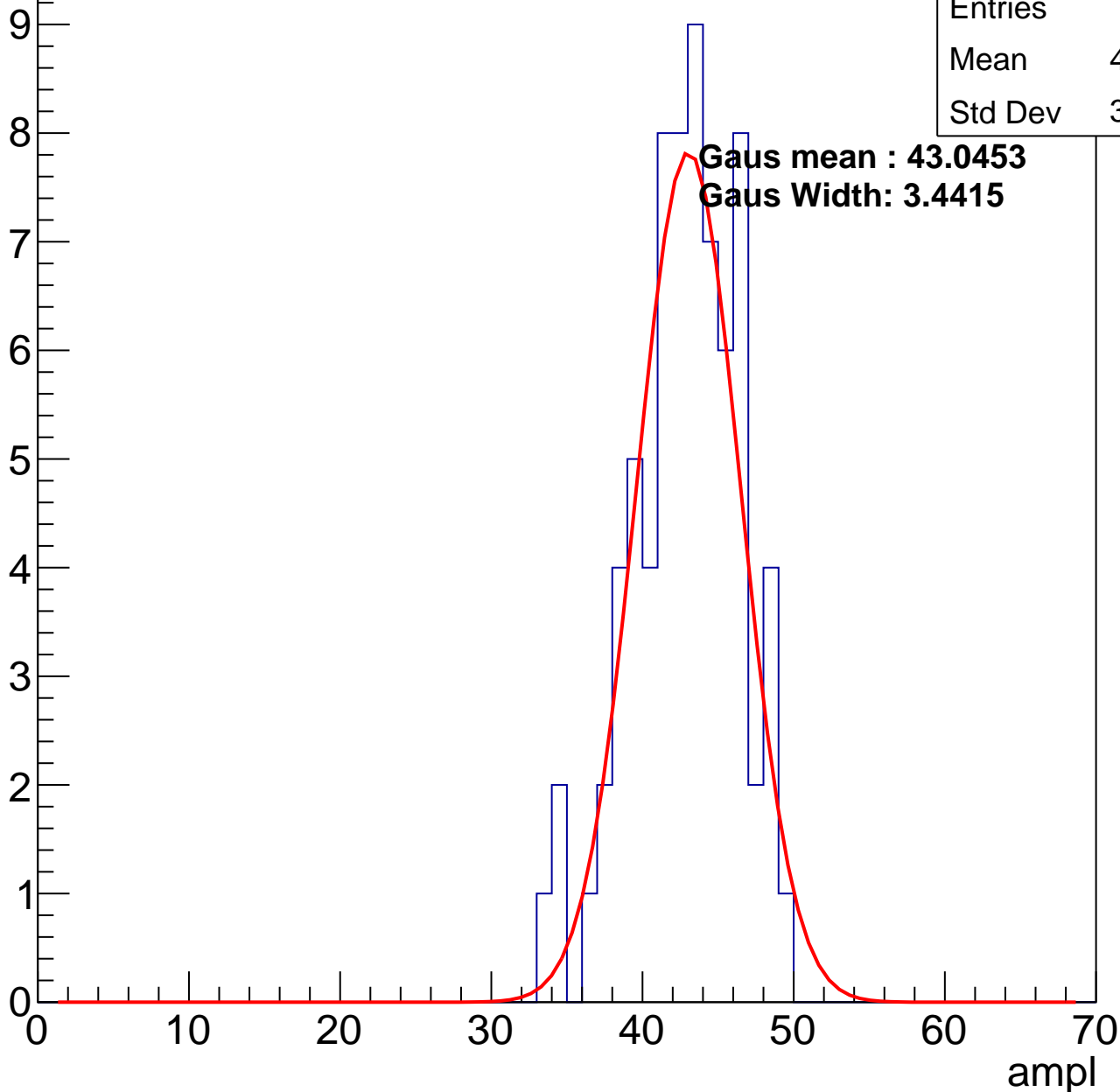
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.36
Std Dev	3.497

**Gaus mean : 43.0453**

**Gaus Width: 3.4415**

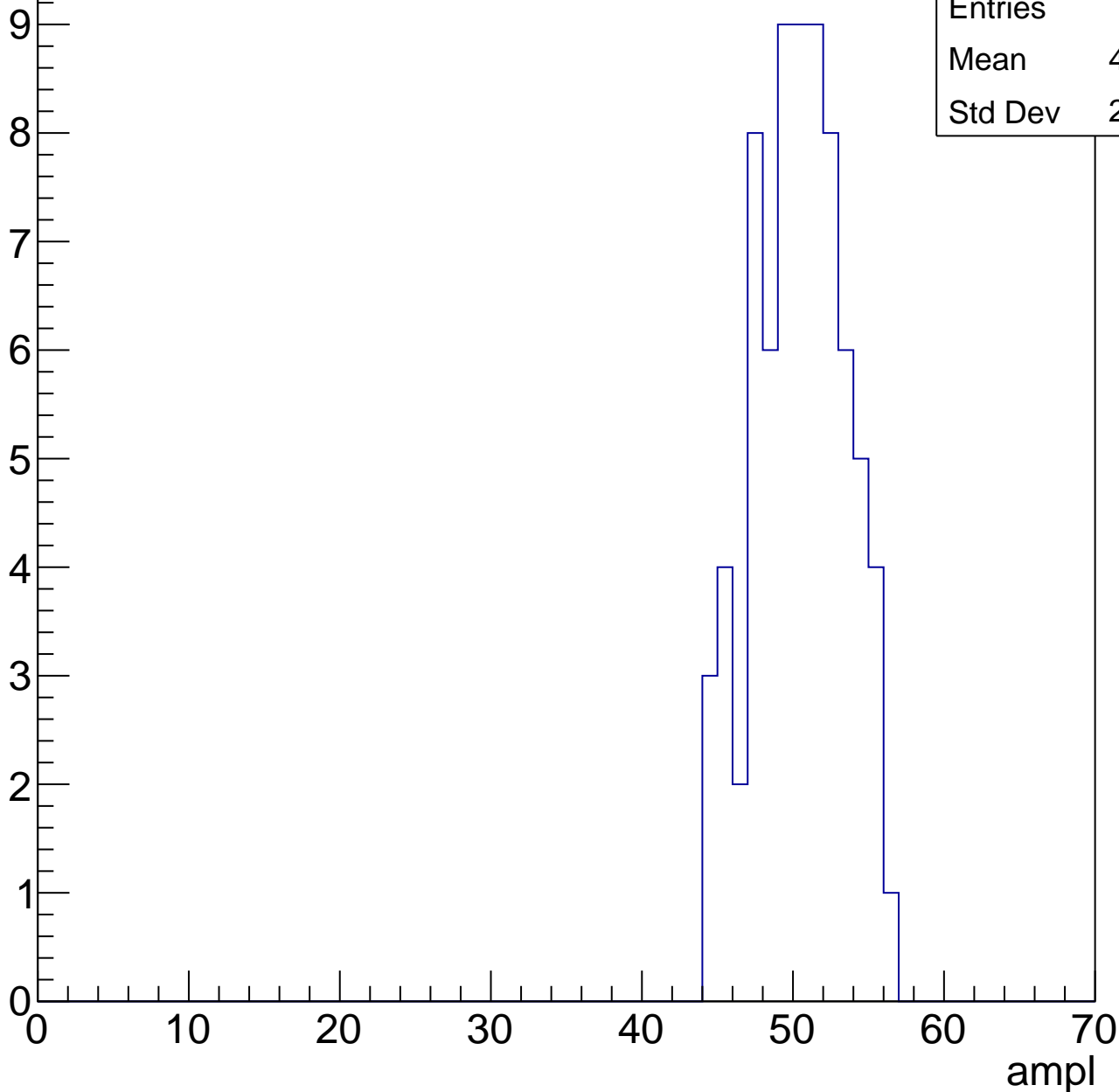


# B1L003S, U22-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

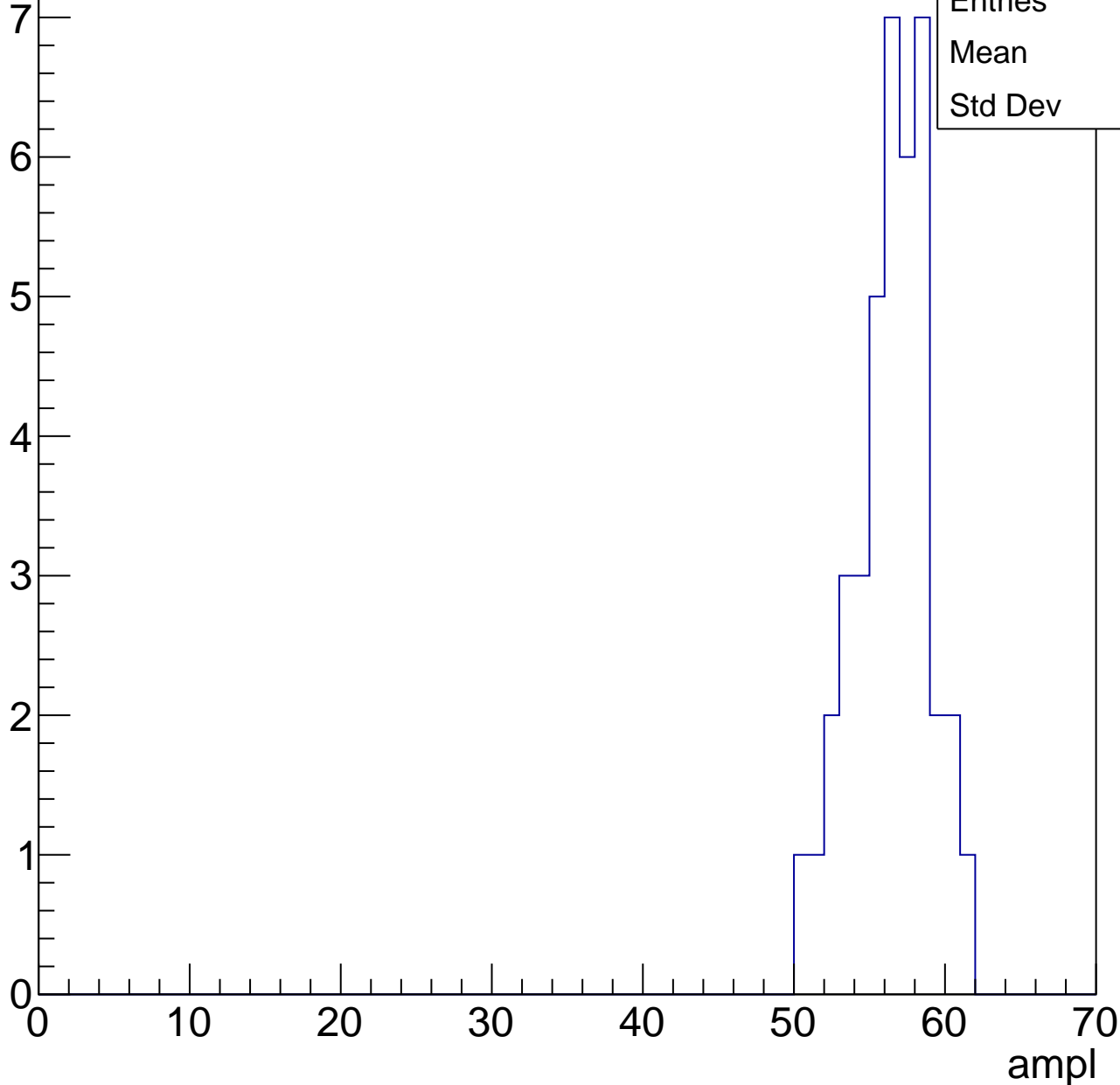
Entries	74
Mean	49.97
Std Dev	2.977



# B1L003S, U22-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



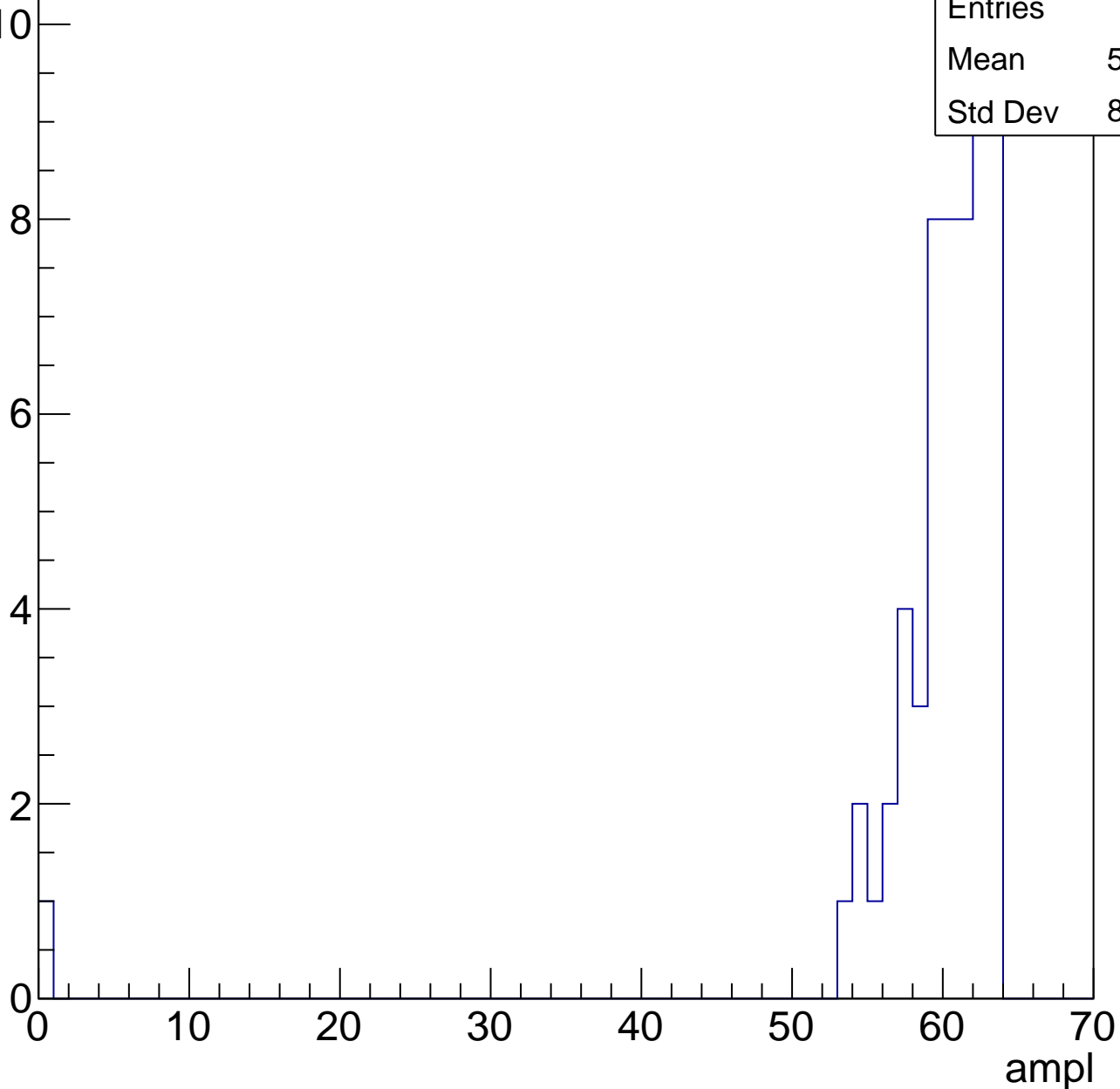
Entries	40
Mean	56
Std Dev	2.48

# B1L003S, U22-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.89
Std Dev	8.265



# B1L003S, U22-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch49, adc0

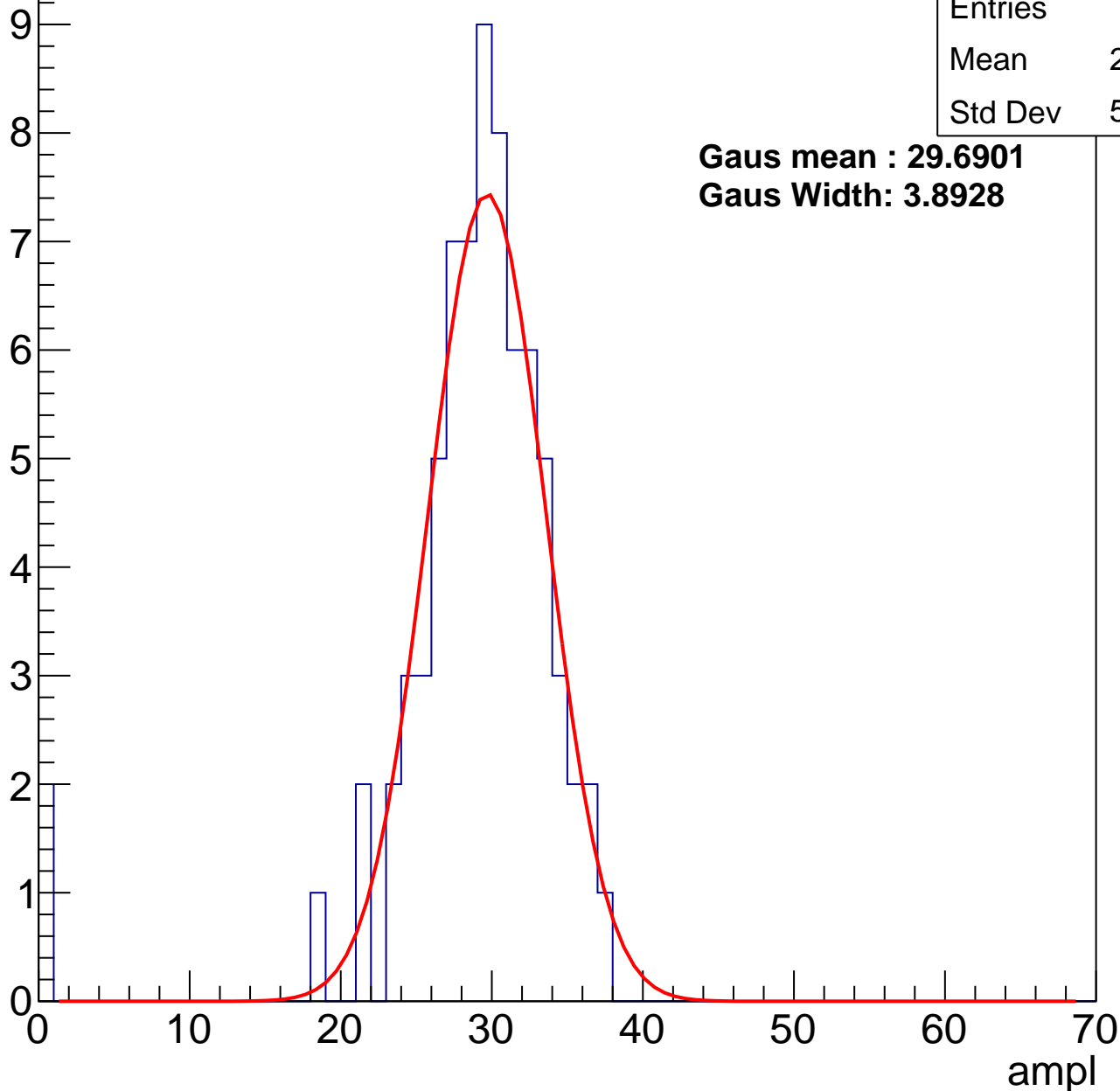
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	28.28
Std Dev	5.976

**Gaus mean : 29.6901**

**Gaus Width: 3.8928**



# B1L003S, U22-ch49, adc1

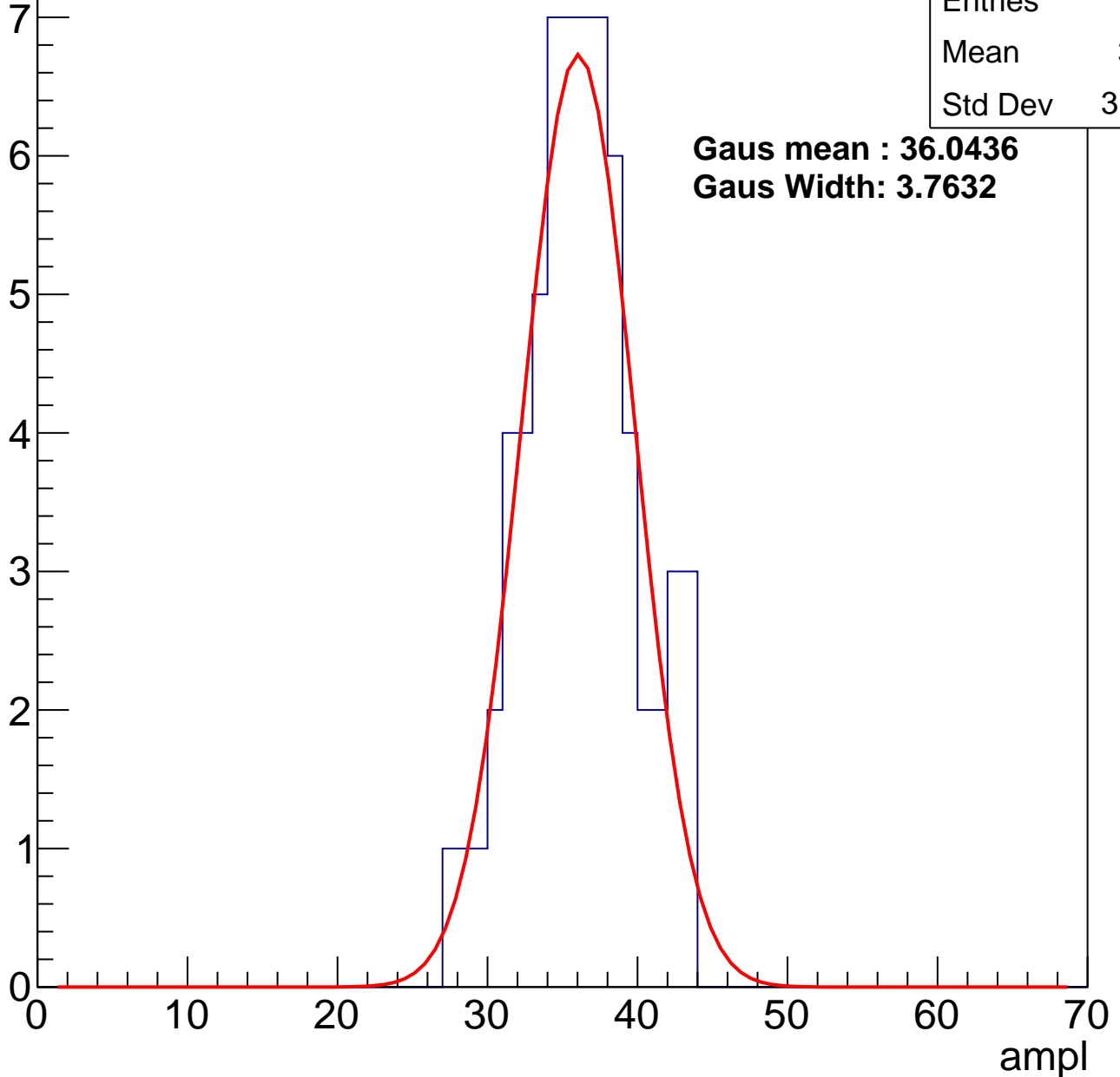
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.7
Std Dev	3.709

**Gaus mean : 36.0436**

**Gaus Width: 3.7632**



# B1L003S, U22-ch49, adc2

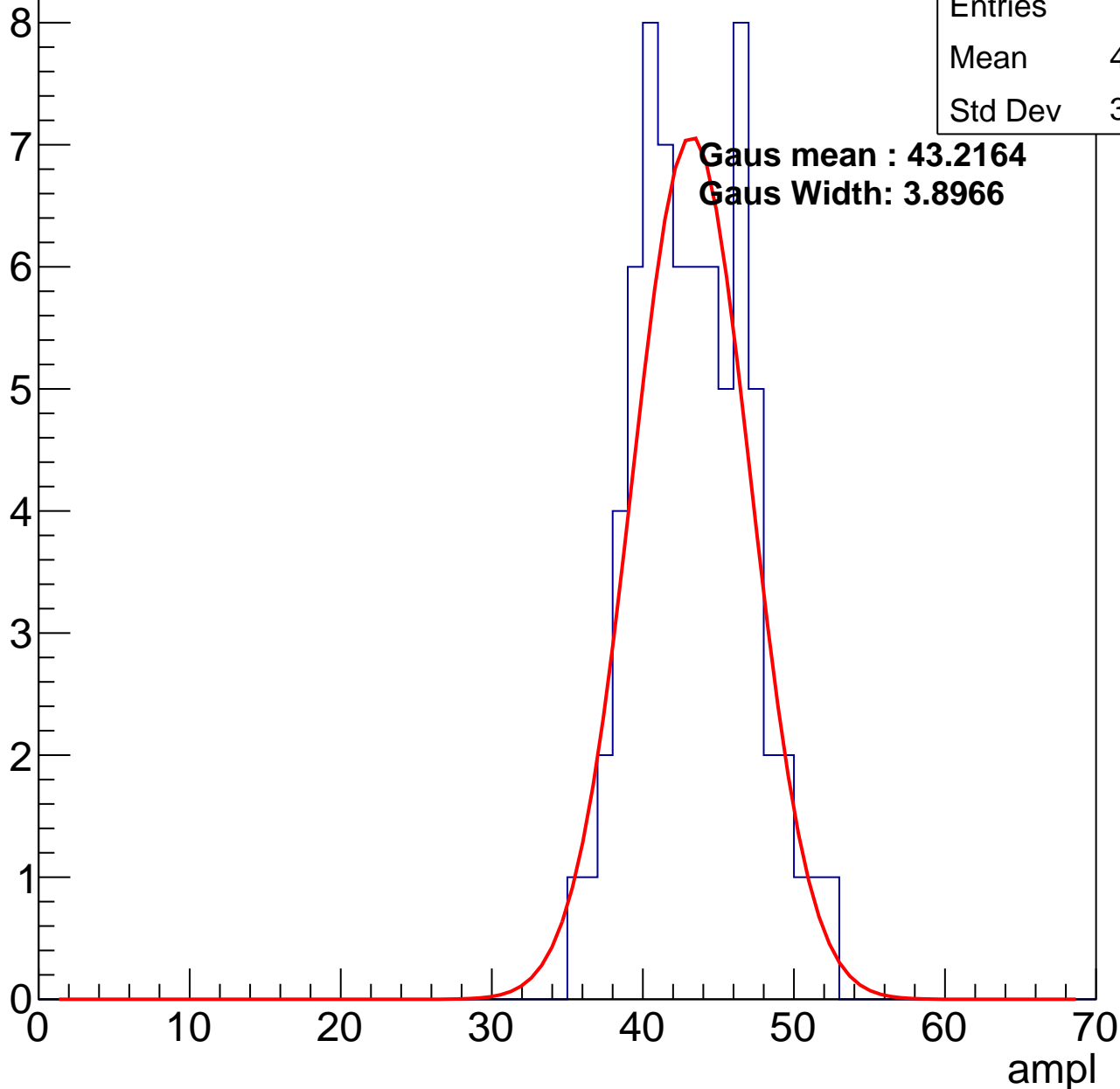
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.88
Std Dev	3.689

**Gaus mean : 43.2164**

**Gaus Width: 3.8966**

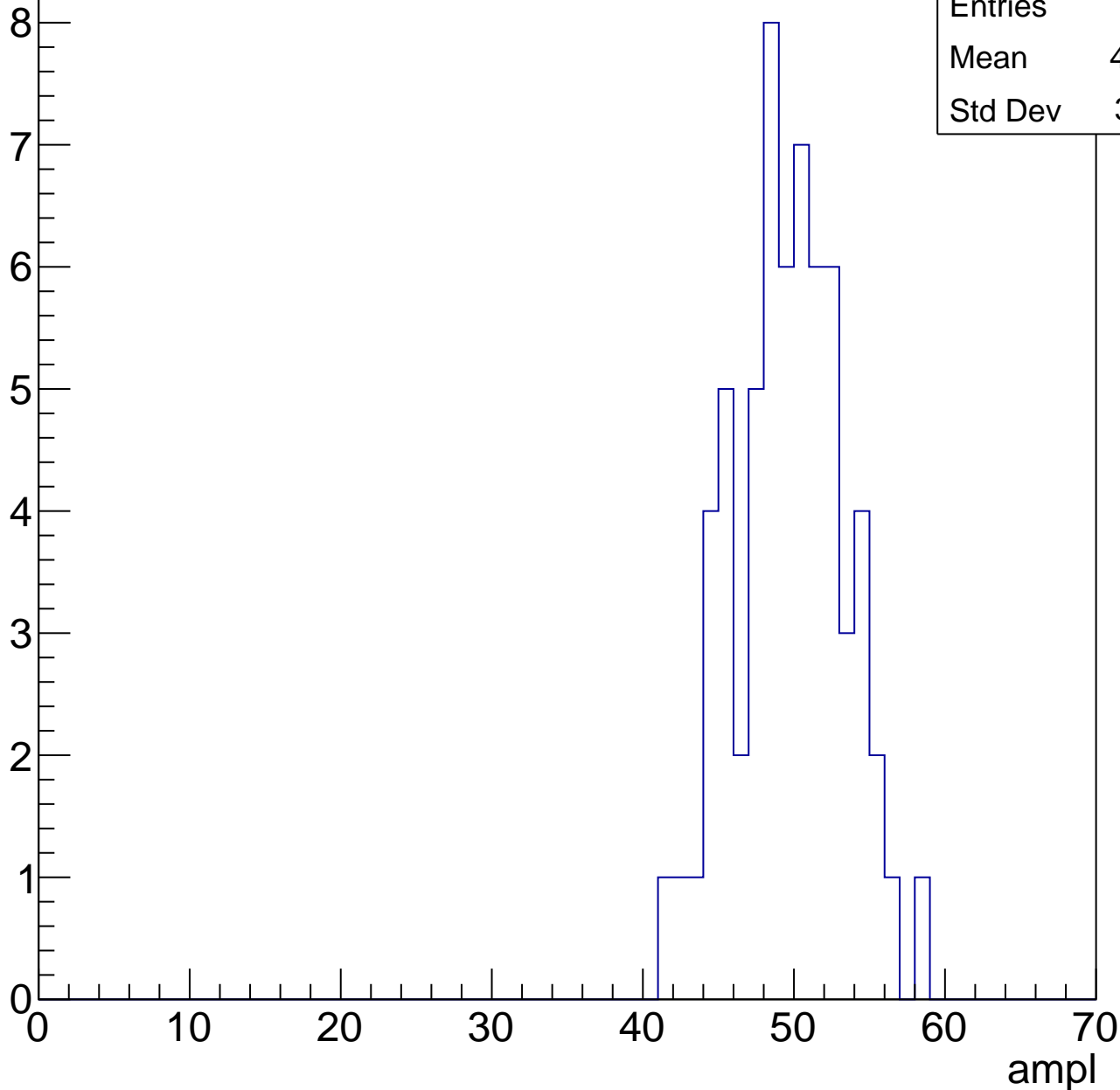


# B1L003S, U22-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.19
Std Dev	3.581

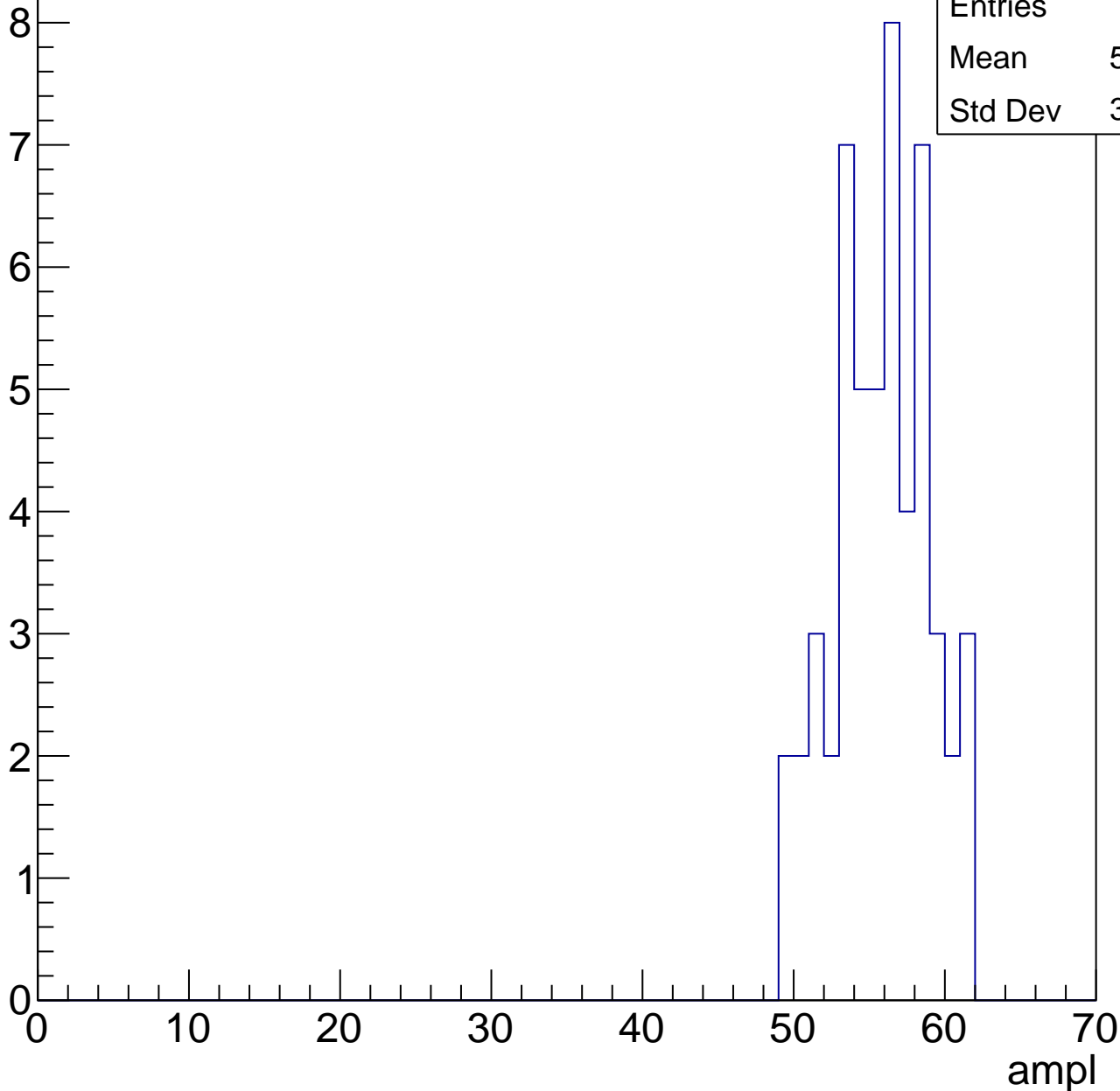


# B1L003S, U22-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.34
Std Dev	3.096

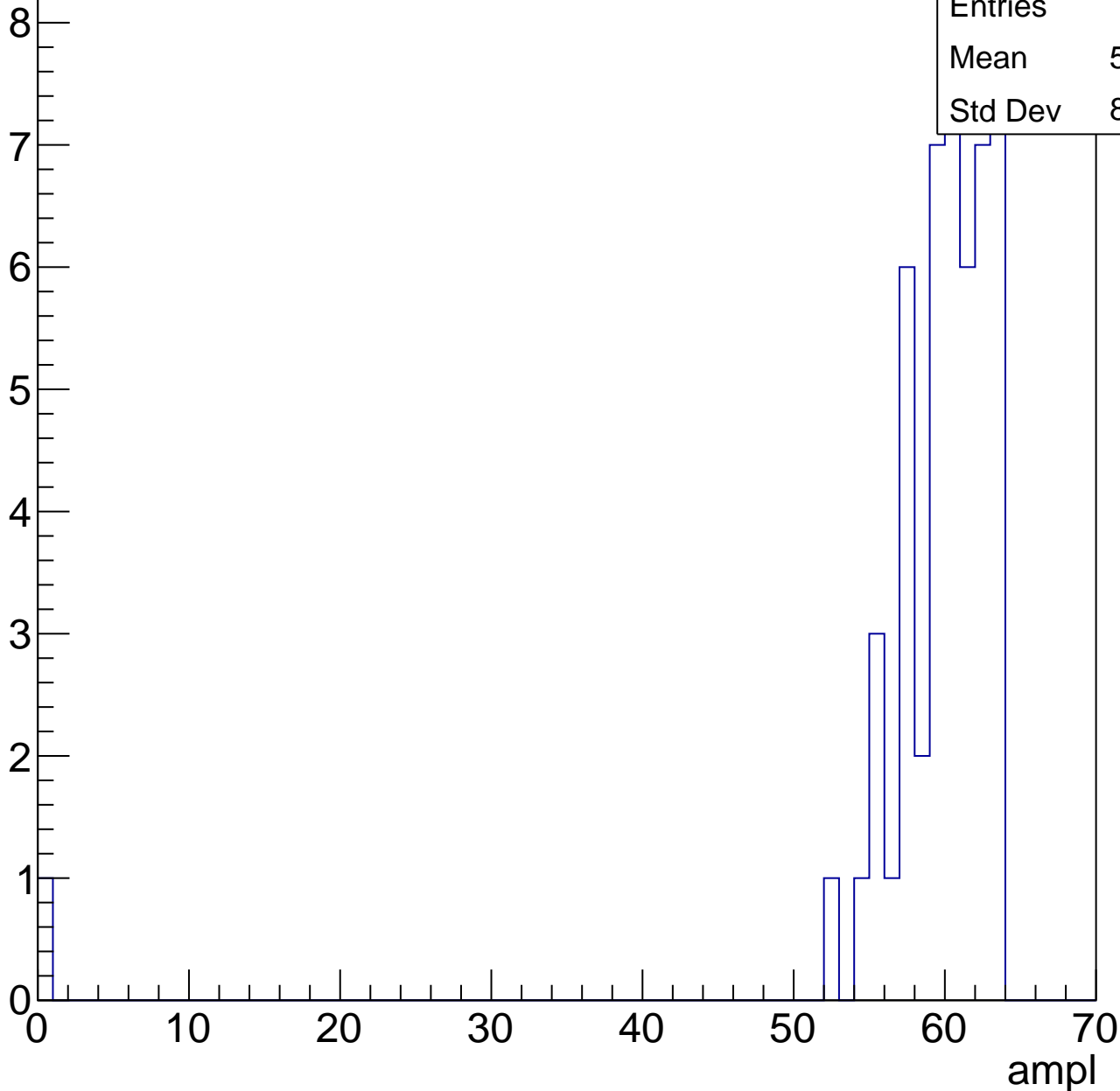


# B1L003S, U22-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.47
Std Dev	8.685



# B1L003S, U22-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch50, adc0

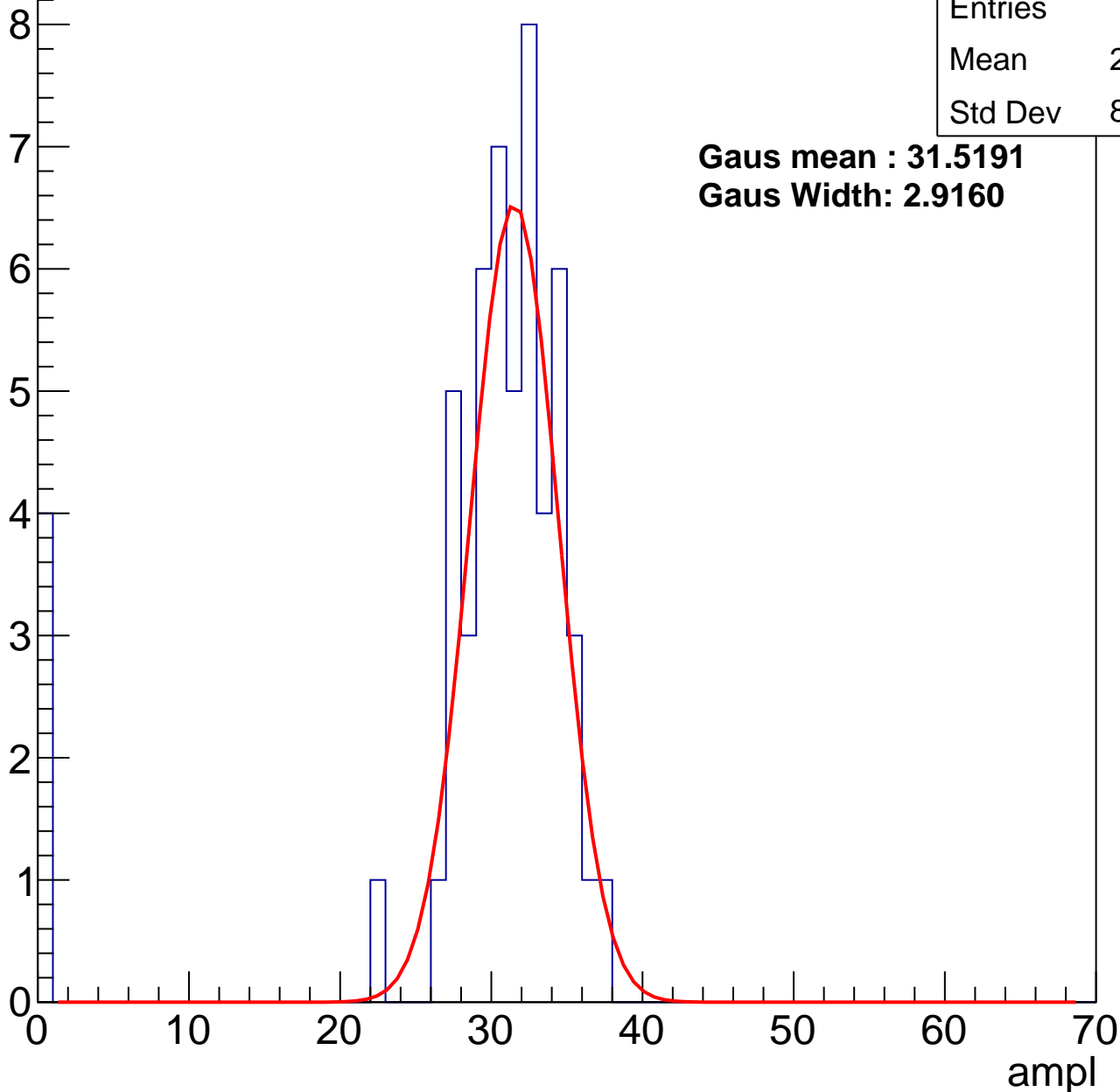
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	28.65
Std Dev	8.499

**Gaus mean : 31.5191**

**Gaus Width: 2.9160**



# B1L003S, U22-ch50, adc1

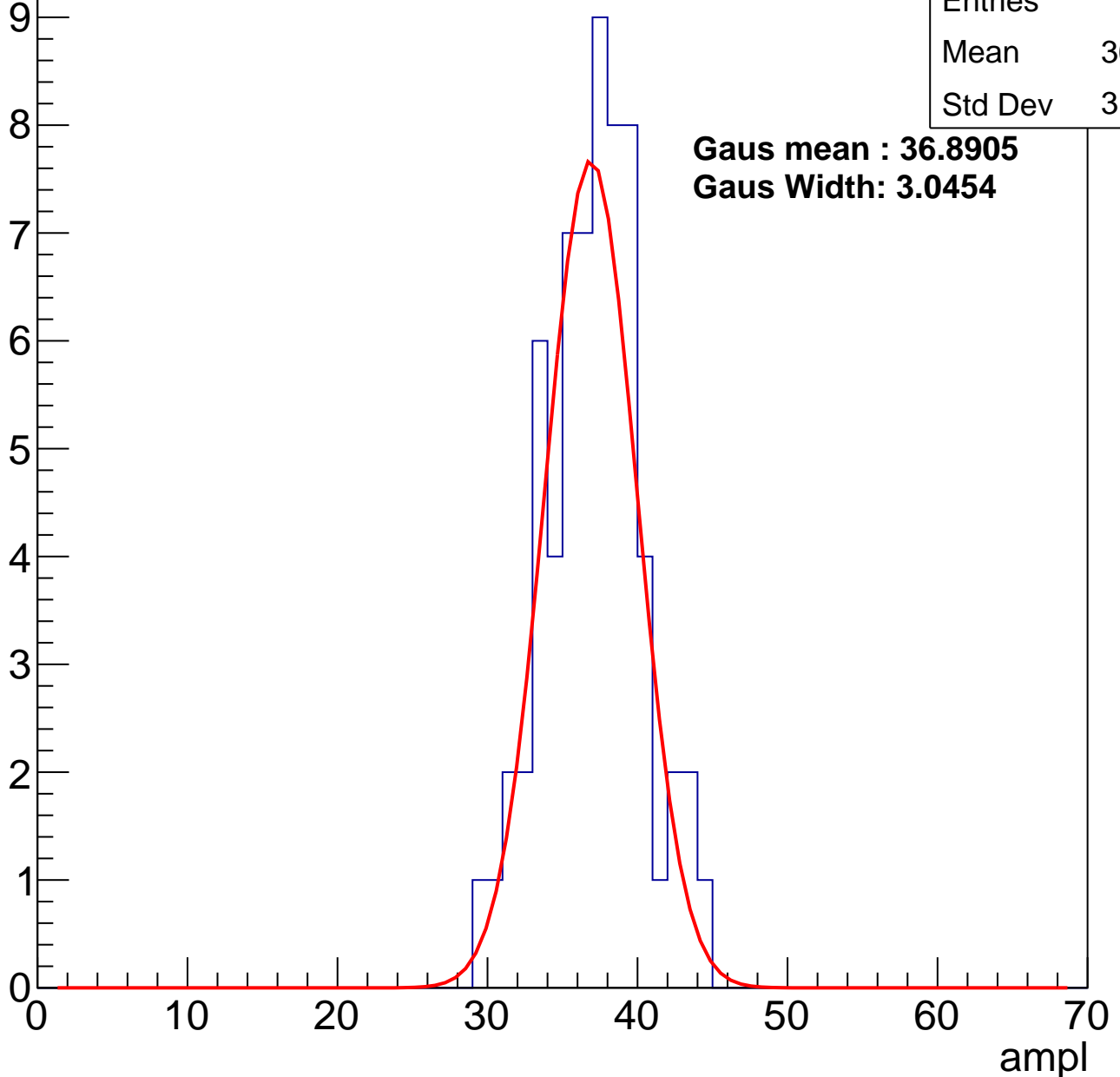
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.62
Std Dev	3.175

**Gaus mean : 36.8905**

**Gaus Width: 3.0454**



# B1L003S, U22-ch50, adc2

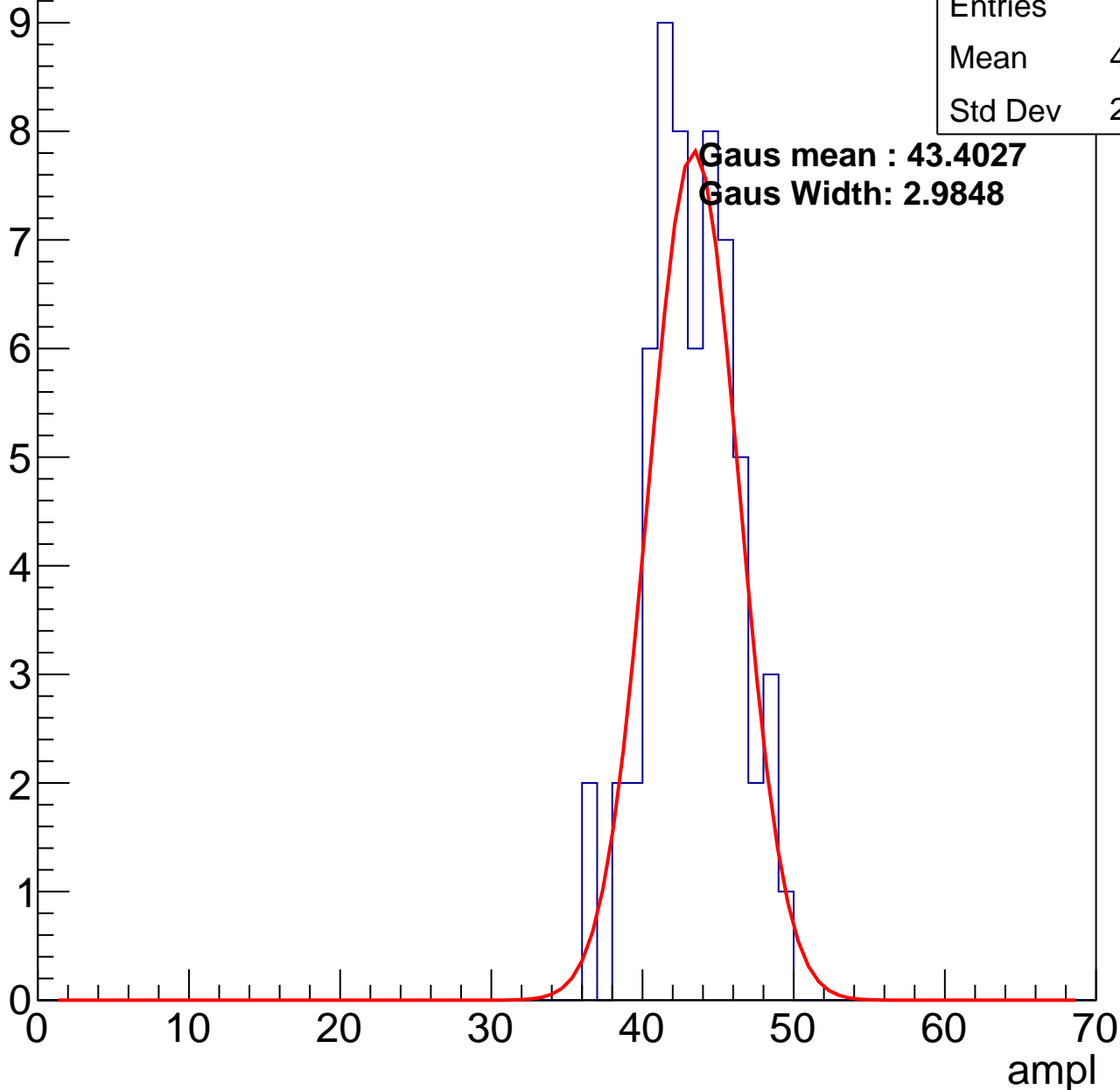
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.84
Std Dev	2.864

**Gaus mean : 43.4027**

**Gaus Width: 2.9848**



# B1L003S, U22-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	49.92
Std Dev	3.149

Entry

10

8

6

4

2

0

0

10

20

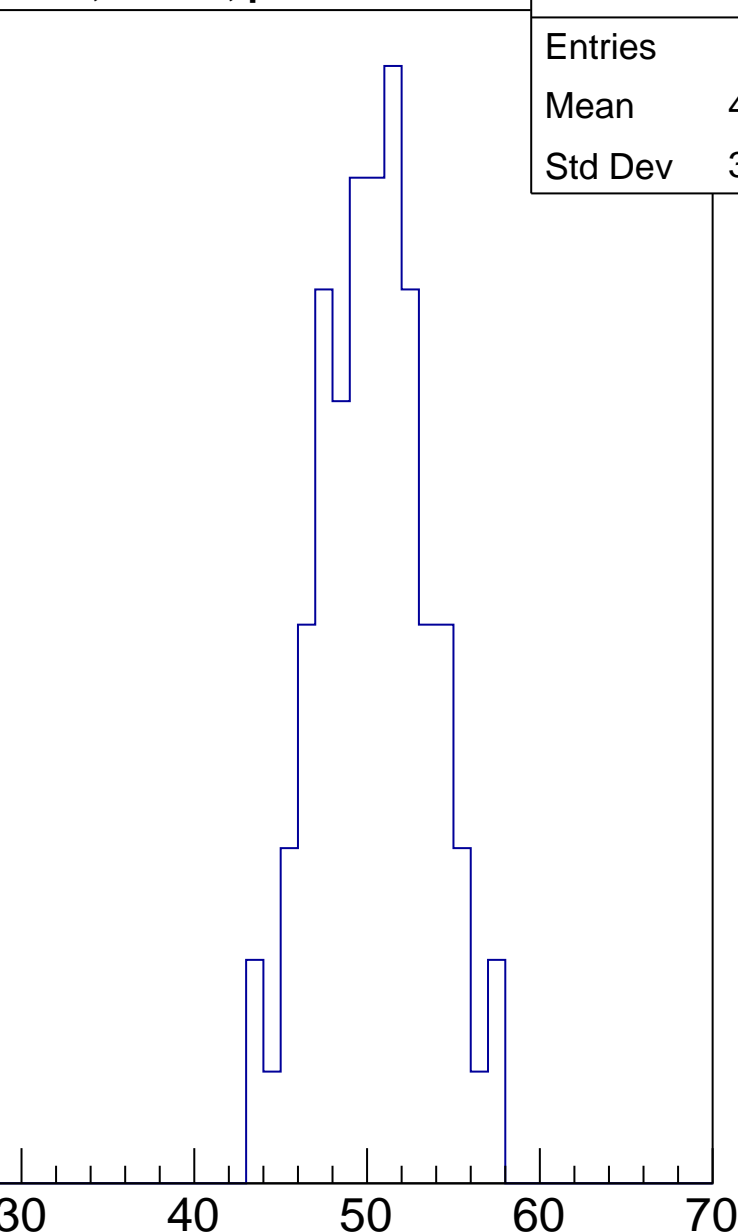
30

40

50

60

ampl



# B1L003S, U22-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	49
Mean	55.14
Std Dev	8.332

Entry

10

8

6

4

2

0

0

10

20

30

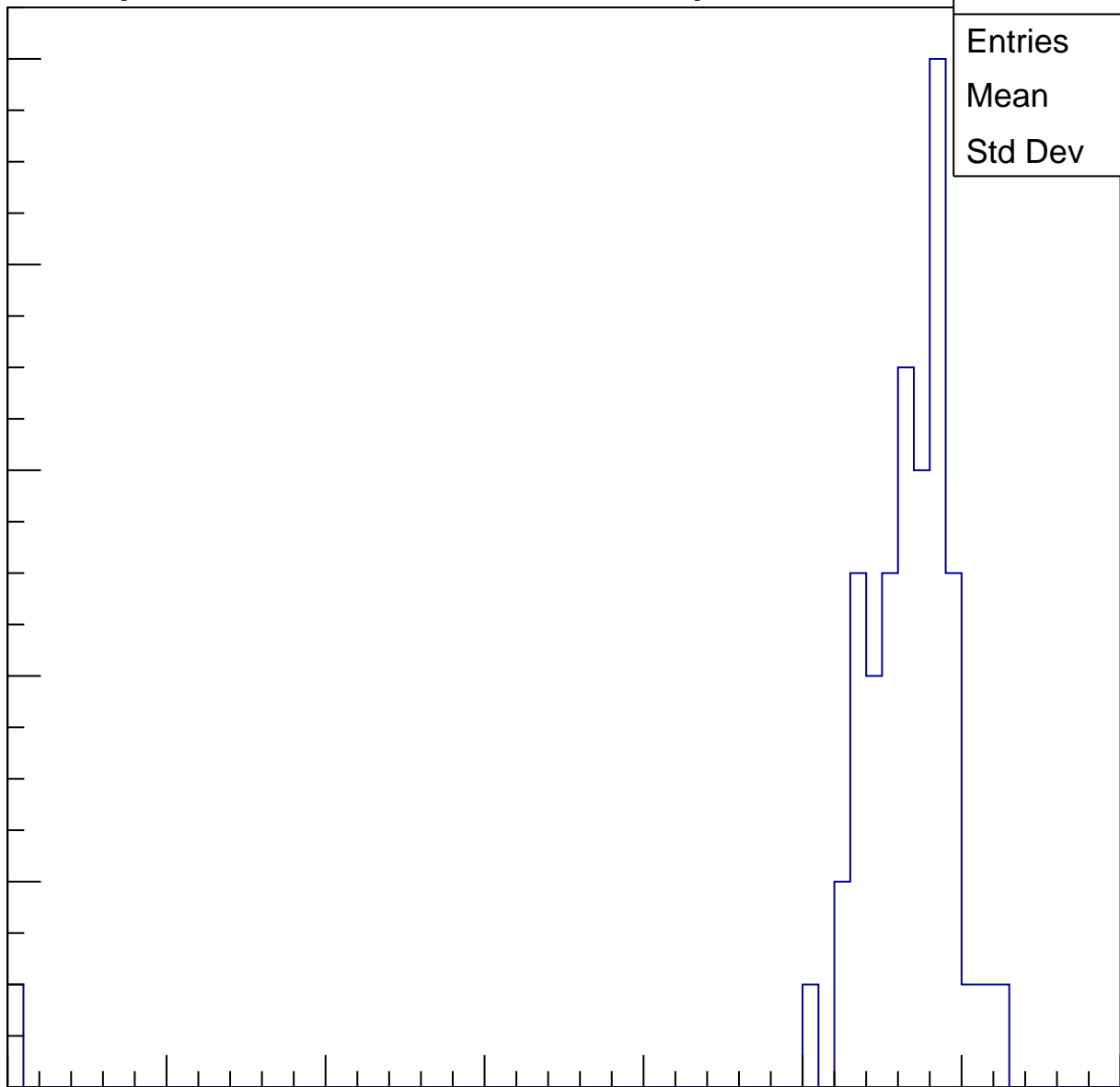
40

50

60

70

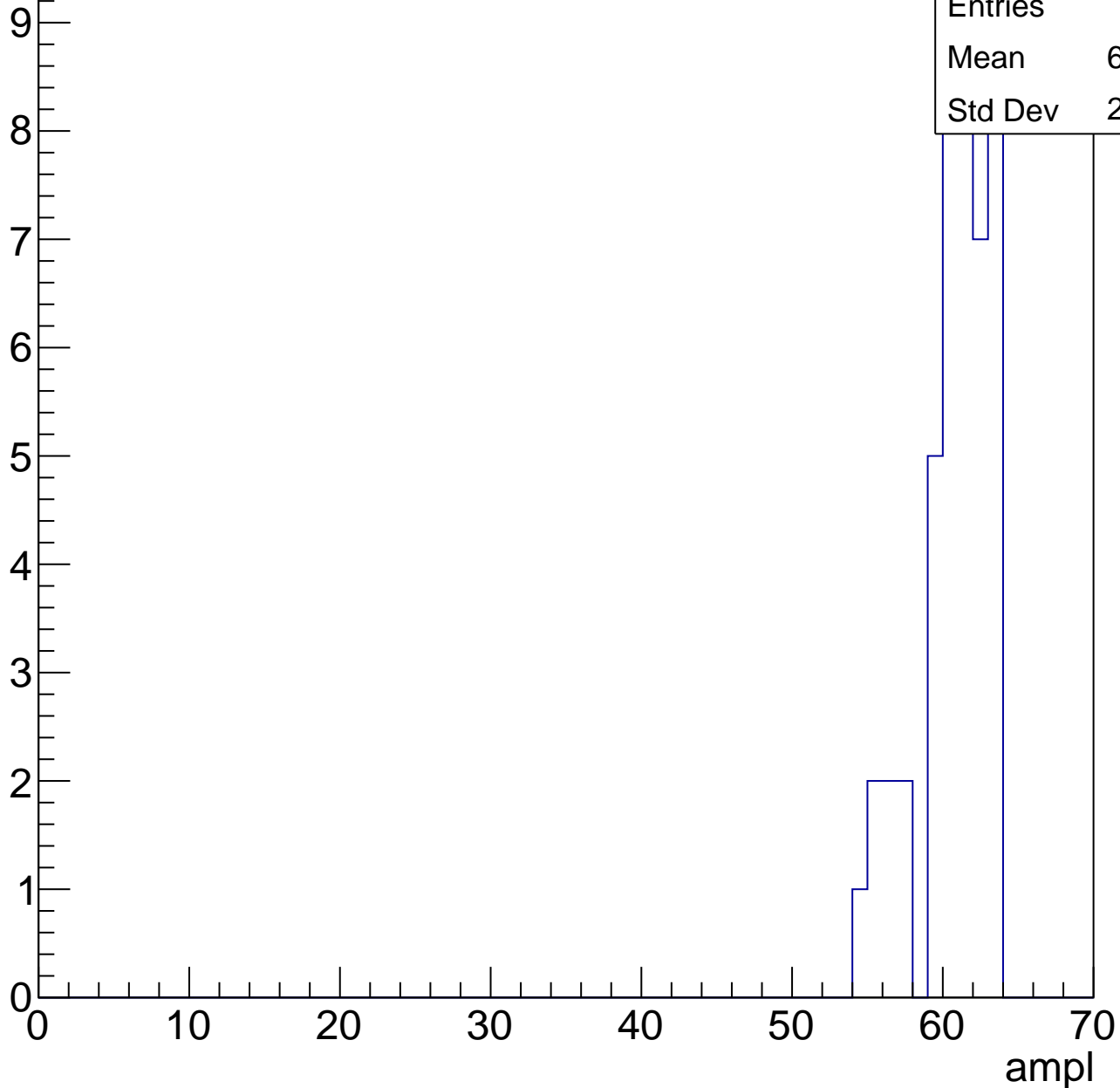
ampl



# B1L003S, U22-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch51, adc0

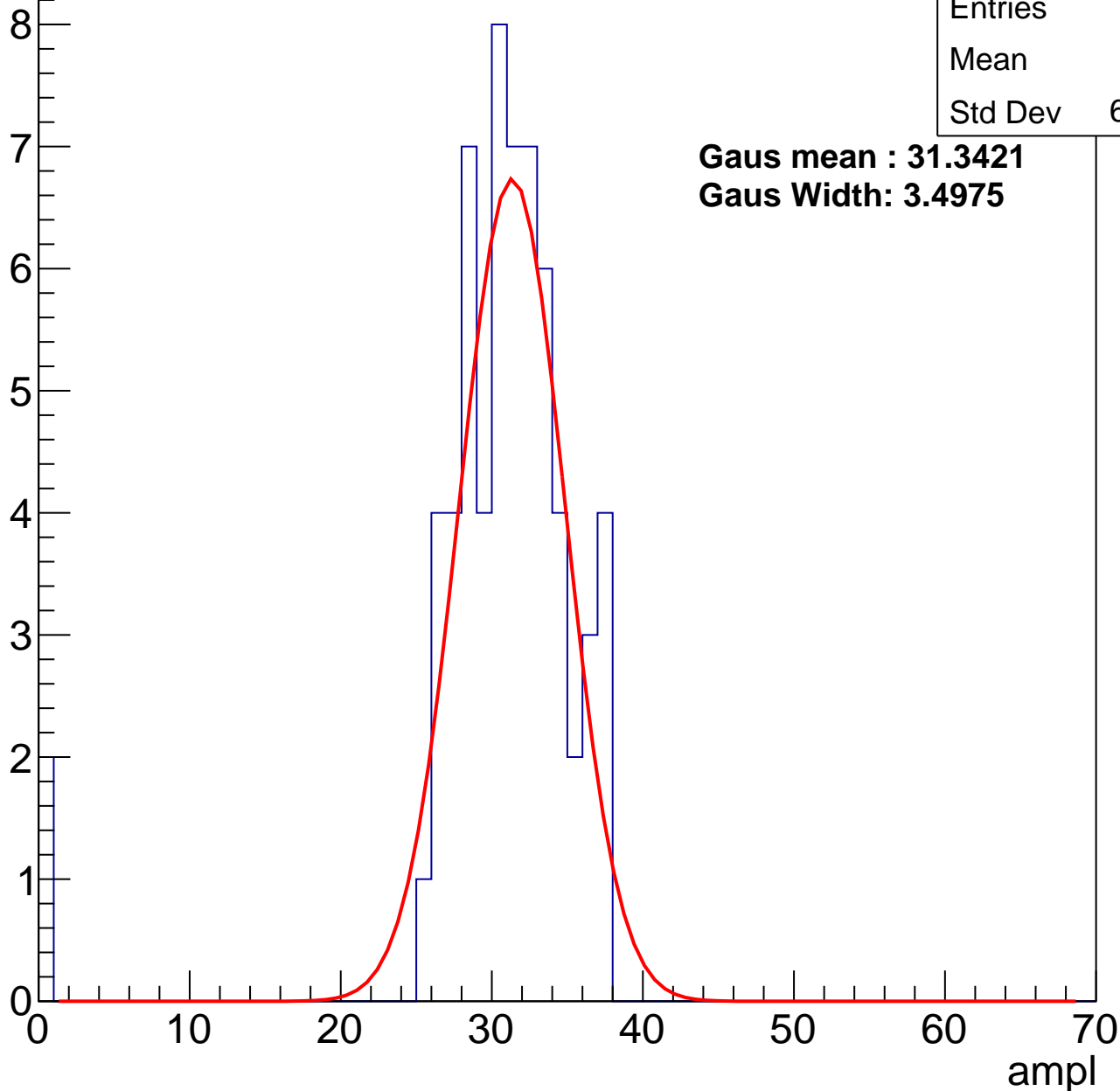
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30
Std Dev	6.254

**Gaus mean : 31.3421**

**Gaus Width: 3.4975**



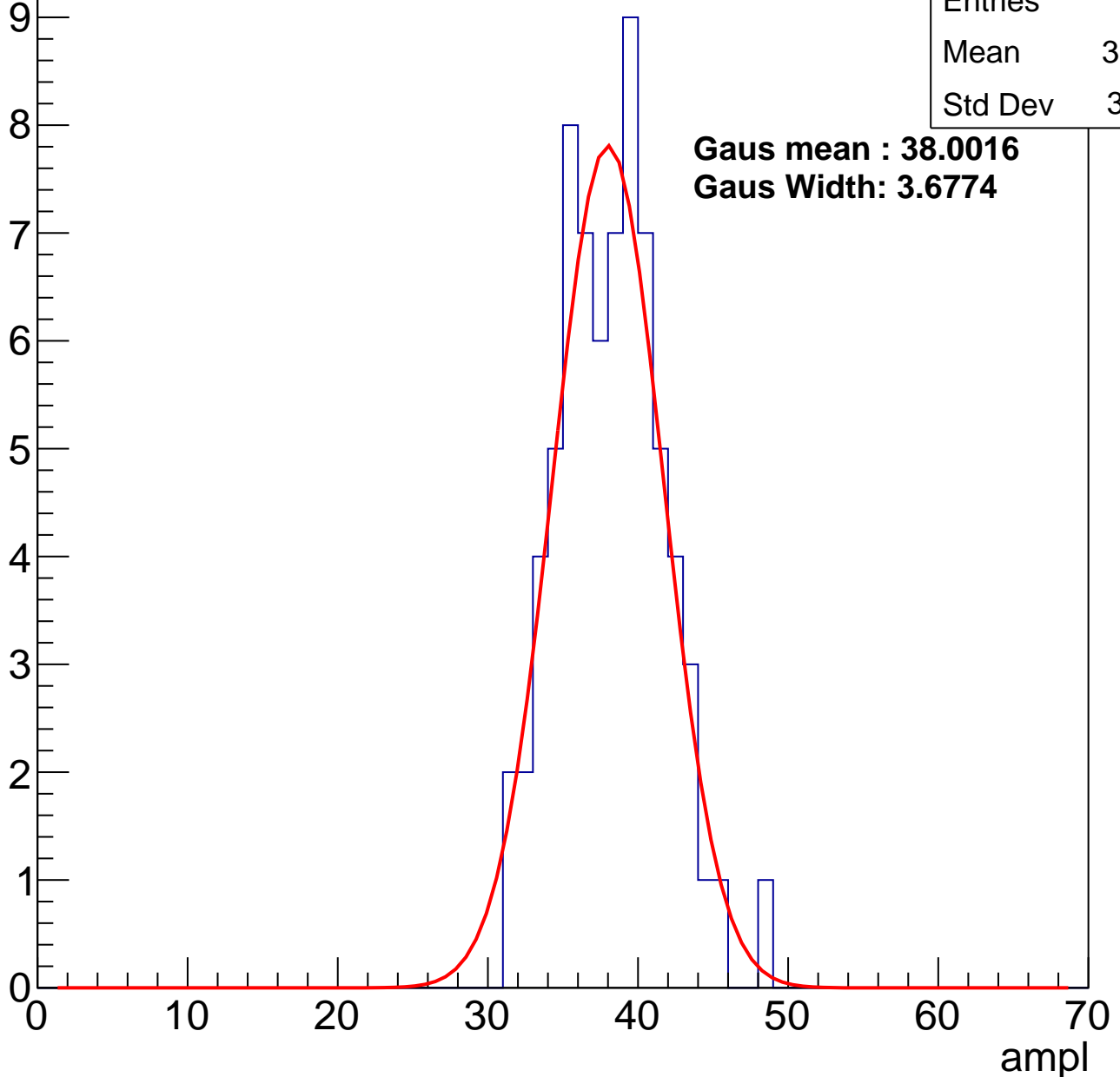
# B1L003S, U22-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	37.75
Std Dev	3.451

**Gaus mean : 38.0016**  
**Gaus Width: 3.6774**



# B1L003S, U22-ch51, adc2

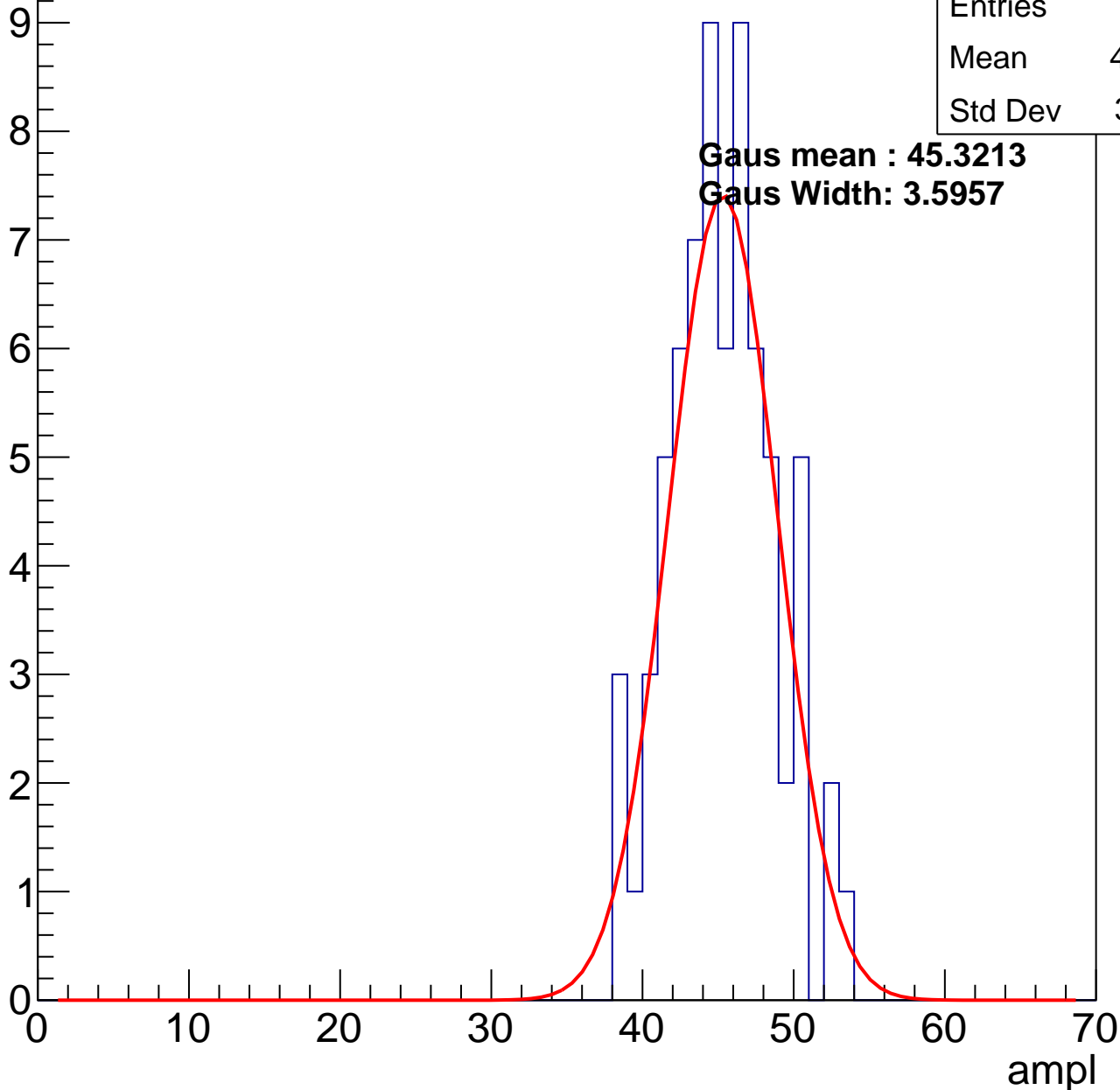
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	44.83
Std Dev	3.431

**Gaus mean : 45.3213**

**Gaus Width: 3.5957**



# B1L003S, U22-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

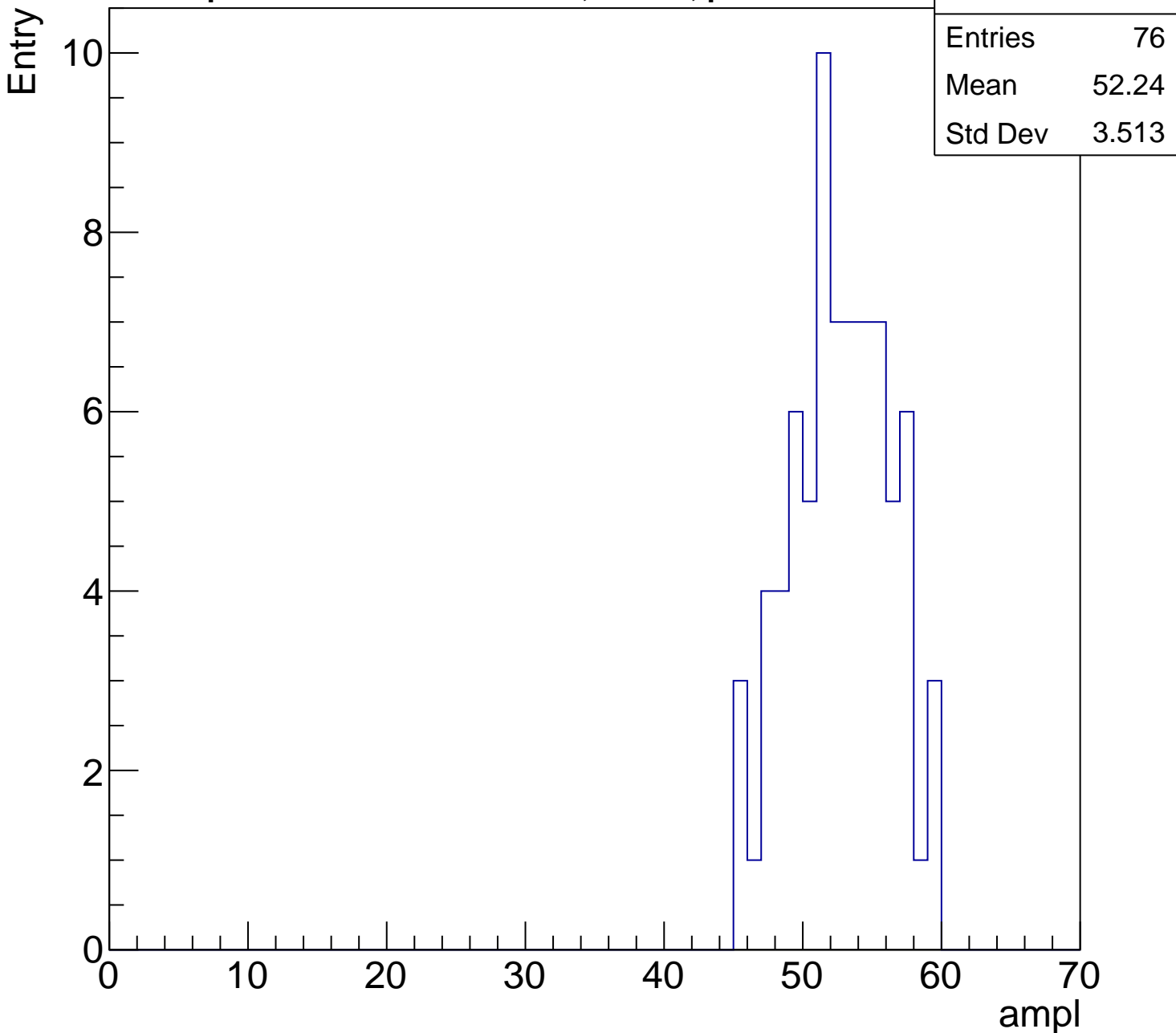
Entries	76
Mean	52.24
Std Dev	3.513

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

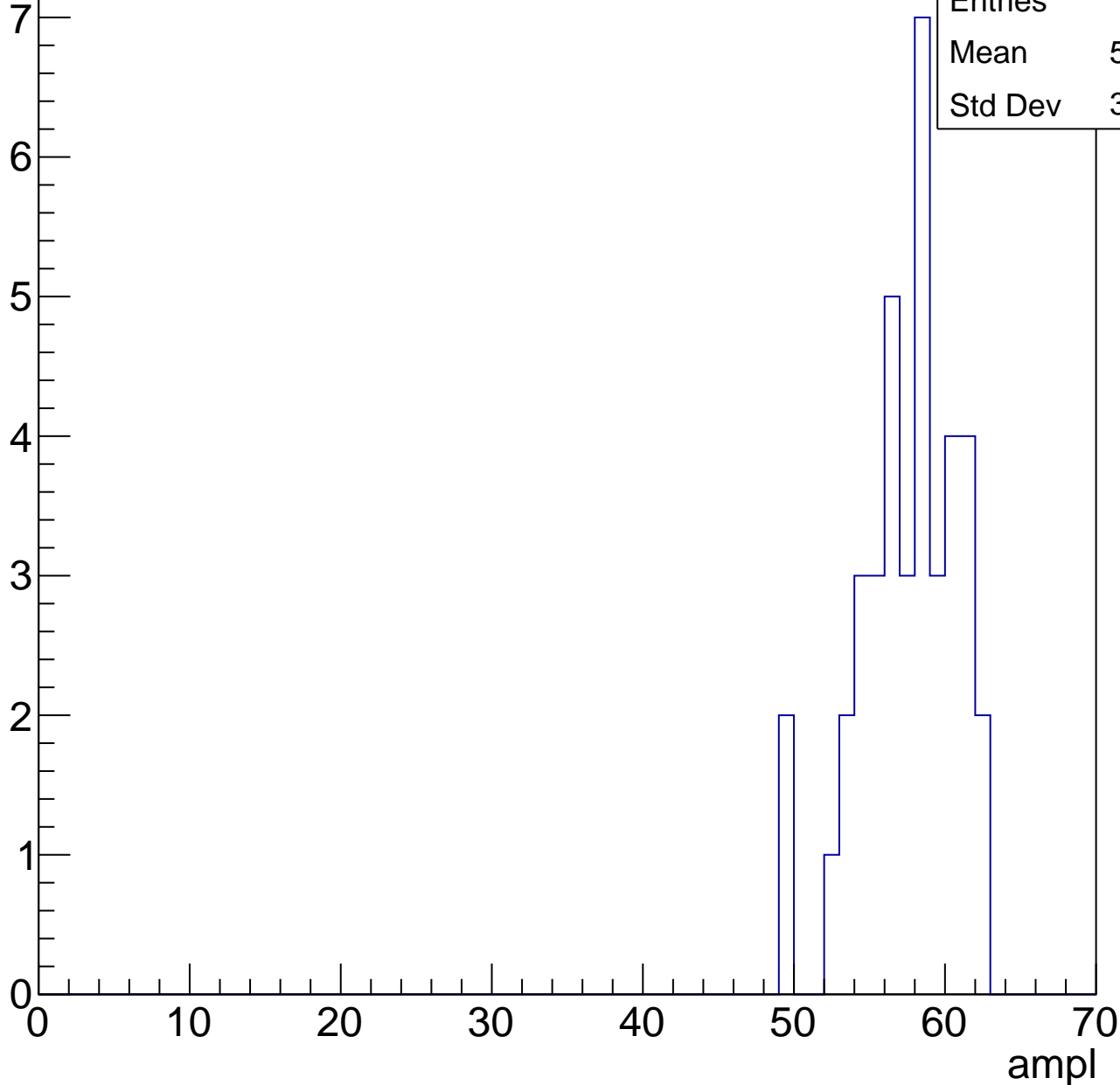


# B1L003S, U22-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	57.05
Std Dev	3.194

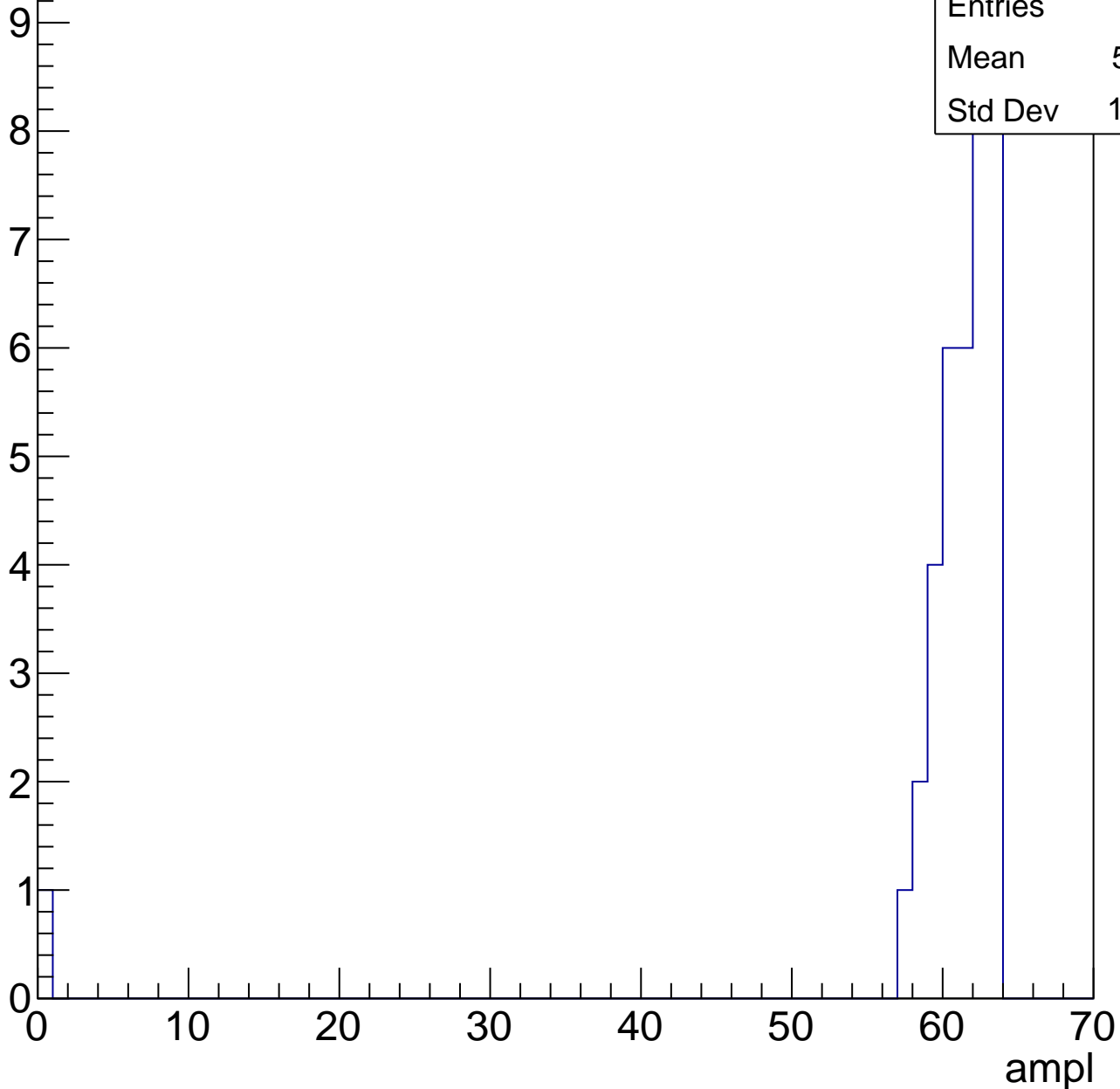


# B1L003S, U22-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	37
Mean	59.41
Std Dev	10.04



# B1L003S, U22-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0



# B1L003S, U22-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch52, adc0

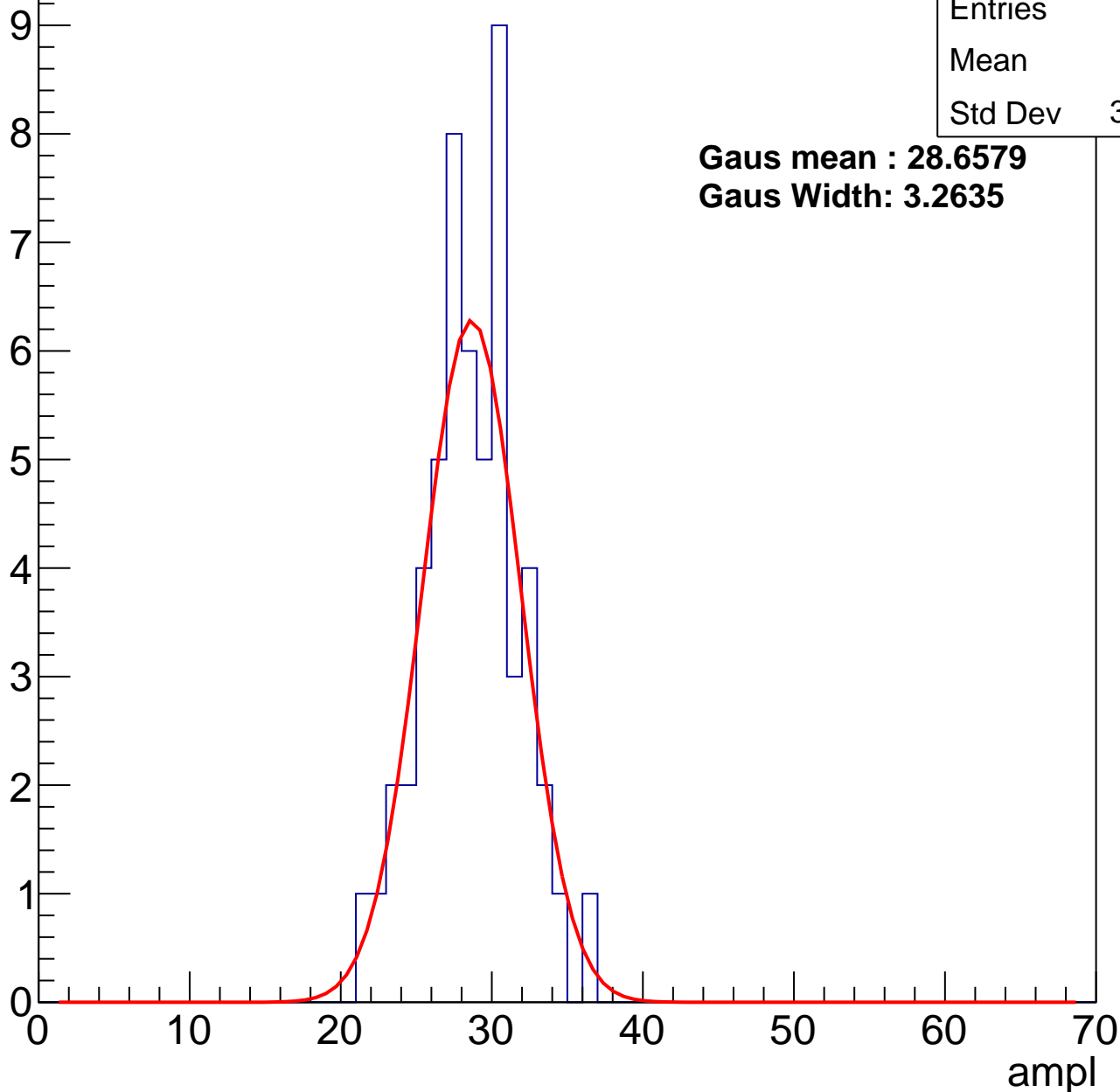
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	28.2
Std Dev	3.075

**Gaus mean : 28.6579**

**Gaus Width: 3.2635**



# B1L003S, U22-ch52, adc1

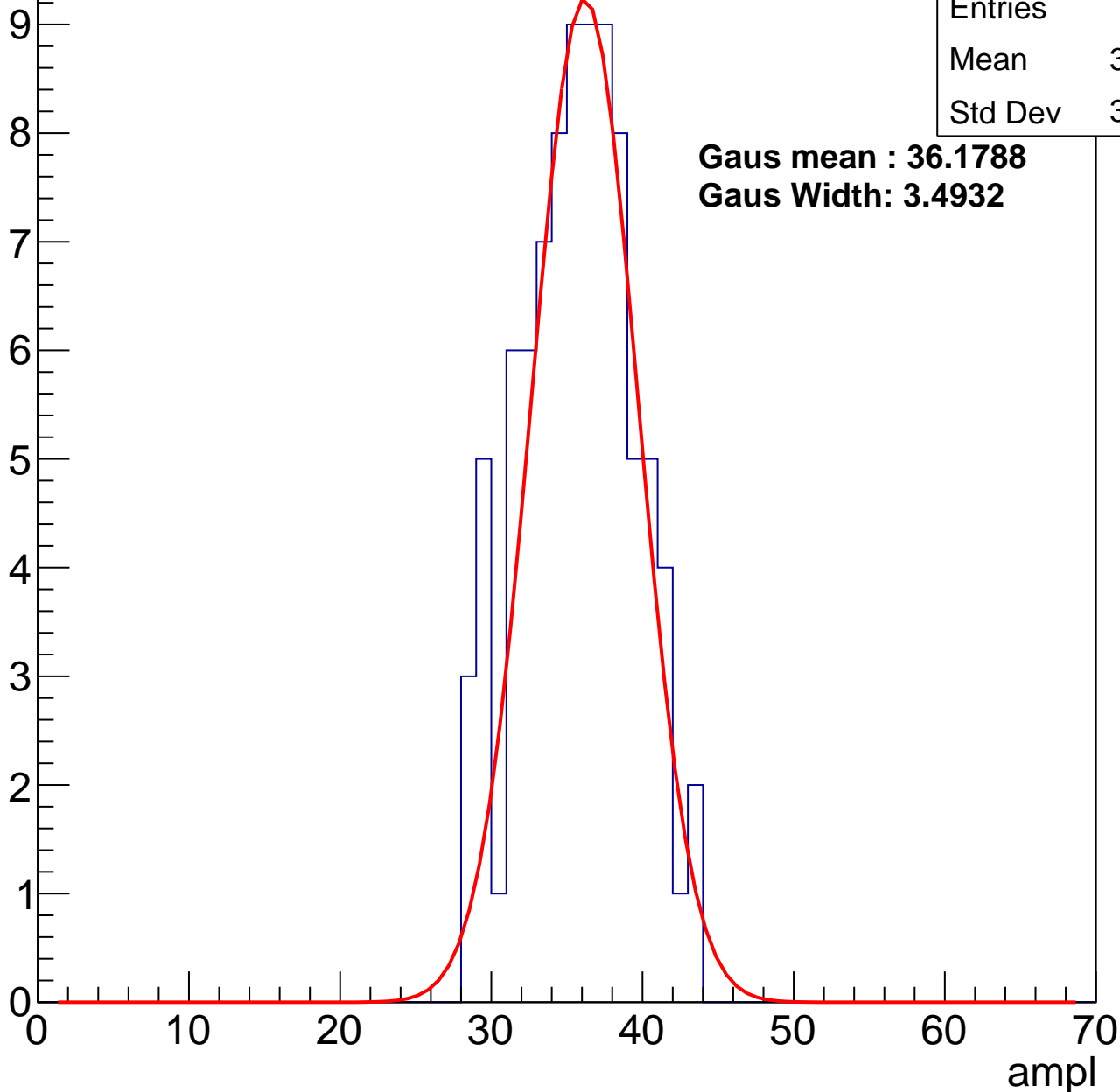
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	88
Mean	35.26
Std Dev	3.654

**Gaus mean : 36.1788**

**Gaus Width: 3.4932**



# B1L003S, U22-ch52, adc2

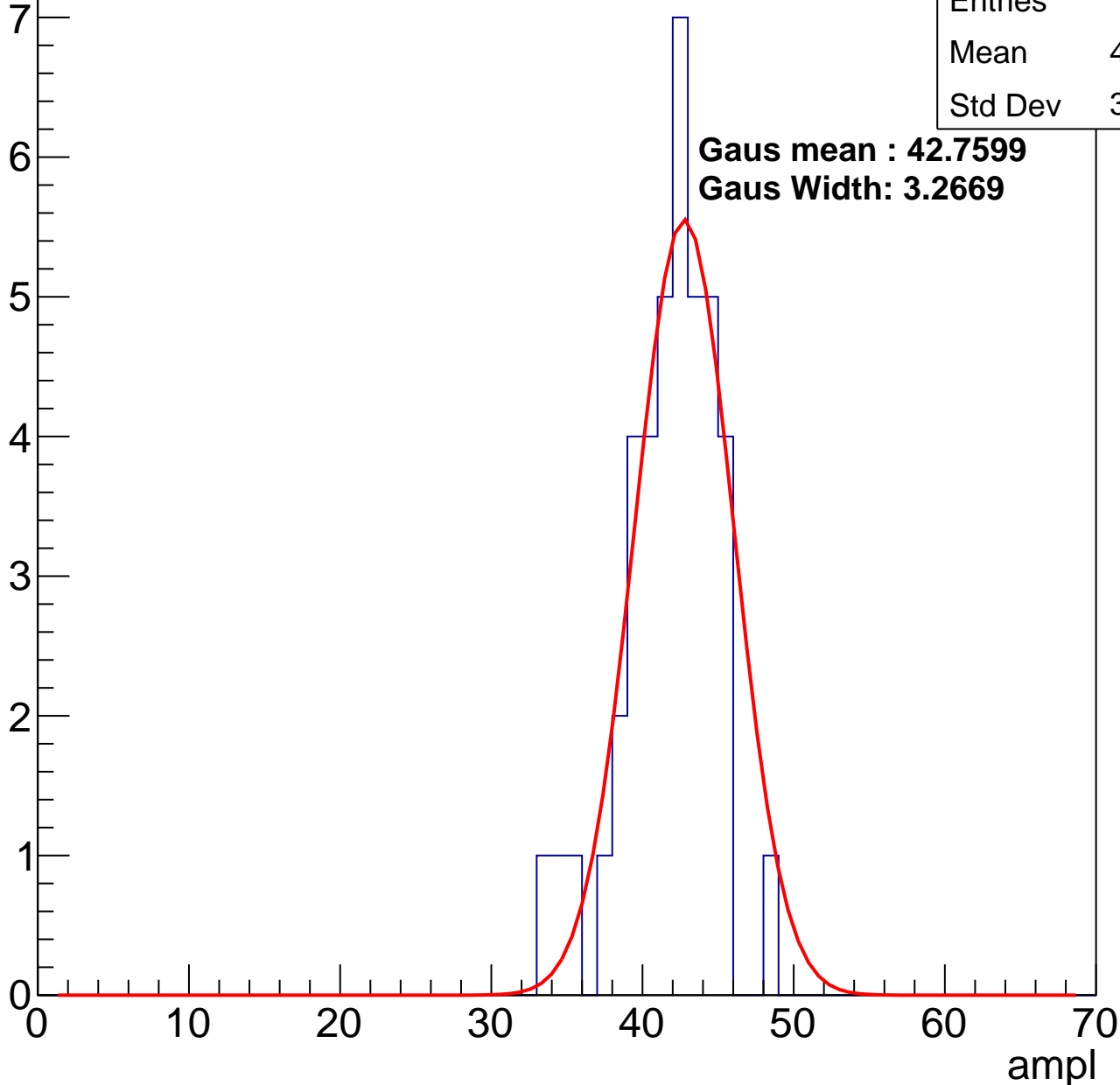
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	41
Mean	41.29
Std Dev	3.062

**Gaus mean : 42.7599**

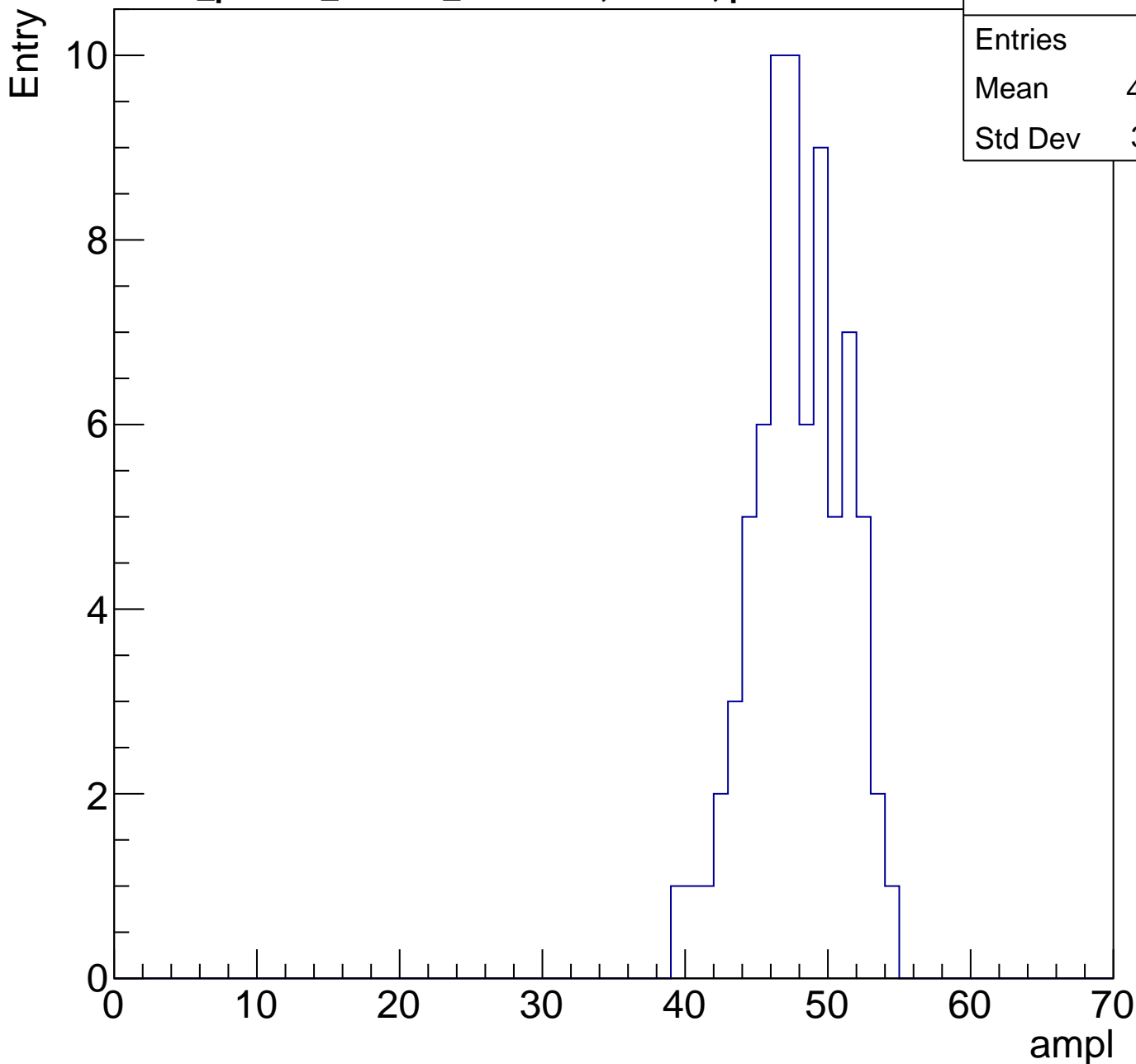
**Gaus Width: 3.2669**



# B1L003S, U22-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

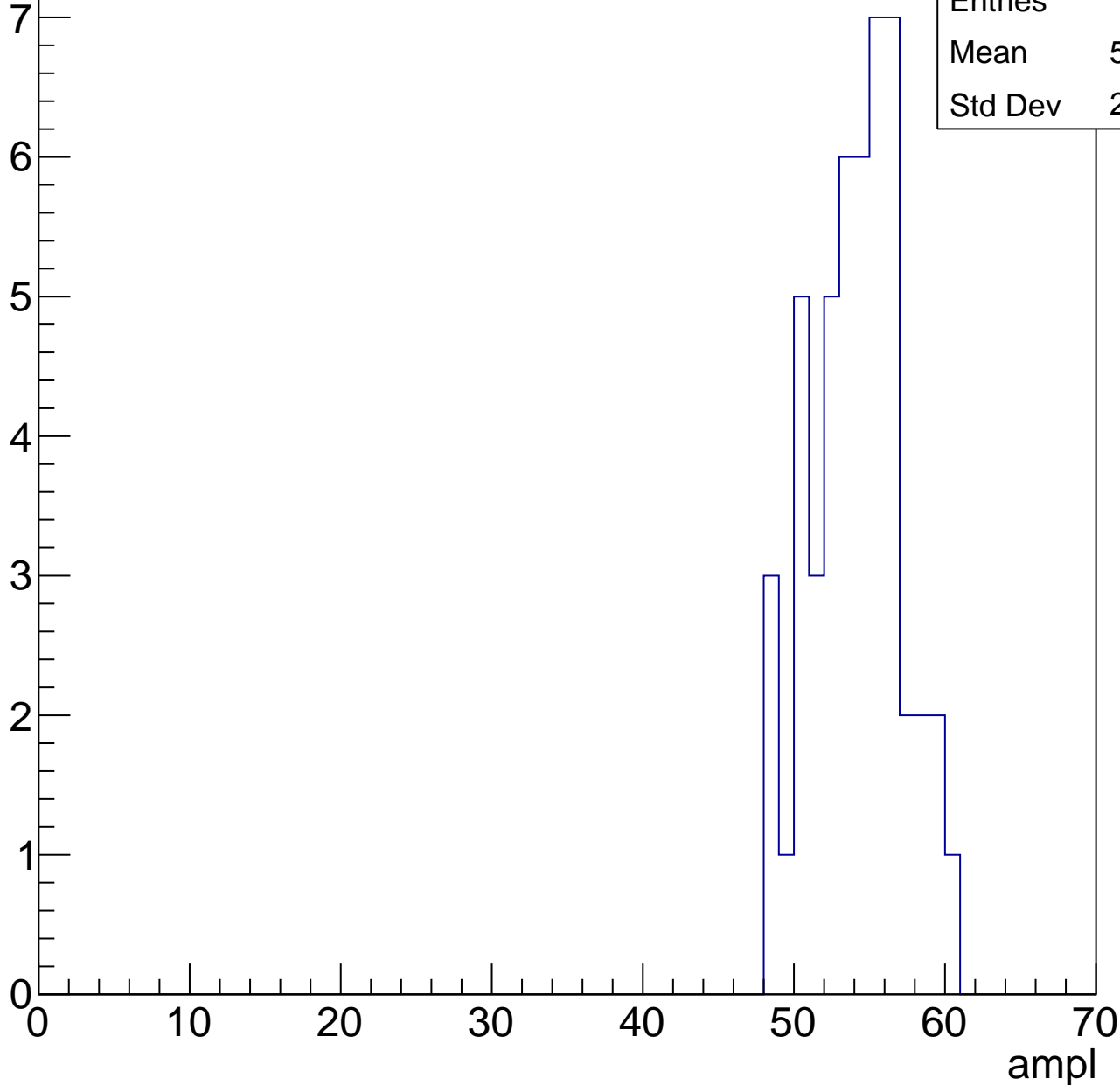
Entries	74
Mean	47.42
Std Dev	3.201



# B1L003S, U22-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

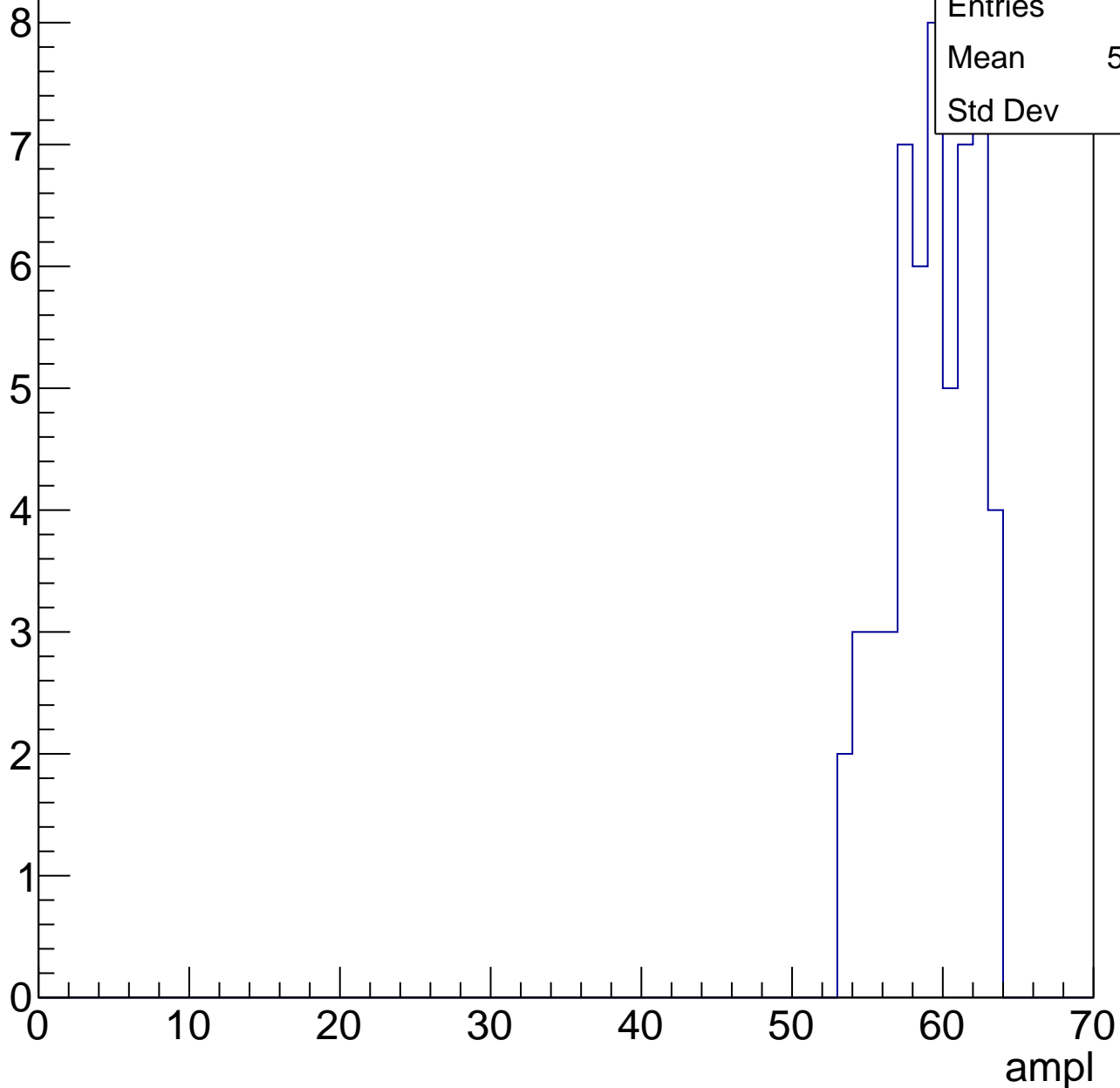
Entry



# B1L003S, U22-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

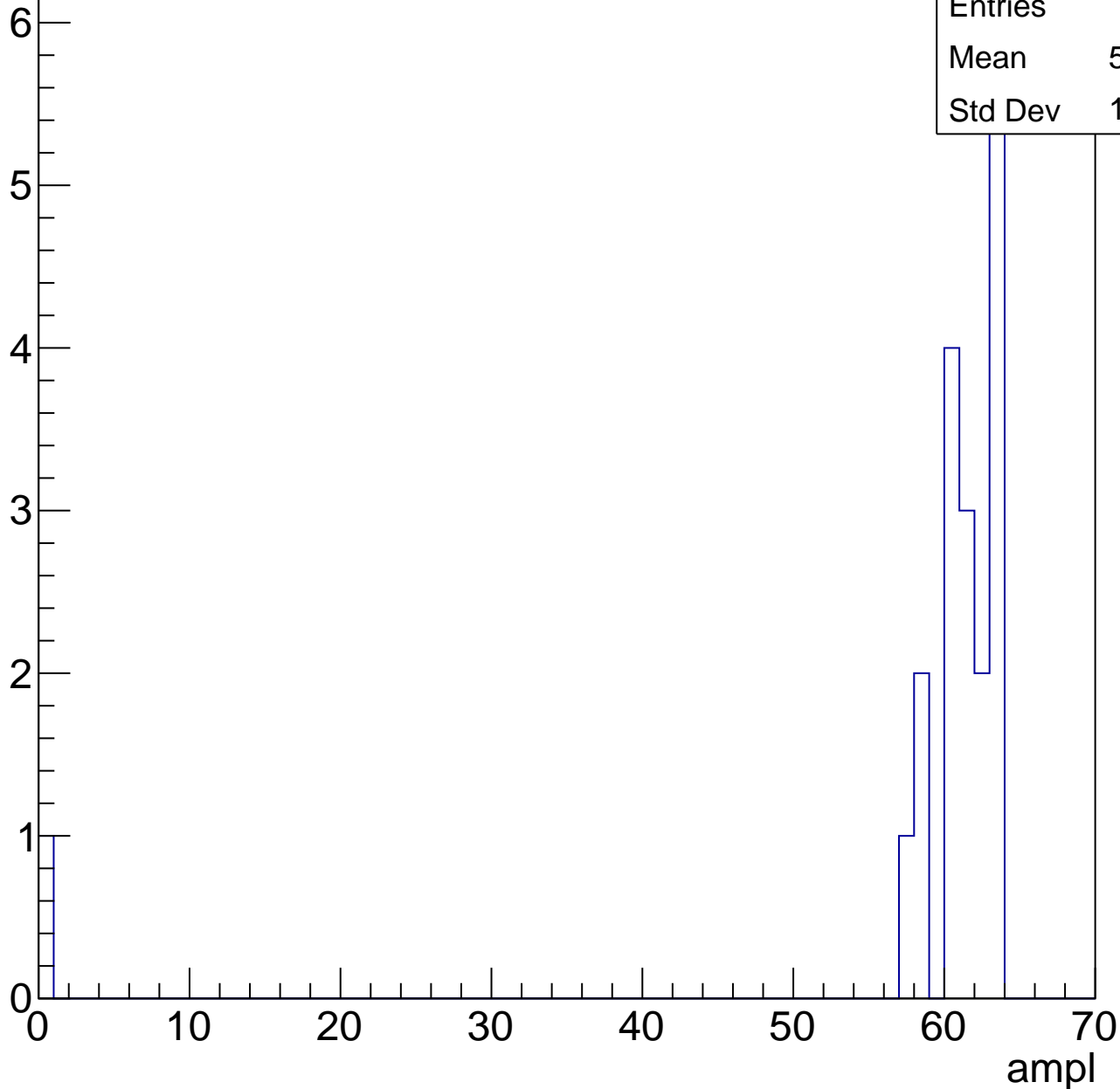


# B1L003S, U22-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	19
Mean	57.79
Std Dev	13.74





# B1L003S, U22-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch53, adc0

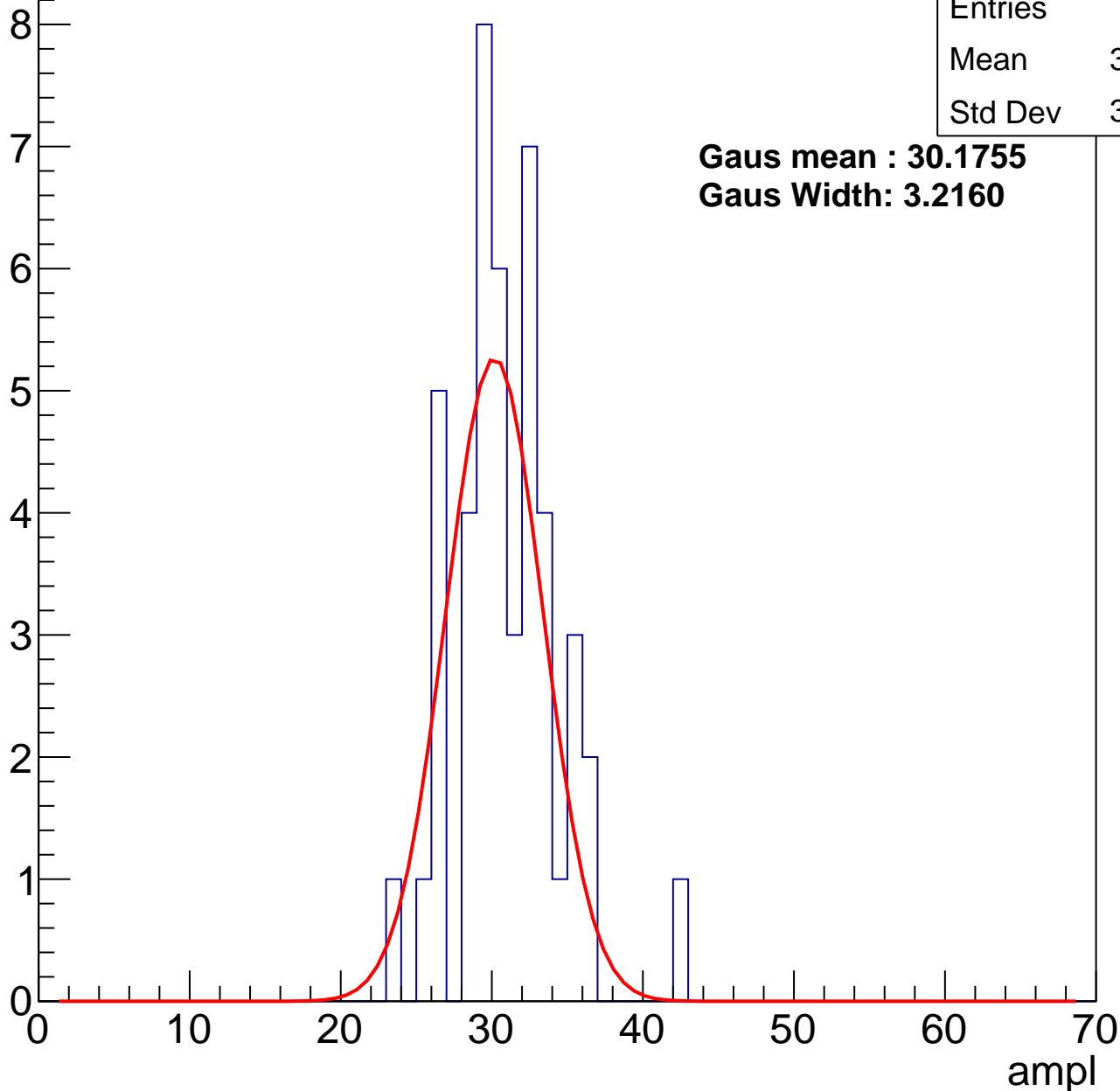
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	30.52
Std Dev	3.425

**Gaus mean : 30.1755**

**Gaus Width: 3.2160**



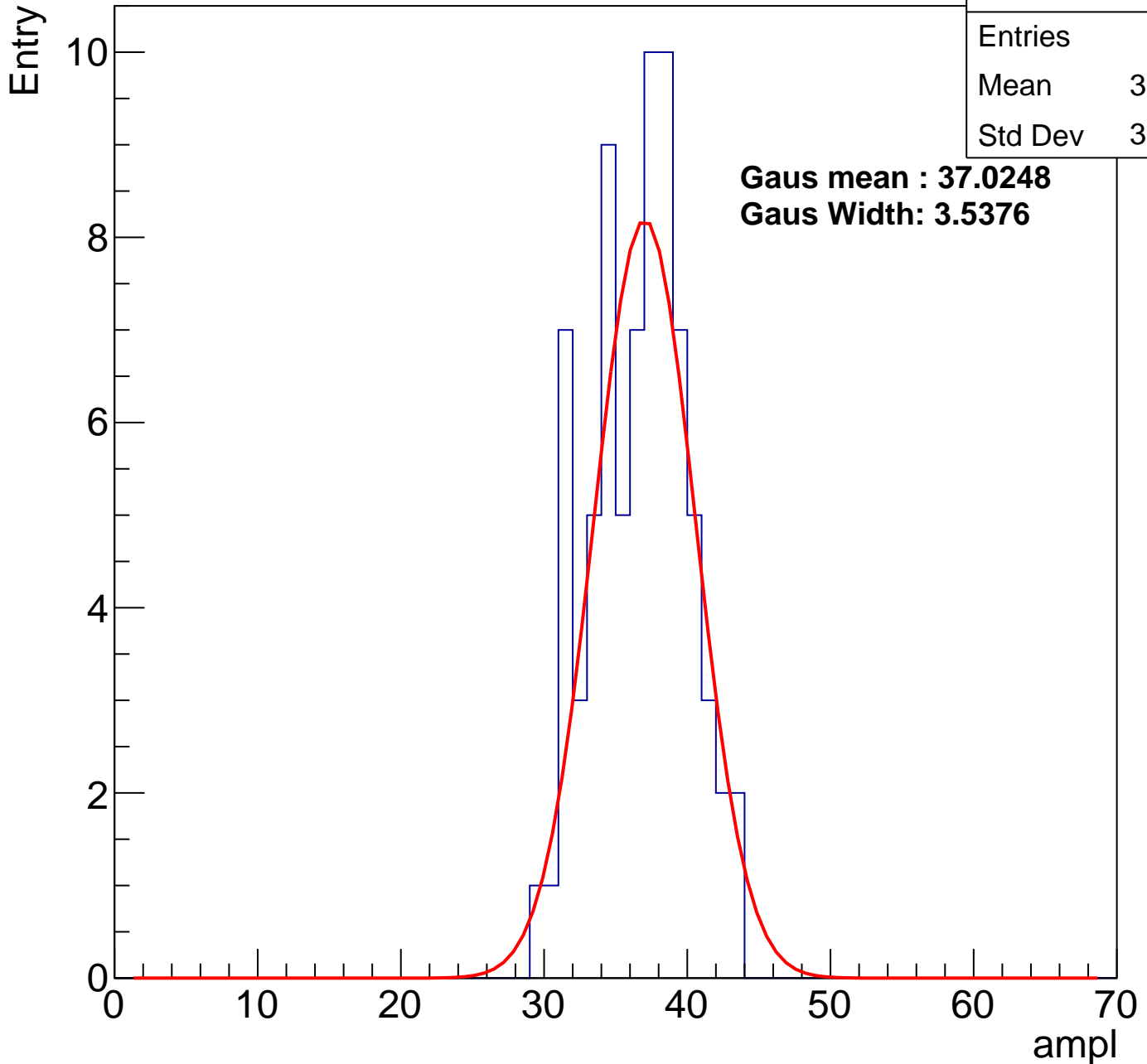
# B1L003S, U22-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	77
Mean	36.18
Std Dev	3.282

**Gaus mean : 37.0248**

**Gaus Width: 3.5376**



# B1L003S, U22-ch53, adc2

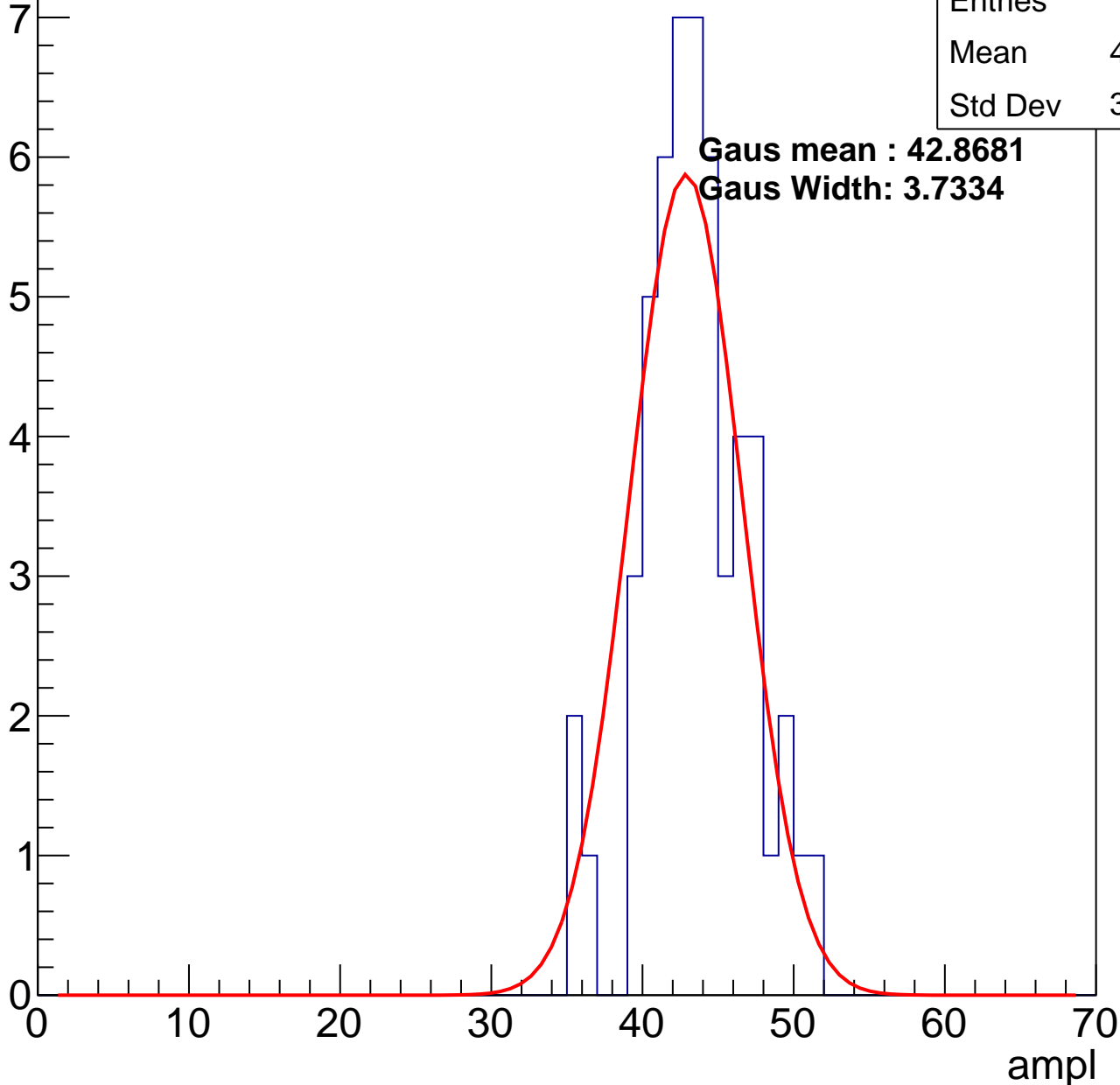
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.06
Std Dev	3.445

**Gaus mean : 42.8681**

**Gaus Width: 3.7334**

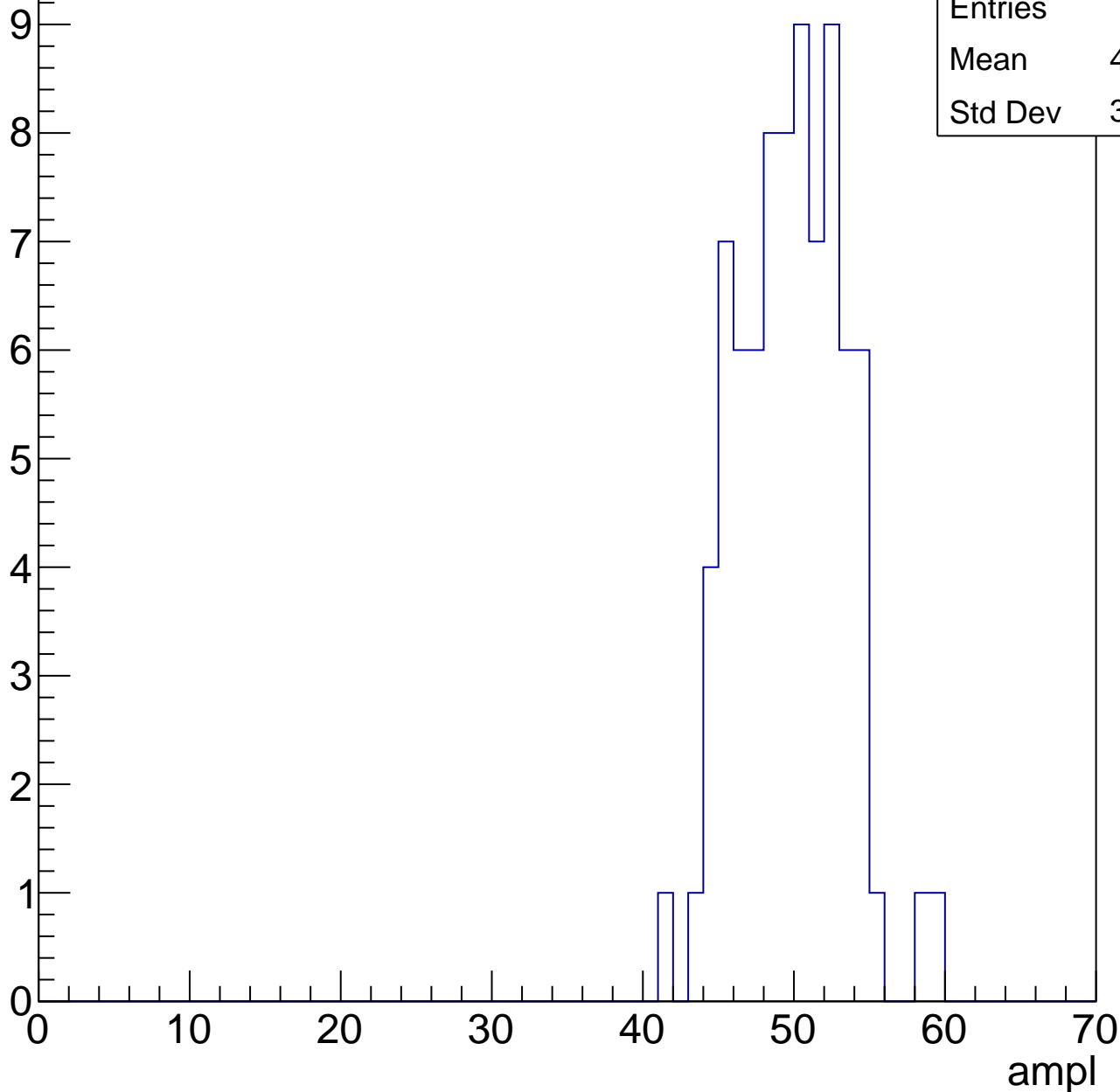


# B1L003S, U22-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	49.36
Std Dev	3.458

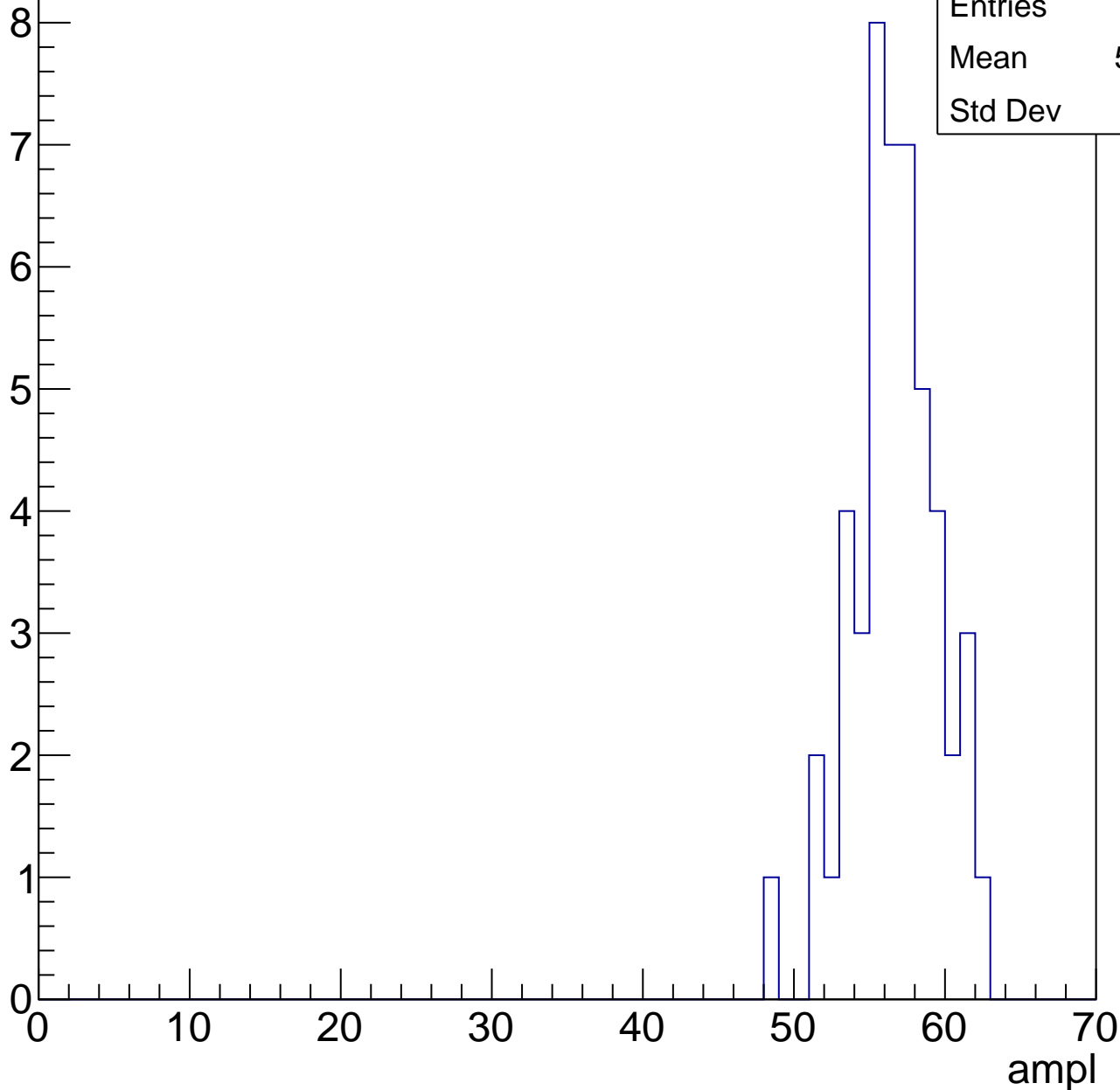


# B1L003S, U22-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	56.21
Std Dev	2.85

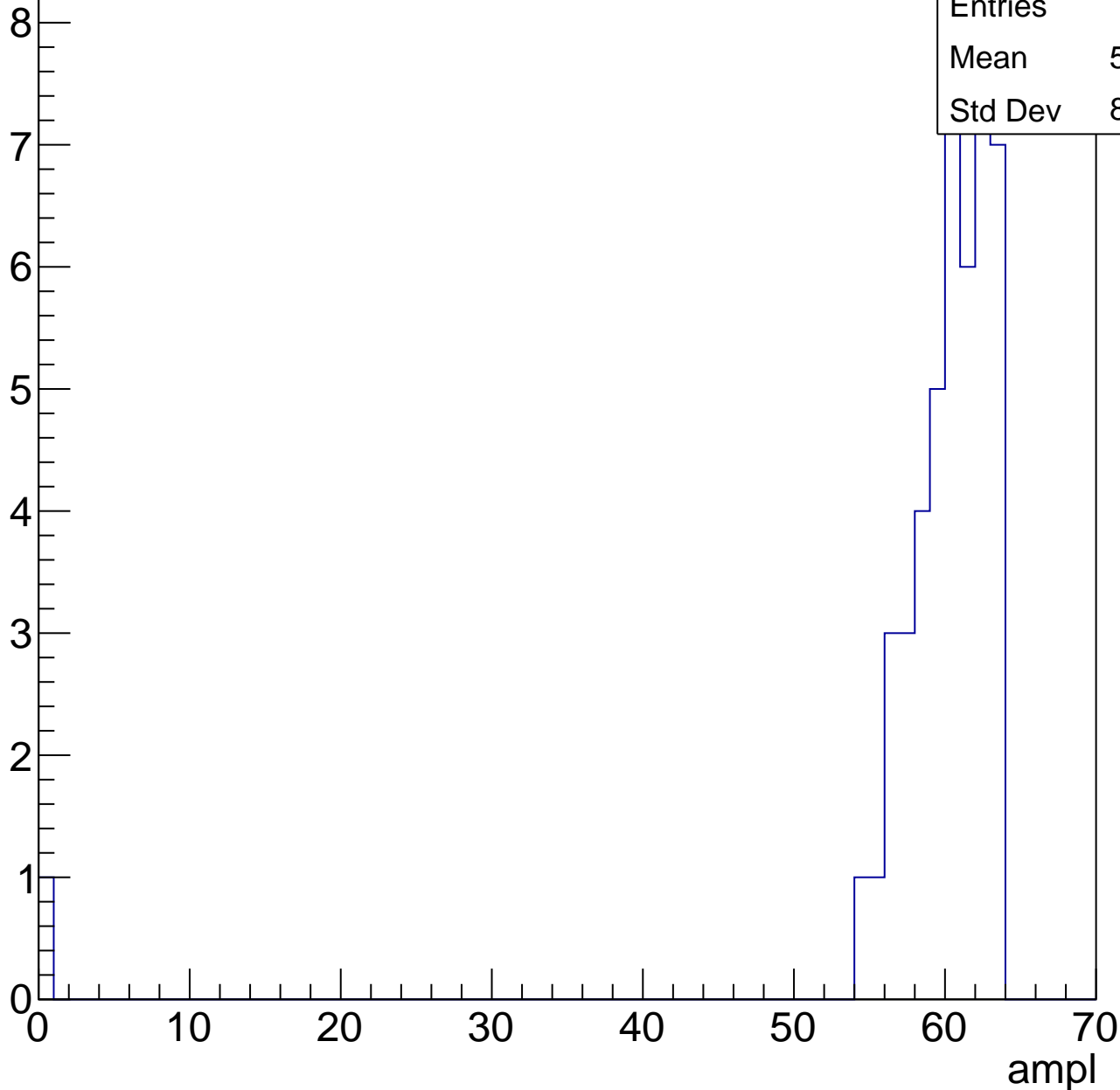


# B1L003S, U22-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	58.68
Std Dev	8.964



# B1L003S, U22-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	28.05
Std Dev	4.922

**Gaus mean : 30.3854**

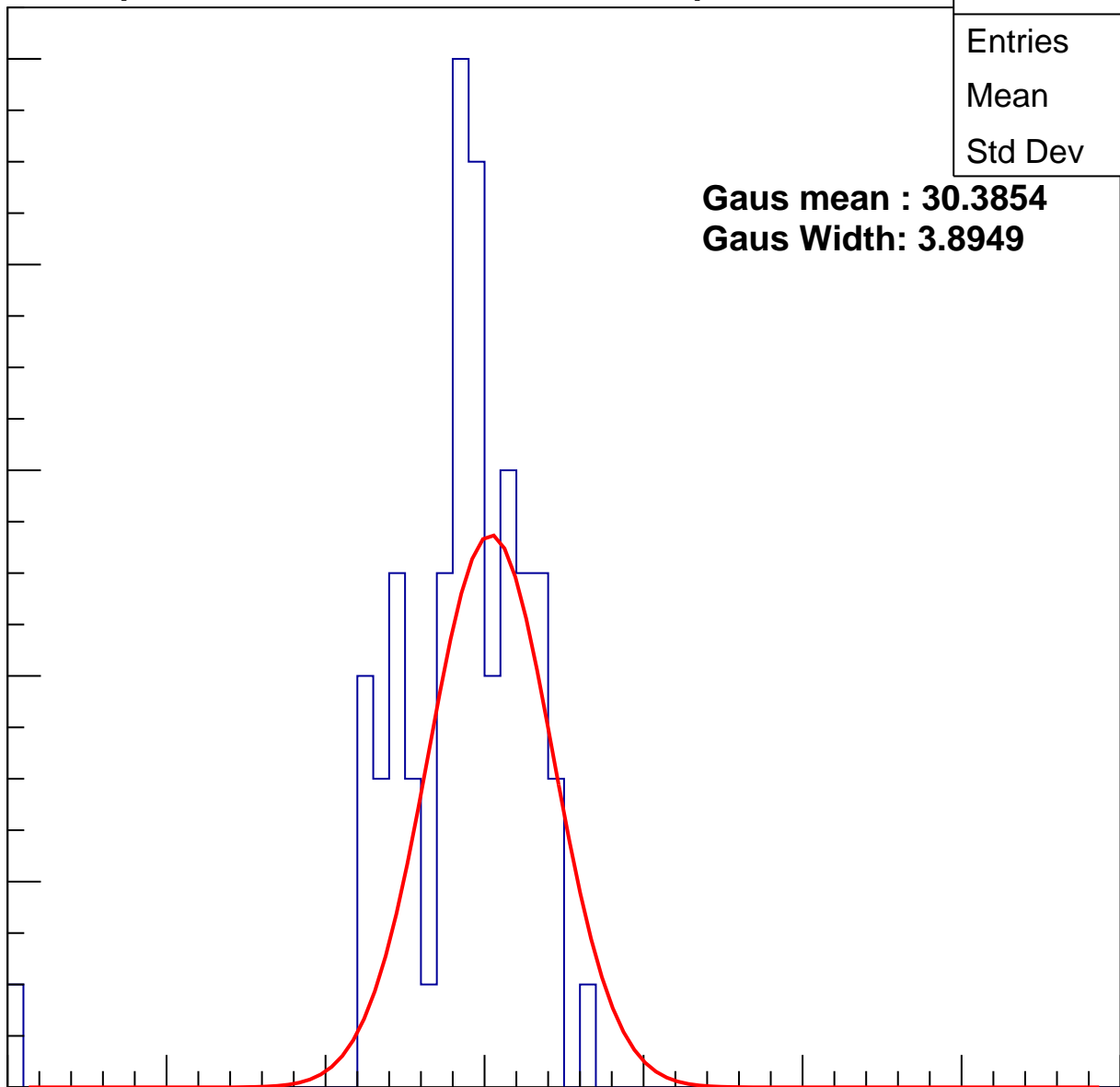
**Gaus Width: 3.8949**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	35.83
Std Dev	3.711

**Gaus mean : 36.0413**

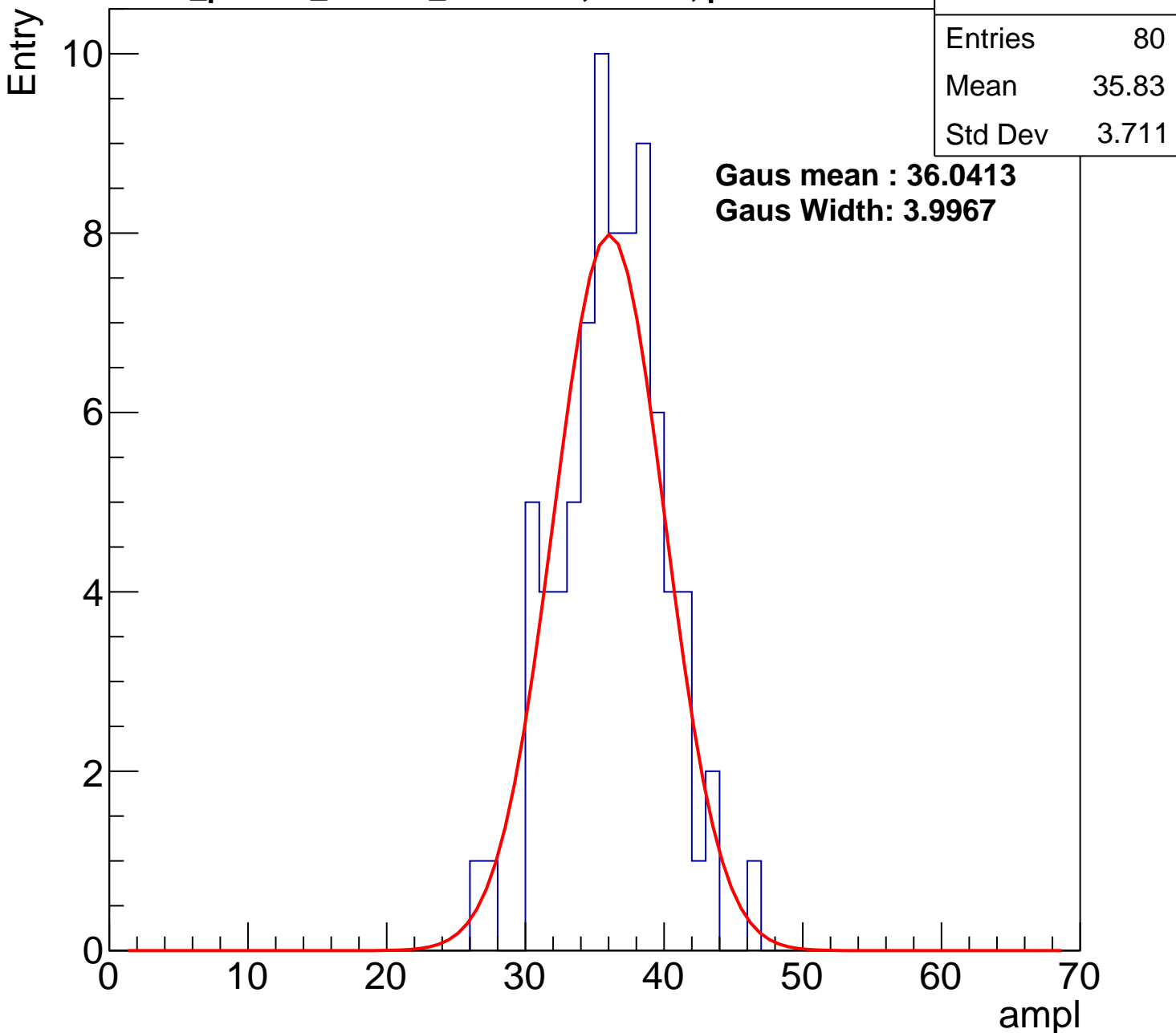
**Gaus Width: 3.9967**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch54, adc2

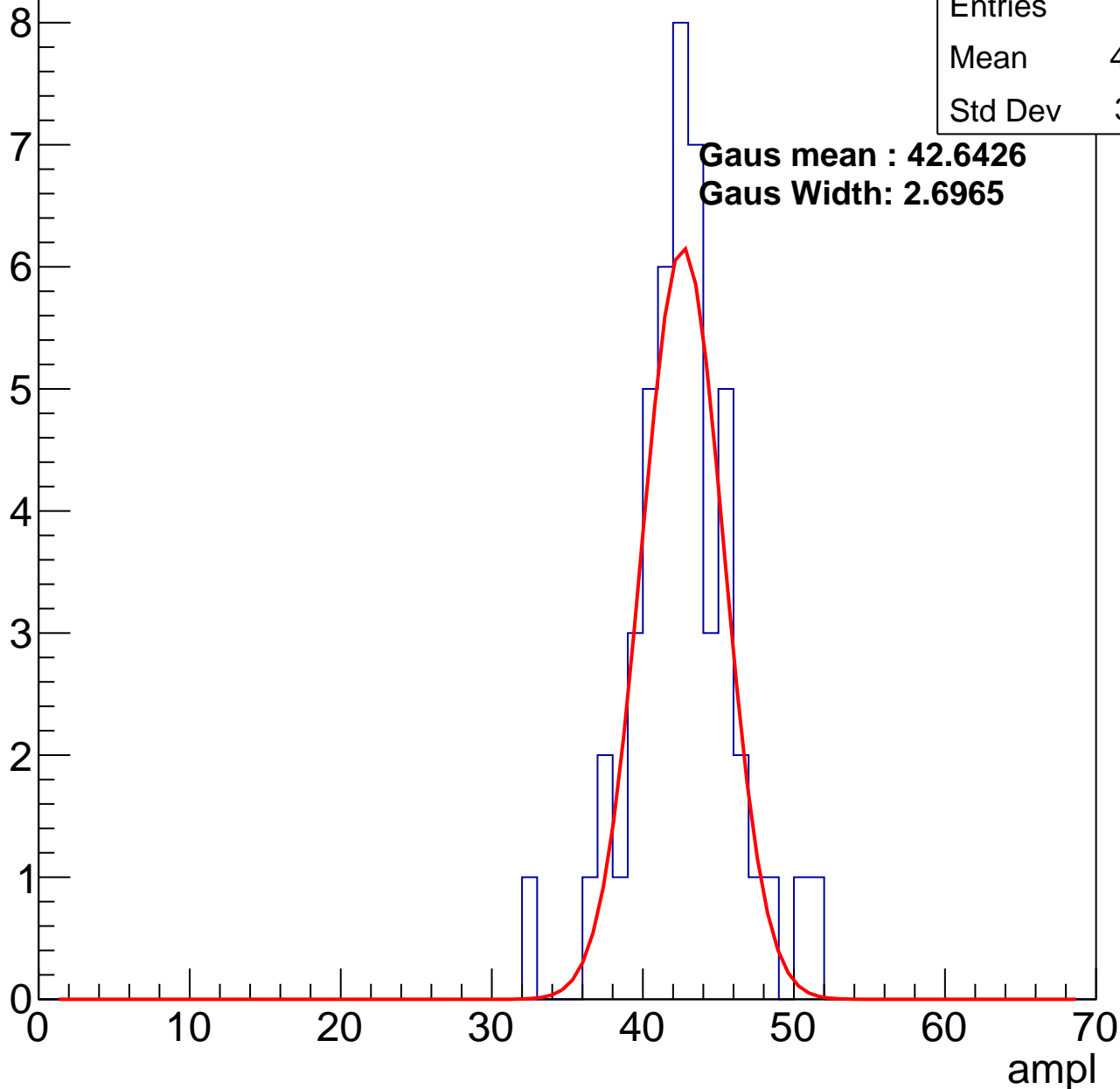
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	42.19
Std Dev	3.401

**Gaus mean : 42.6426**

**Gaus Width: 2.6965**

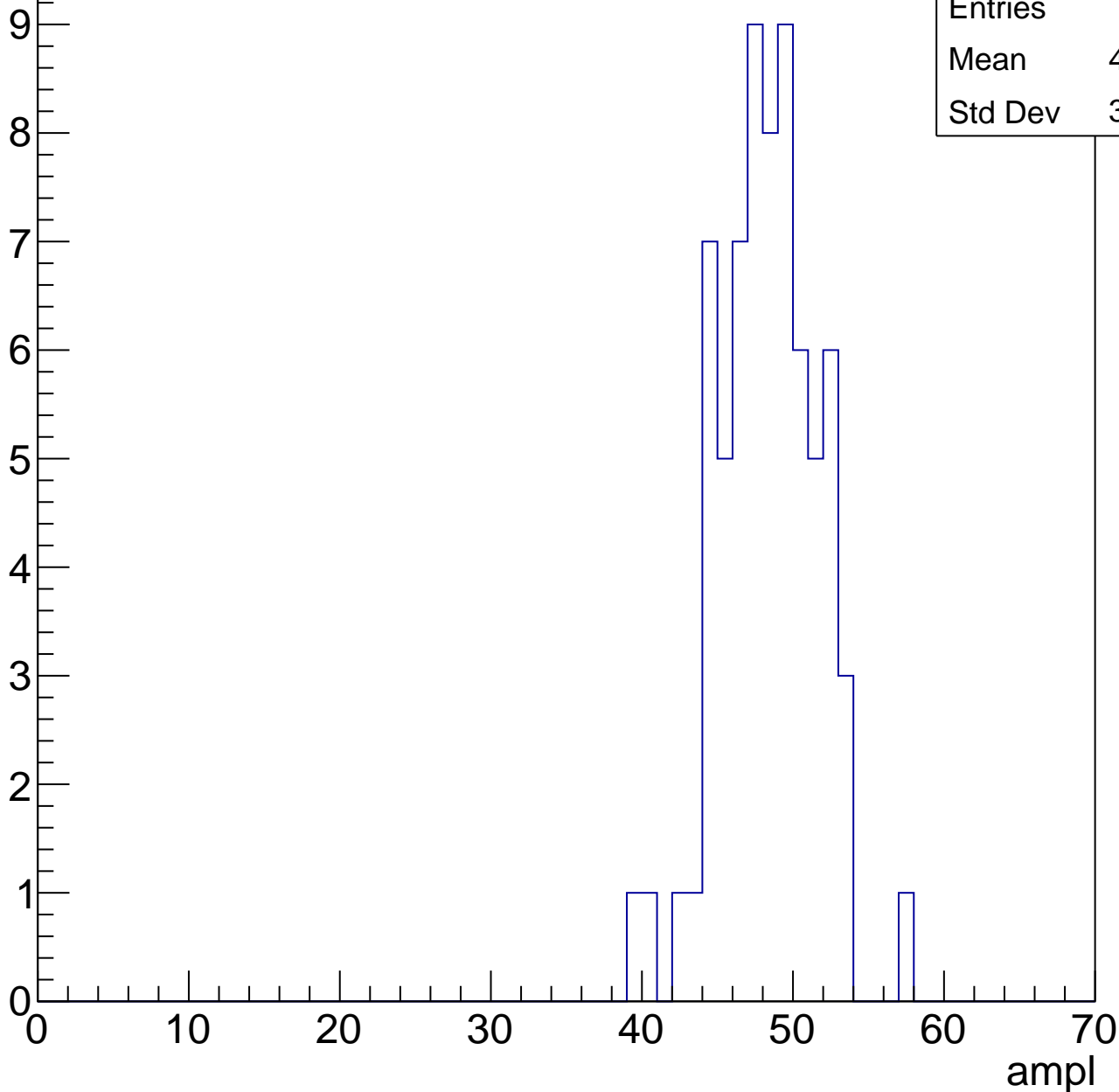


# B1L003S, U22-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	47.86
Std Dev	3.226

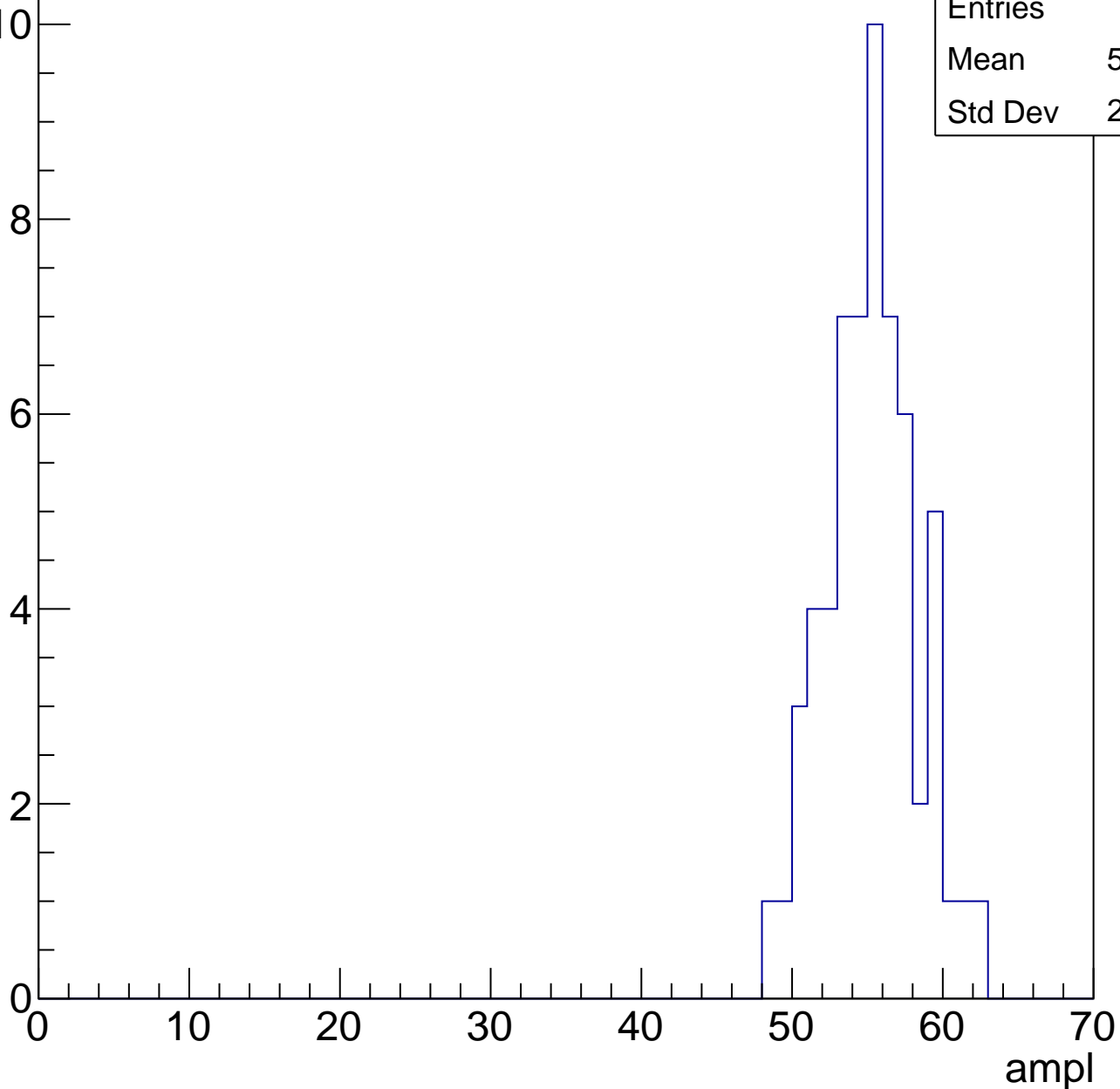


# B1L003S, U22-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.77
Std Dev	2.974

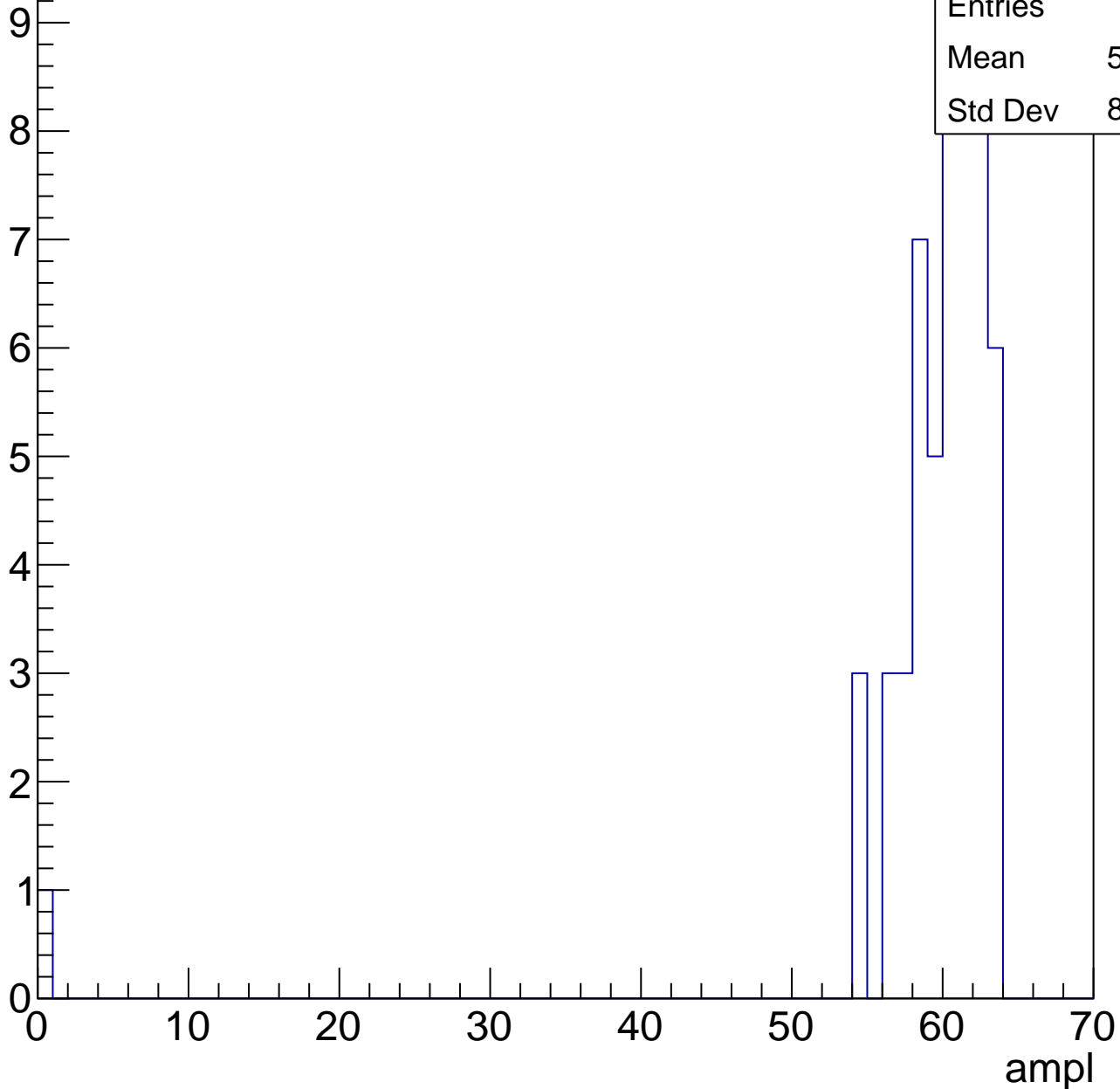


# B1L003S, U22-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

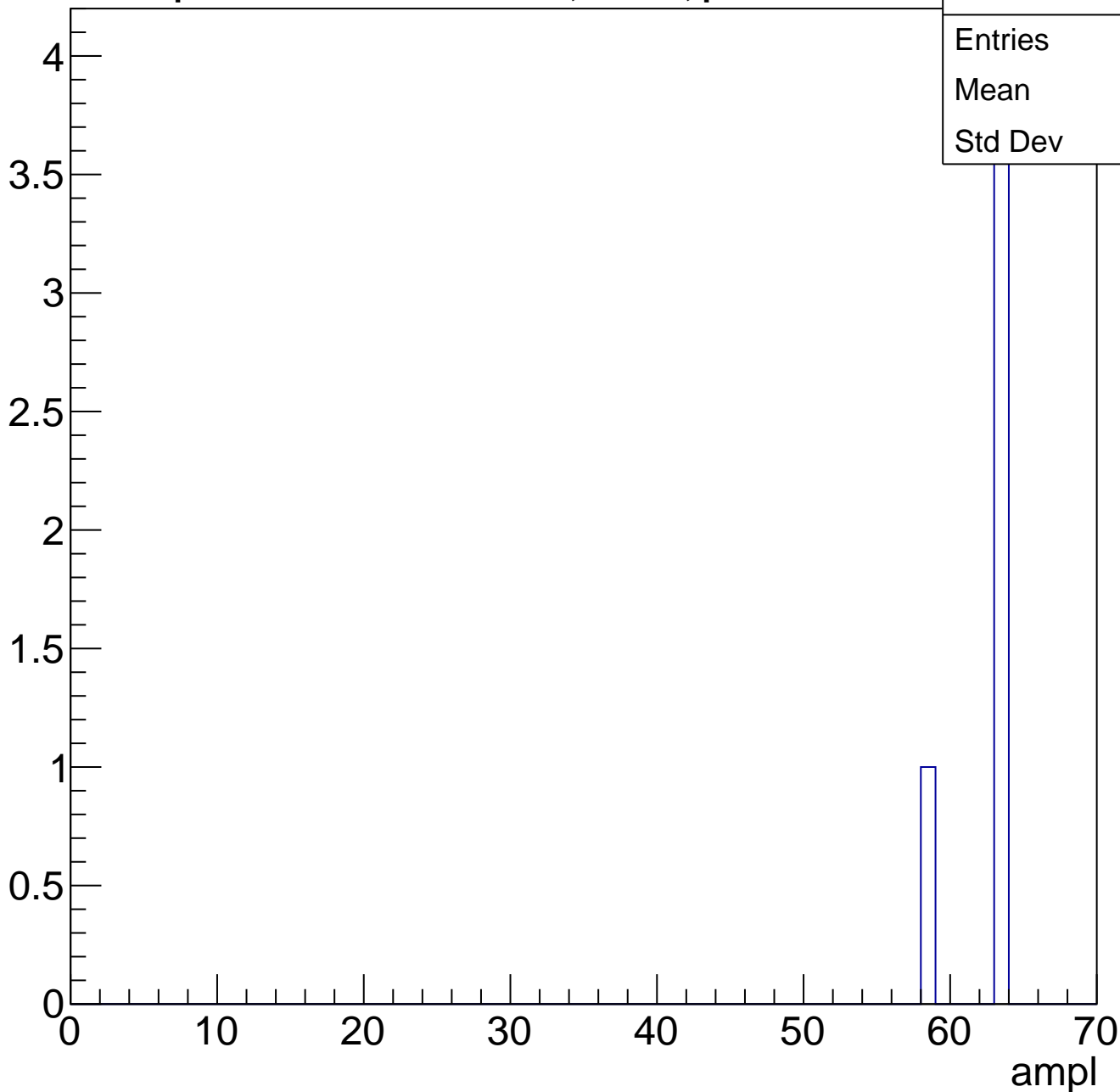
Entries	53
Mean	58.57
Std Dev	8.469



# B1L003S, U22-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

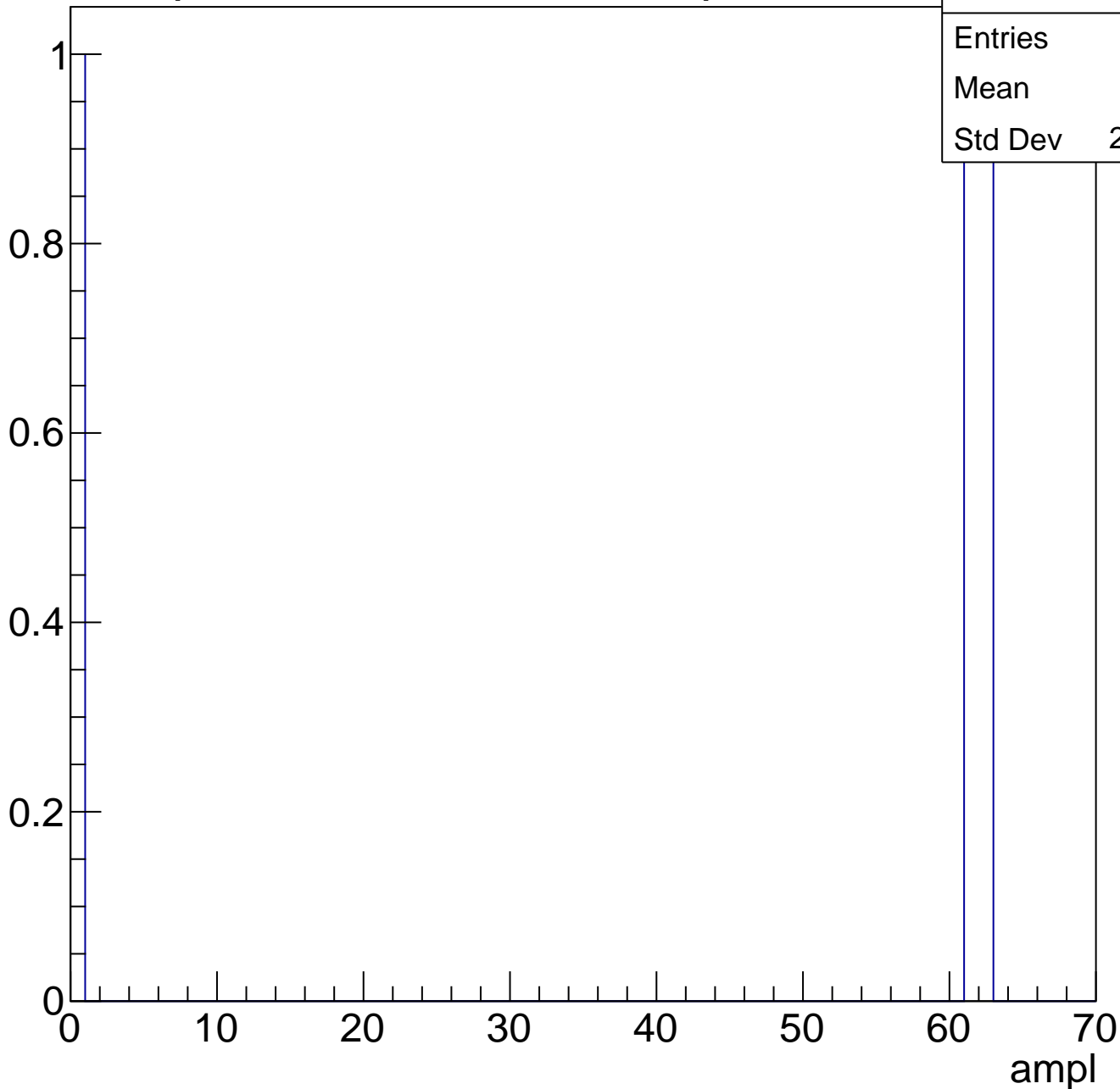




# B1L003S, U22-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch55, adc0

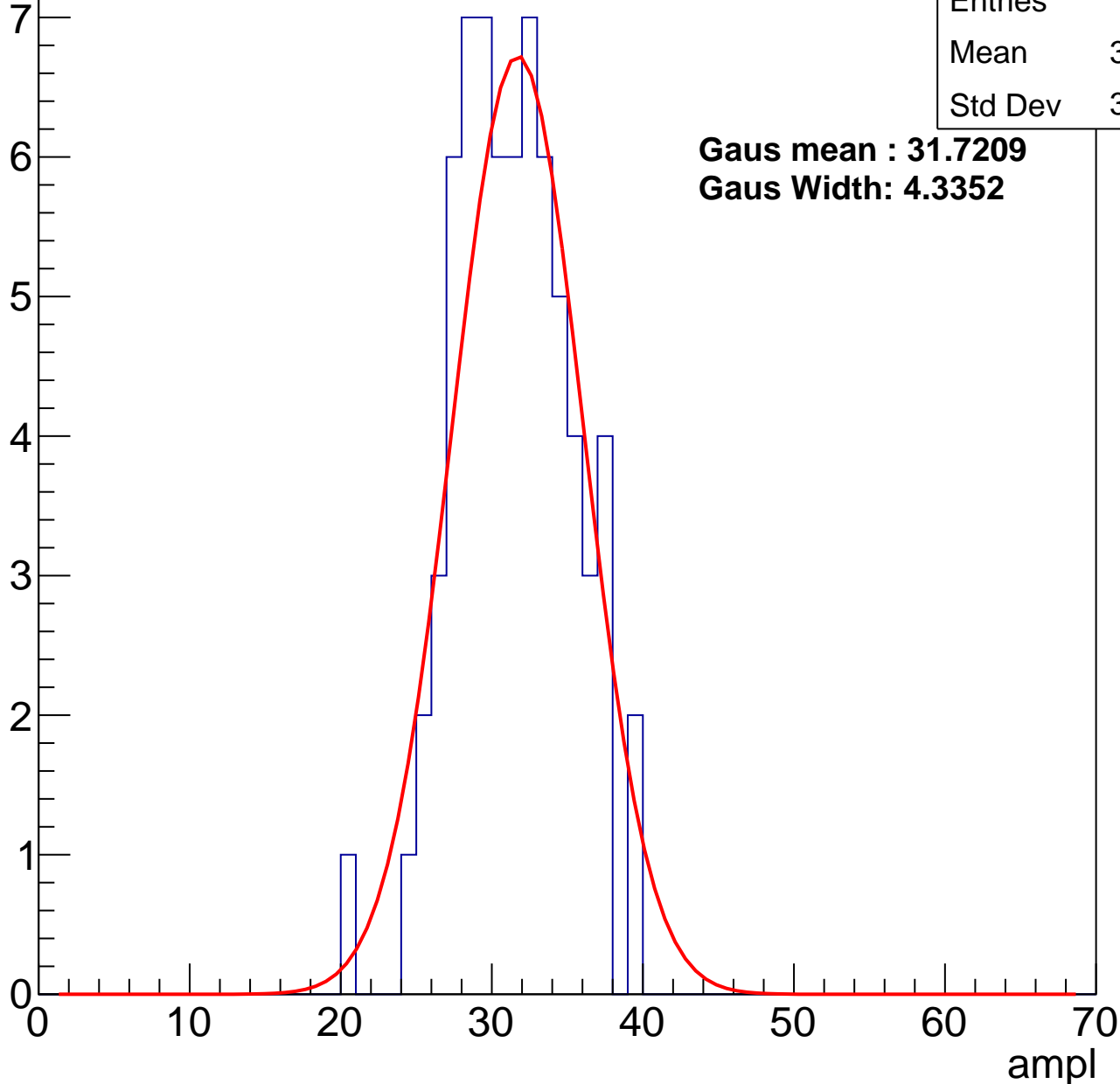
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.93
Std Dev	3.773

**Gaus mean : 31.7209**

**Gaus Width: 4.3352**



# B1L003S, U22-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	36.63
Std Dev	3.435

**Gaus mean : 37.2159**

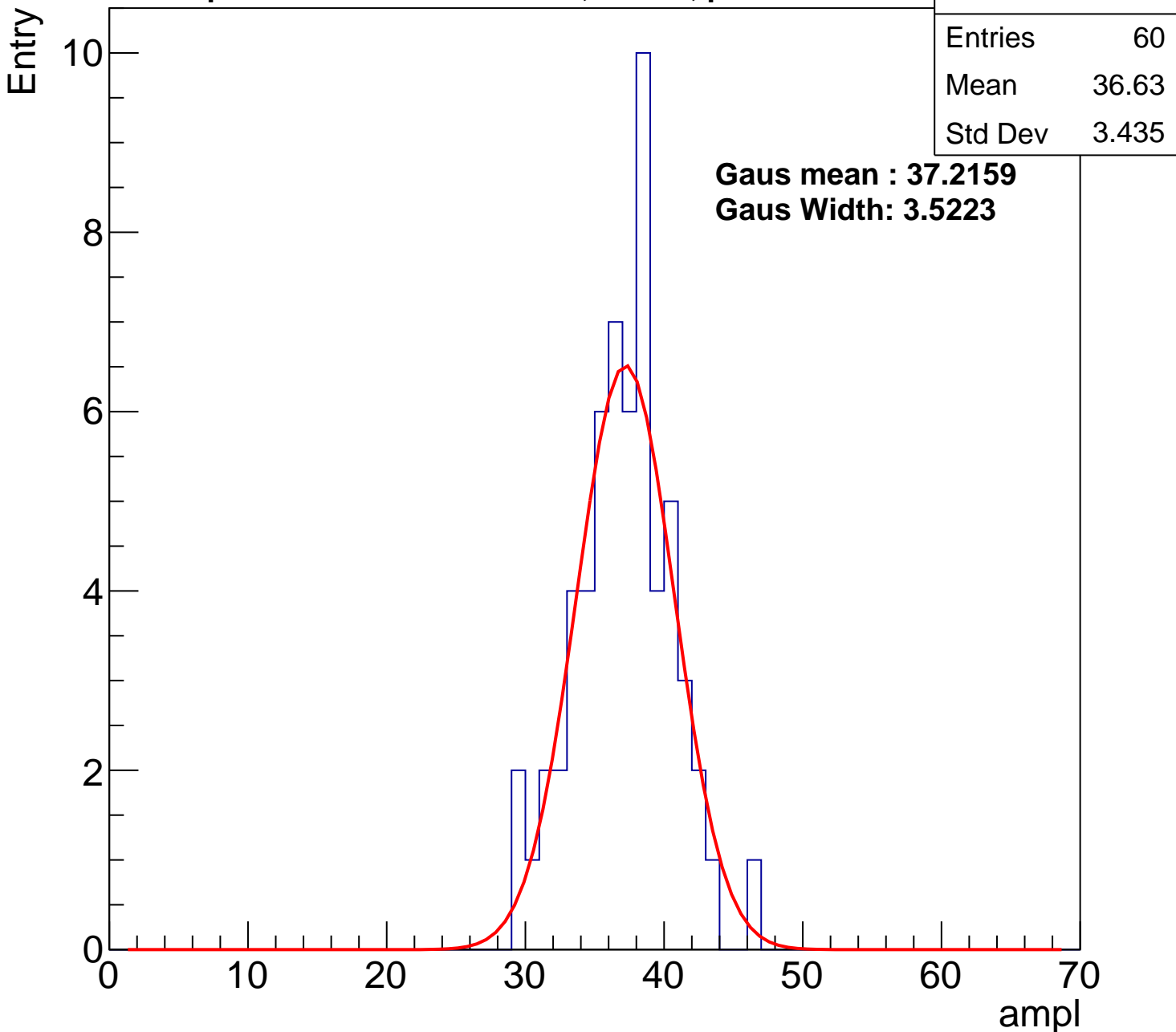
**Gaus Width: 3.5223**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch55, adc2

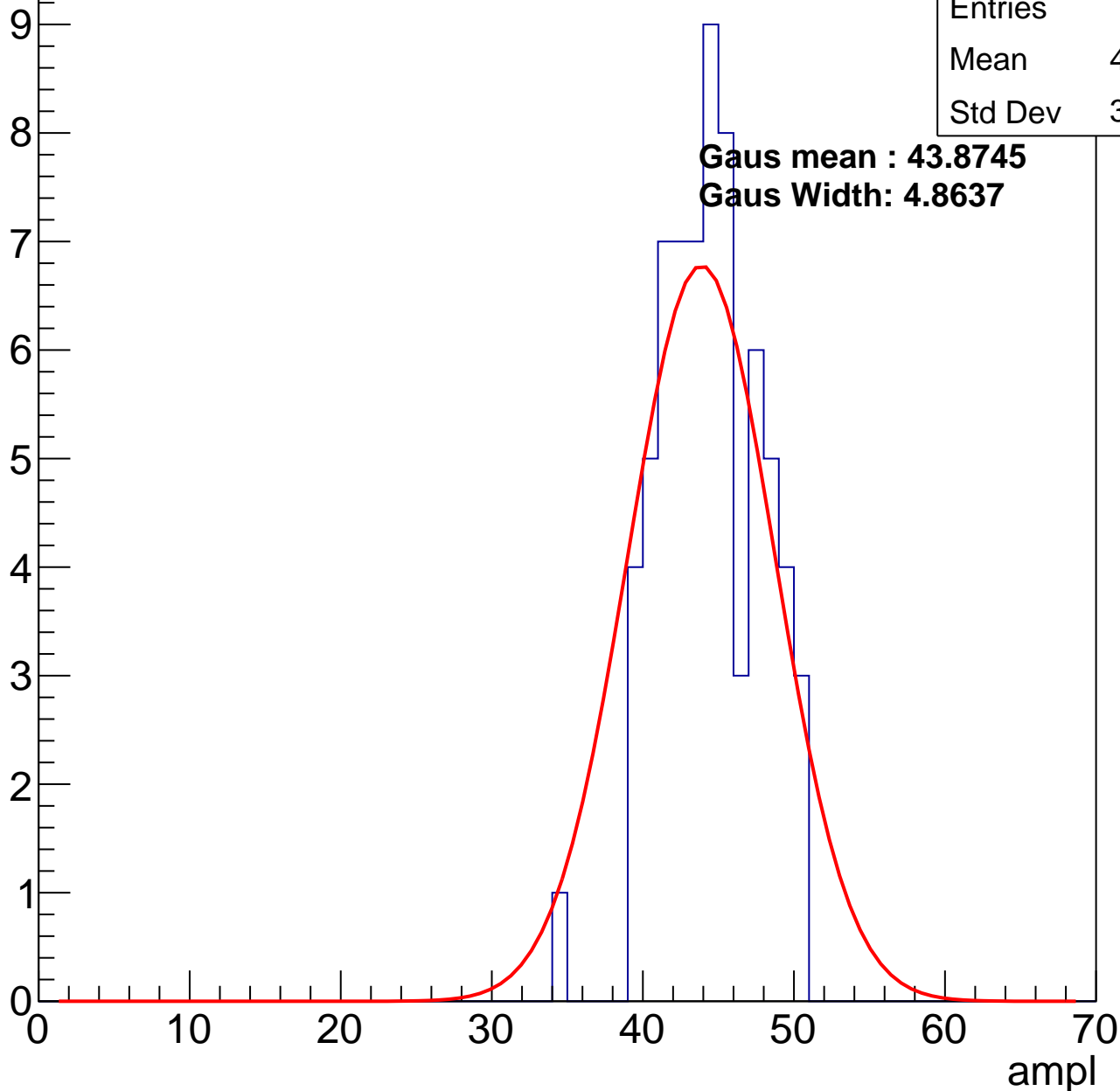
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.97
Std Dev	3.275

**Gaus mean : 43.8745**

**Gaus Width: 4.8637**

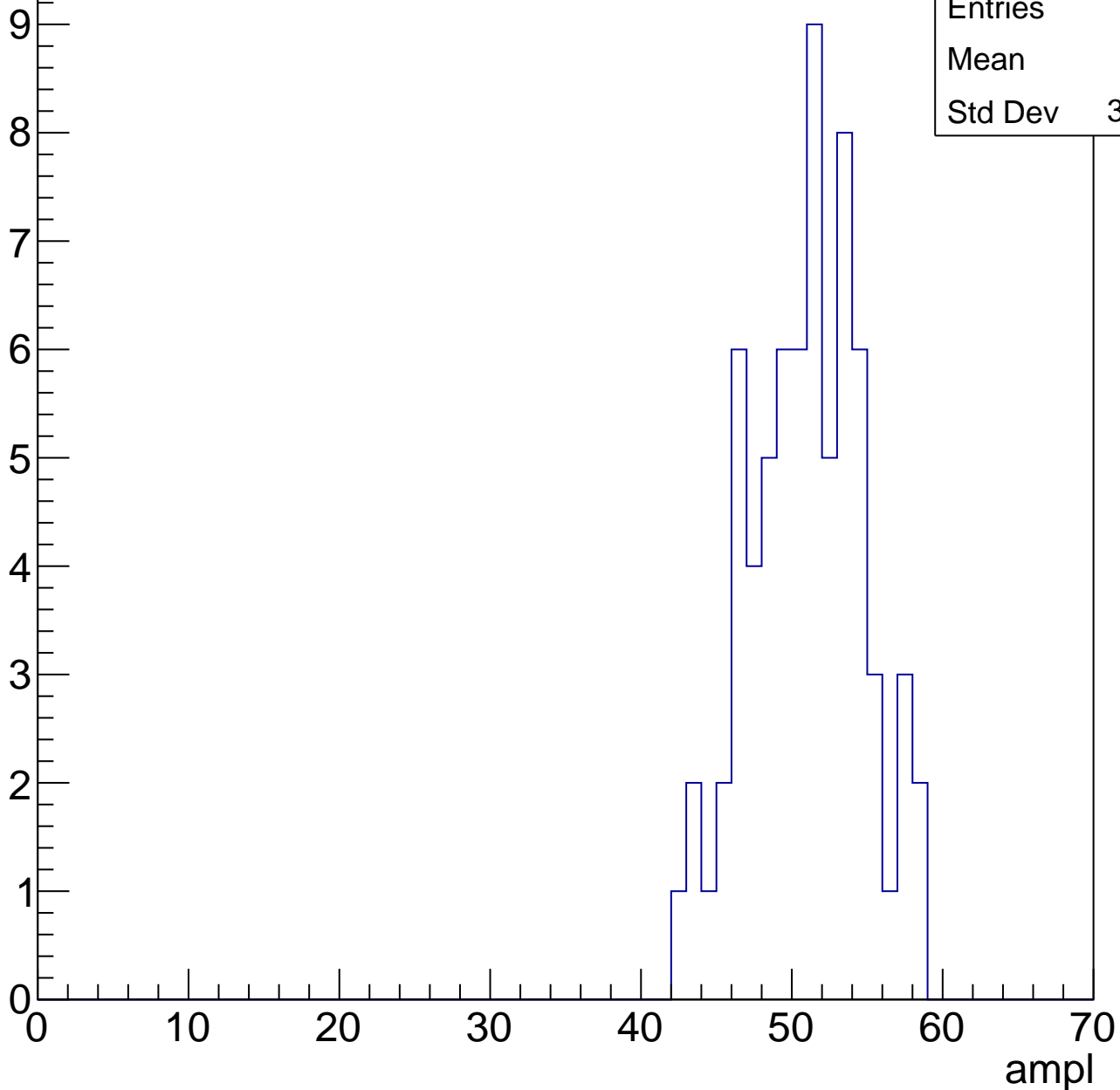


# B1L003S, U22-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	50.5
Std Dev	3.725



# B1L003S, U22-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

10

Entries 57

Mean 56.56

Std Dev 2.902

8

6

4

2

0

0

10

20

30

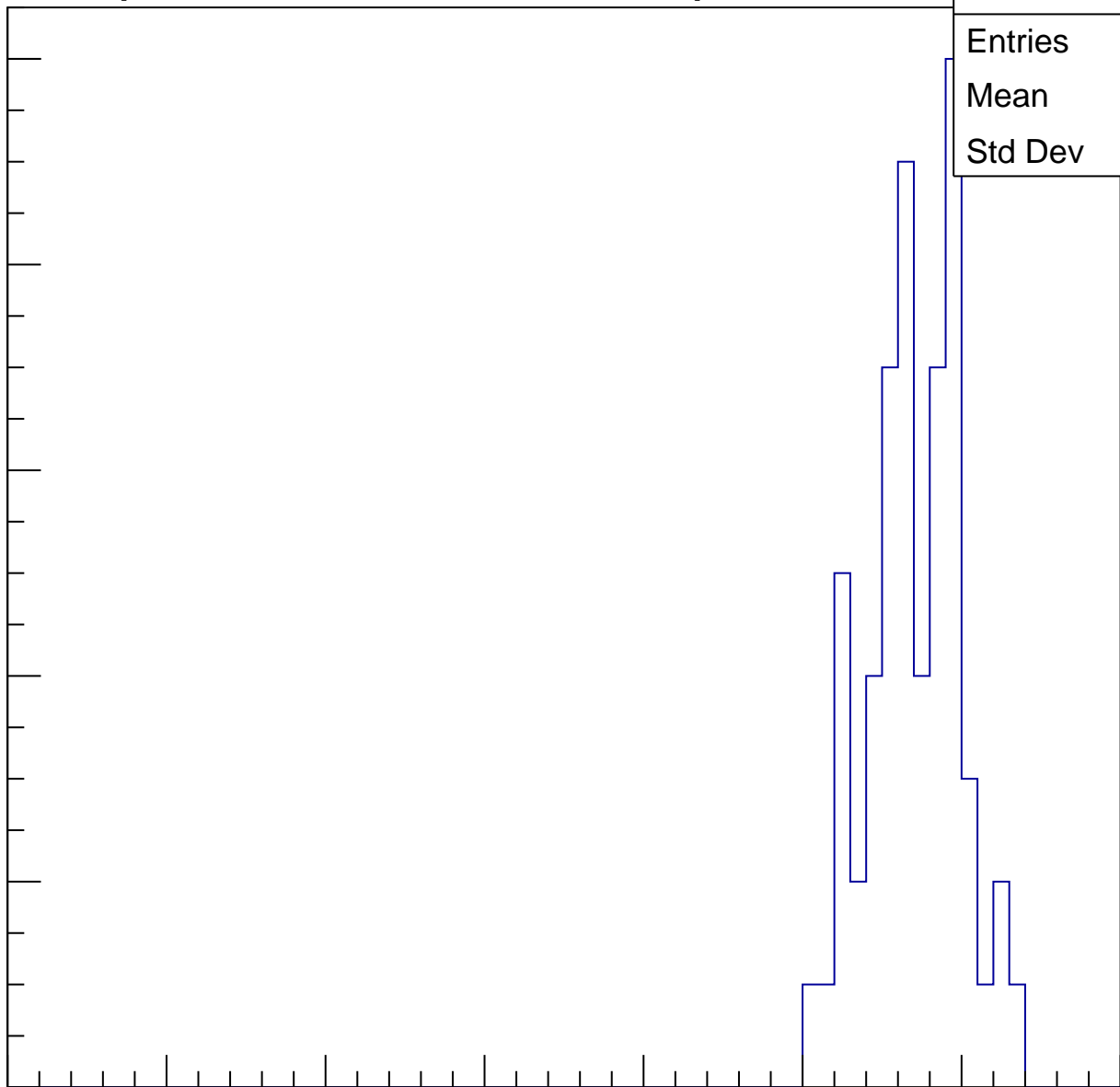
40

50

60

70

ampl

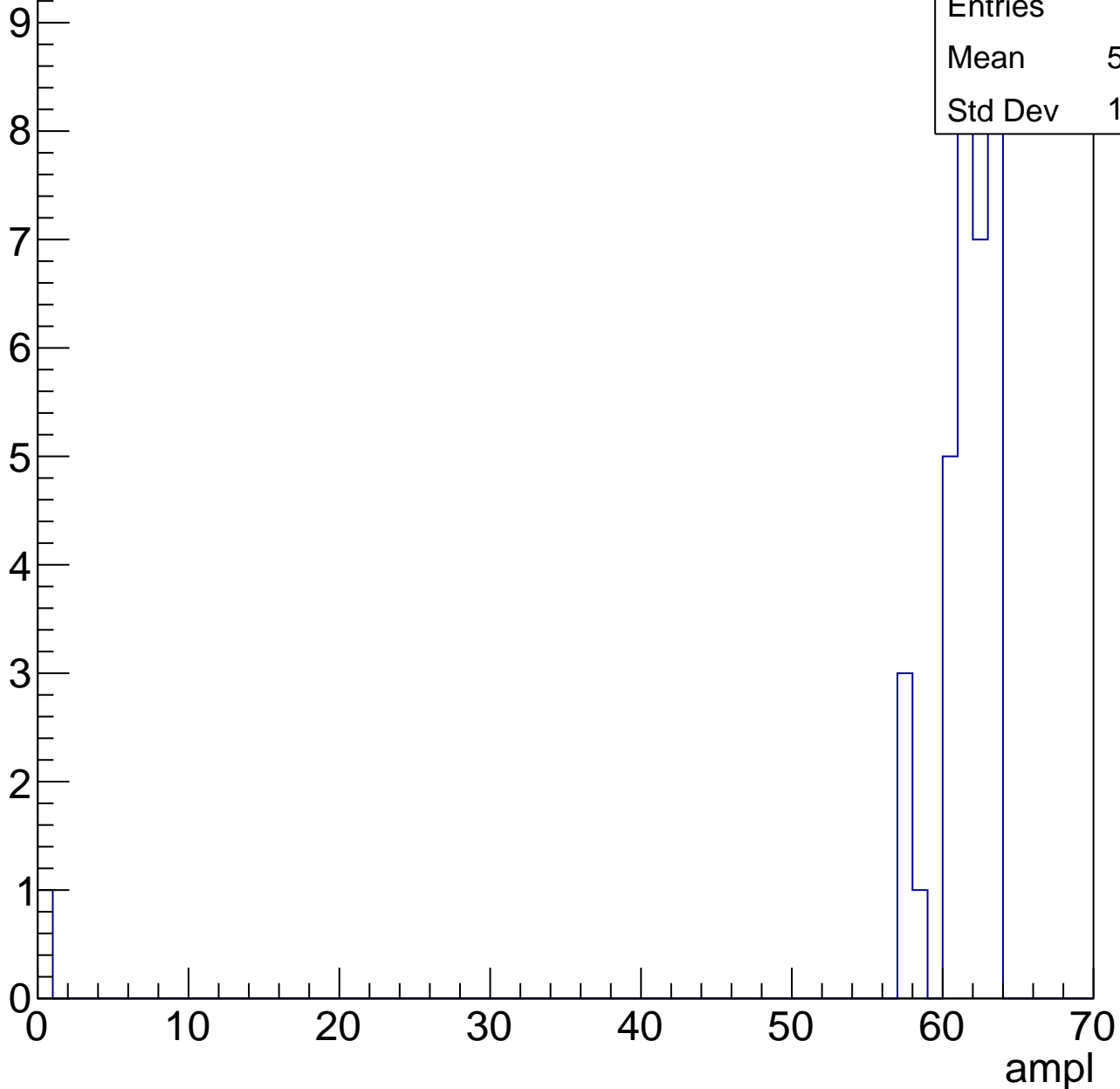


# B1L003S, U22-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

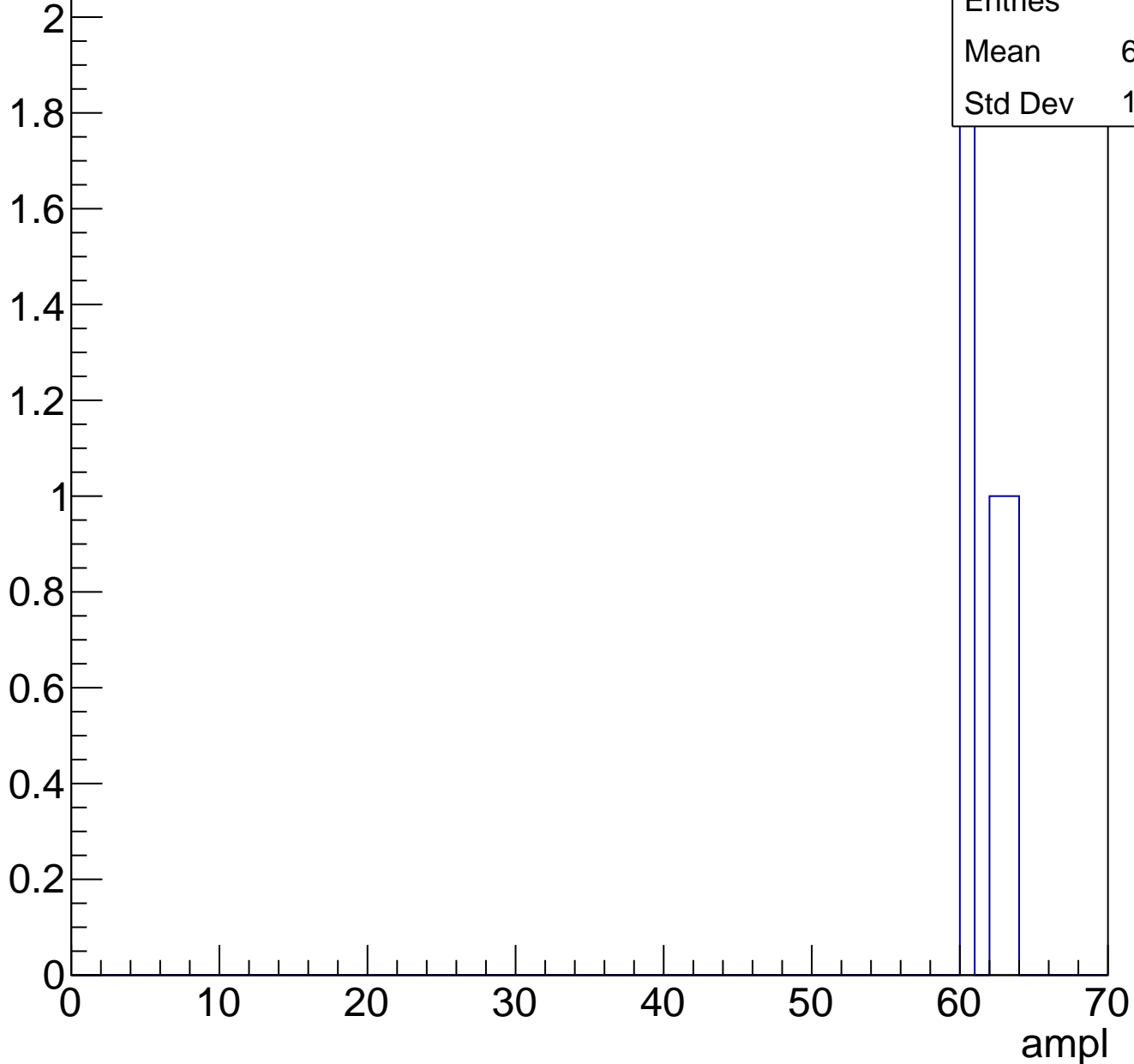
Entries	34
Mean	59.29
Std Dev	10.46



# B1L003S, U22-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch56, adc0

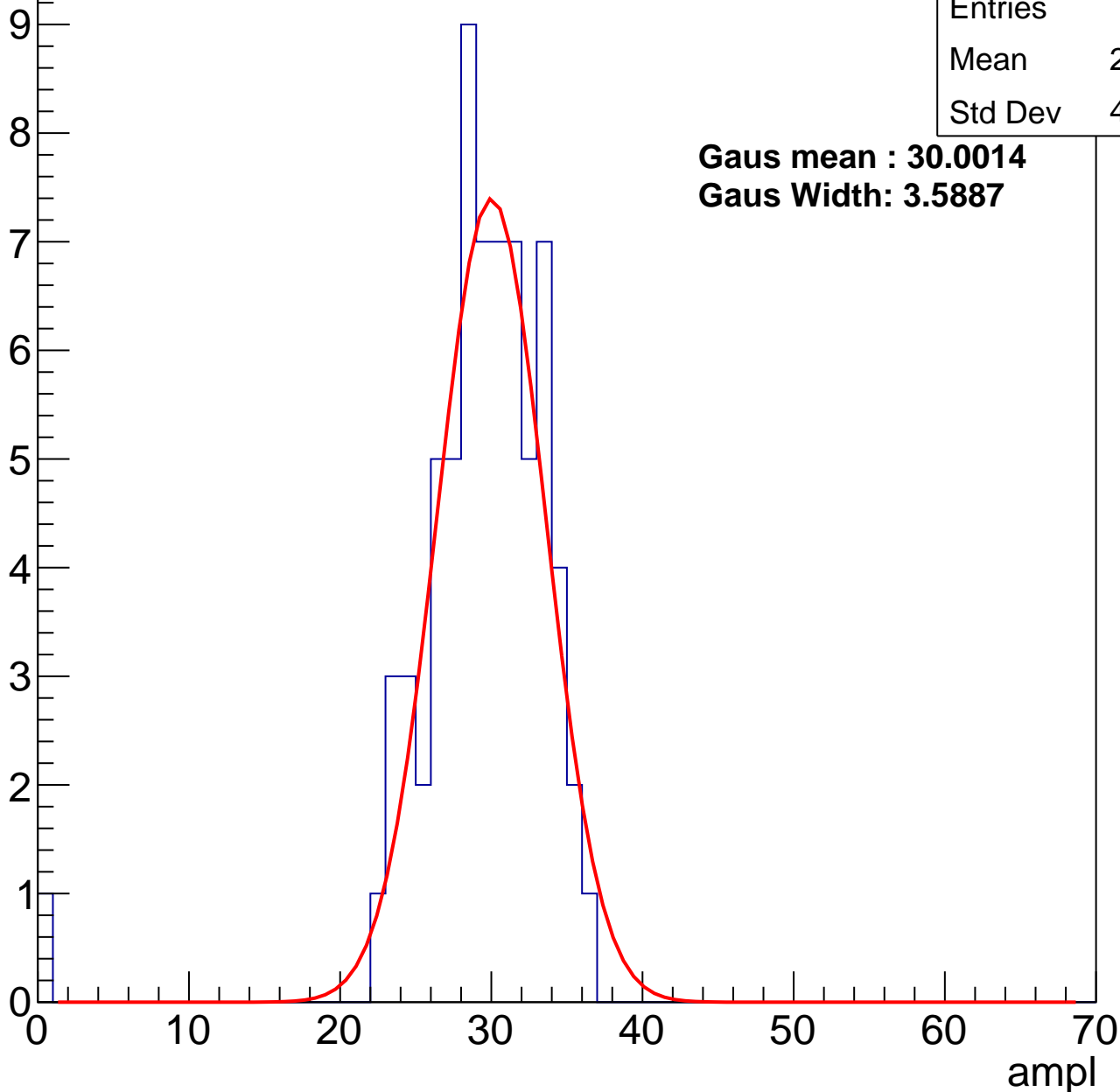
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	28.88
Std Dev	4.802

**Gaus mean : 30.0014**

**Gaus Width: 3.5887**



# B1L003S, U22-ch56, adc1

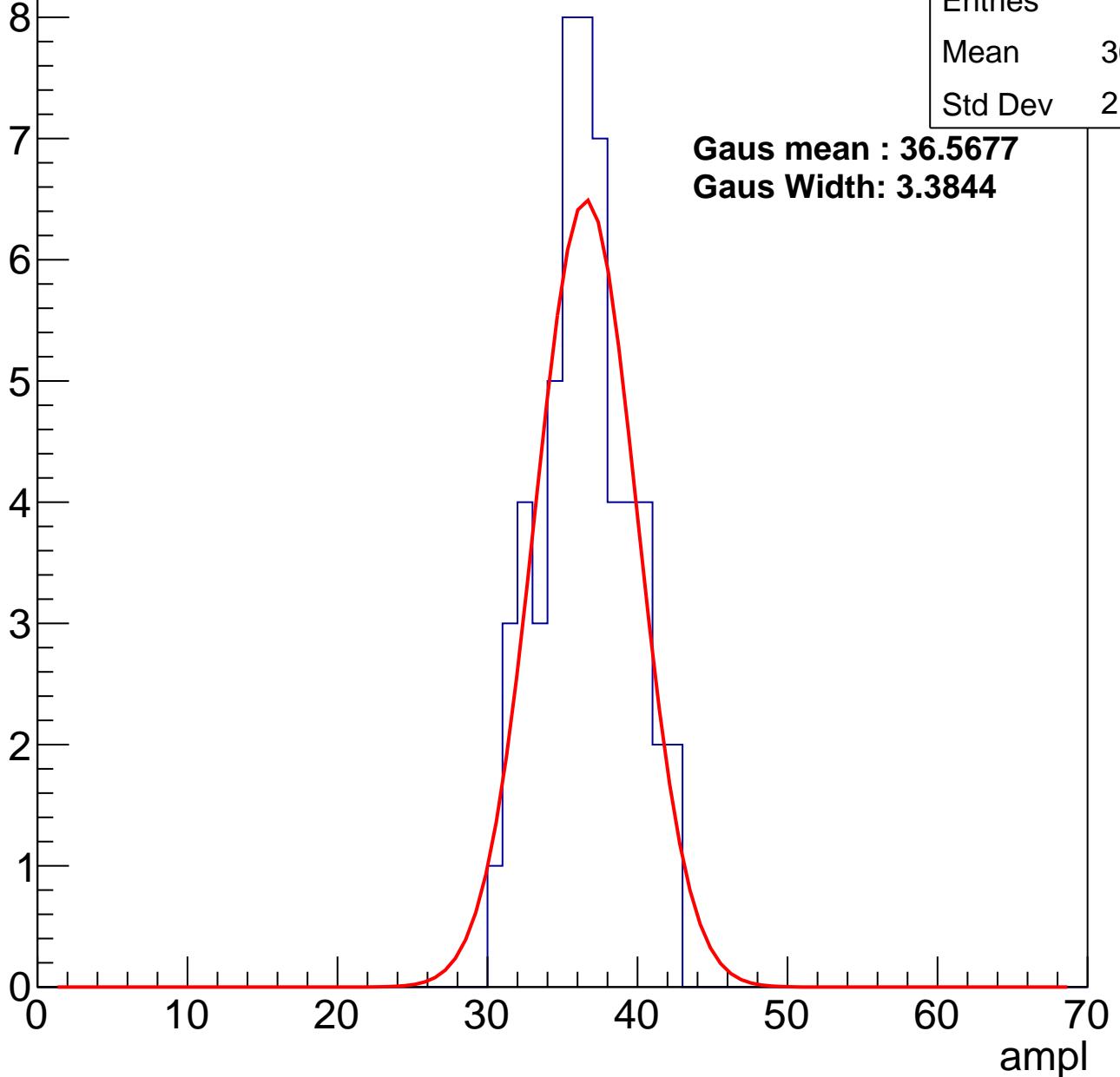
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.02
Std Dev	2.939

**Gaus mean : 36.5677**

**Gaus Width: 3.3844**



# B1L003S, U22-ch56, adc2

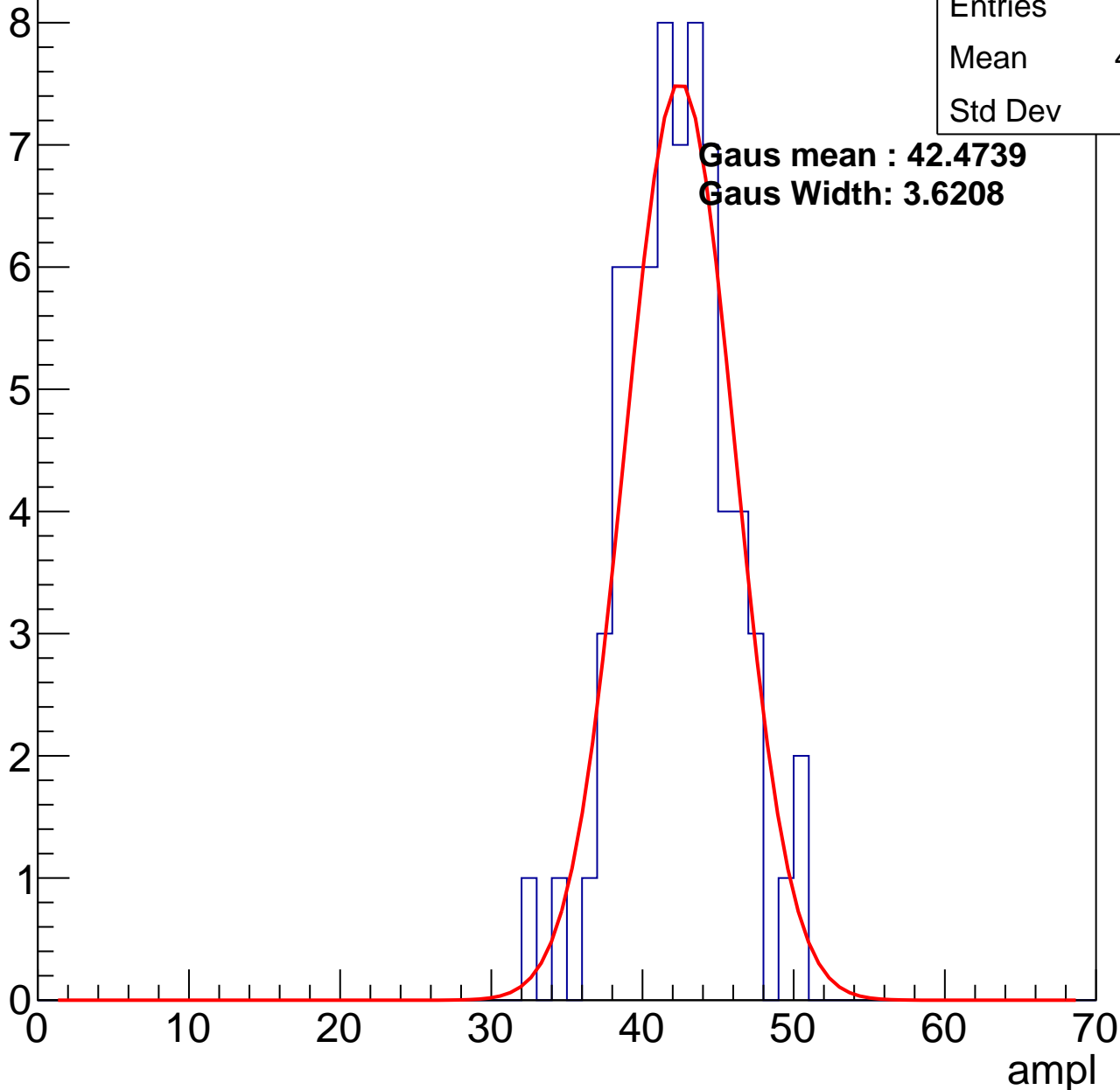
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.81
Std Dev	3.52

**Gaus mean : 42.4739**

**Gaus Width: 3.6208**

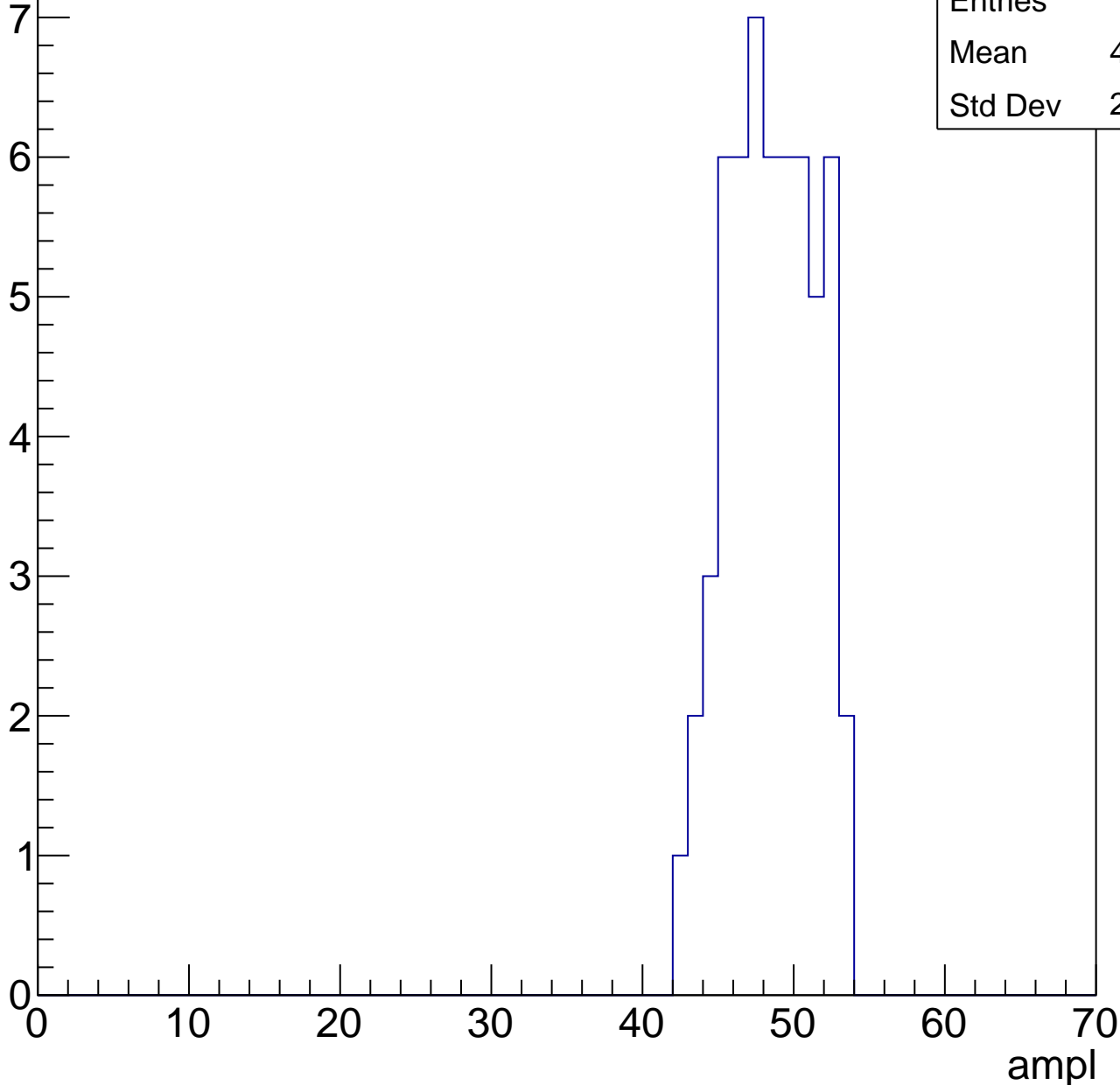


# B1L003S, U22-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	48.04
Std Dev	2.803

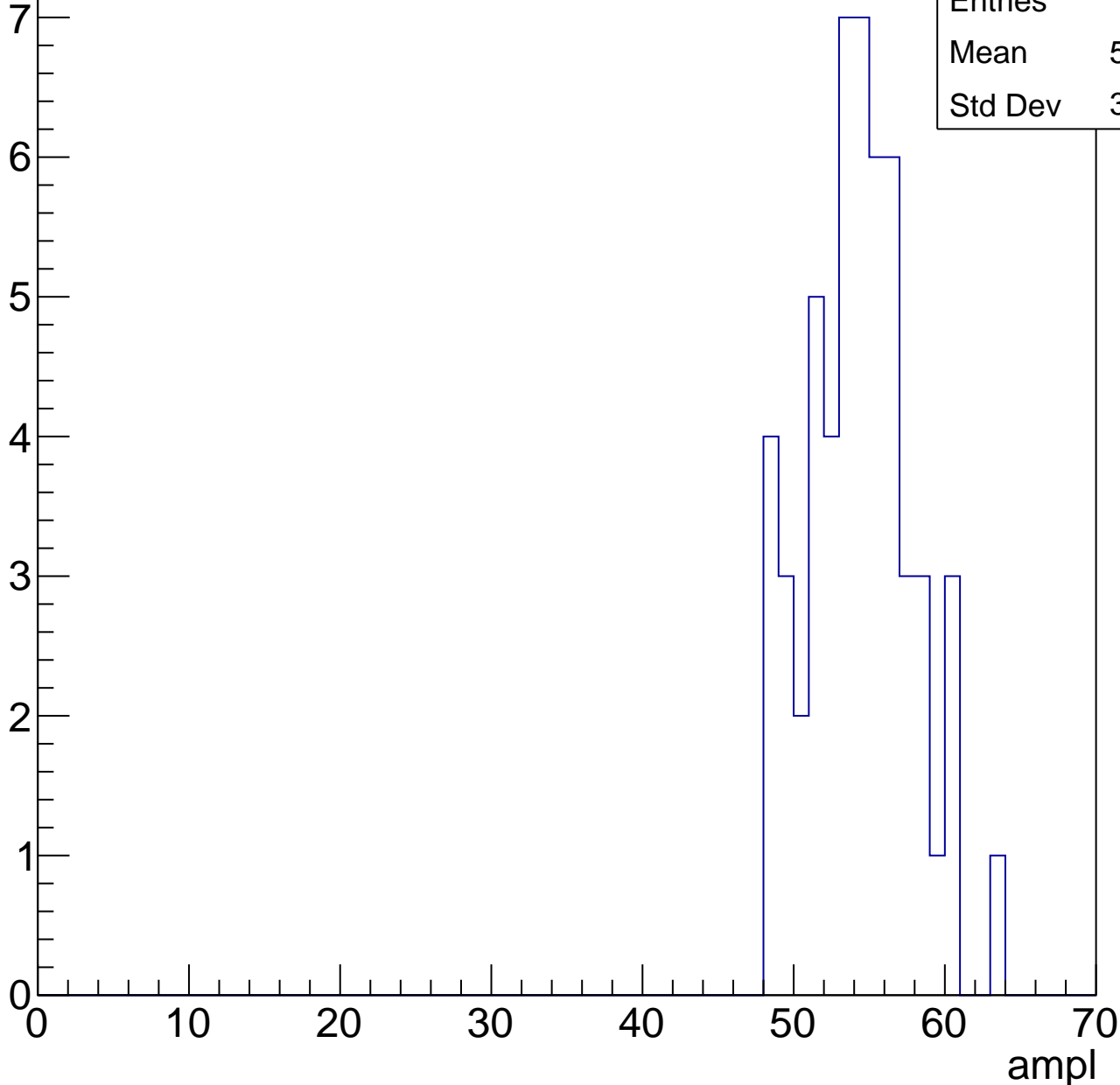


# B1L003S, U22-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

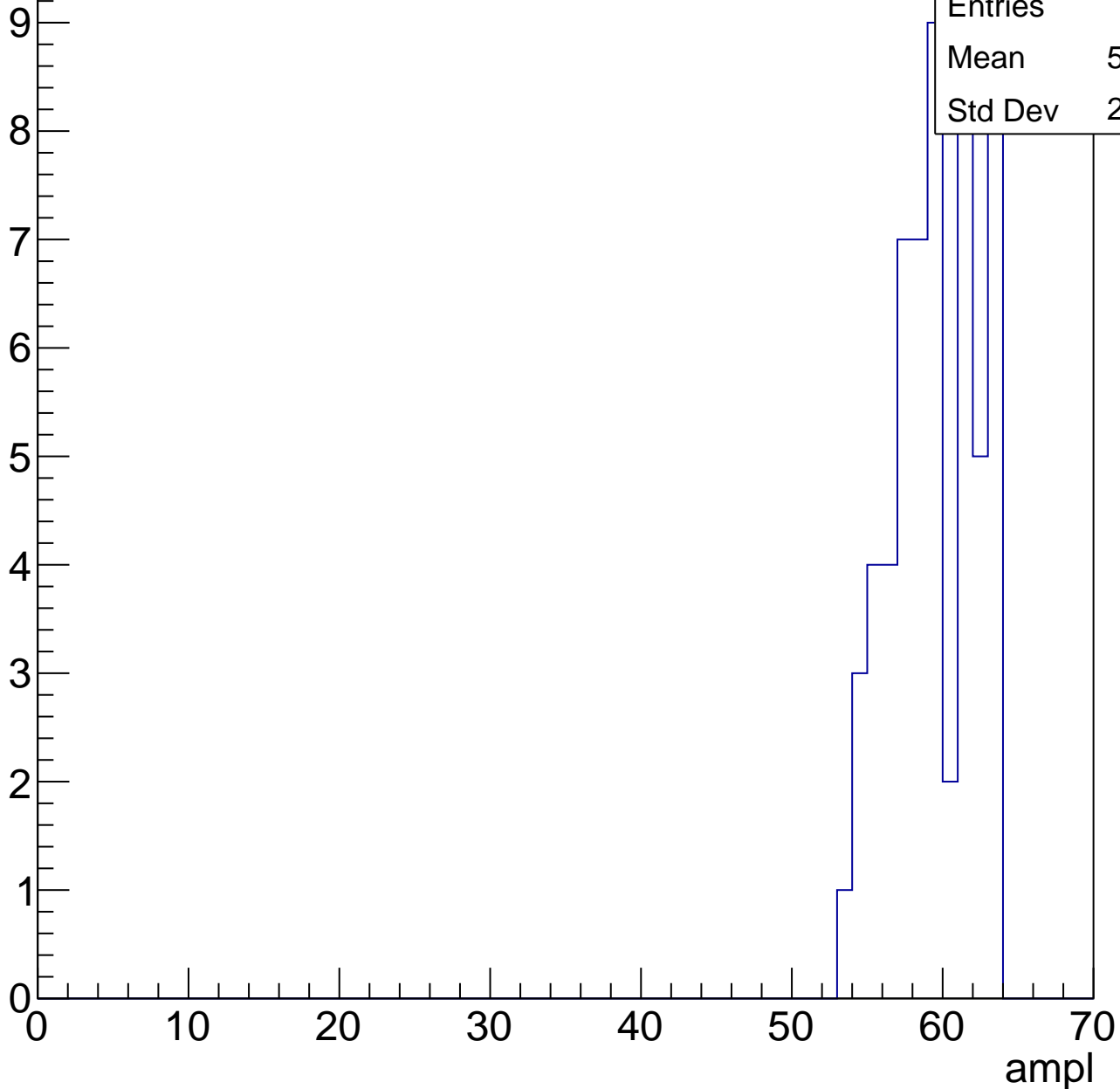
Entries	55
Mean	53.89
Std Dev	3.404



# B1L003S, U22-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

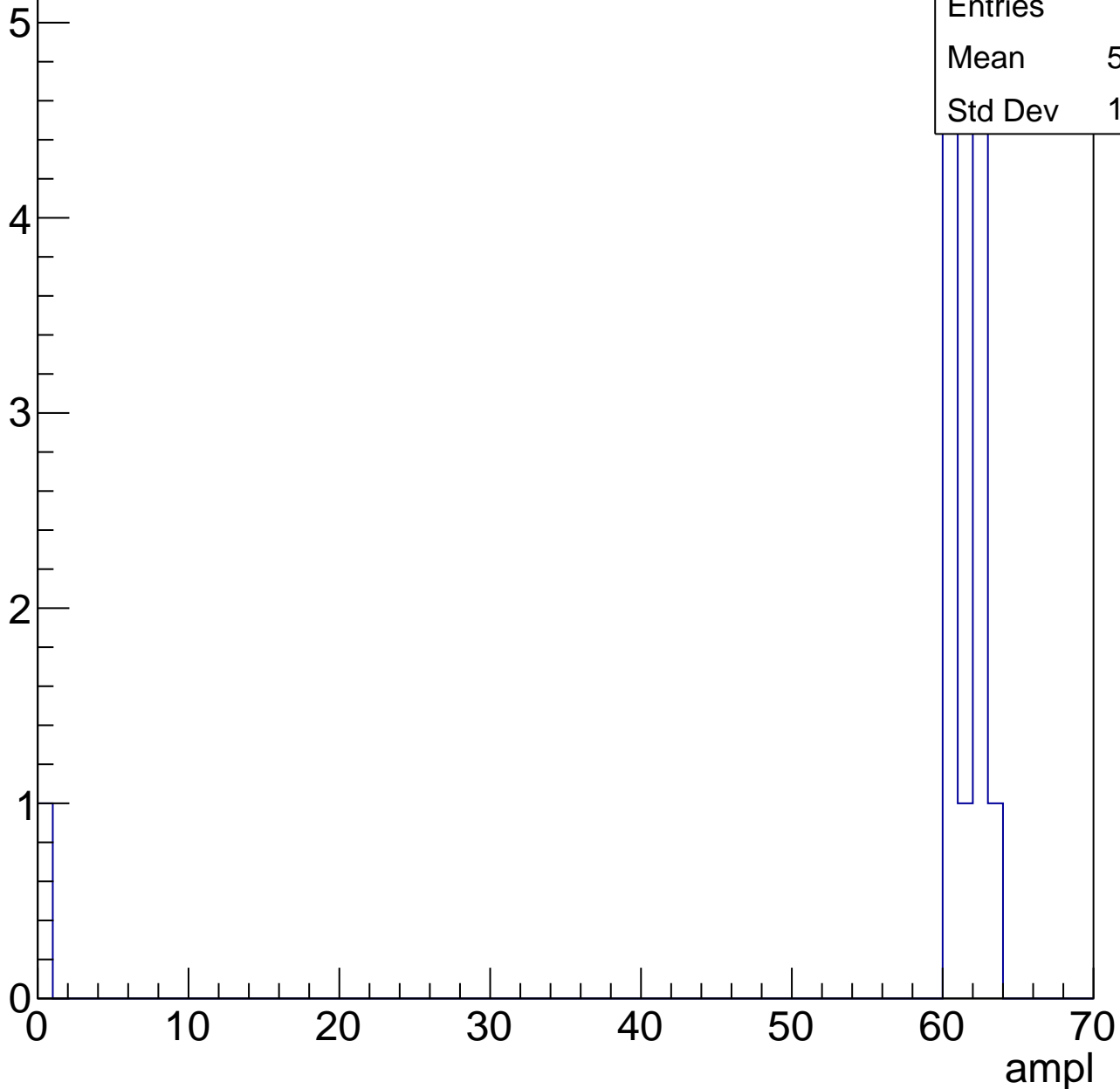


# B1L003S, U22-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	13
Mean	56.46
Std Dev	16.33

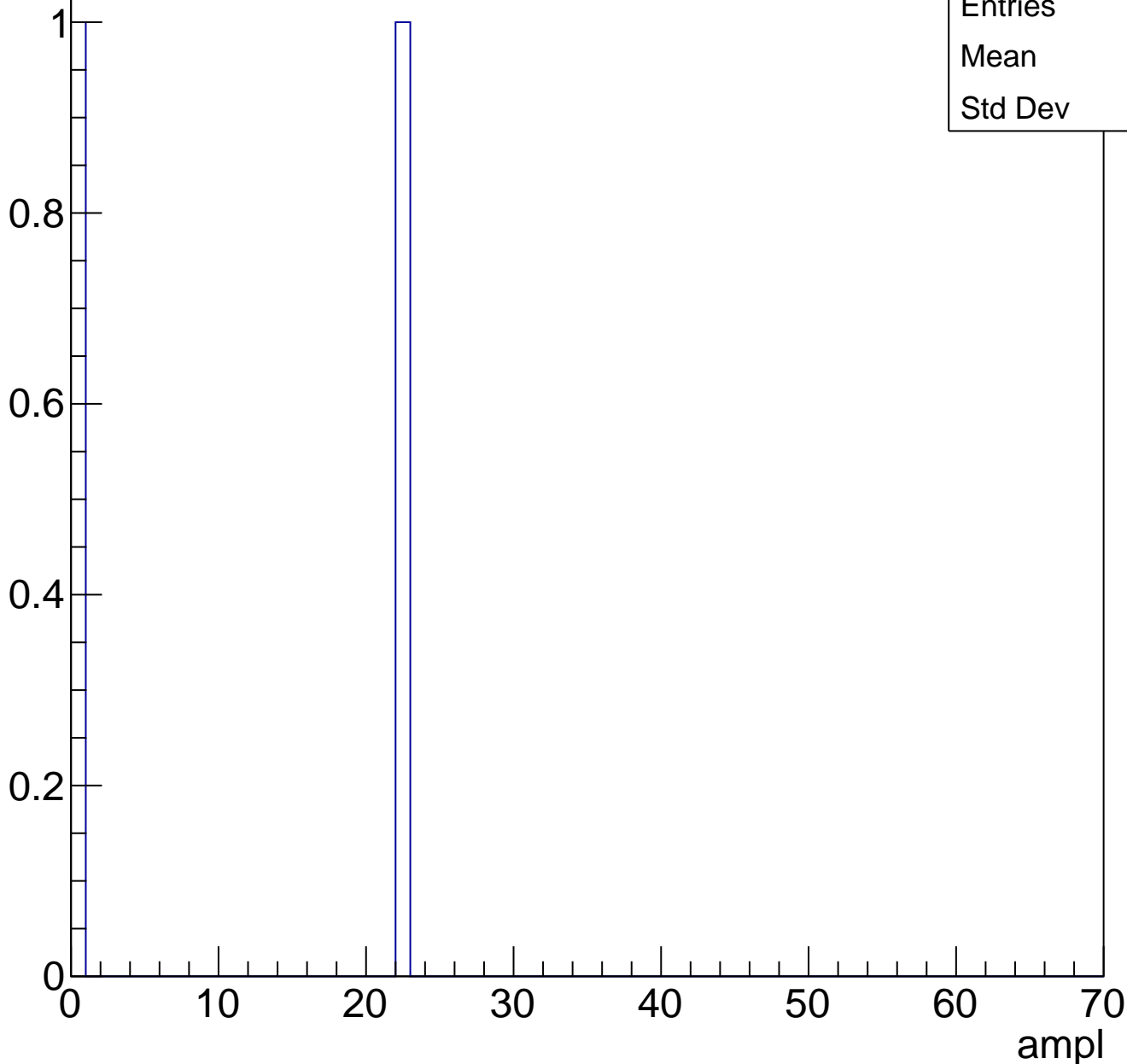




# B1L003S, U22-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U22-ch57, adc0

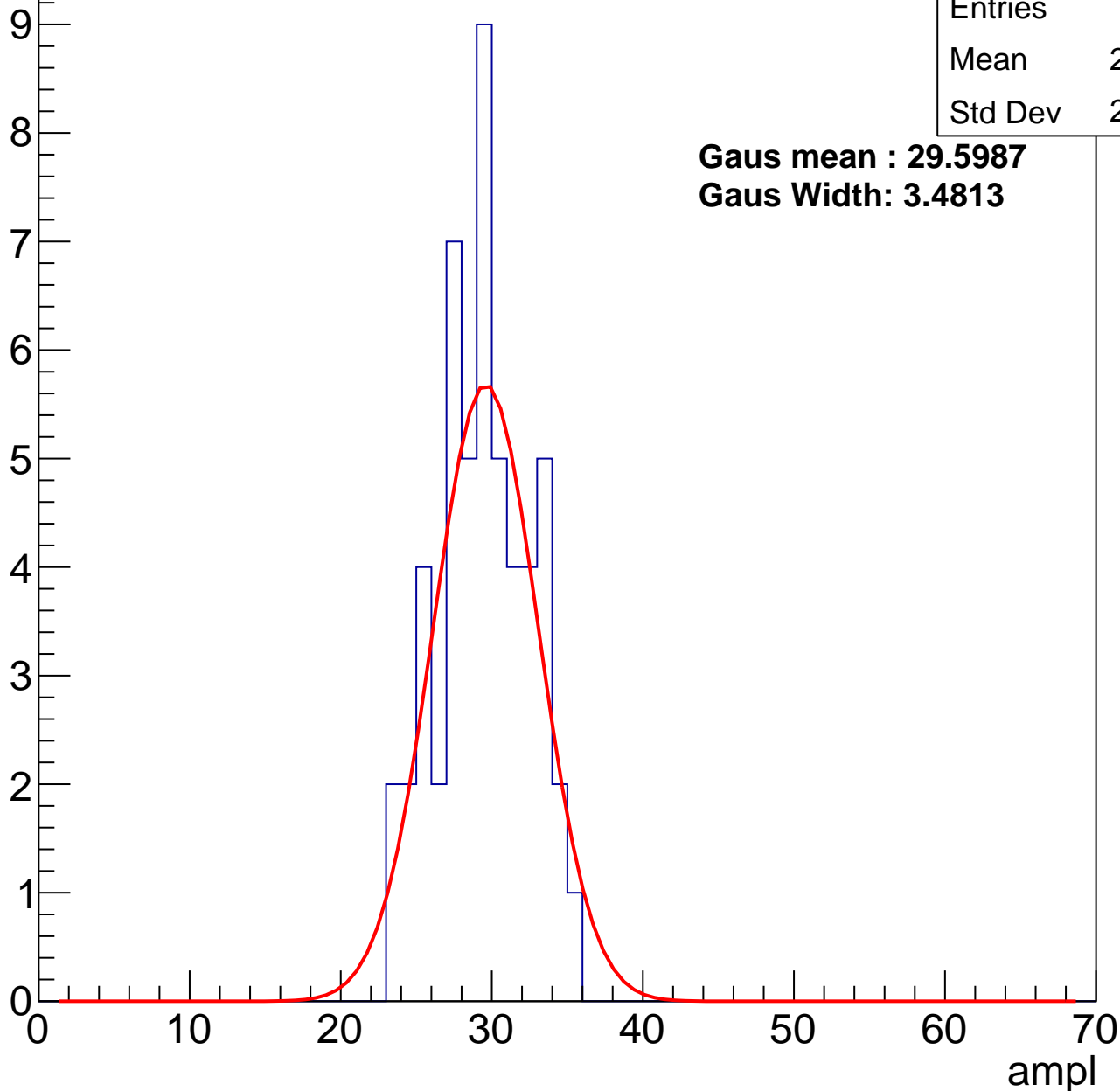
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	28.96
Std Dev	2.974

**Gaus mean : 29.5987**

**Gaus Width: 3.4813**



# B1L003S, U22-ch57, adc1

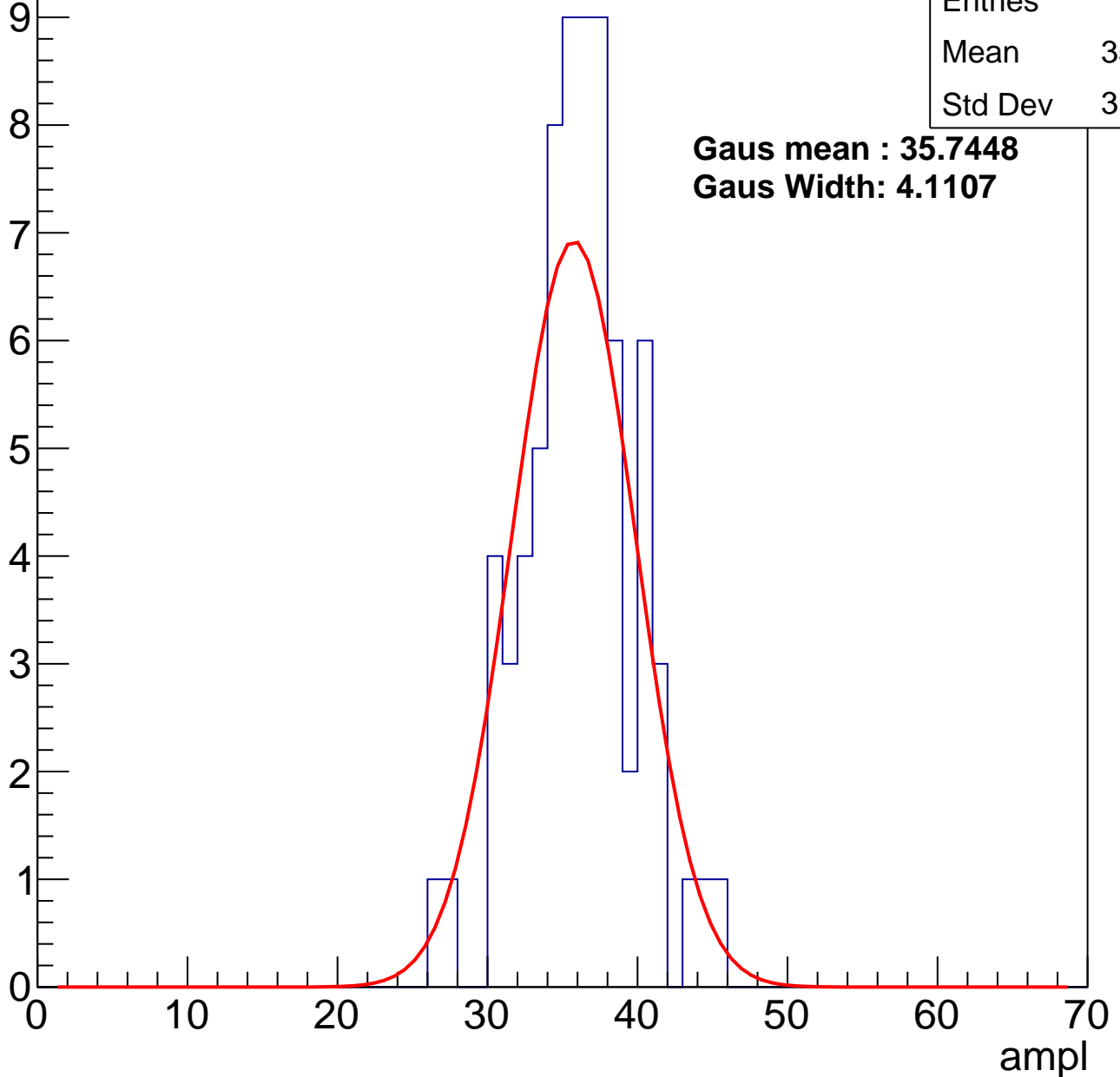
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.67
Std Dev	3.623

**Gaus mean : 35.7448**

**Gaus Width: 4.1107**

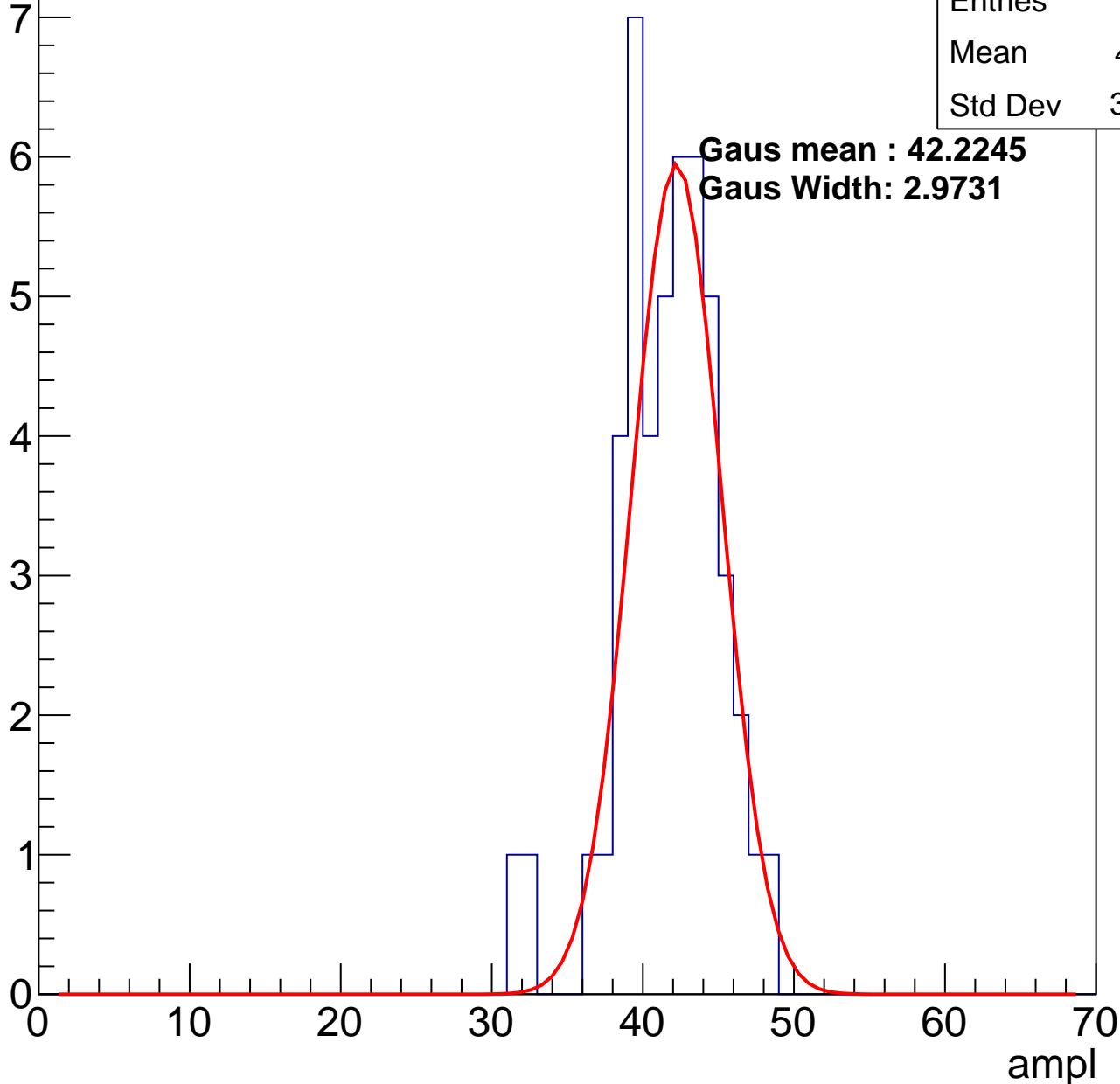


# B1L003S, U22-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	41.21
Std Dev	3.372

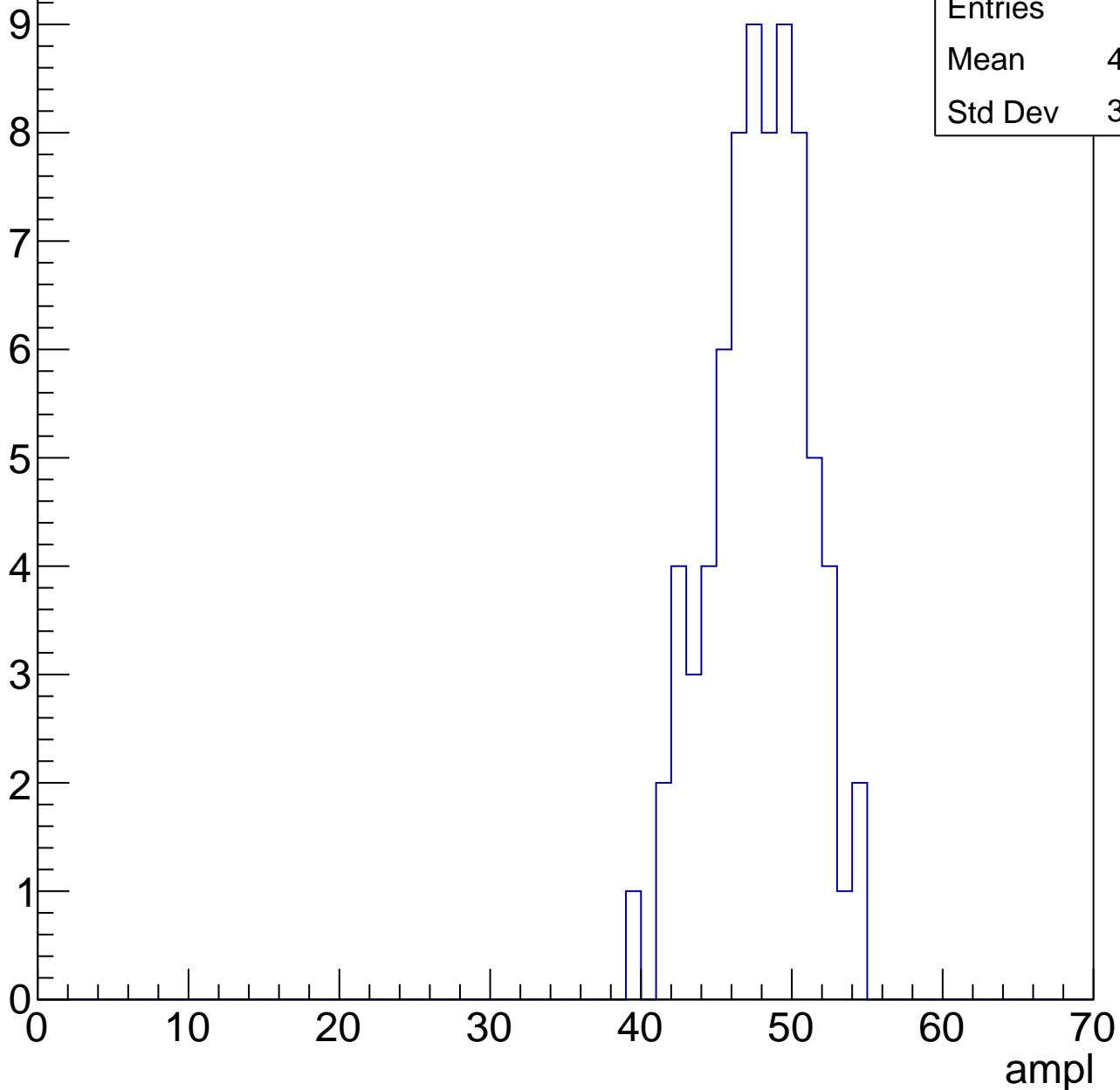


# B1L003S, U22-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	47.35
Std Dev	3.232

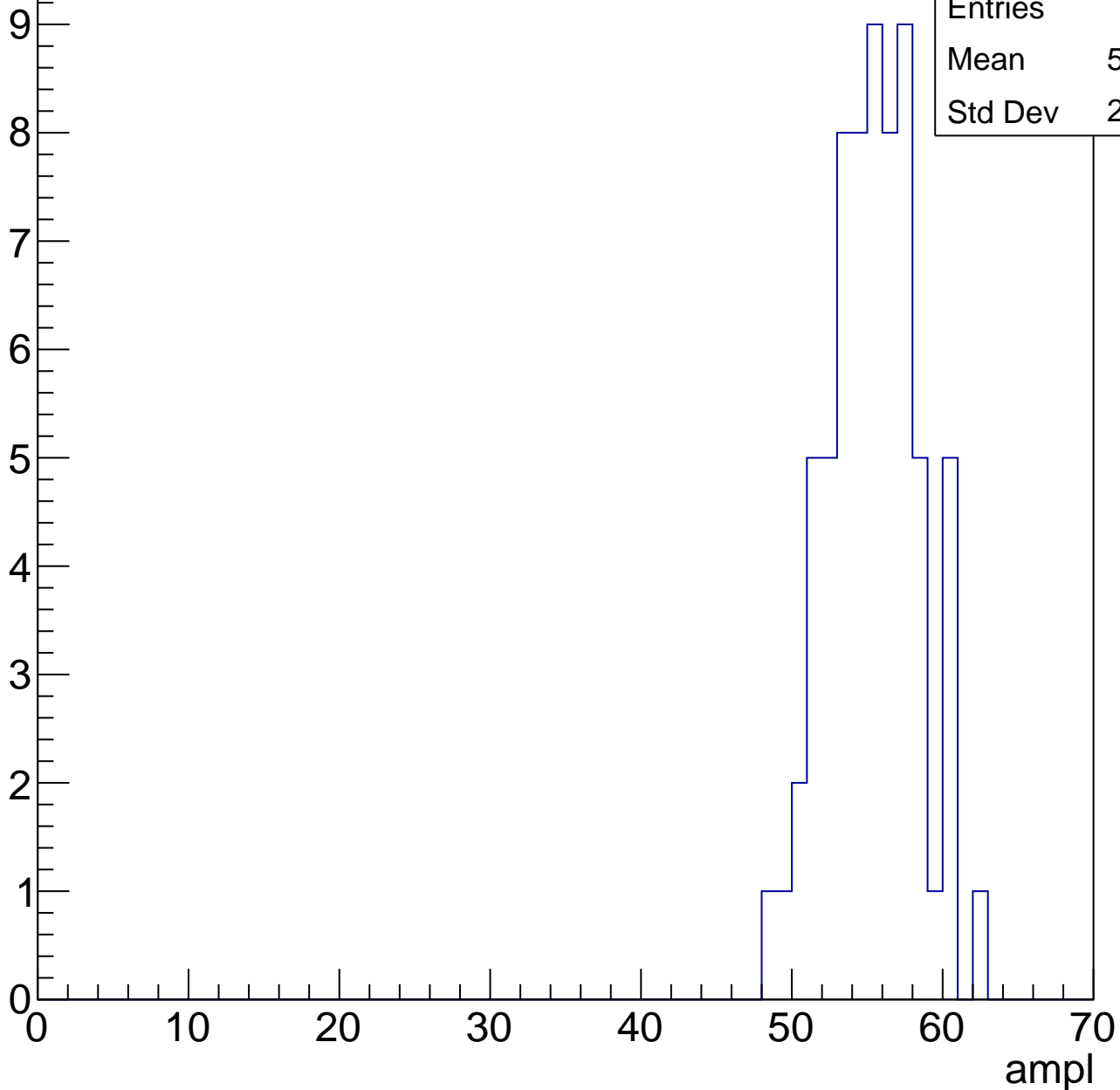


# B1L003S, U22-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	54.93
Std Dev	2.917

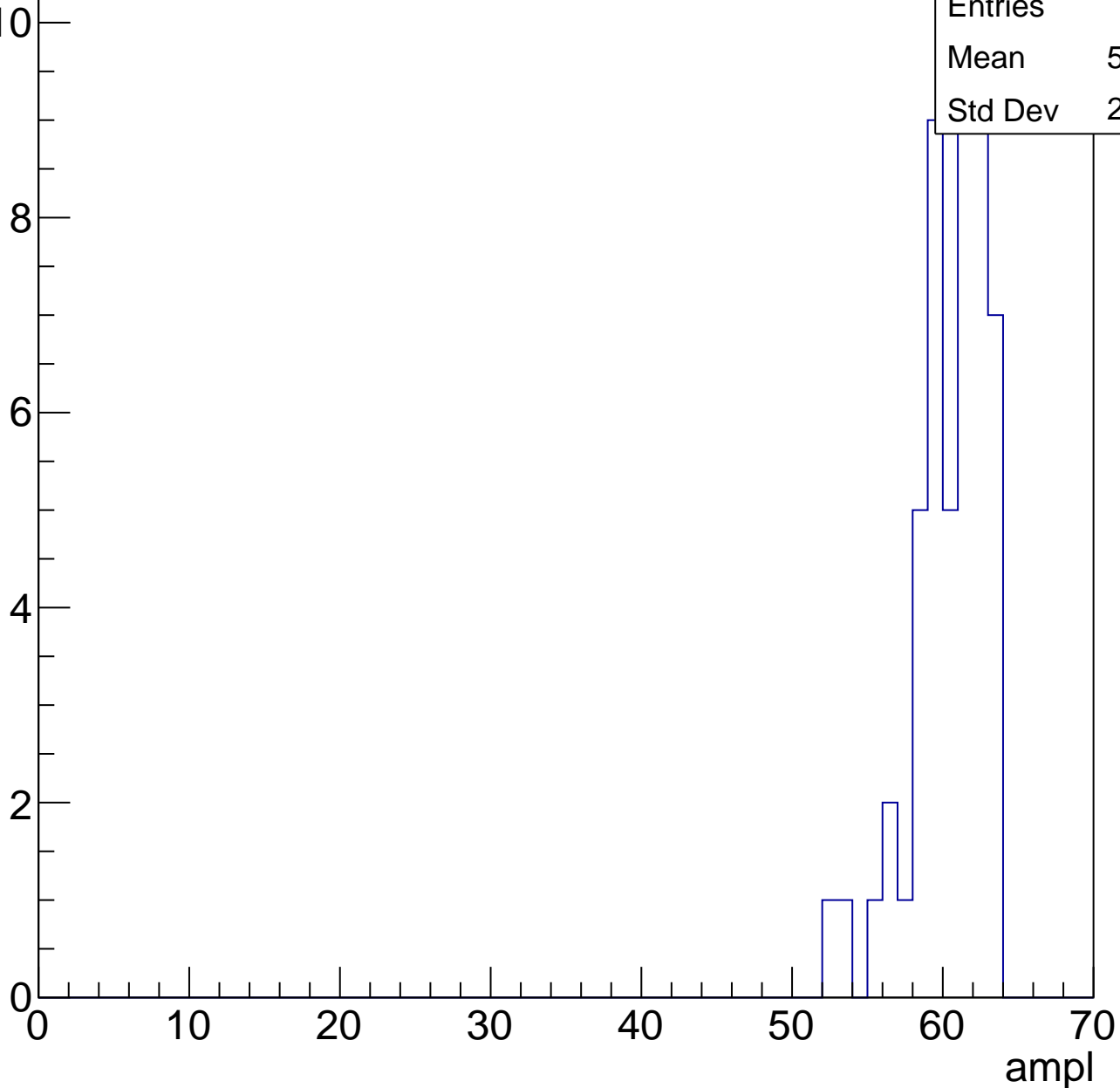


# B1L003S, U22-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

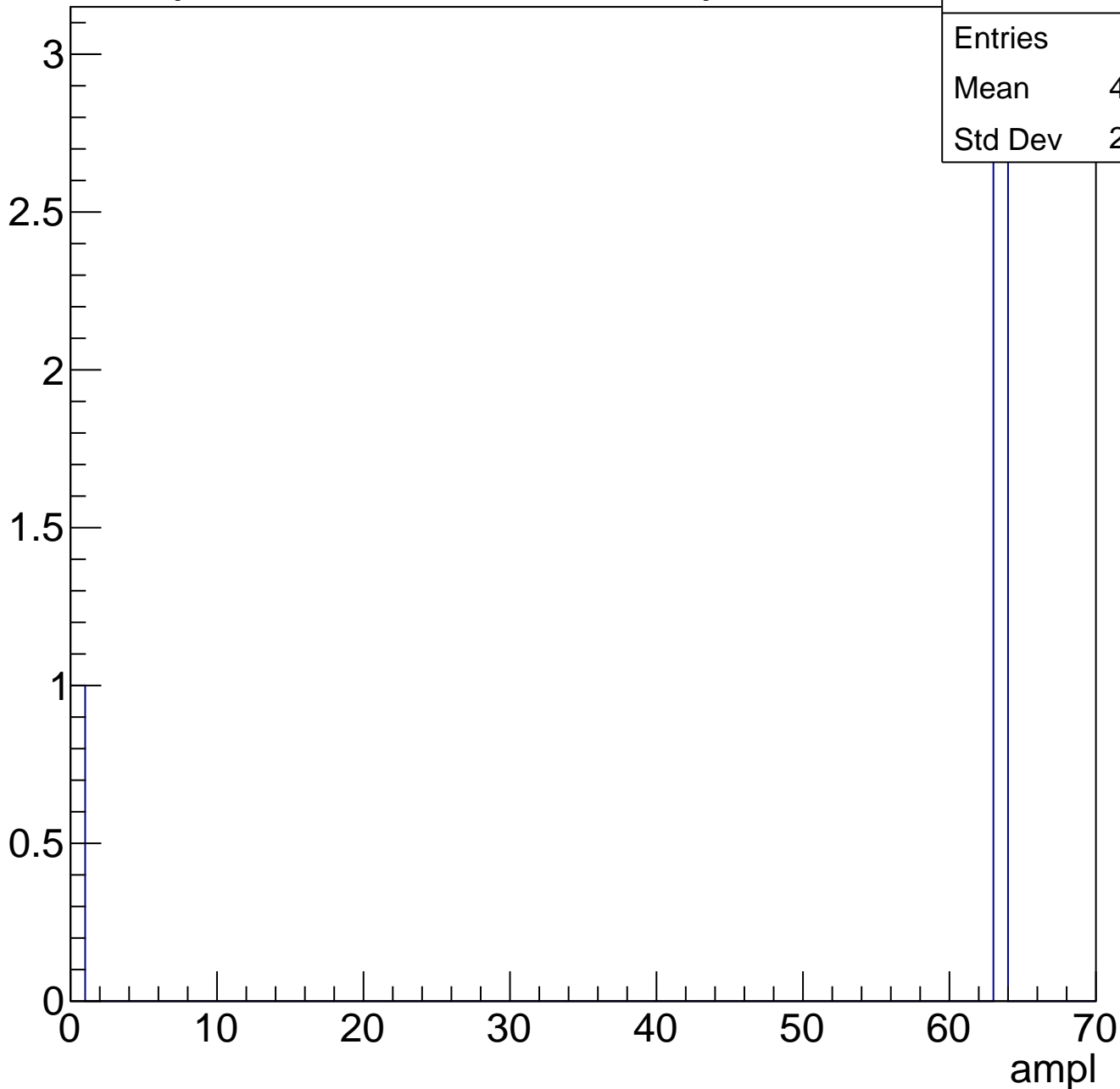
Entries	51
Mean	59.98
Std Dev	2.493



# B1L003S, U22-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch58, adc0

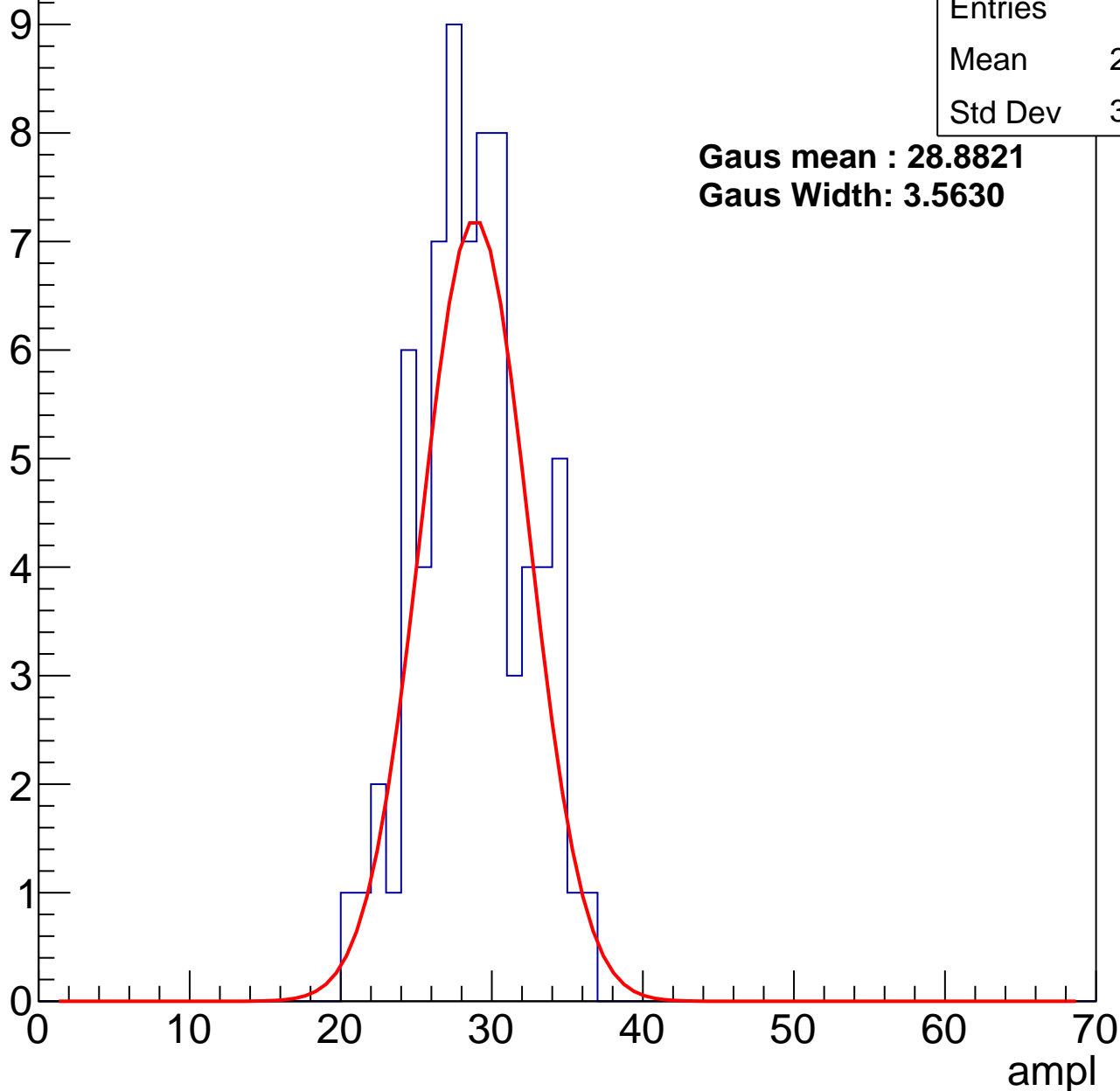
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	28.32
Std Dev	3.527

**Gaus mean : 28.8821**

**Gaus Width: 3.5630**



# B1L003S, U22-ch58, adc1

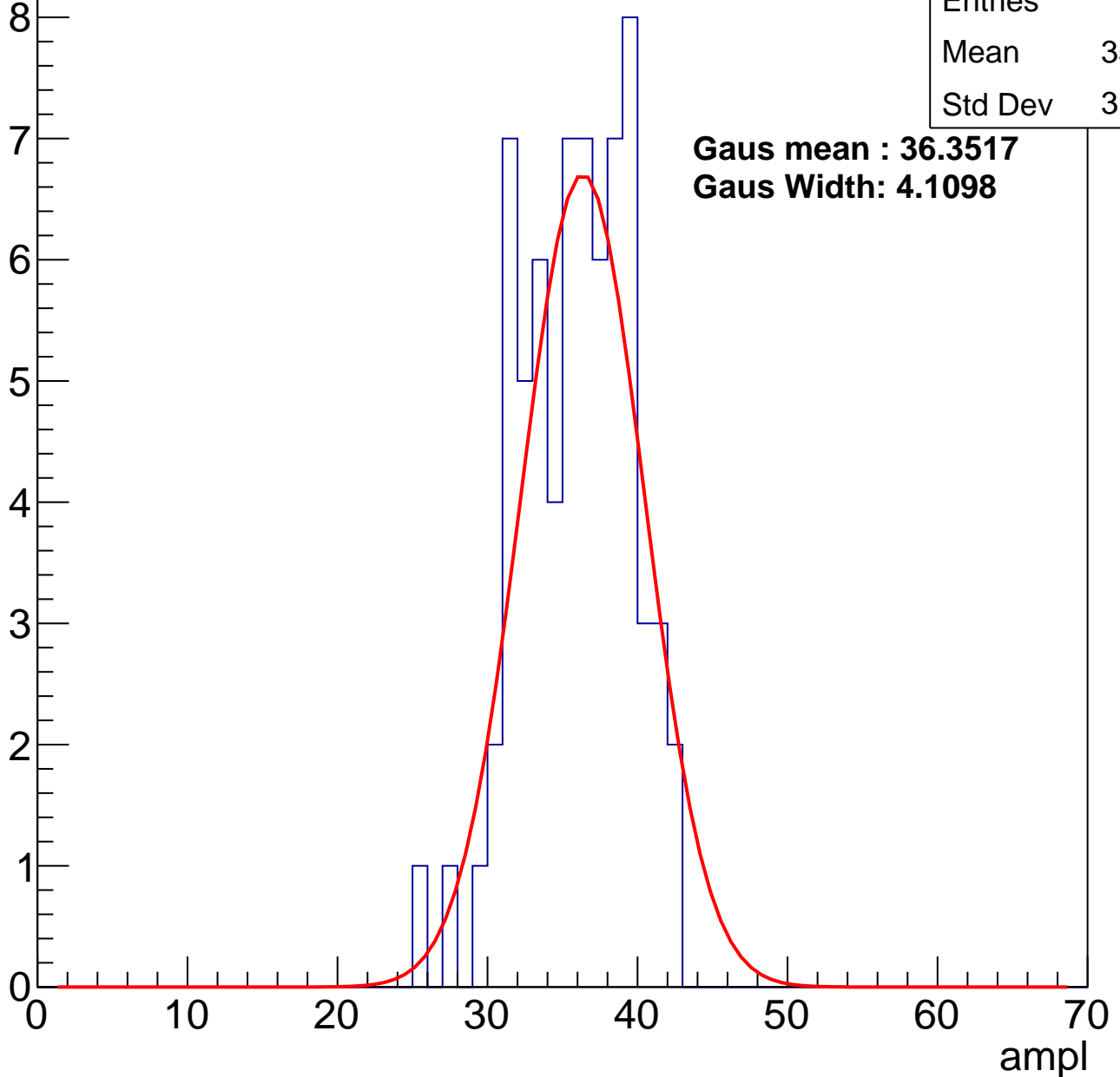
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.37
Std Dev	3.642

**Gaus mean : 36.3517**

**Gaus Width: 4.1098**



# B1L003S, U22-ch58, adc2

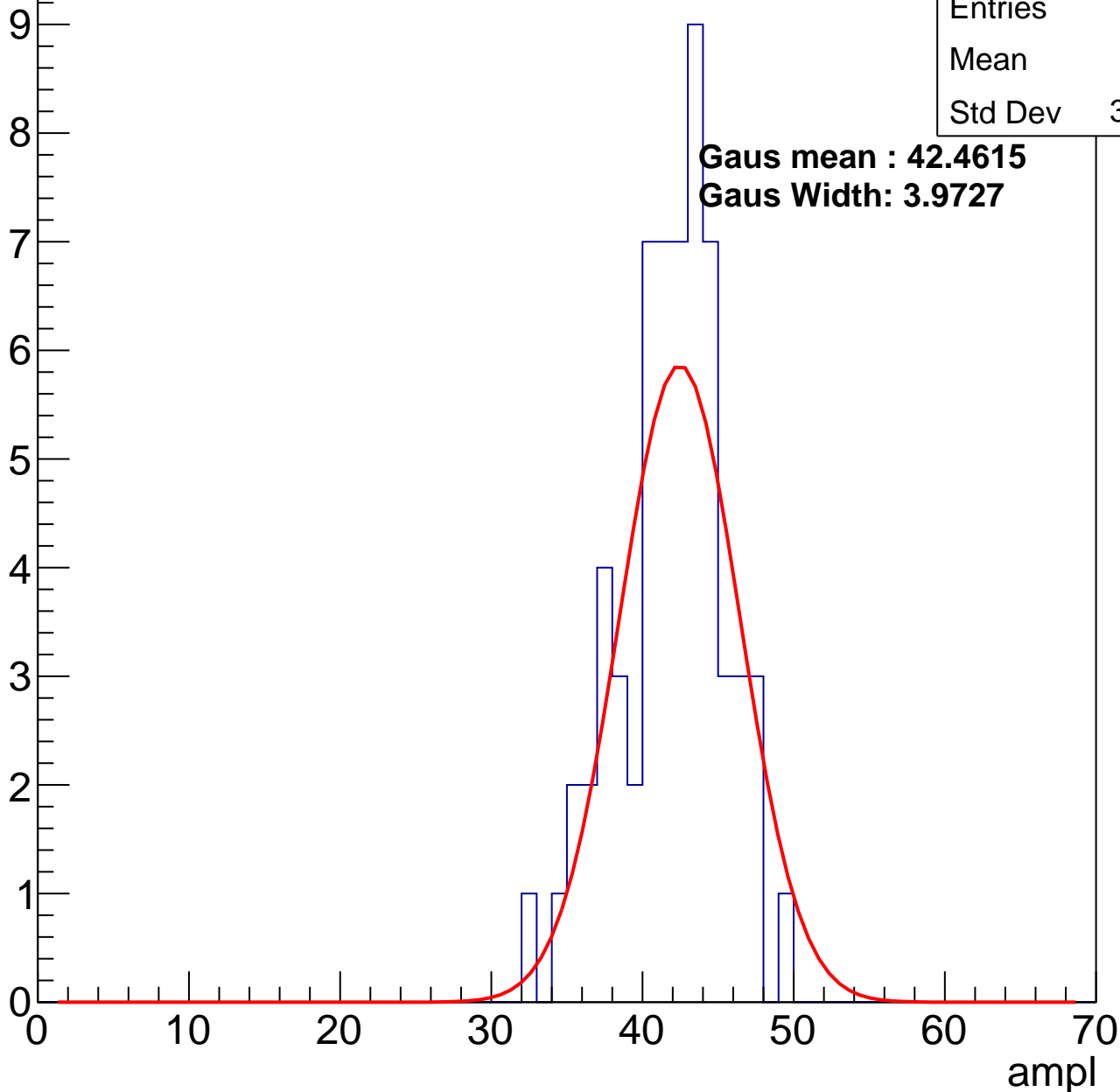
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	41.4
Std Dev	3.476

**Gaus mean : 42.4615**

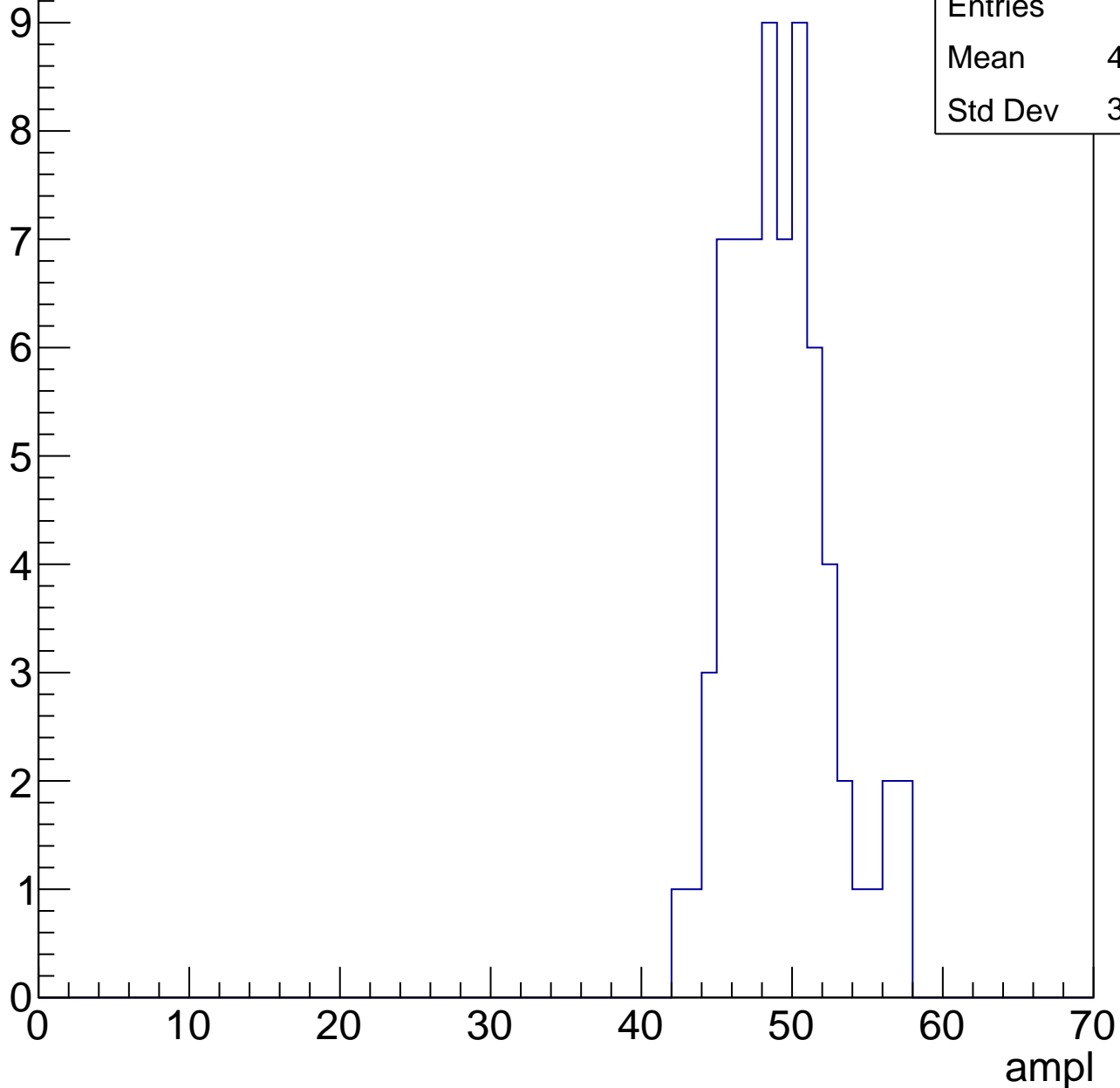
**Gaus Width: 3.9727**



# B1L003S, U22-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

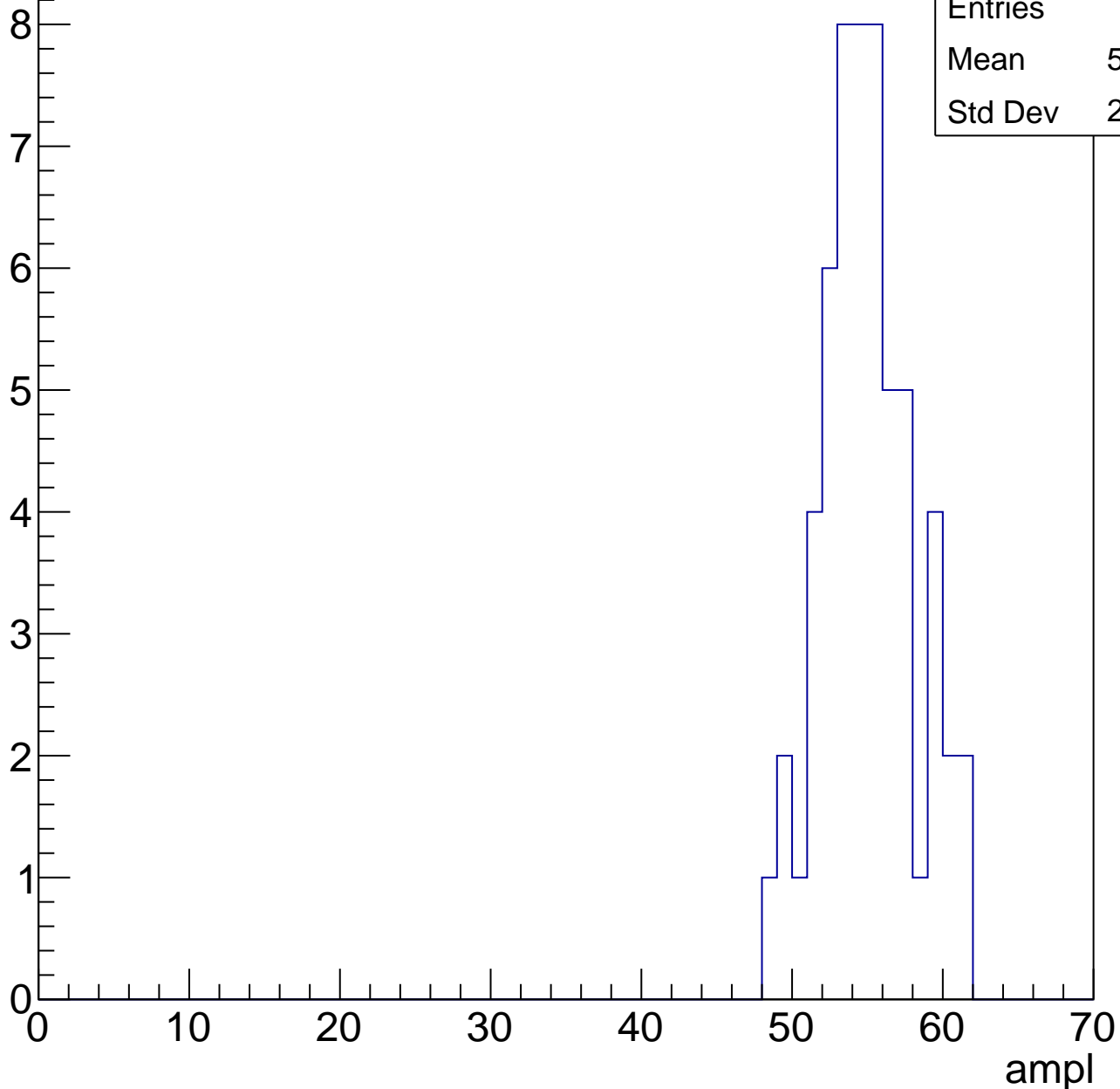


# B1L003S, U22-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

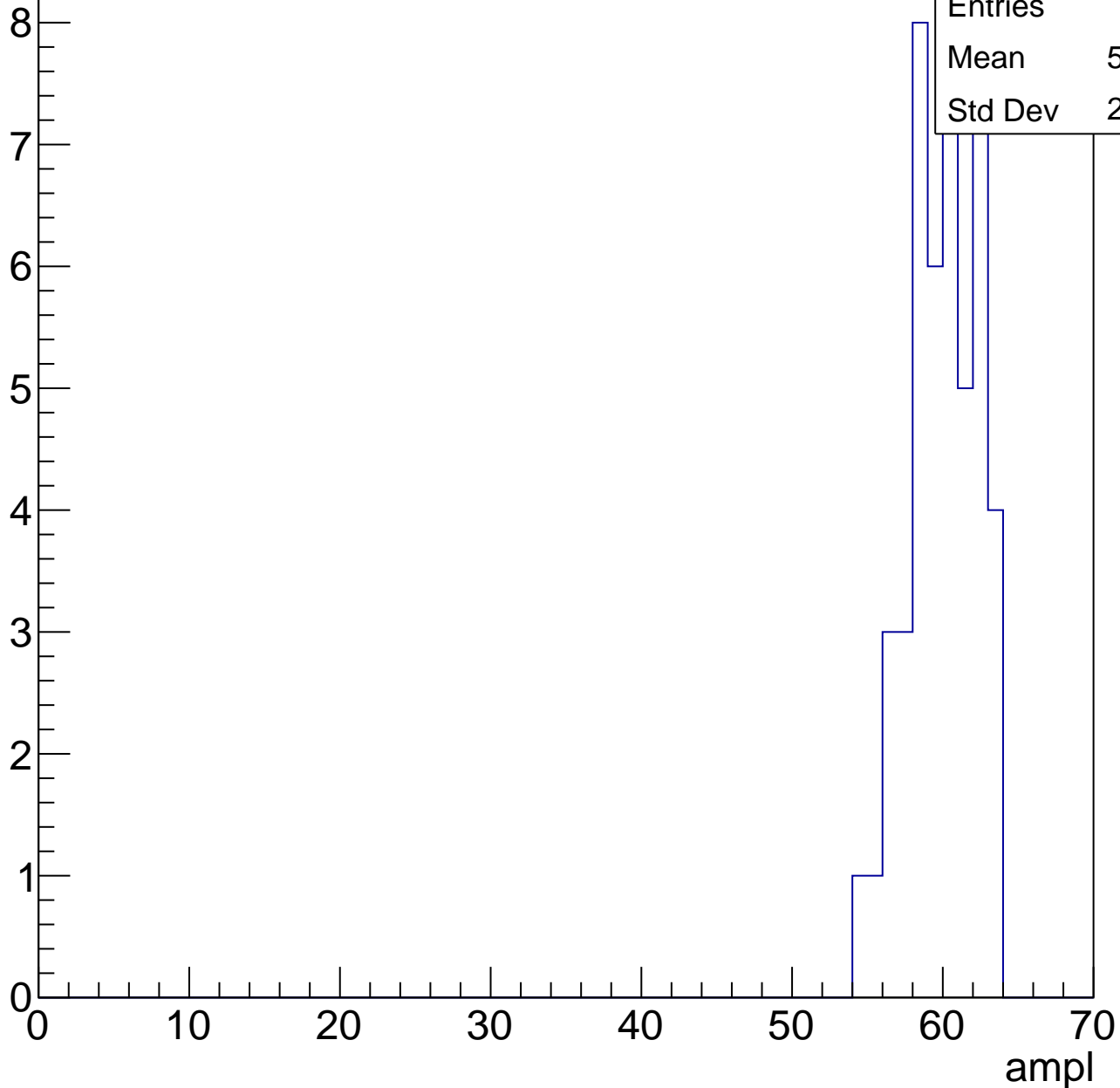
Entries	57
Mean	54.54
Std Dev	2.997



# B1L003S, U22-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



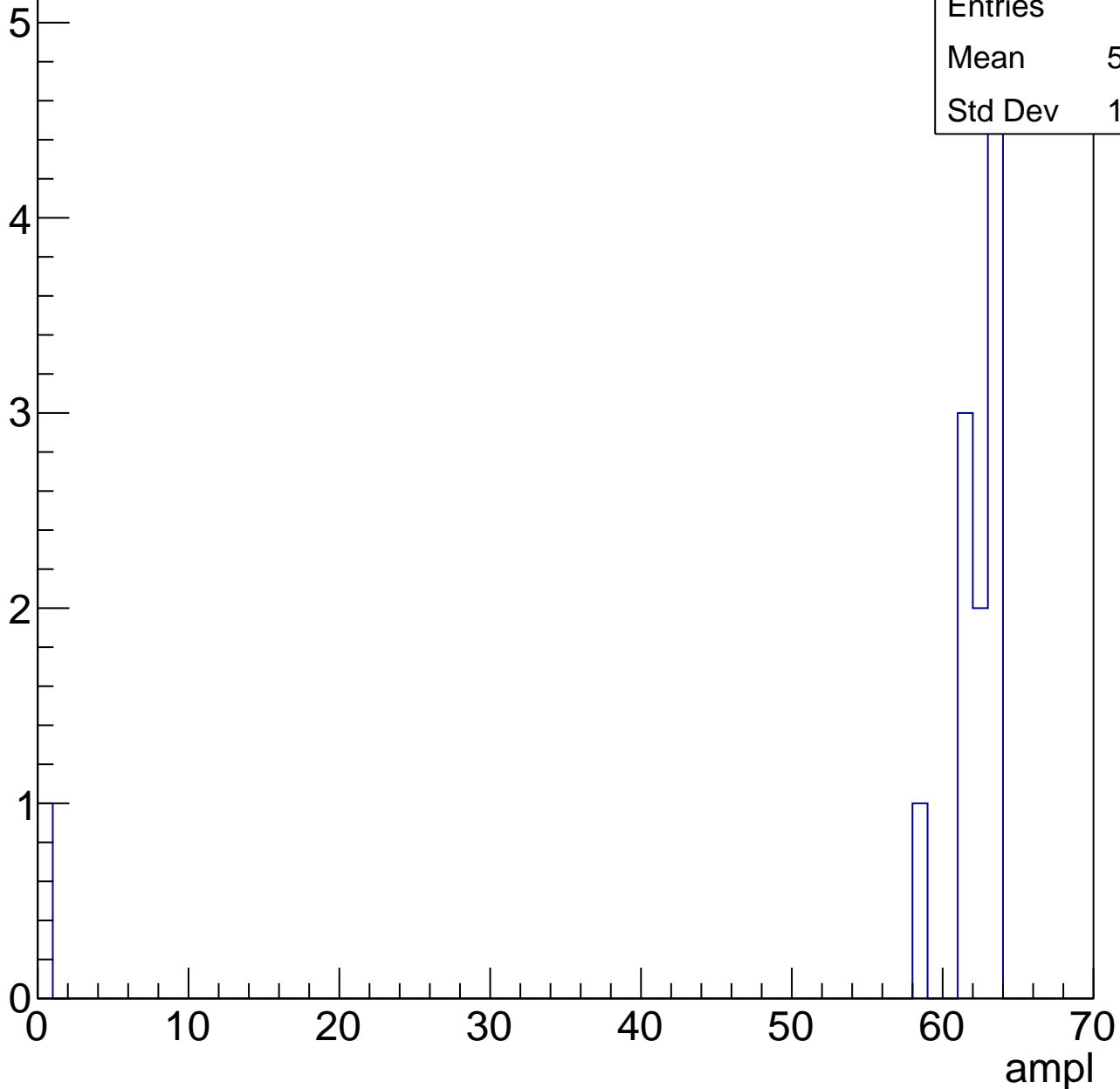
Entries	47
Mean	59.55
Std Dev	2.248

# B1L003S, U22-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	56.67
Std Dev	17.14





# B1L003S, U22-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch59, adc0

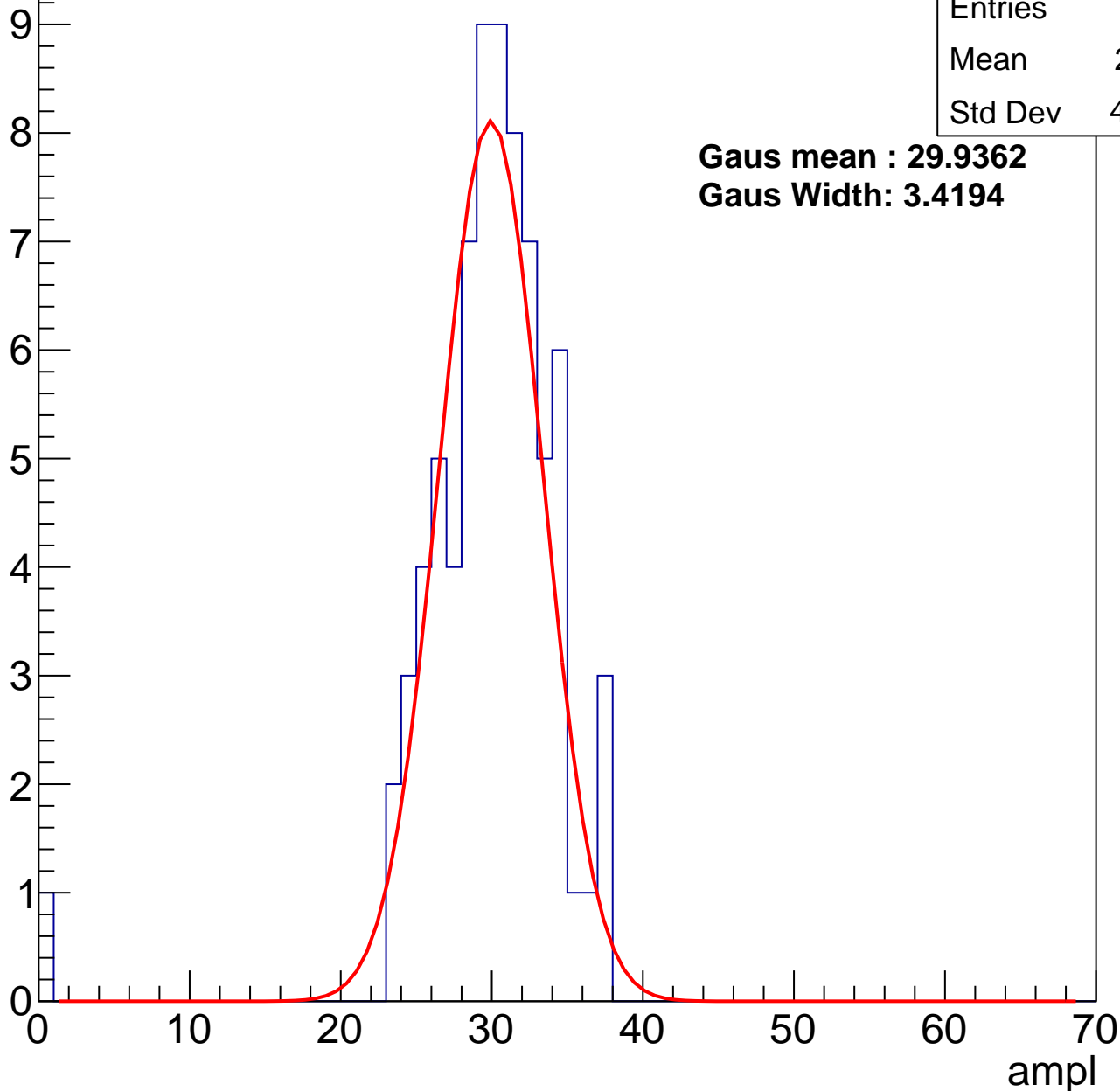
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.41
Std Dev	4.786

**Gaus mean : 29.9362**

**Gaus Width: 3.4194**



# B1L003S, U22-ch59, adc1

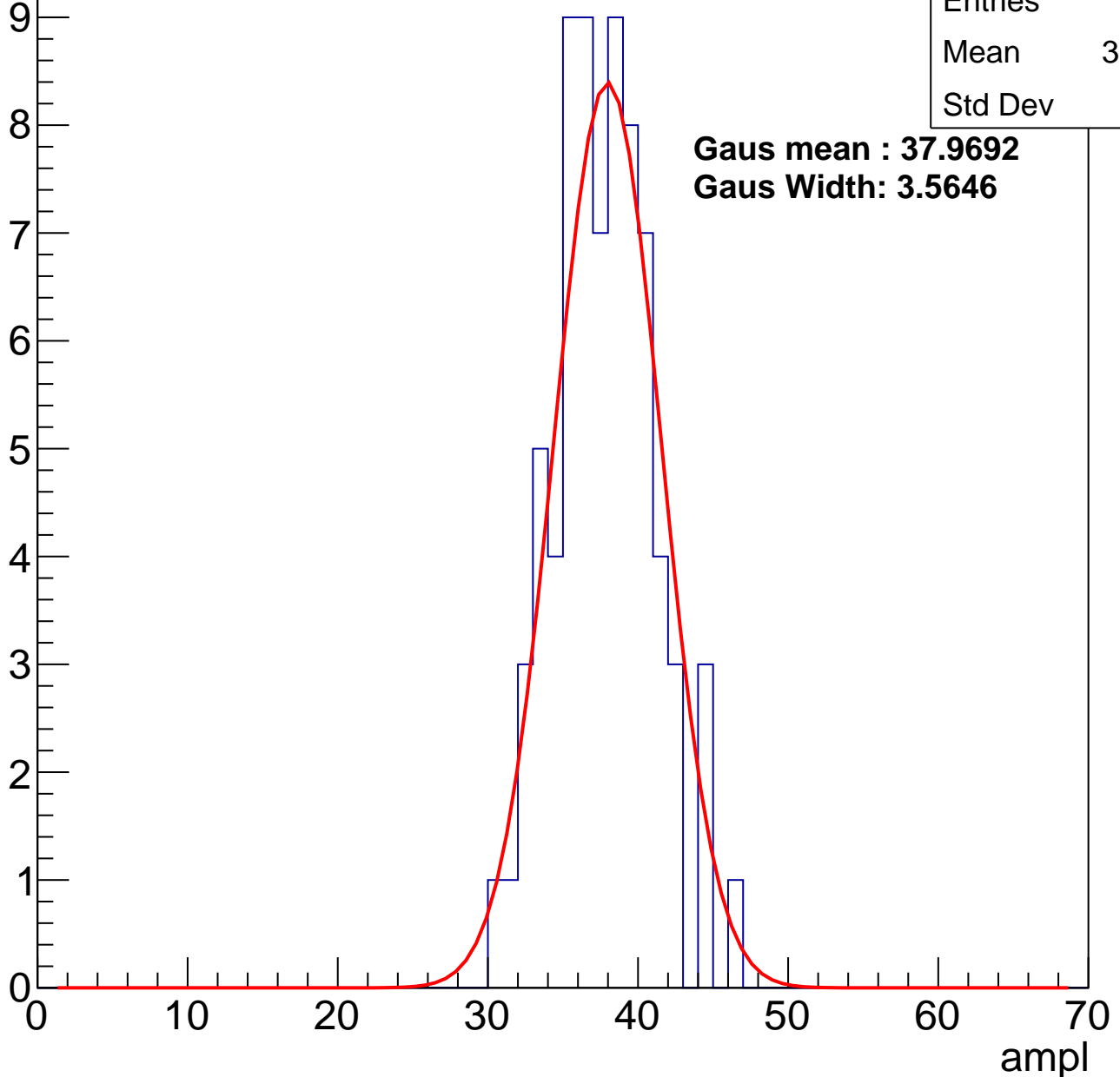
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	37.27
Std Dev	3.26

**Gaus mean : 37.9692**

**Gaus Width: 3.5646**



# B1L003S, U22-ch59, adc2

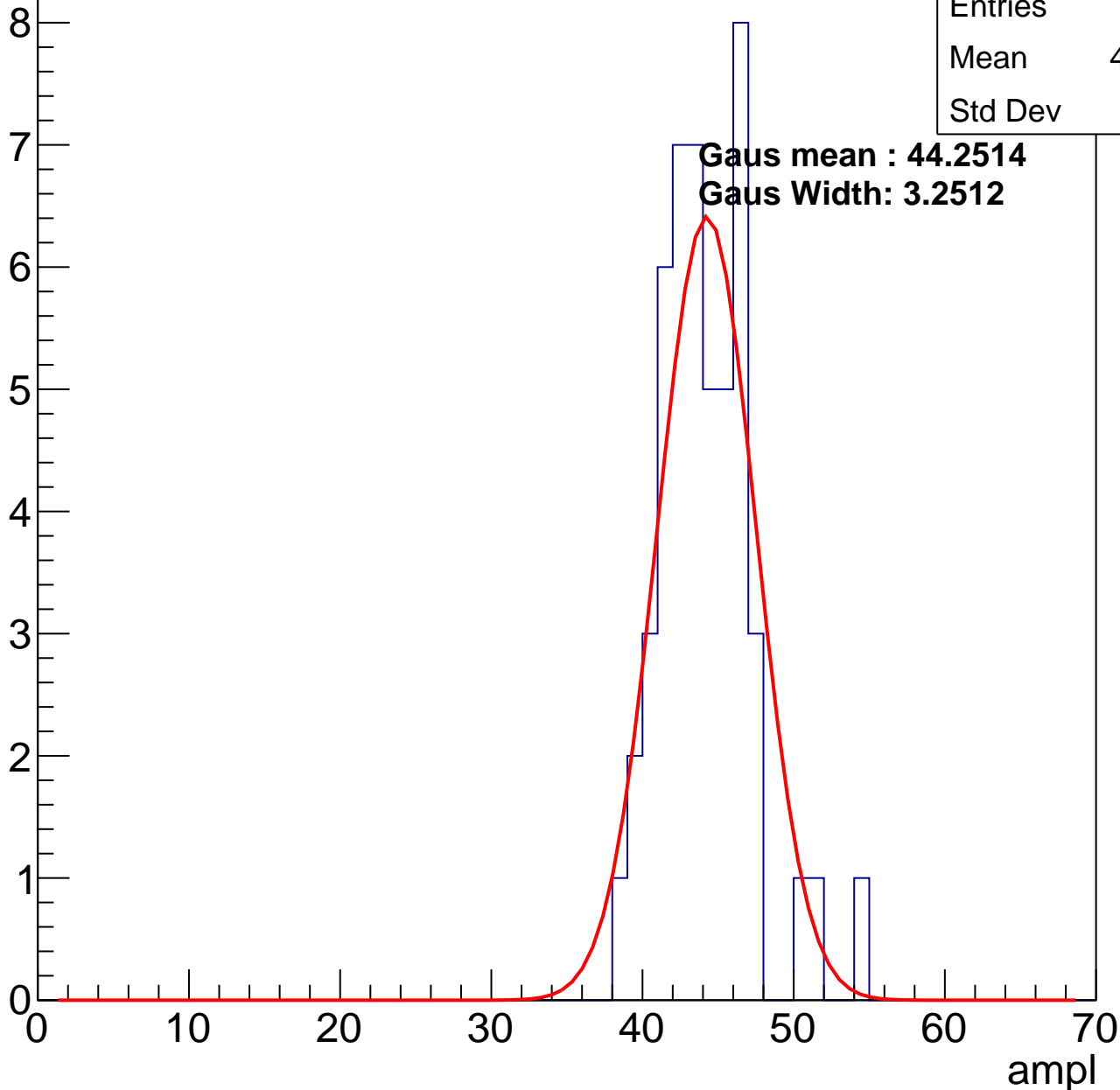
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.72
Std Dev	3.06

**Gaus mean : 44.2514**

**Gaus Width: 3.2512**

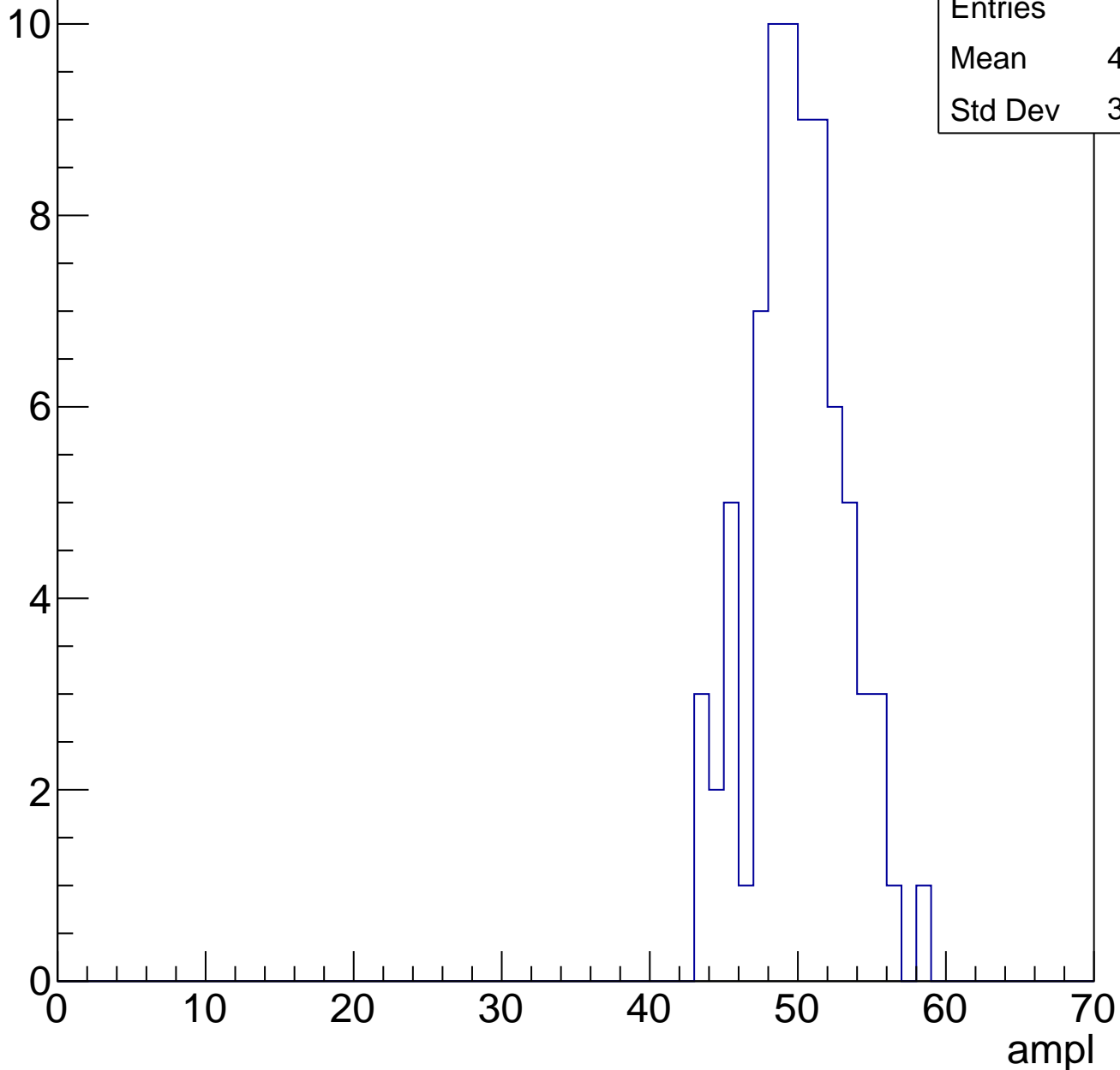


# B1L003S, U22-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	49.52
Std Dev	3.176

Entry



# B1L003S, U22-ch59, adc4

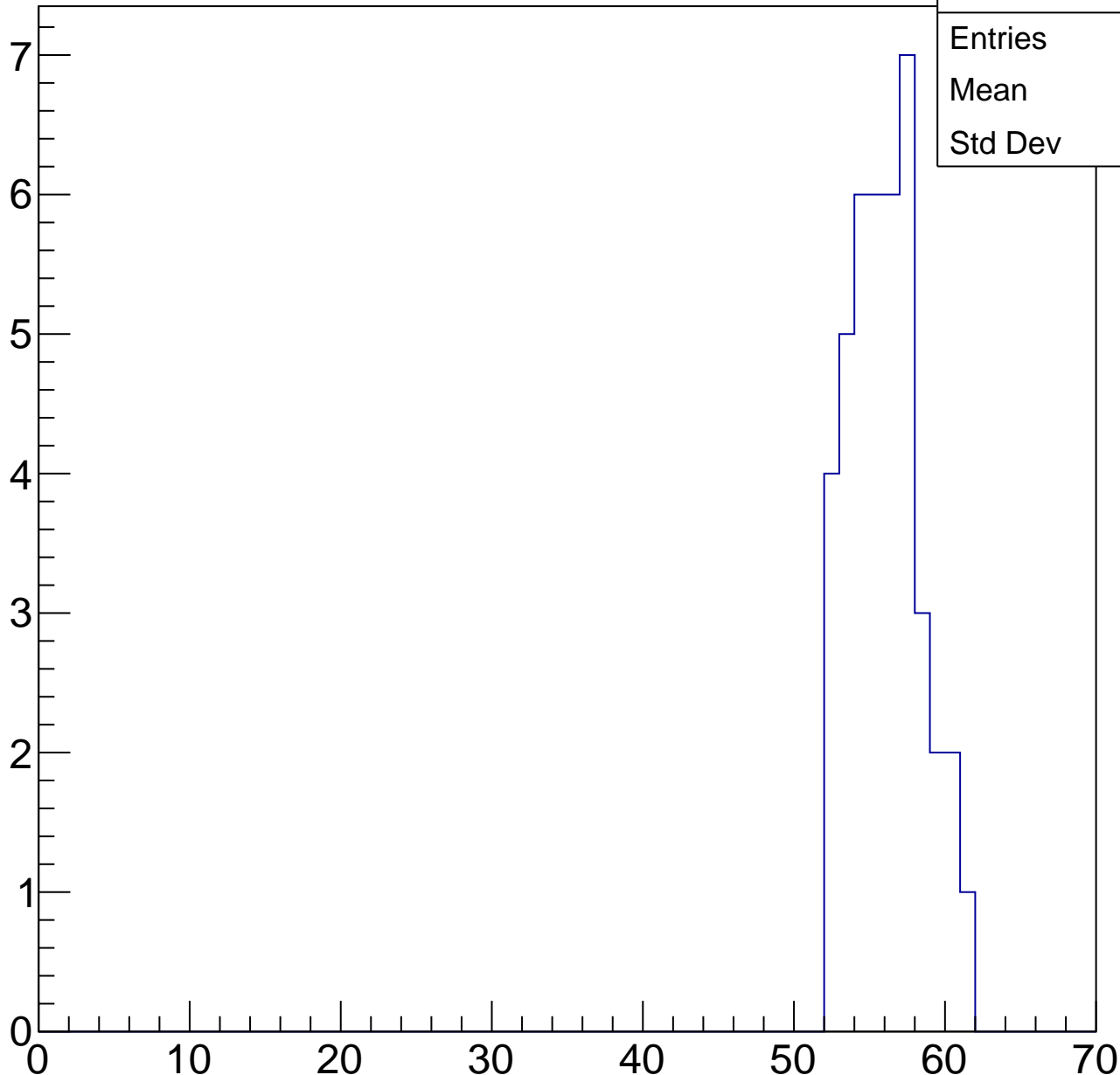
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	55.6
Std Dev	2.32

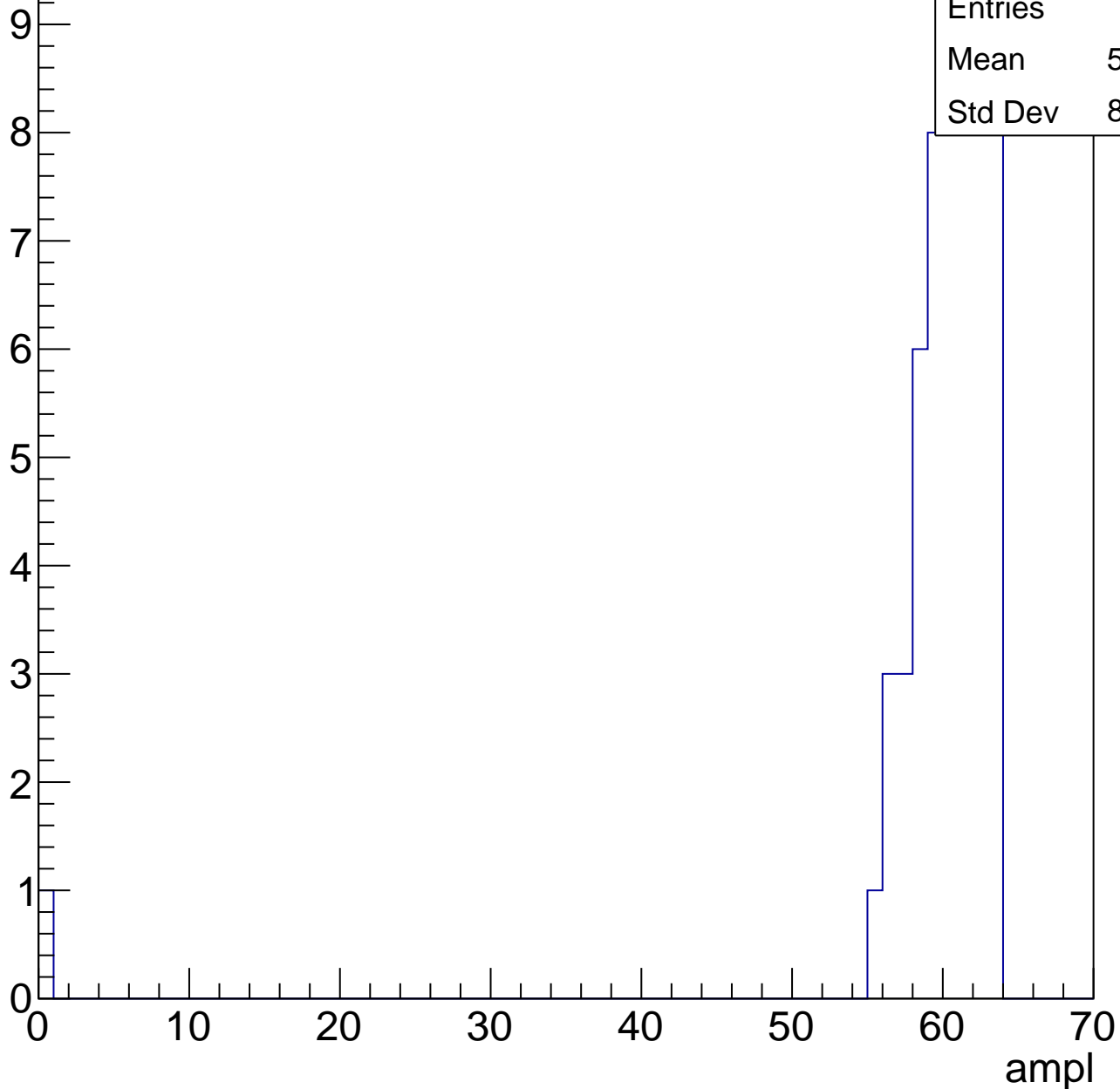
ampl



# B1L003S, U22-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	27.88
Std Dev	4.879

**Gaus mean : 28.9217**

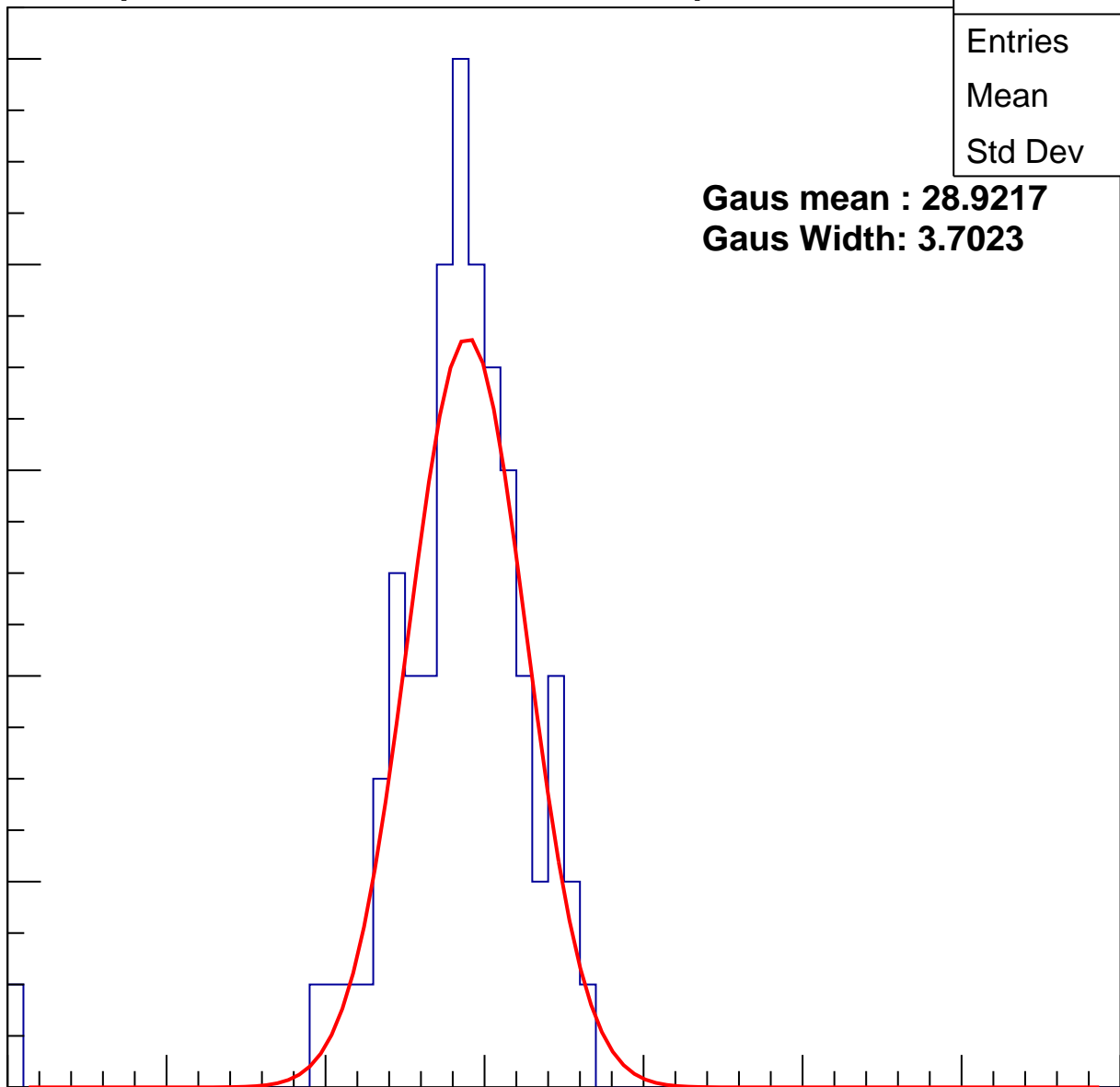
**Gaus Width: 3.7023**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch60, adc1

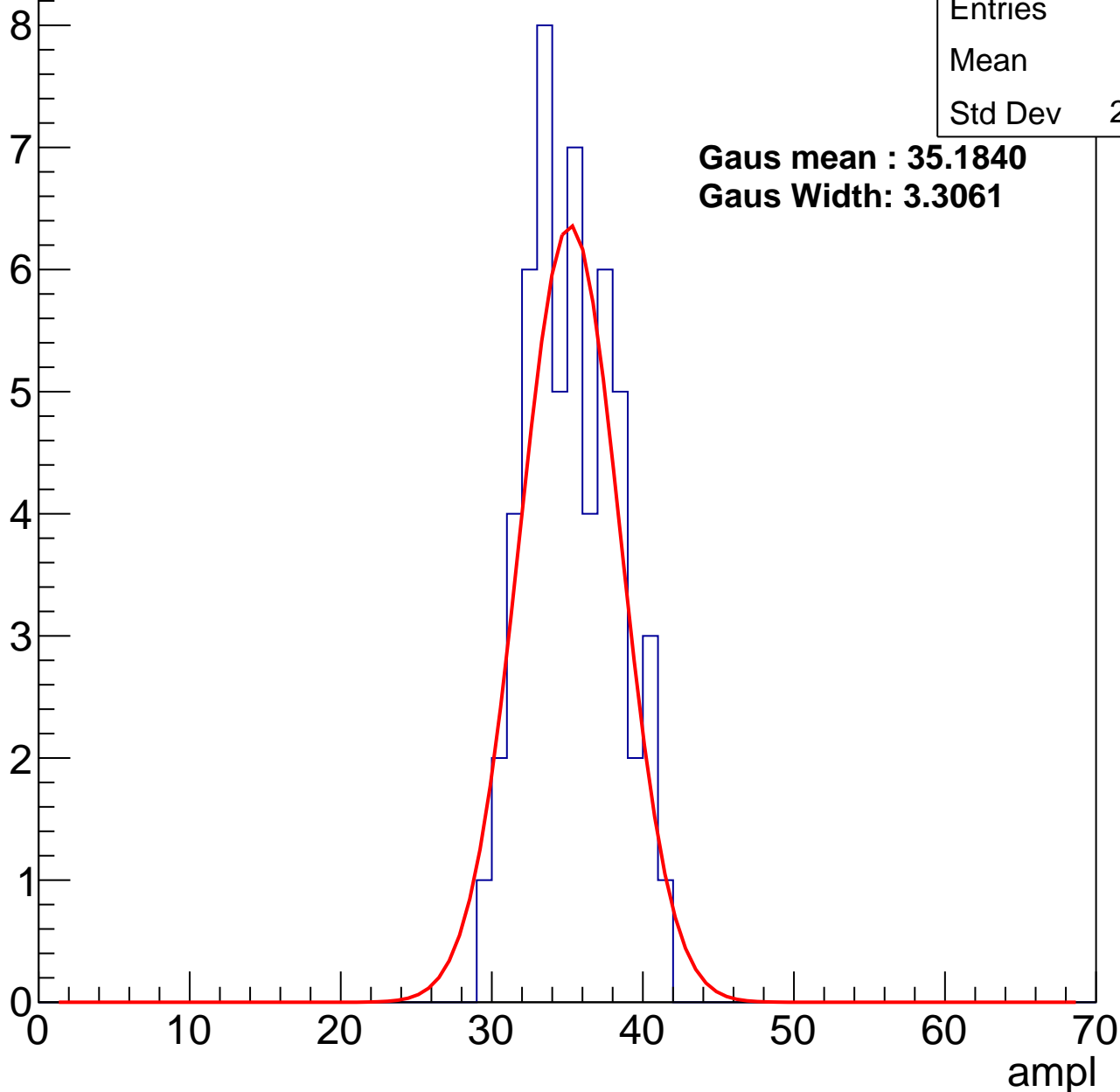
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	34.8
Std Dev	2.902

**Gaus mean : 35.1840**

**Gaus Width: 3.3061**



# B1L003S, U22-ch60, adc2

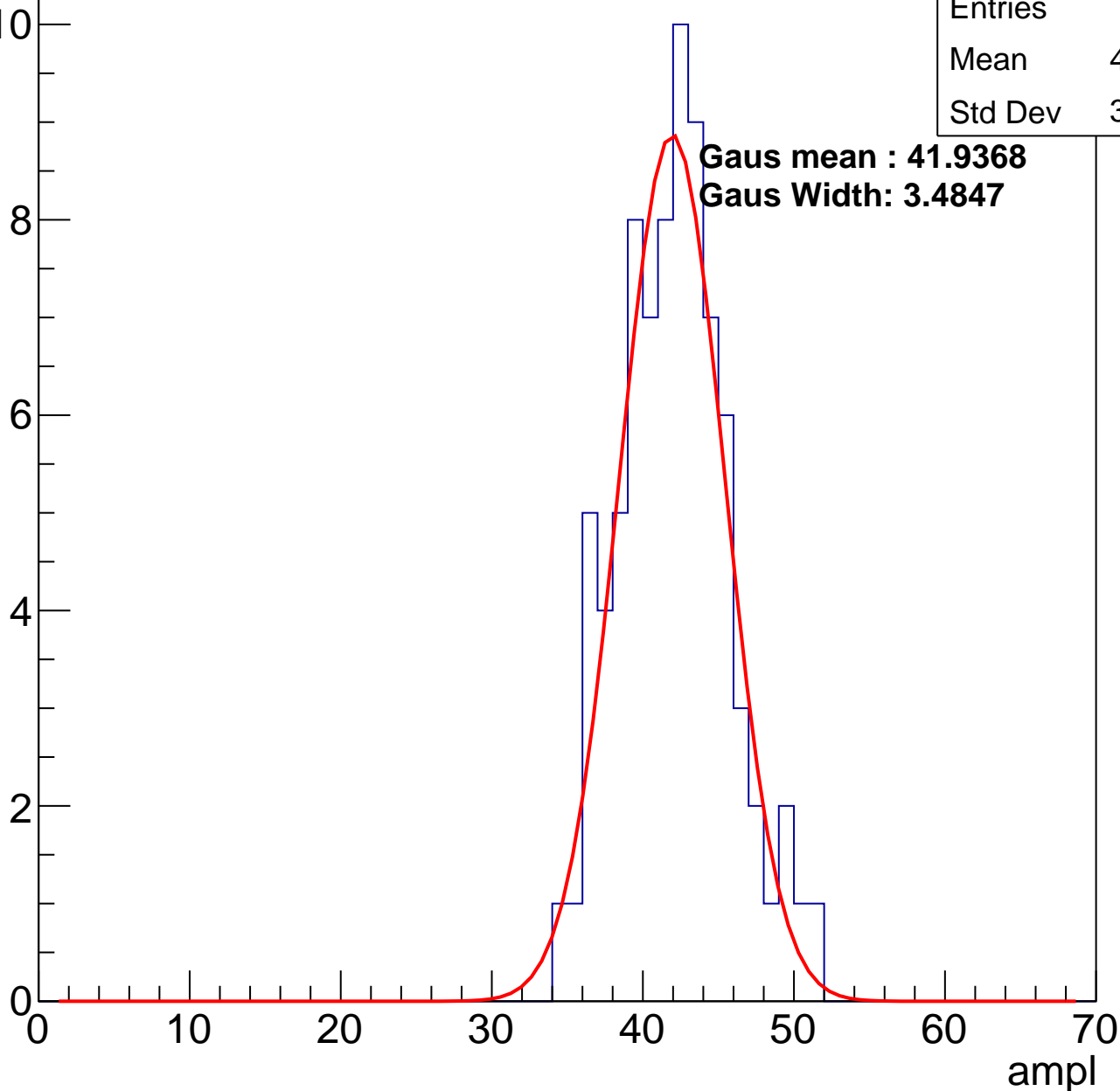
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	41.62
Std Dev	3.578

**Gaus mean : 41.9368**

**Gaus Width: 3.4847**

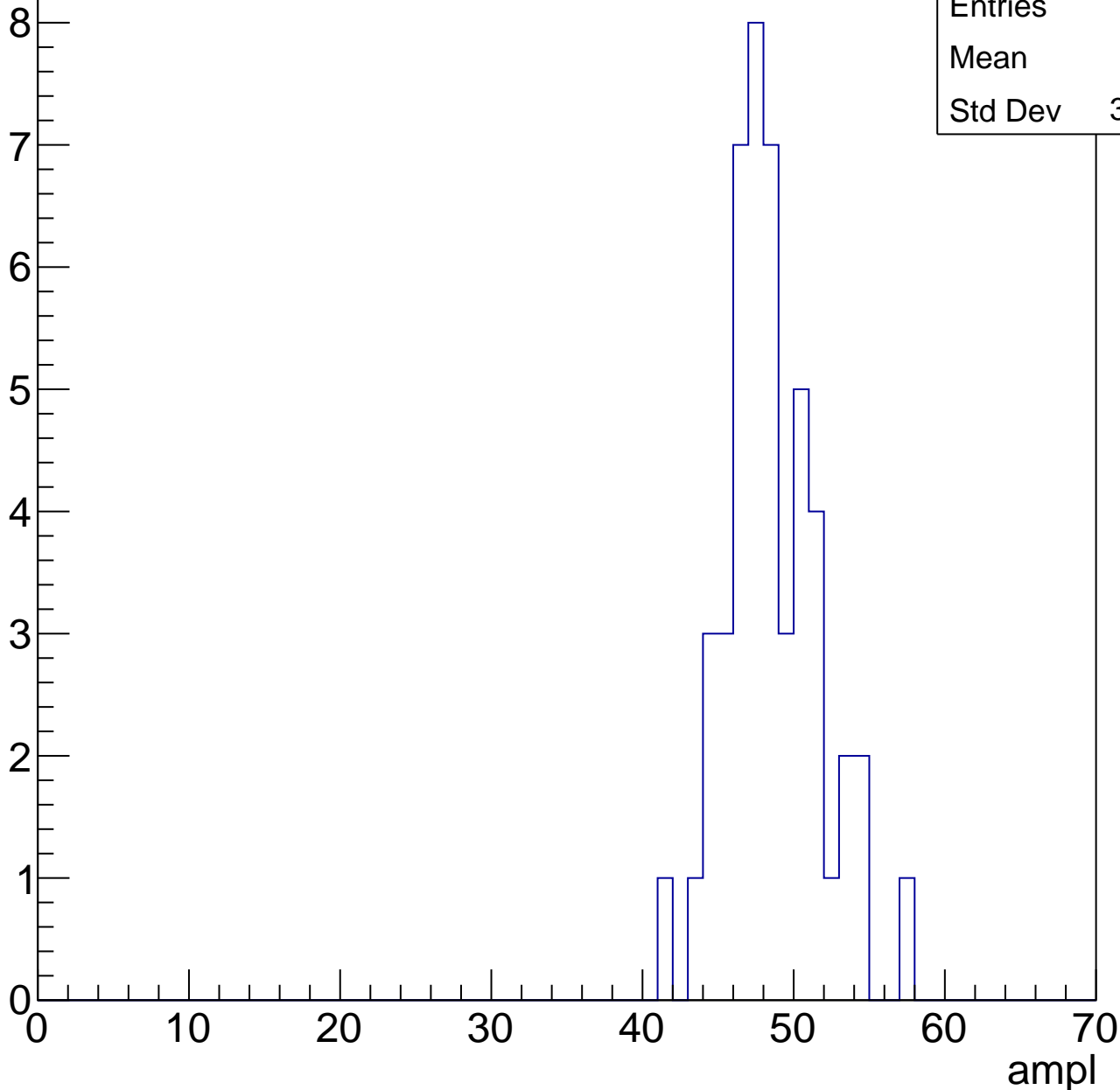


# B1L003S, U22-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	48.1
Std Dev	3.104

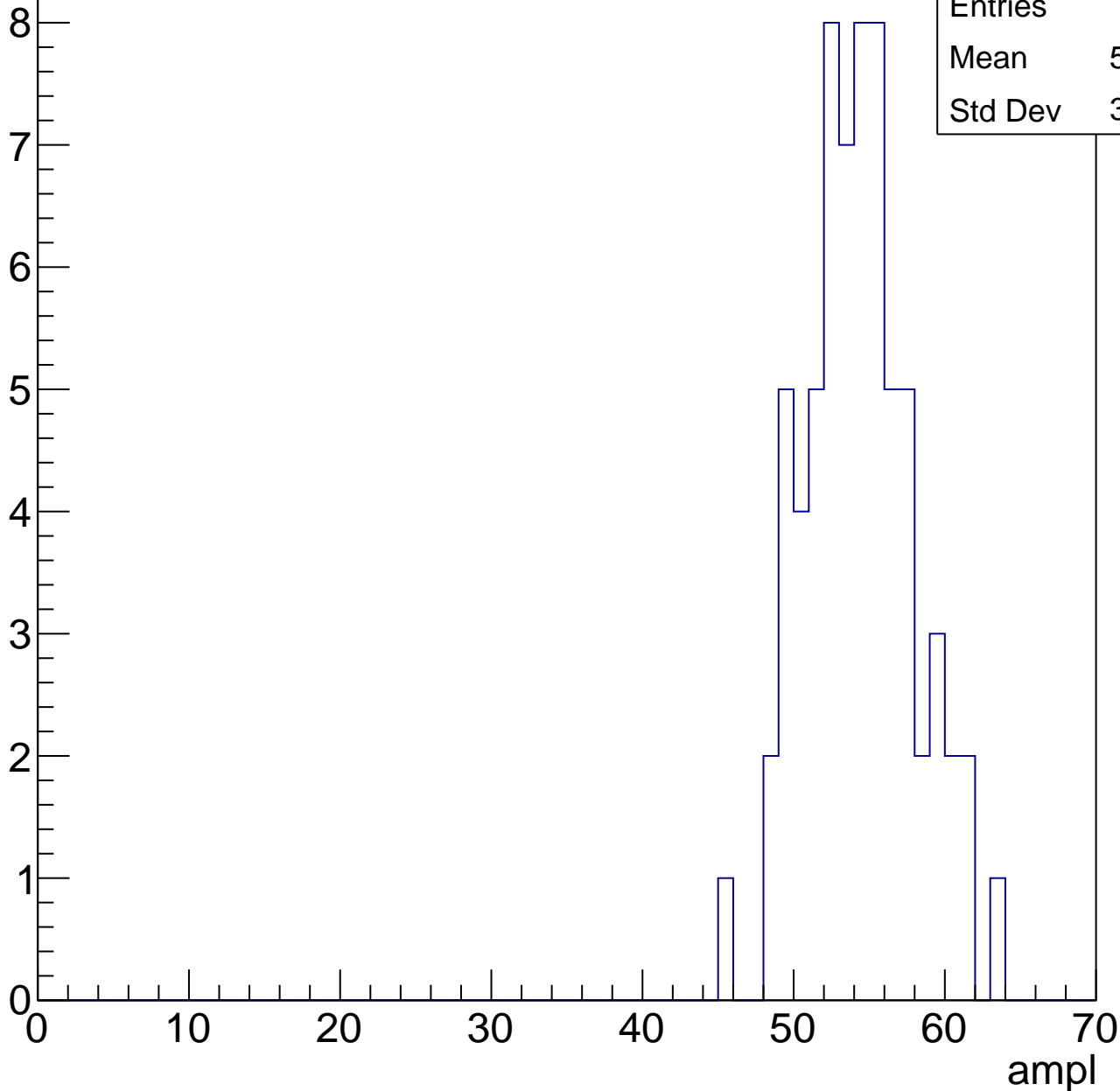


# B1L003S, U22-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	53.87
Std Dev	3.548



# B1L003S, U22-ch60, adc5

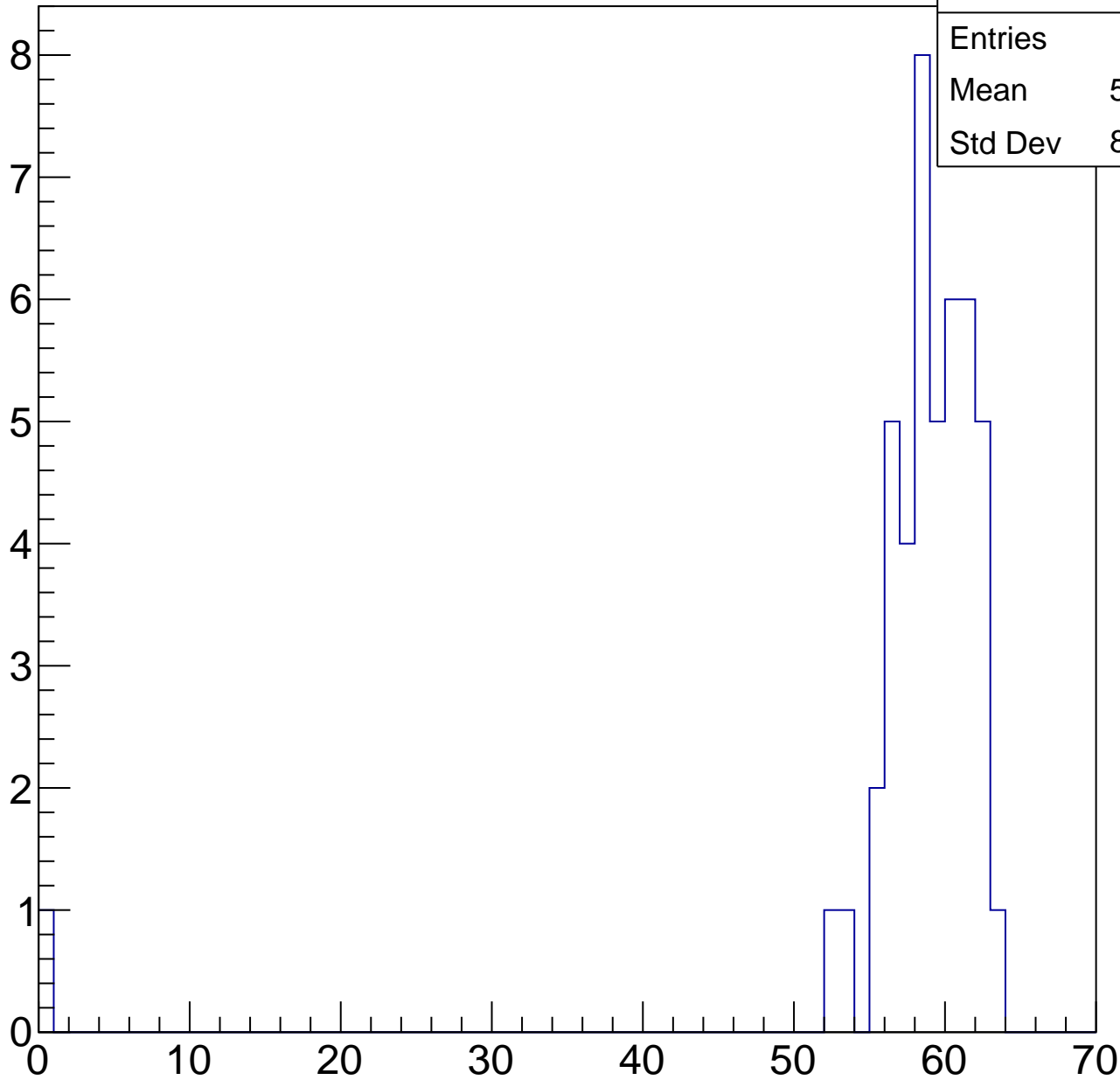
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	57.36
Std Dev	8.989

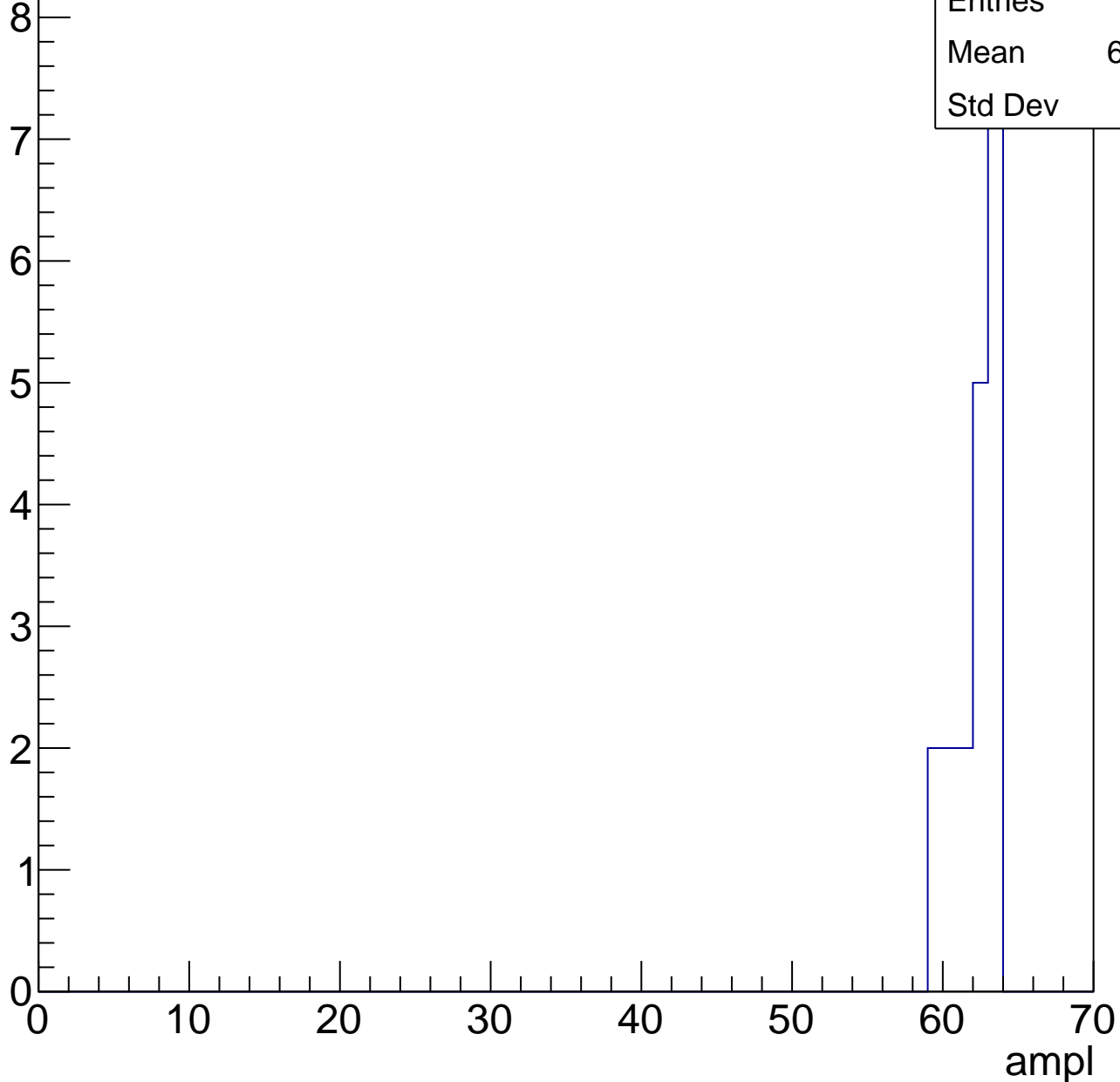
ampl



# B1L003S, U22-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch61, adc0

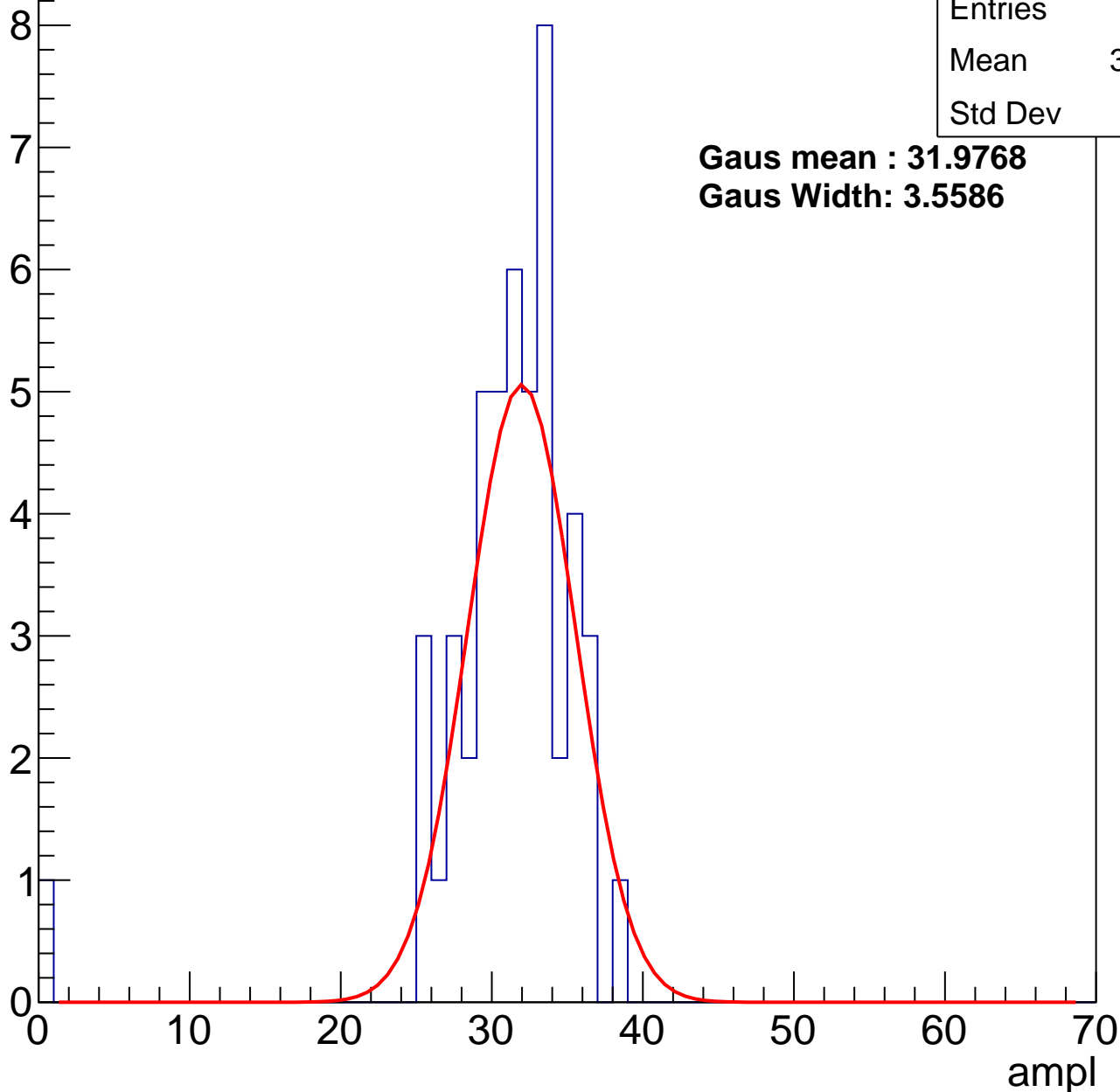
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	30.55
Std Dev	5.38

**Gaus mean : 31.9768**

**Gaus Width: 3.5586**

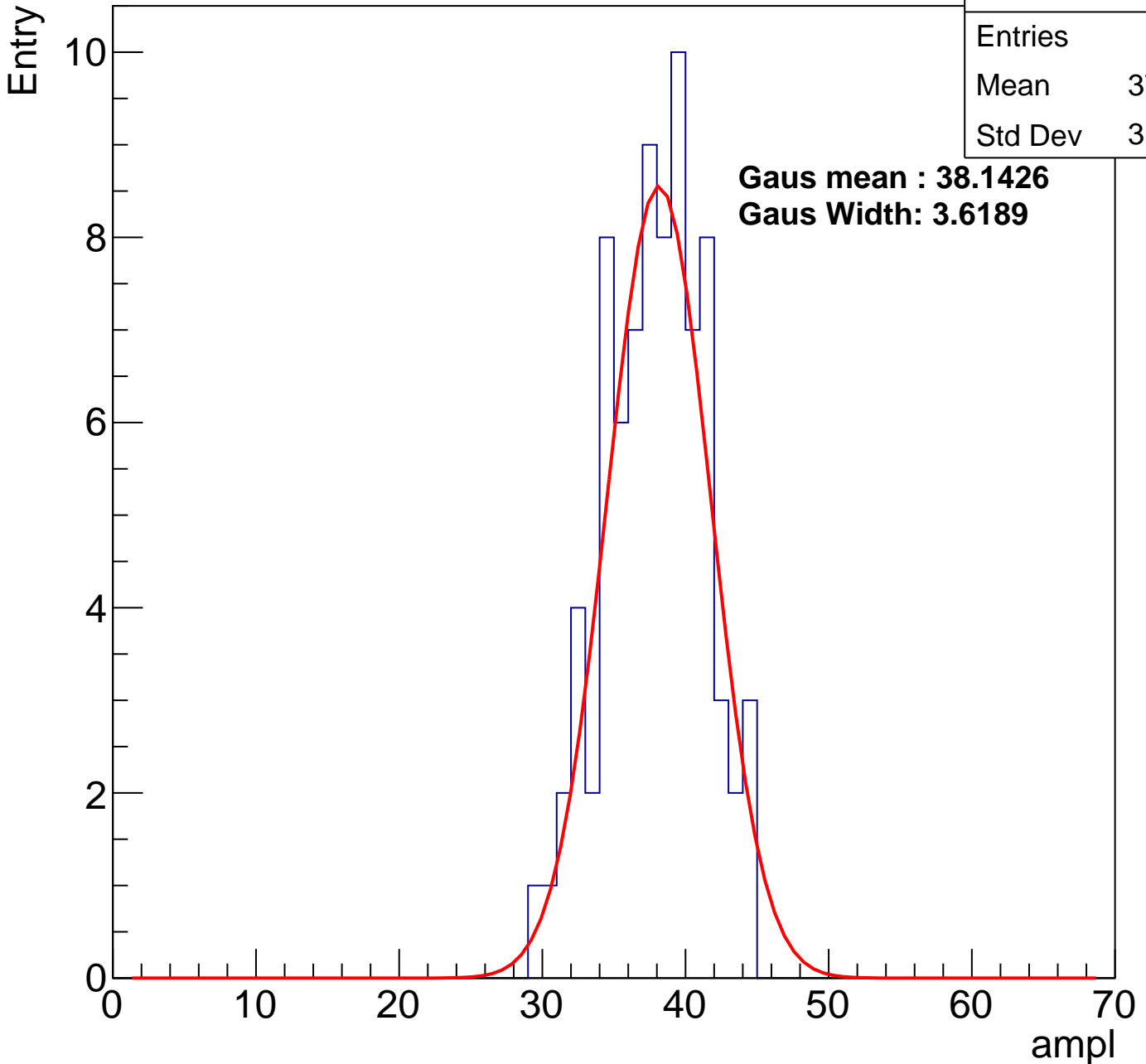


# B1L003S, U22-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	81
Mean	37.38
Std Dev	3.409

**Gaus mean : 38.1426**  
**Gaus Width: 3.6189**



# B1L003S, U22-ch61, adc2

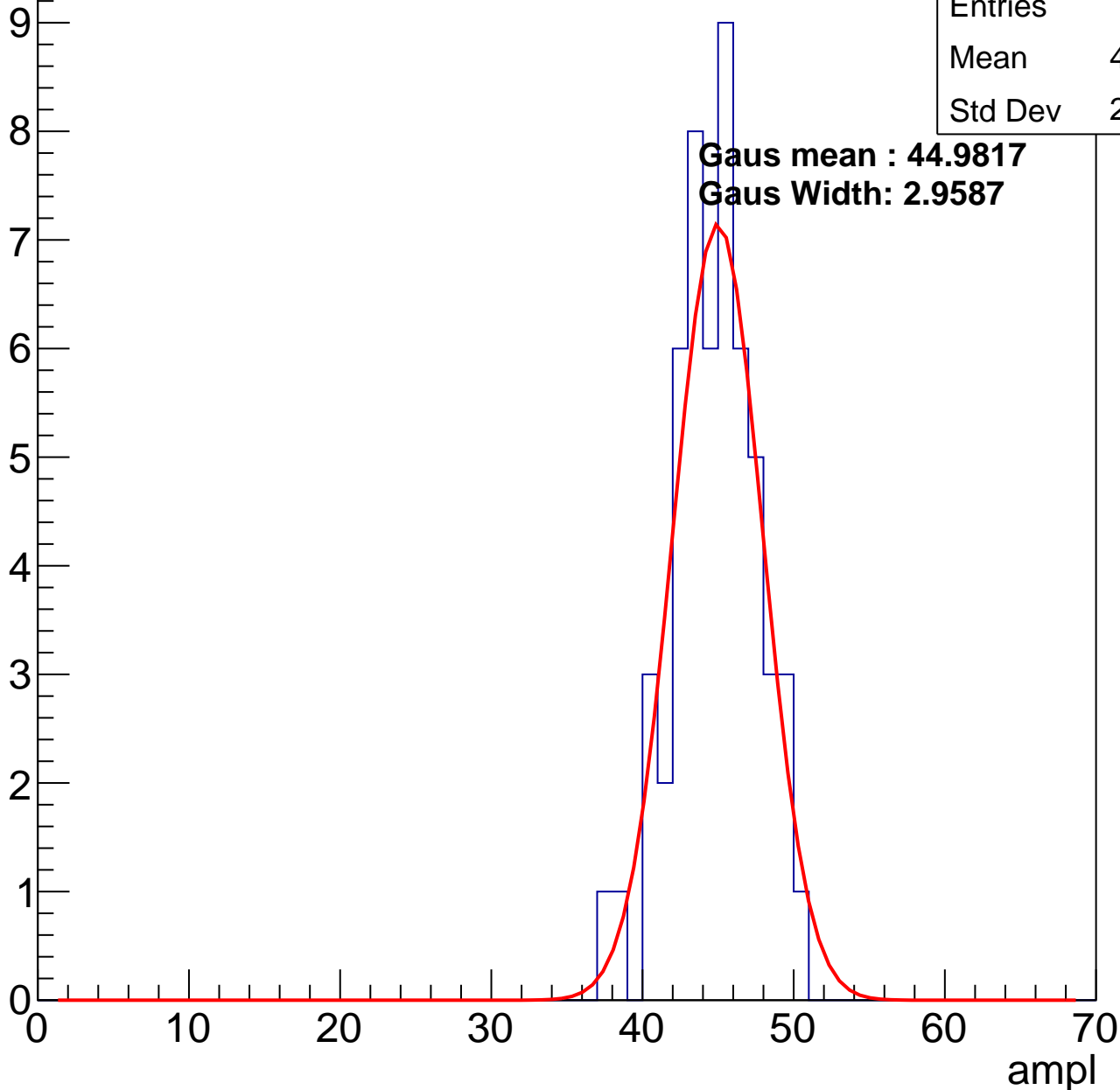
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	44.33
Std Dev	2.769

**Gaus mean : 44.9817**

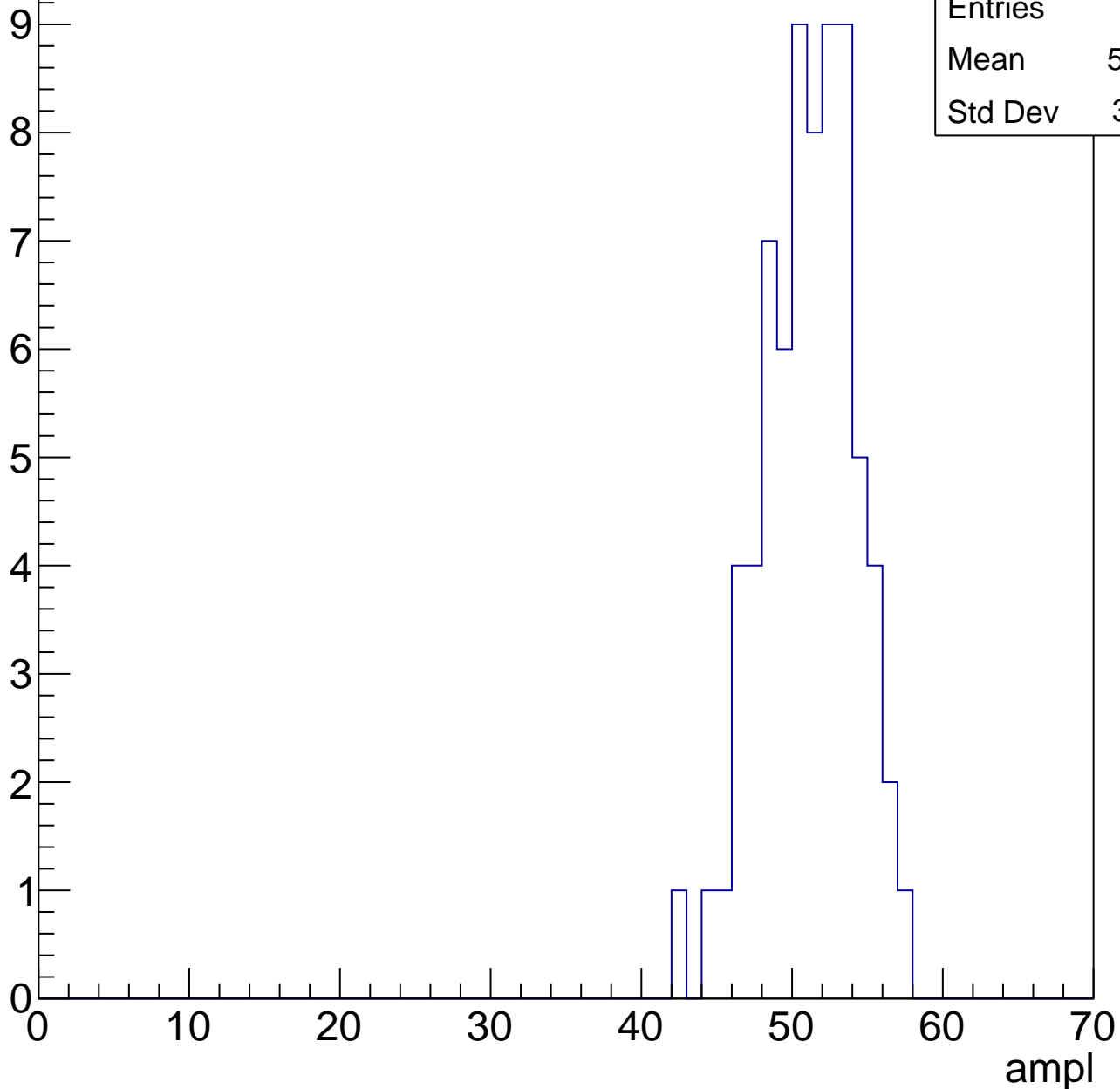
**Gaus Width: 2.9587**



# B1L003S, U22-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



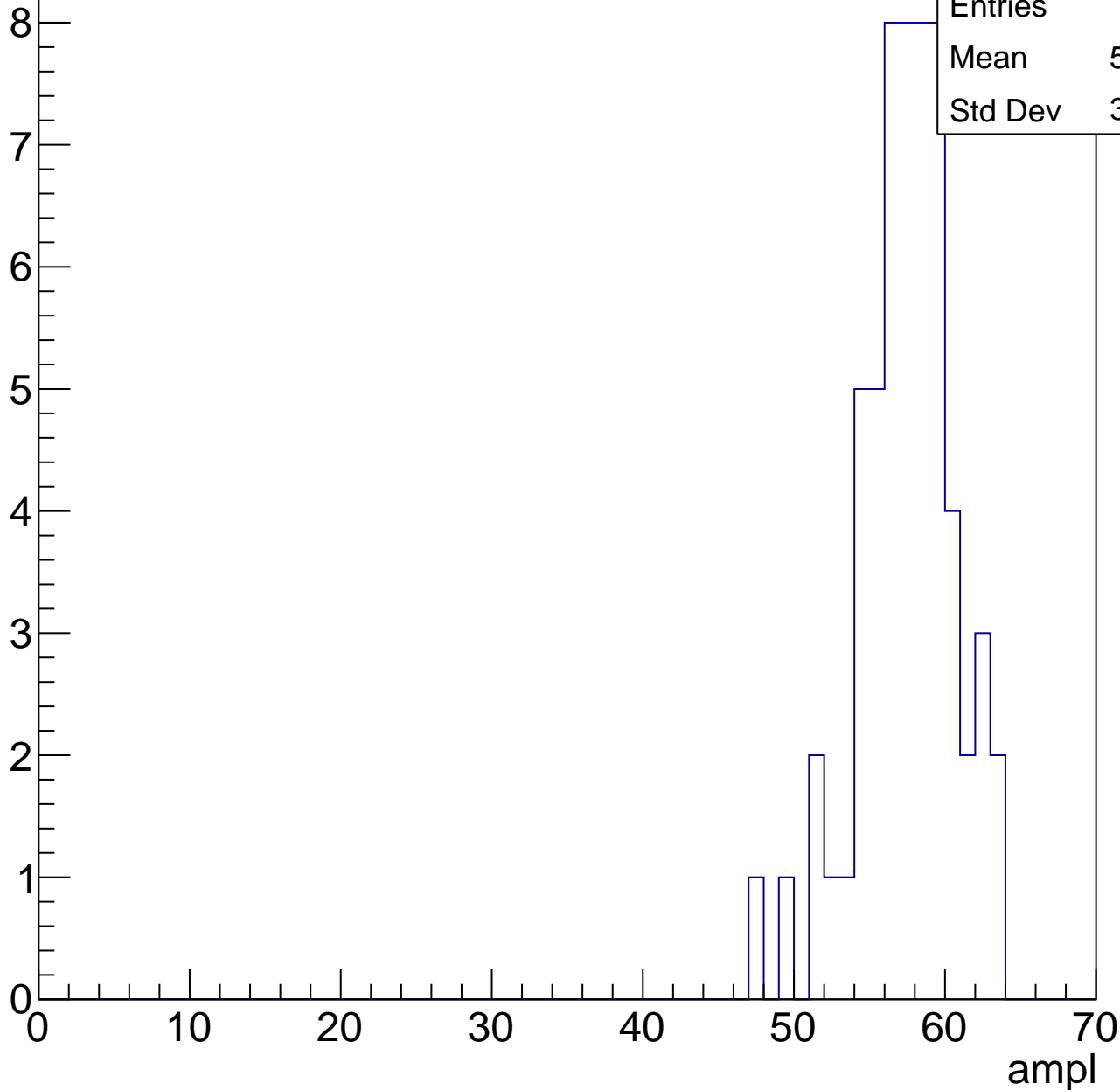
Entries	71
Mean	50.63
Std Dev	3.041

# B1L003S, U22-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.98
Std Dev	3.202

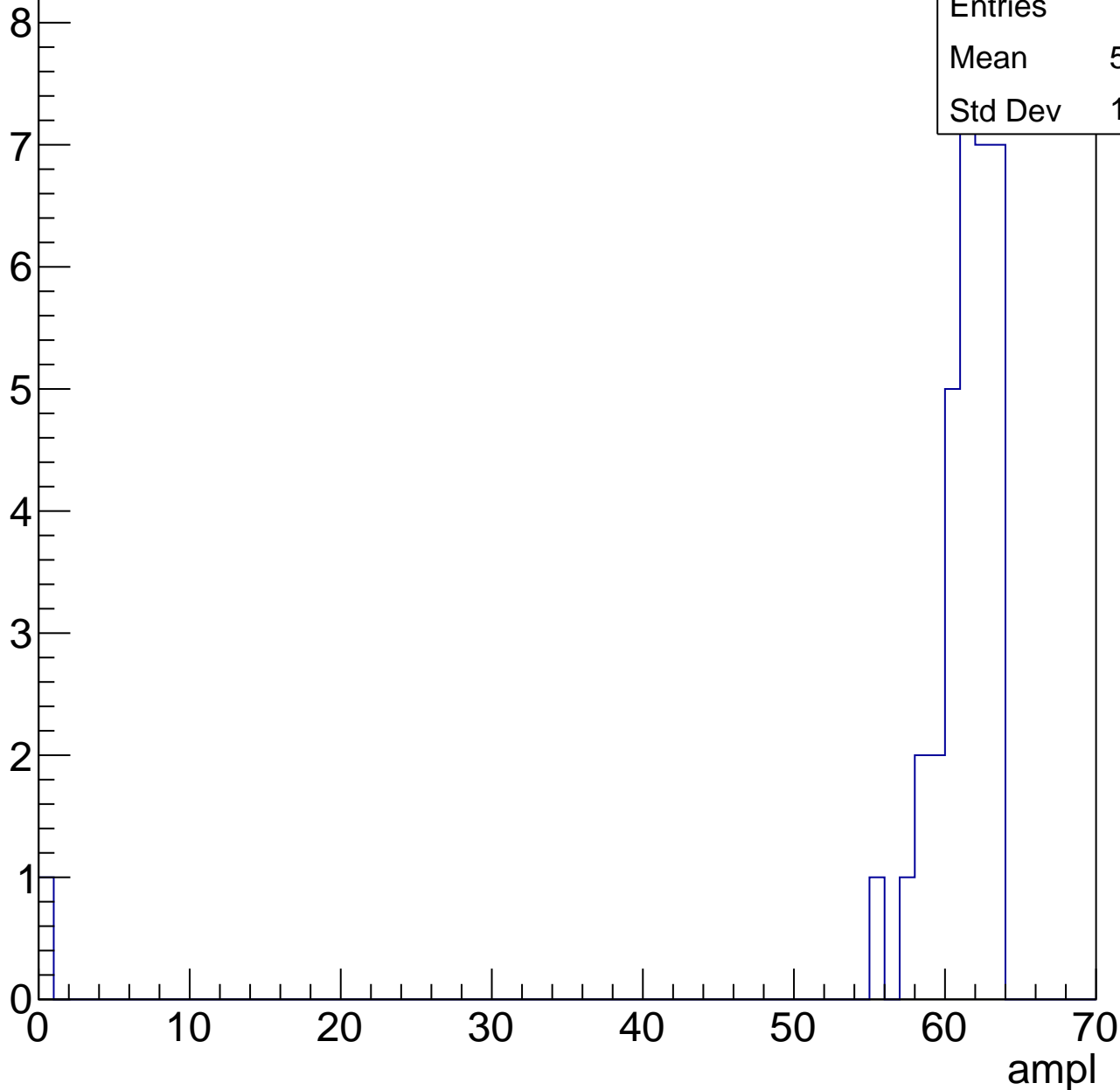


# B1L003S, U22-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	34
Mean	59.09
Std Dev	10.45



# B1L003S, U22-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl

# B1L003S, U22-ch62, adc0

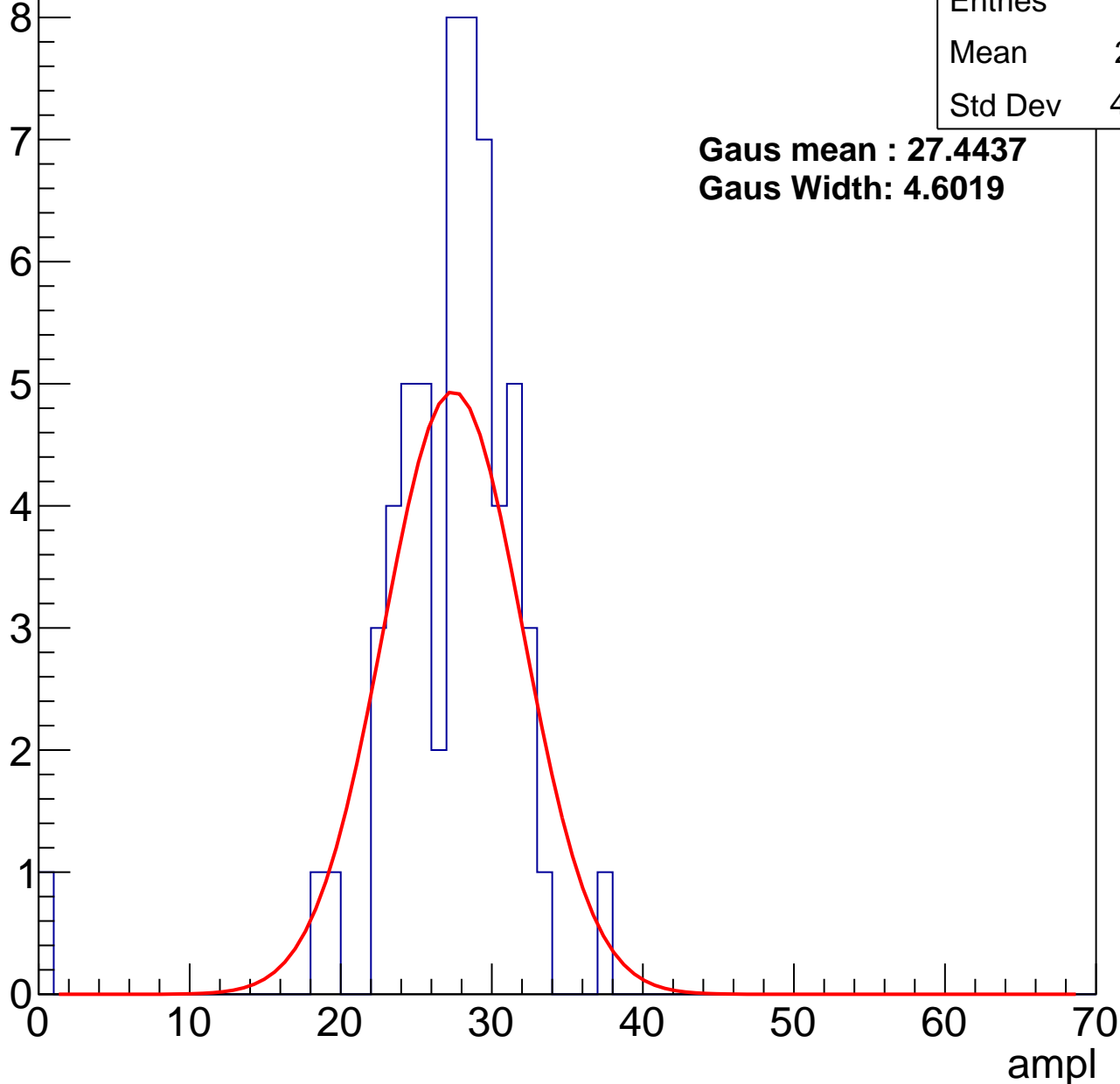
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	26.71
Std Dev	4.937

**Gaus mean : 27.4437**

**Gaus Width: 4.6019**



# B1L003S, U22-ch62, adc1

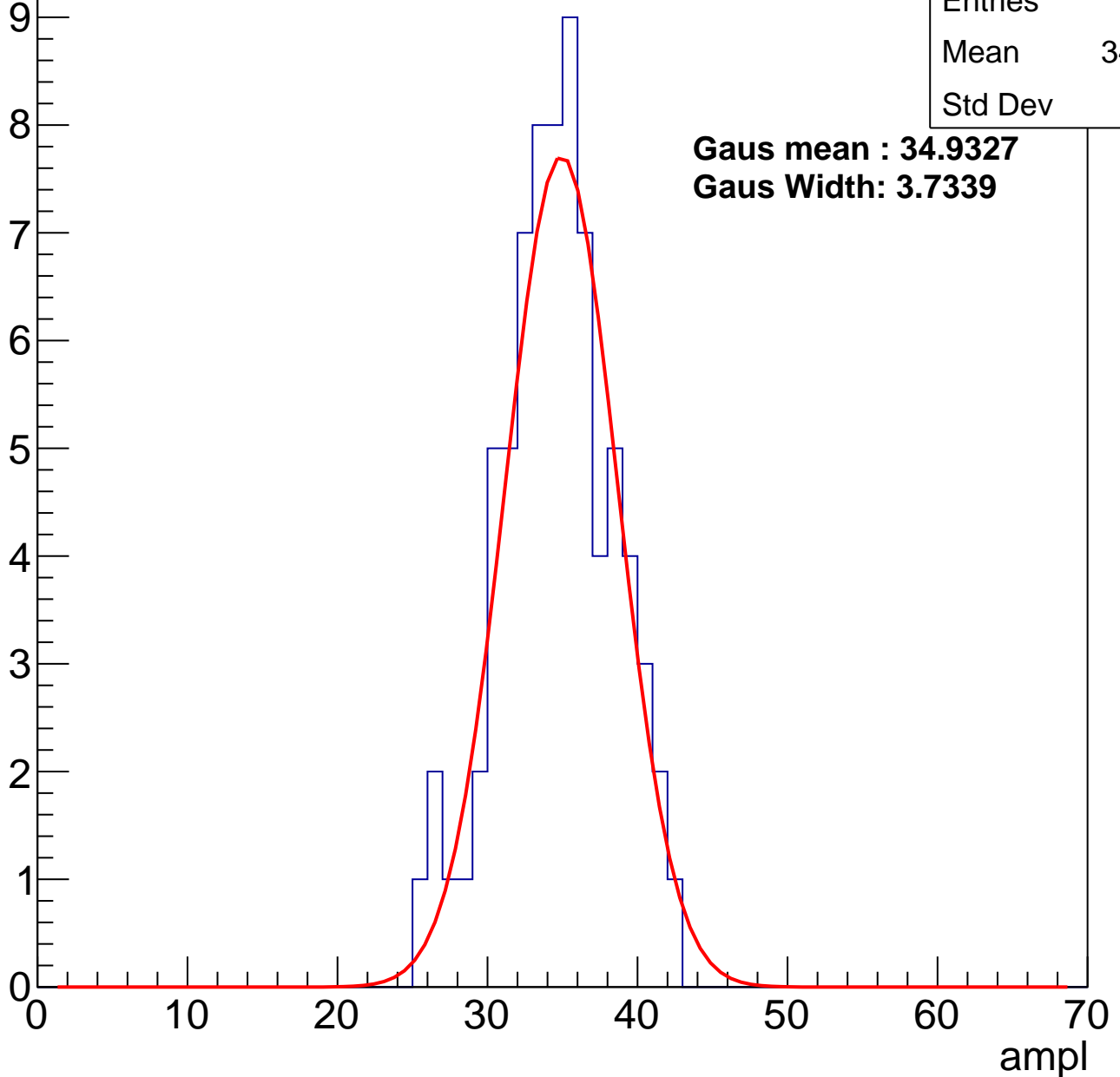
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	34.13
Std Dev	3.7

**Gaus mean : 34.9327**

**Gaus Width: 3.7339**



# B1L003S, U22-ch62, adc2

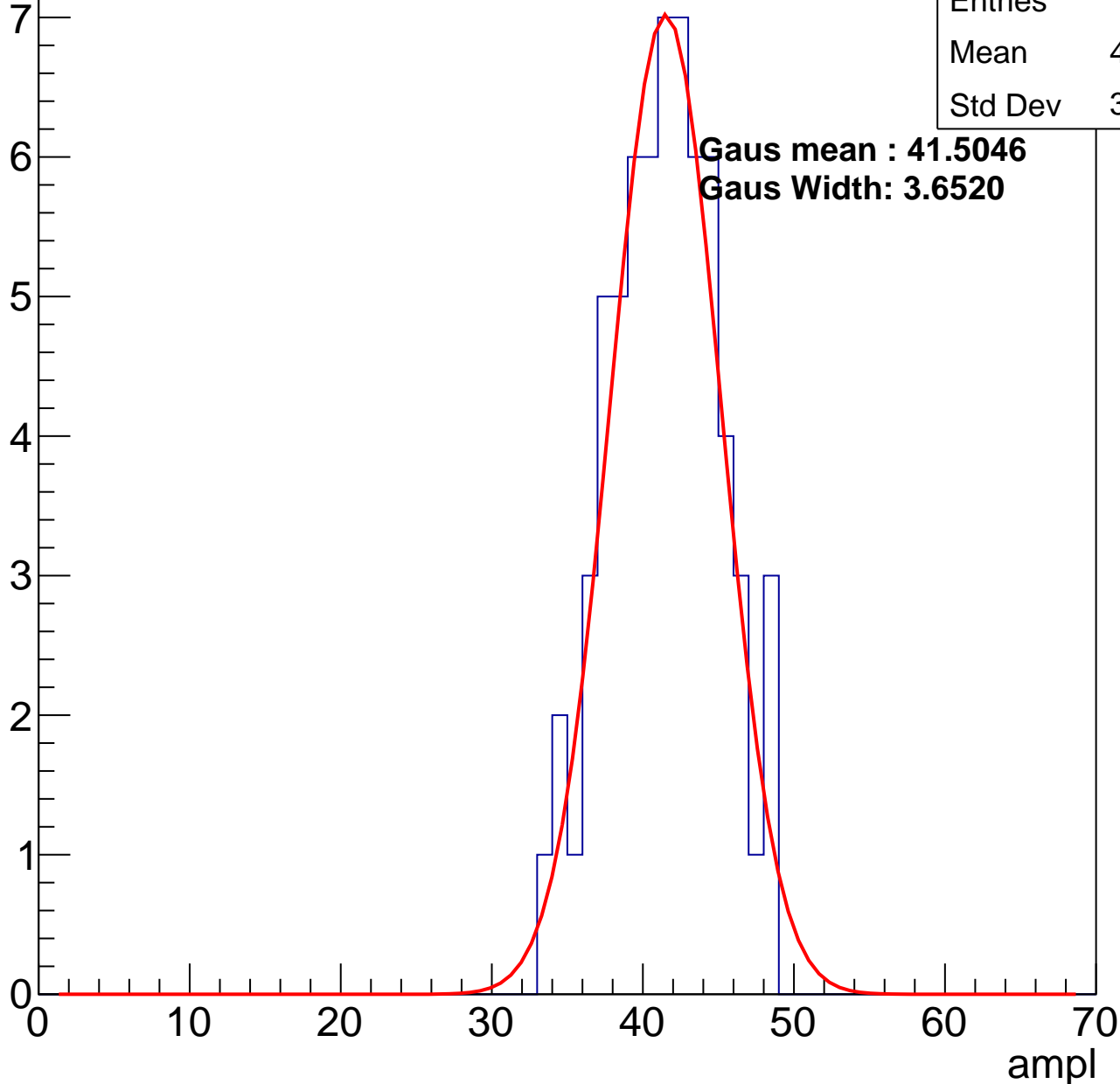
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	40.98
Std Dev	3.557

**Gaus mean : 41.5046**

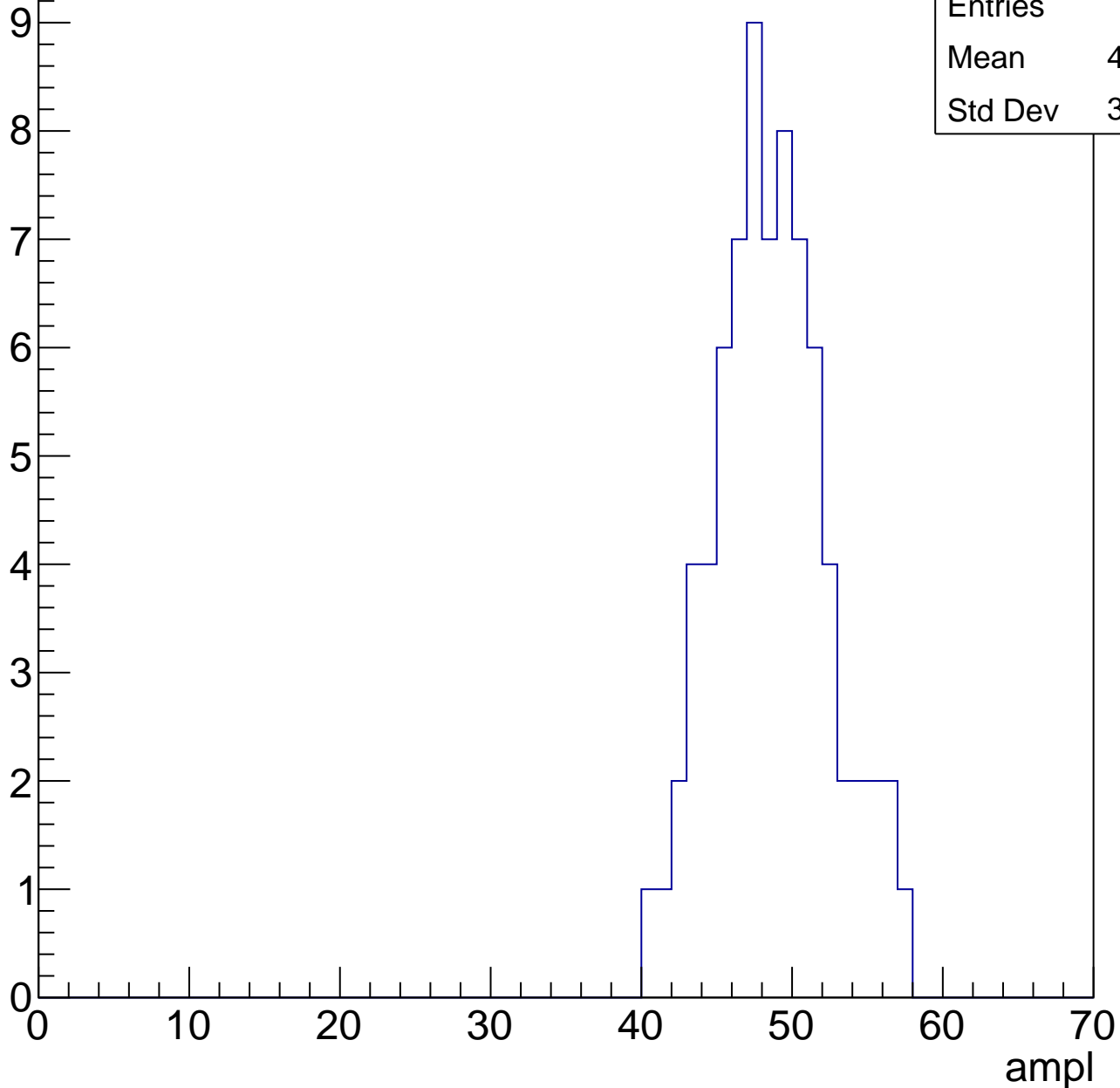
**Gaus Width: 3.6520**



# B1L003S, U22-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

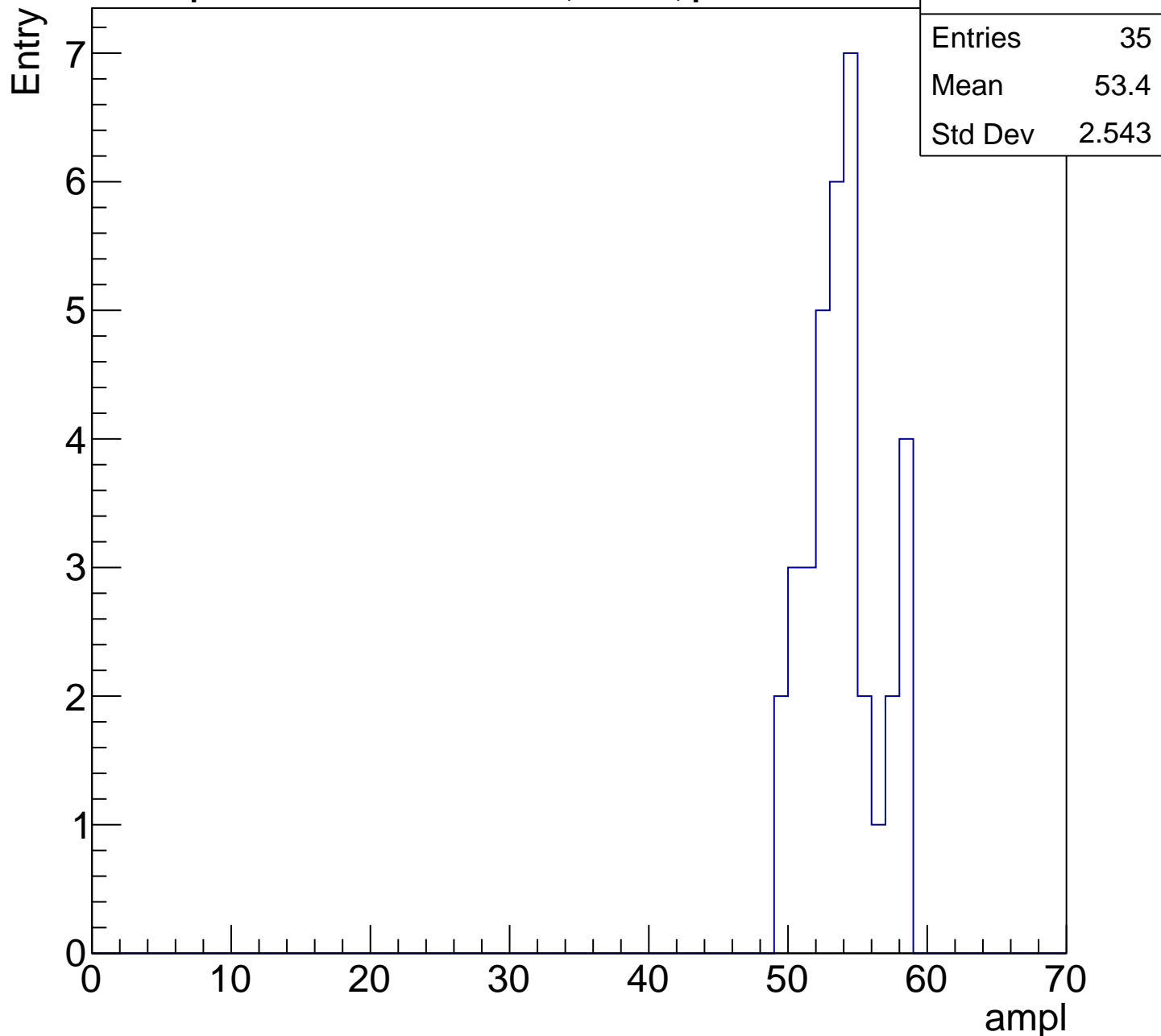
Entry



Entries	75
Mean	48.17
Std Dev	3.689

# B1L003S, U22-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

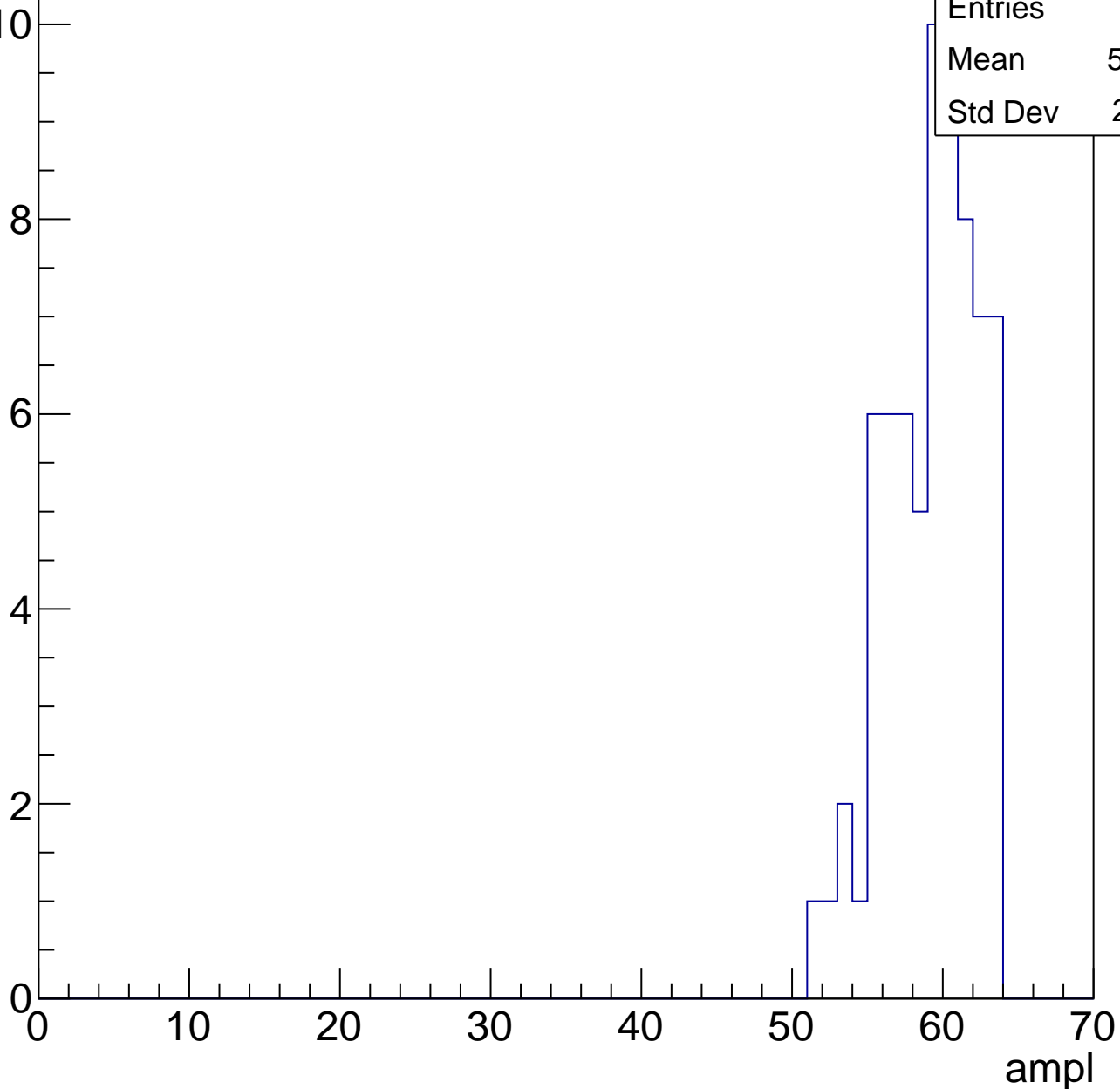


# B1L003S, U22-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	58.75
Std Dev	2.951



# B1L003S, U22-ch62, adc6

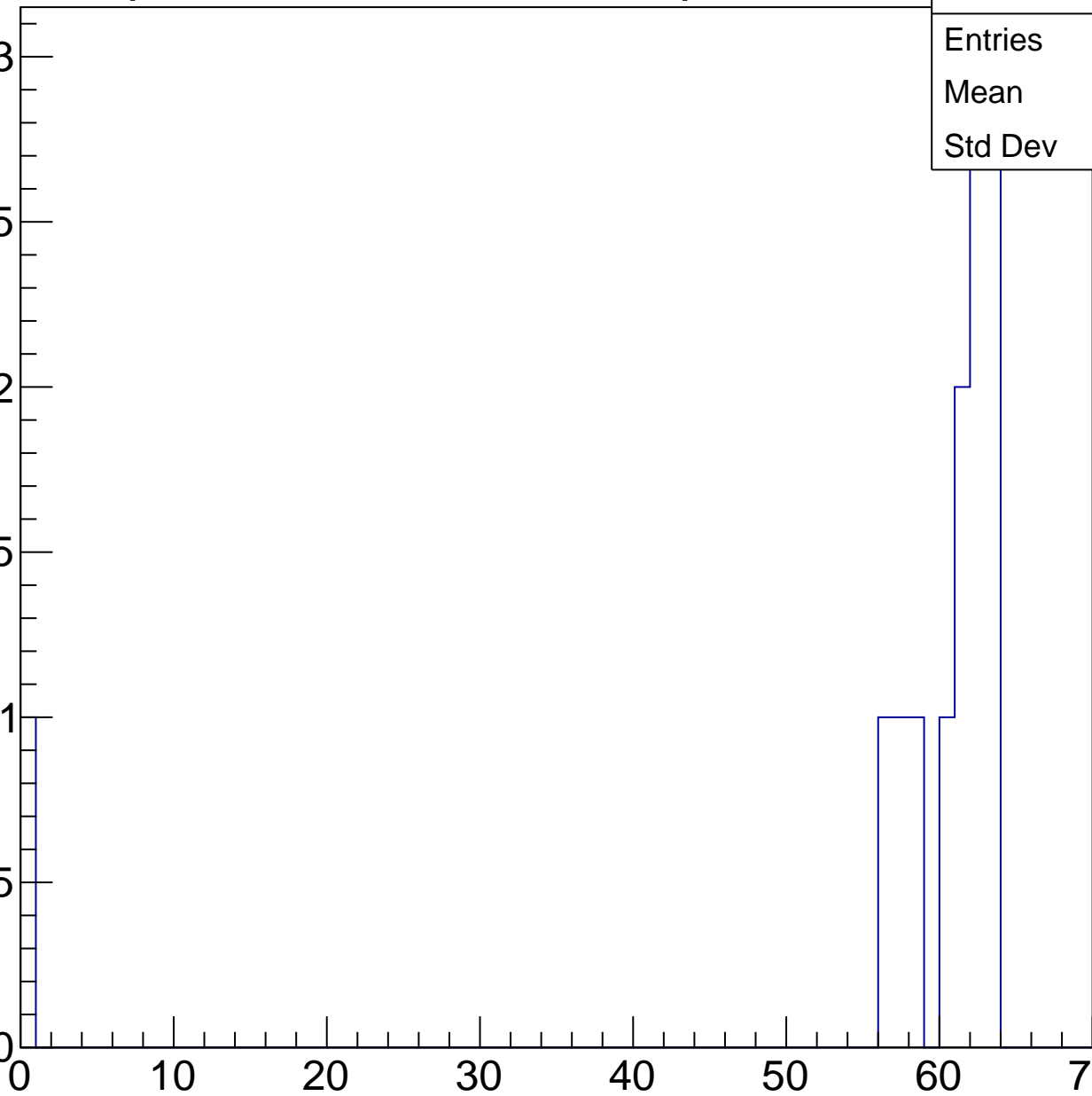
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	13
Mean	56
Std Dev	16.32

ampl





# B1L003S, U22-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch63, adc0

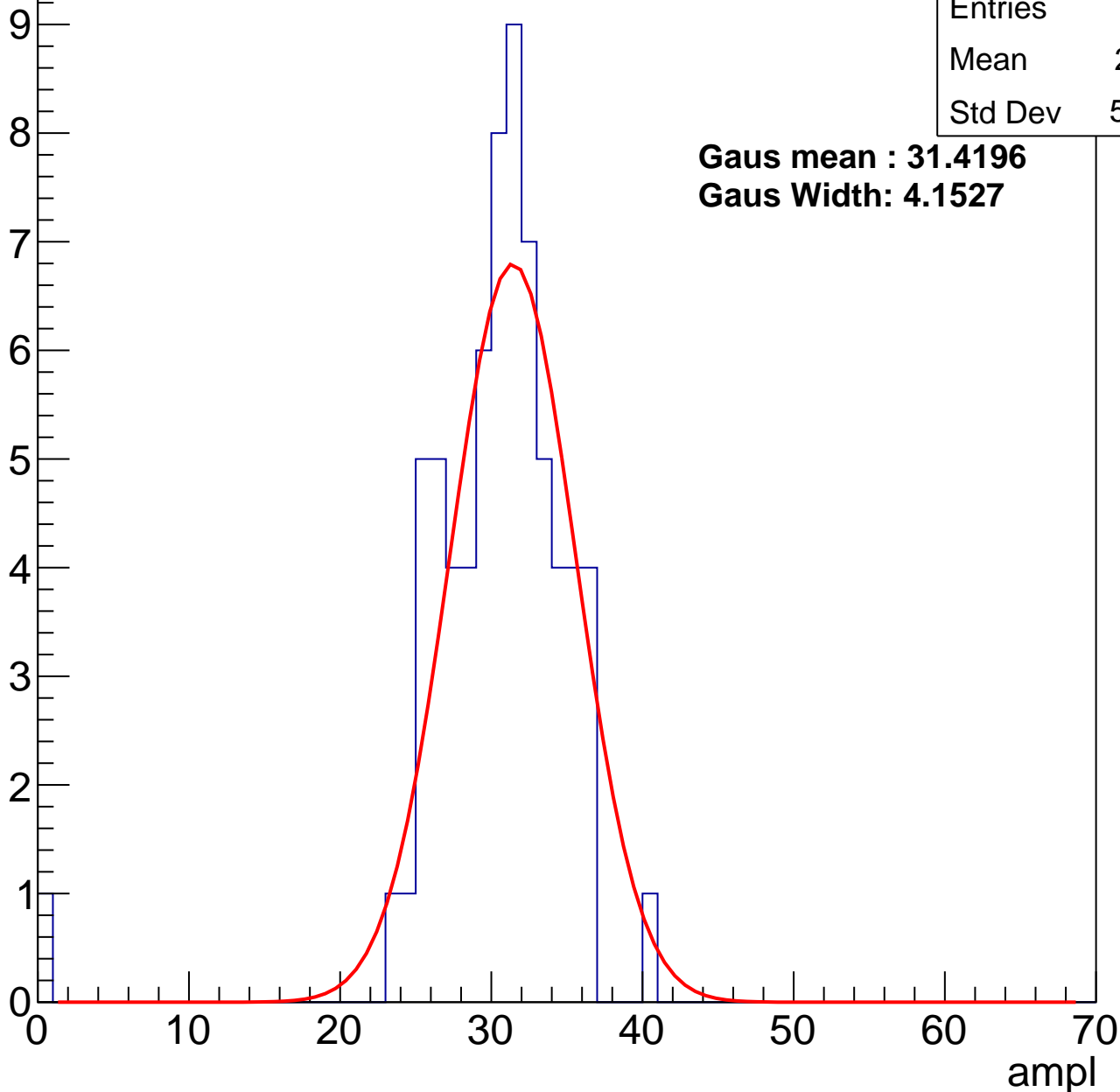
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.91
Std Dev	5.018

**Gaus mean : 31.4196**

**Gaus Width: 4.1527**



# B1L003S, U22-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.88
Std Dev	3.116

**Gaus mean : 37.4920**

**Gaus Width: 3.3884**

10

8

6

4

2

0

0

10

20

30

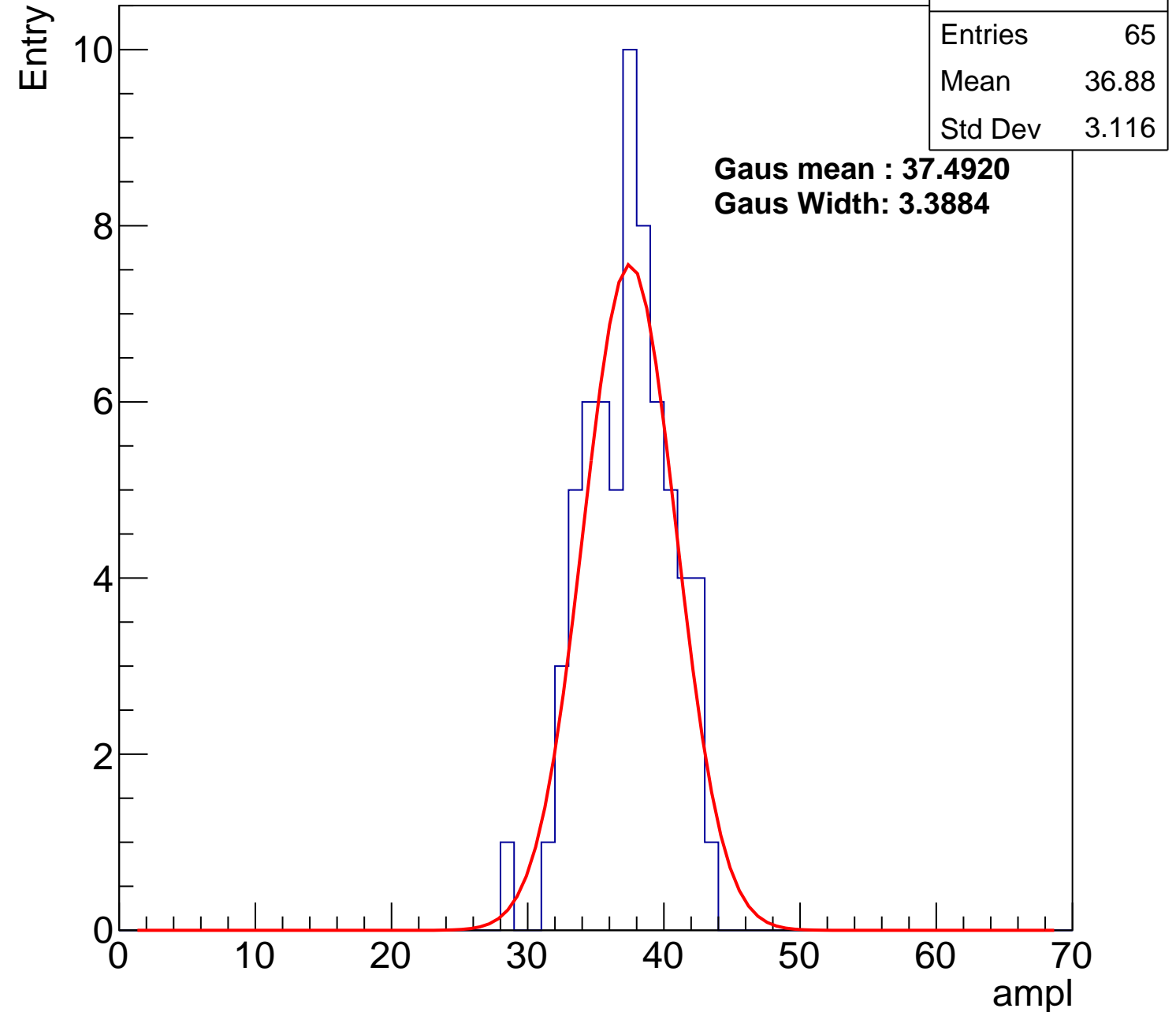
40

50

60

70

ampl



# B1L003S, U22-ch63, adc2

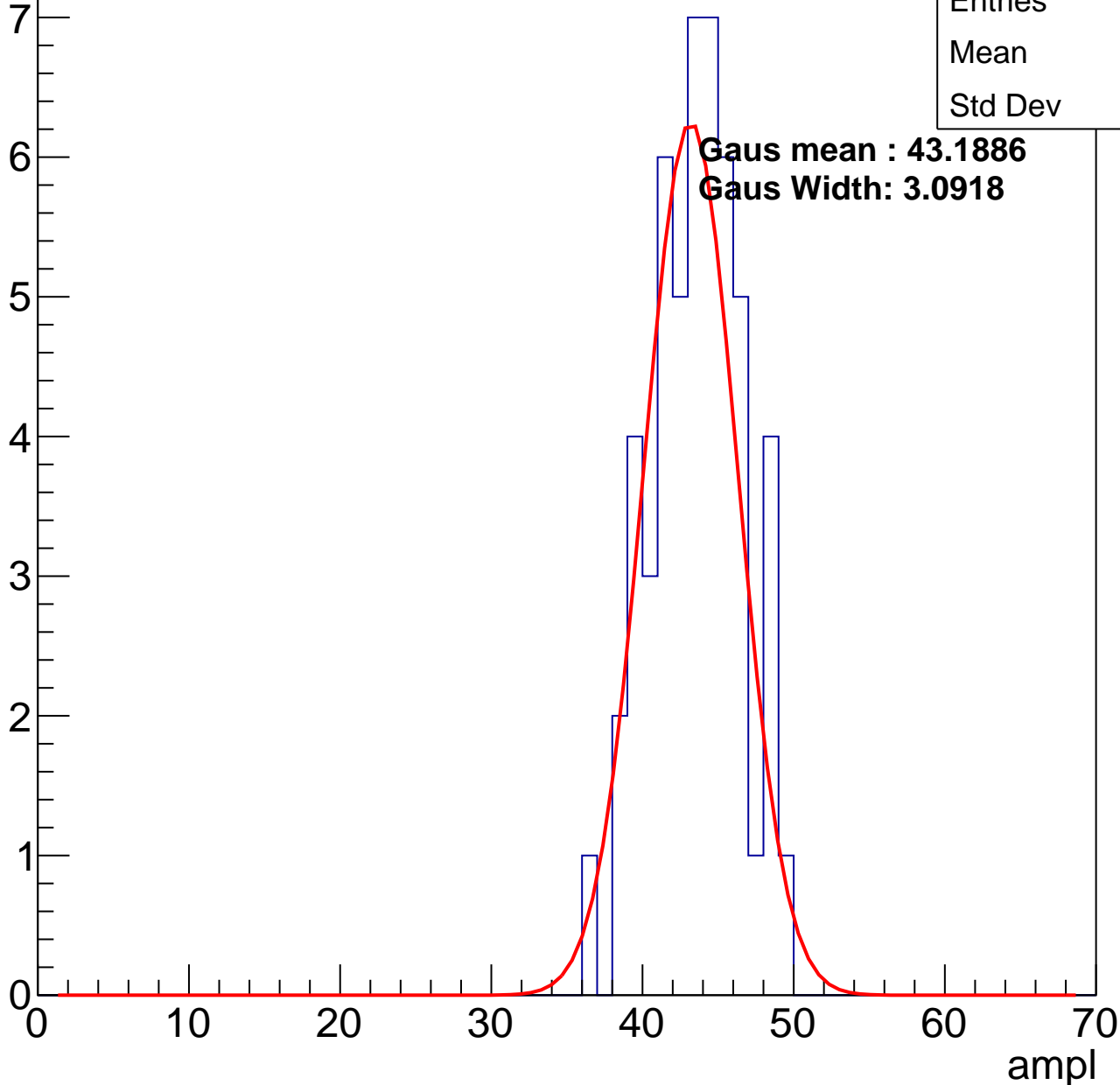
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	43.1
Std Dev	2.93

**Gaus mean : 43.1886**

**Gaus Width: 3.0918**

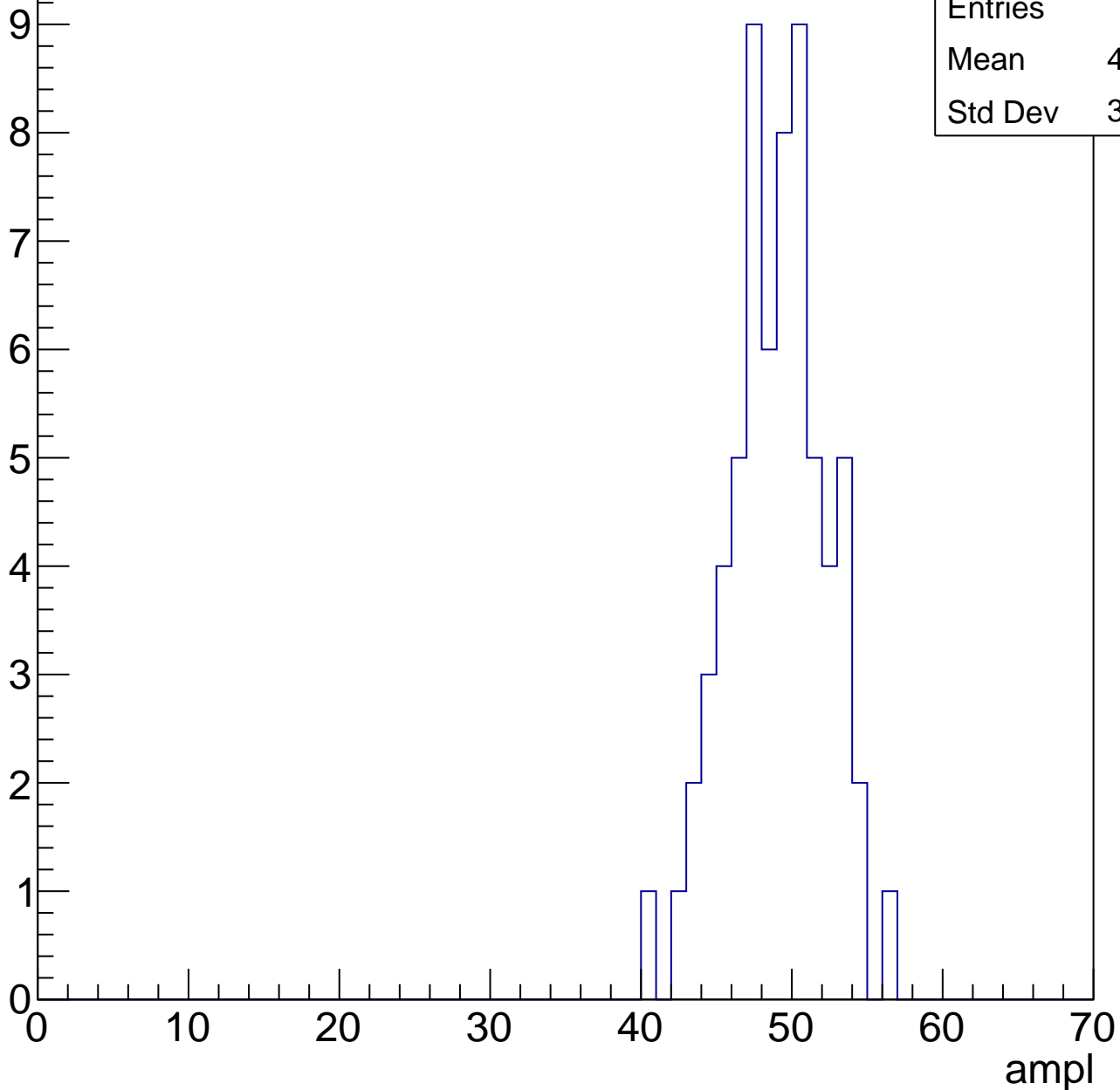


# B1L003S, U22-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	48.54
Std Dev	3.182

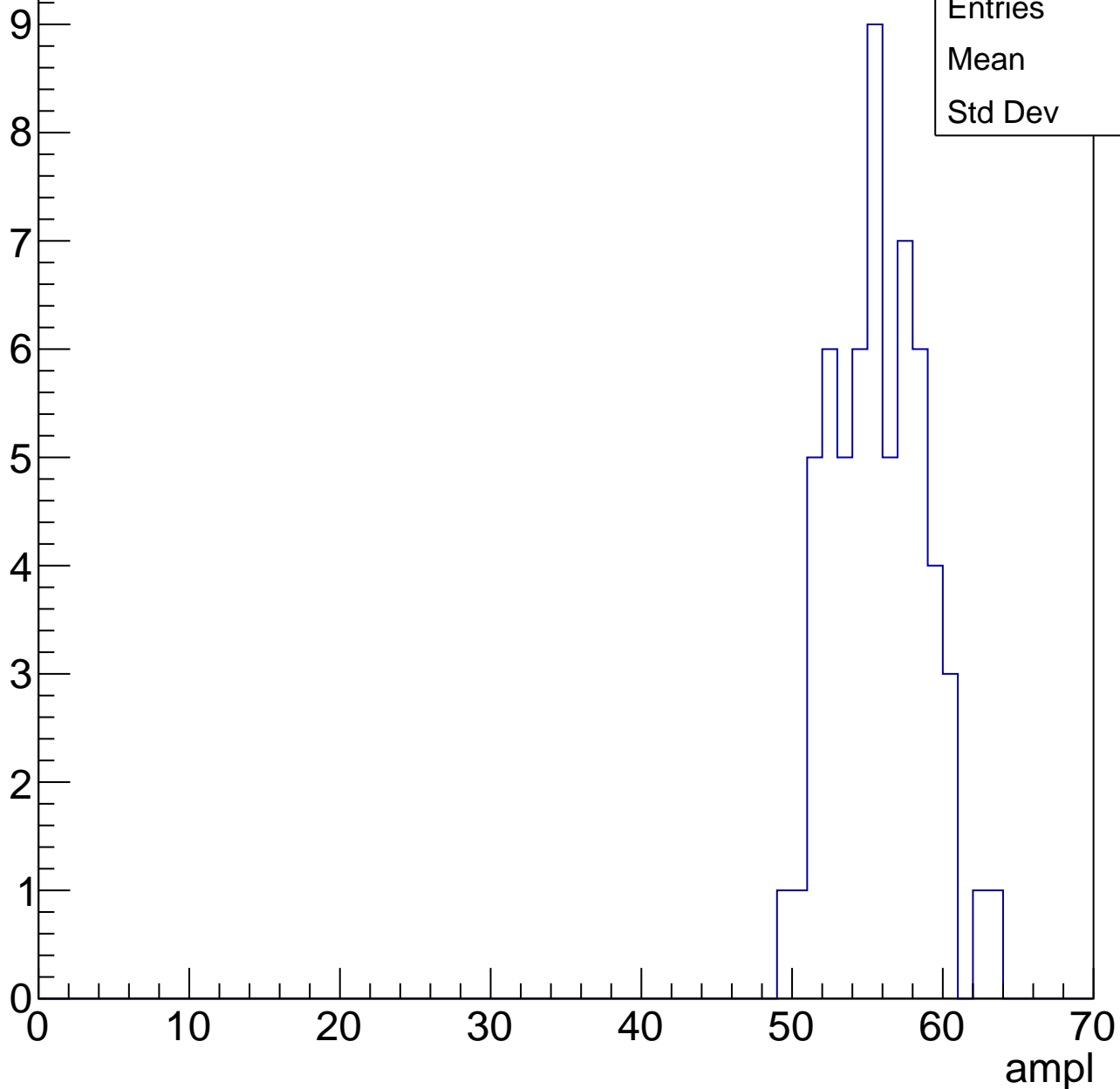


# B1L003S, U22-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.3
Std Dev	3.04



# B1L003S, U22-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

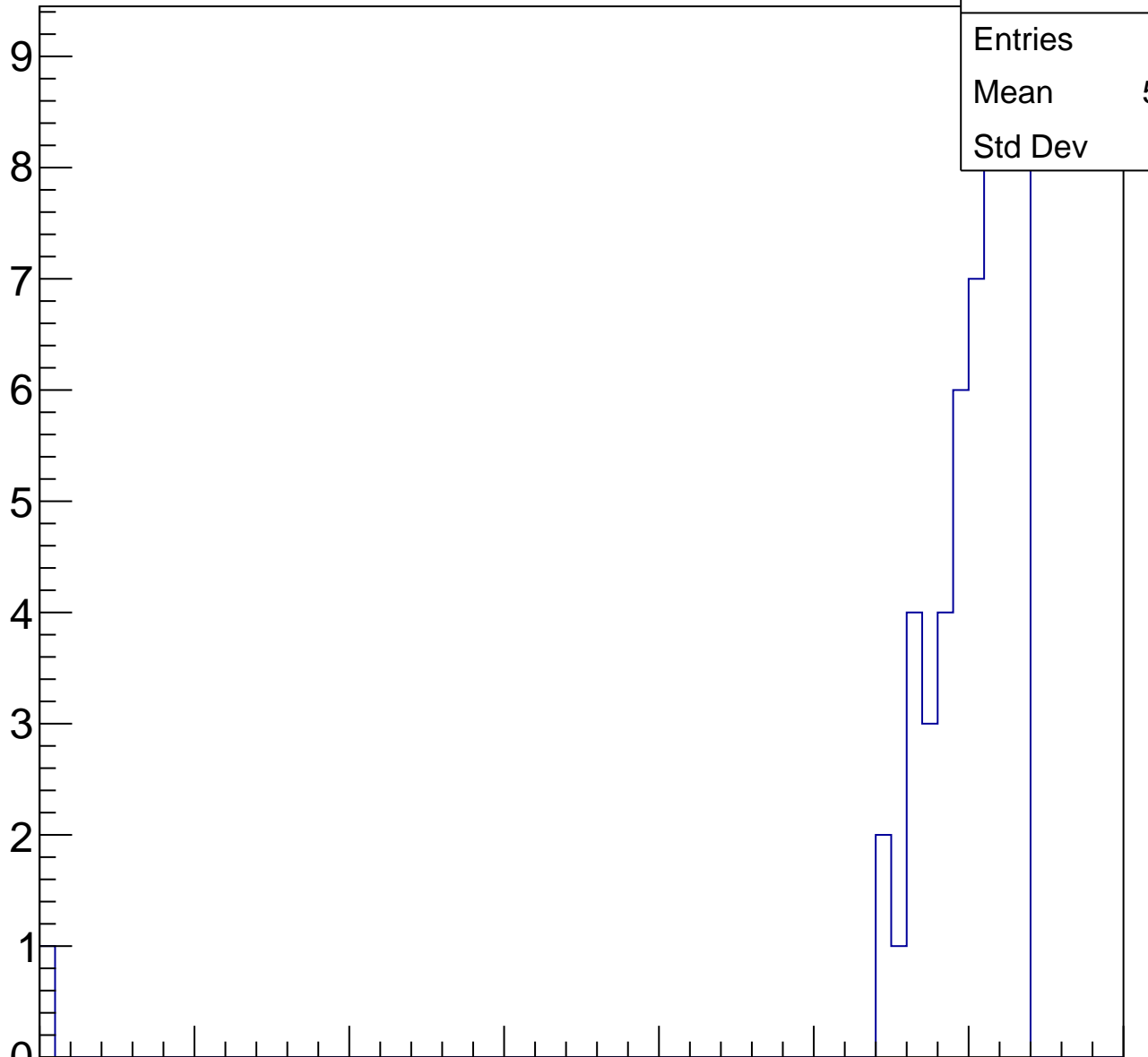
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.81
Std Dev	8.45

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch64, adc0

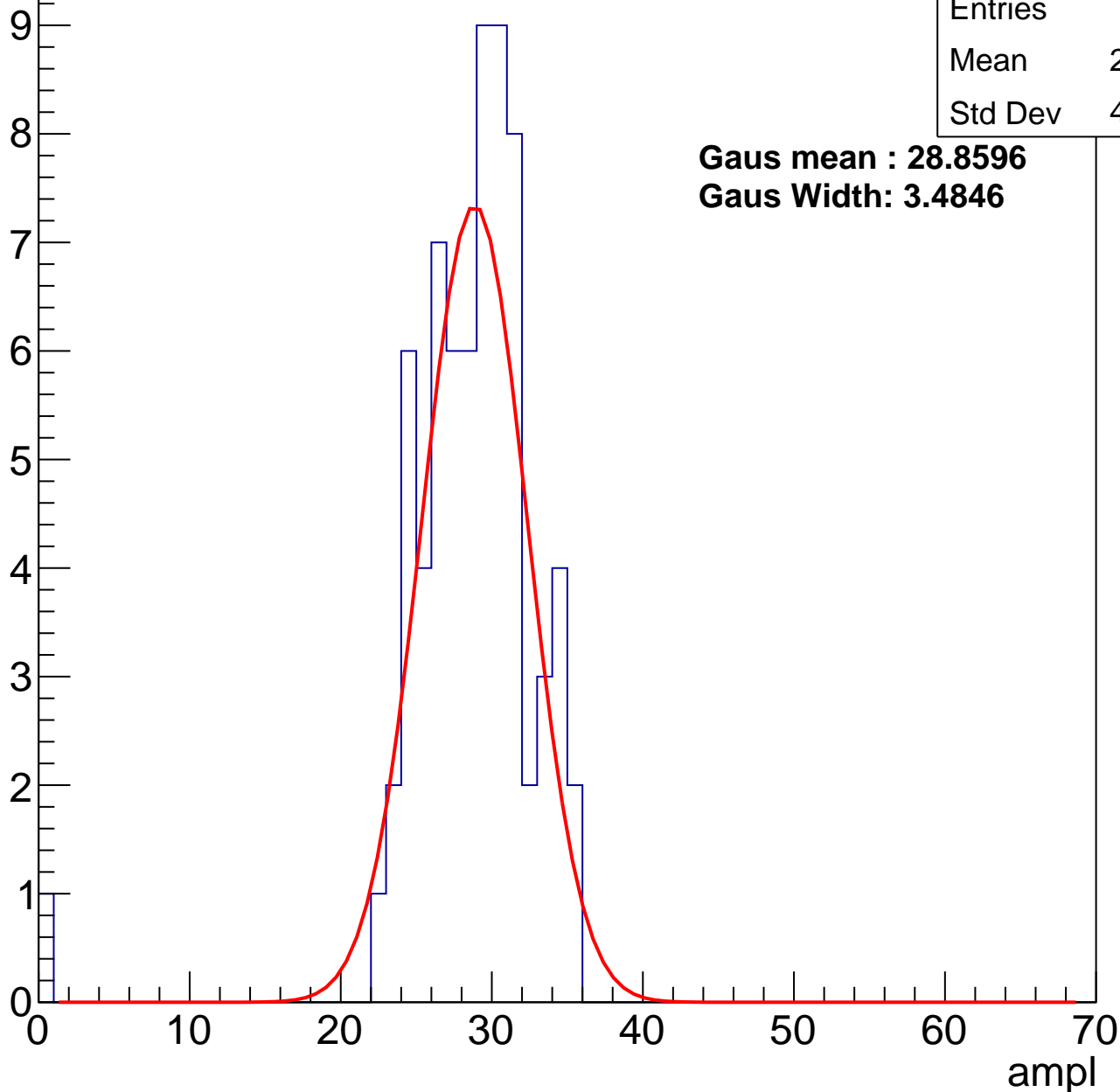
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.17
Std Dev	4.629

**Gaus mean : 28.8596**

**Gaus Width: 3.4846**



# B1L003S, U22-ch64, adc1

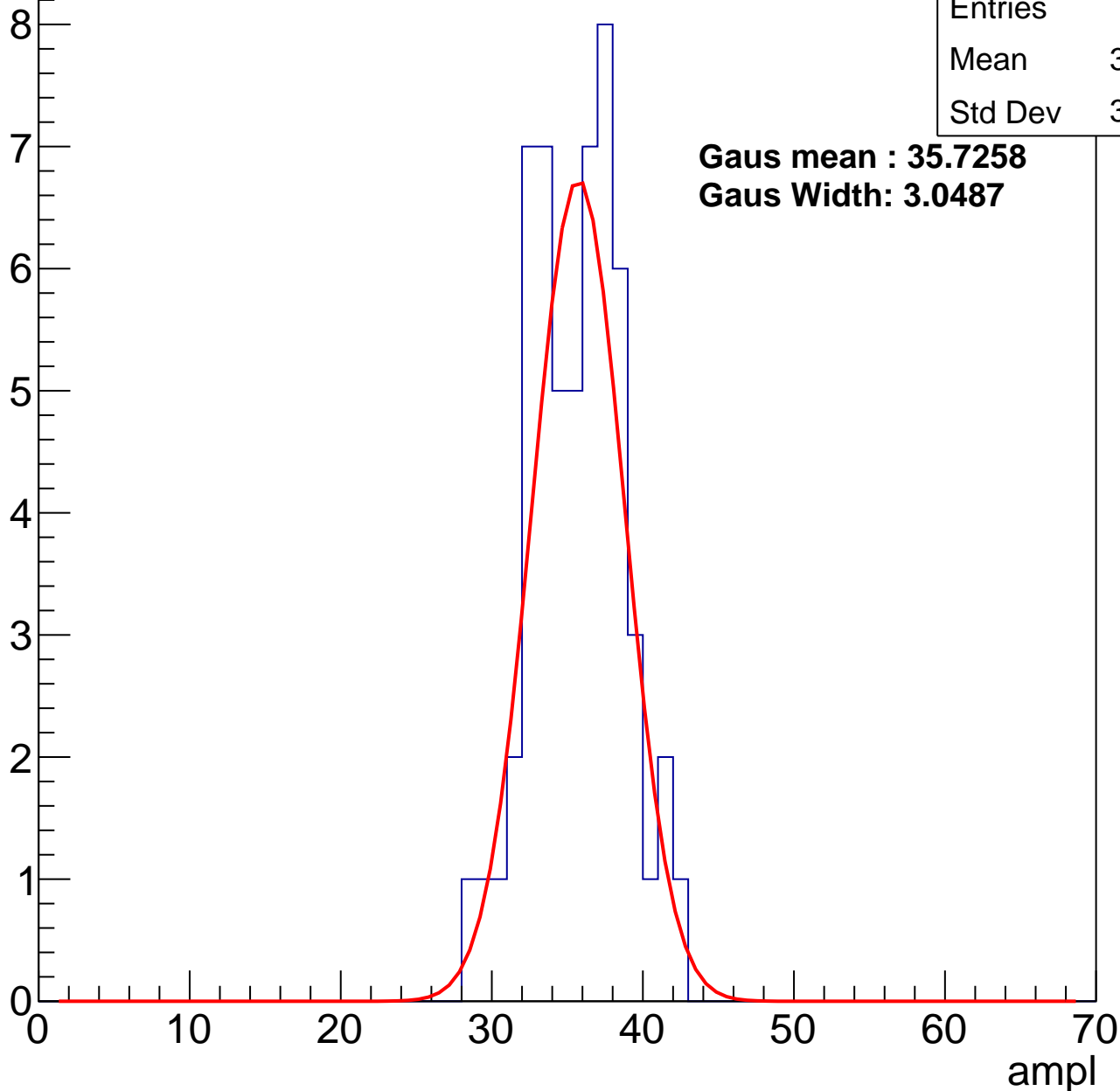
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.19
Std Dev	3.029

**Gaus mean : 35.7258**

**Gaus Width: 3.0487**



# B1L003S, U22-ch64, adc2

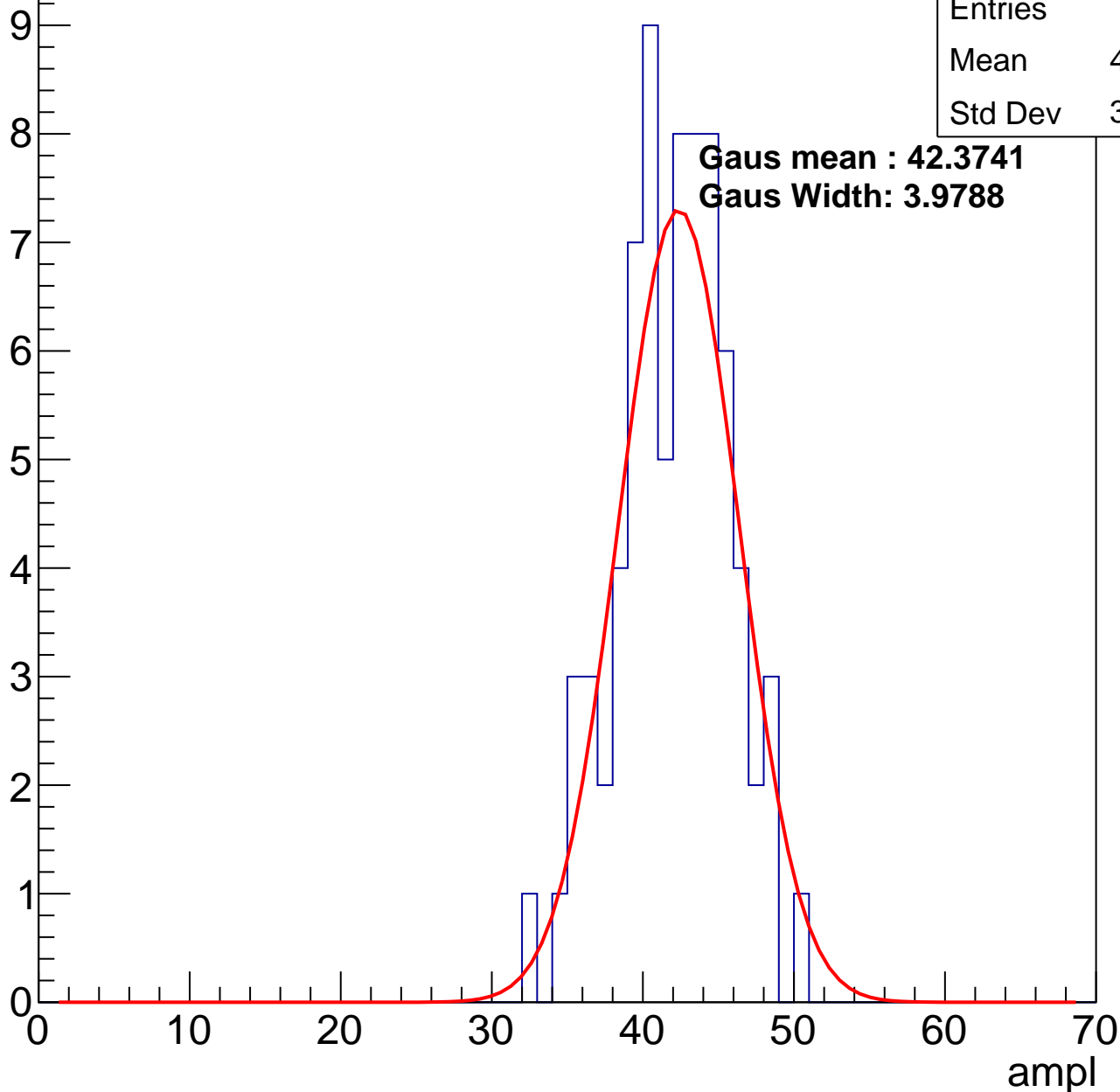
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	41.56
Std Dev	3.656

**Gaus mean : 42.3741**

**Gaus Width: 3.9788**



# B1L003S, U22-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	61
Mean	48.1
Std Dev	3.217

Entry

10

8

6

4

2

0

0

10

20

30

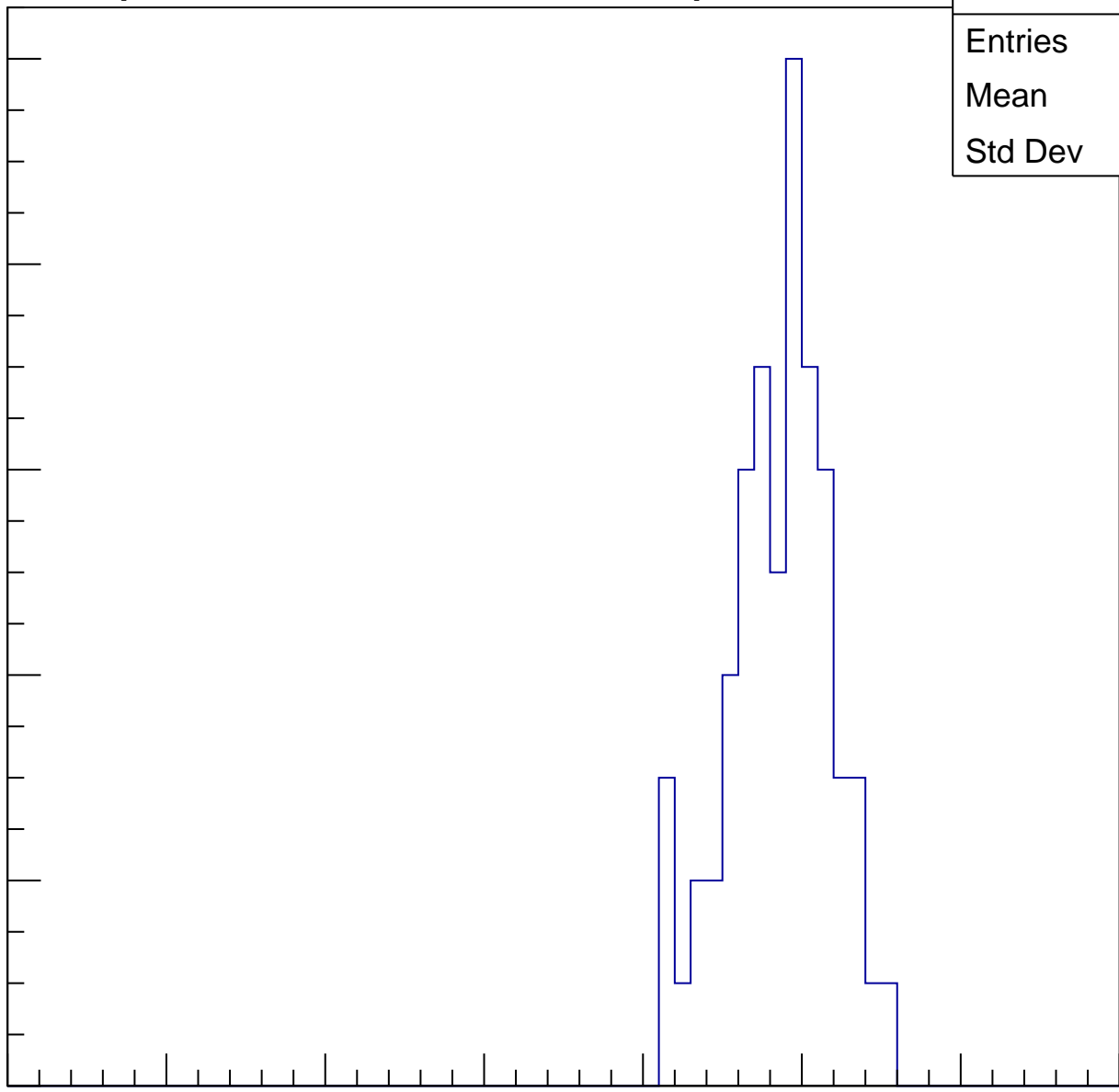
40

50

60

70

ampl

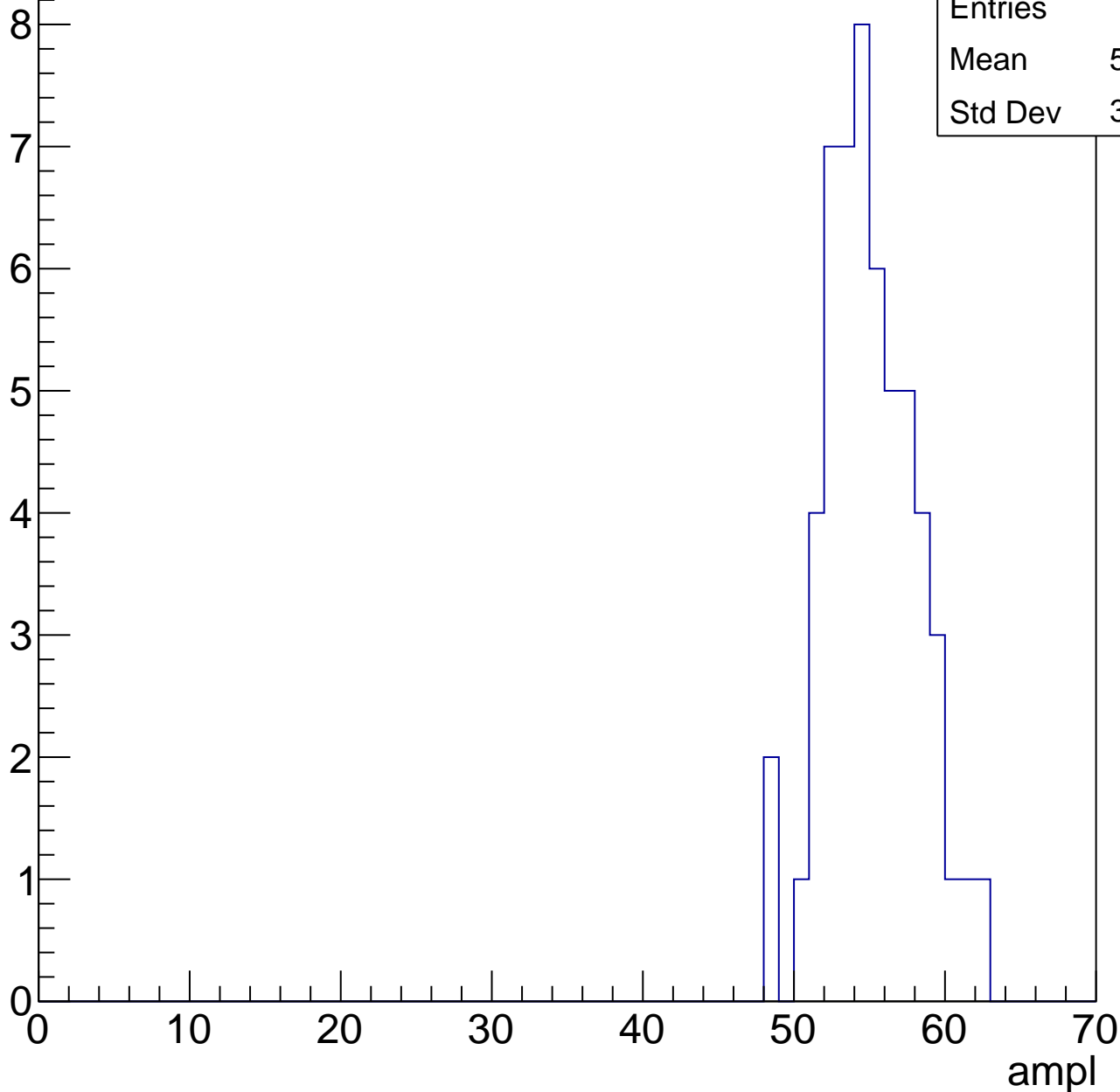


# B1L003S, U22-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	54.62
Std Dev	3.006

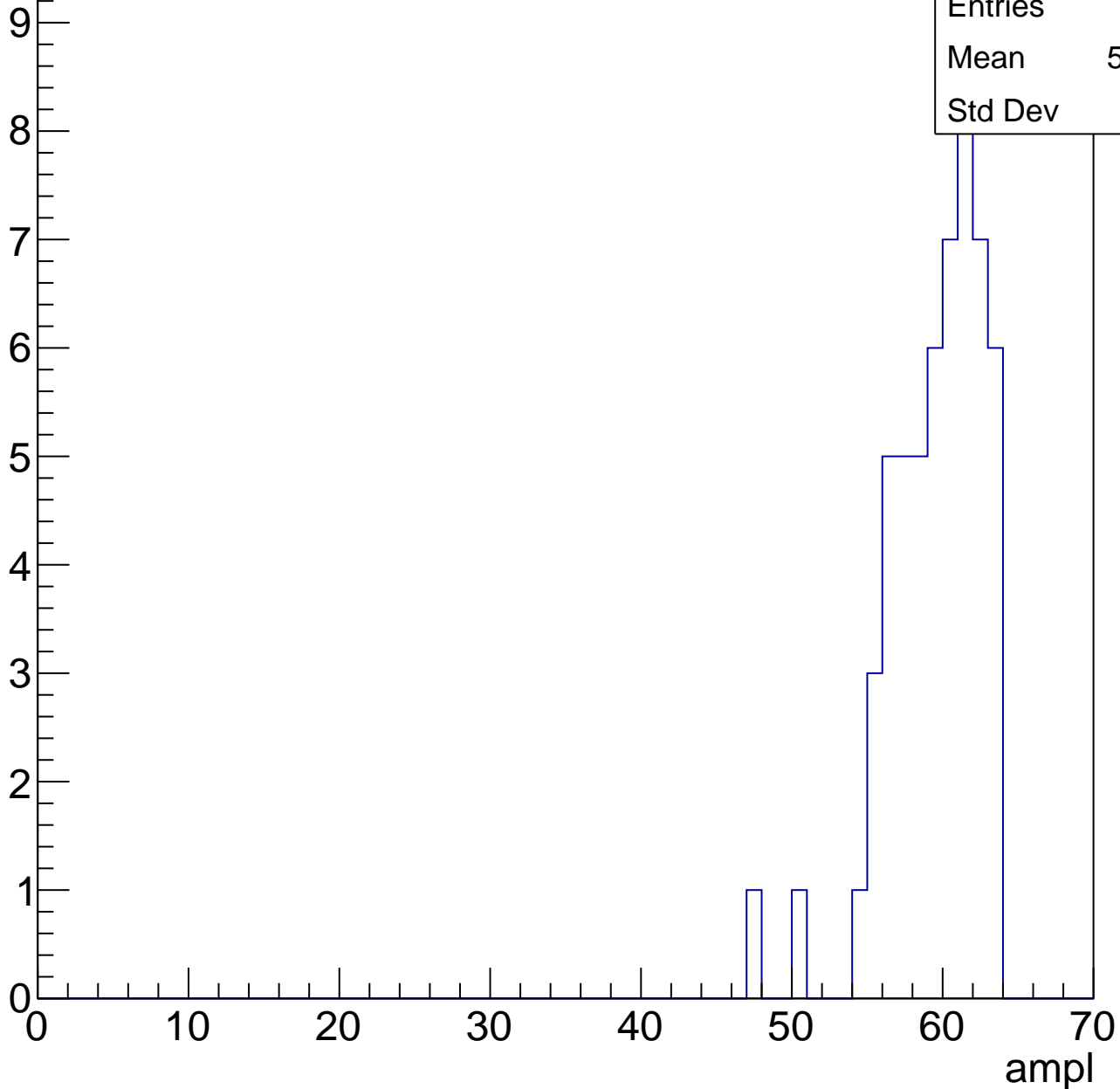


# B1L003S, U22-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

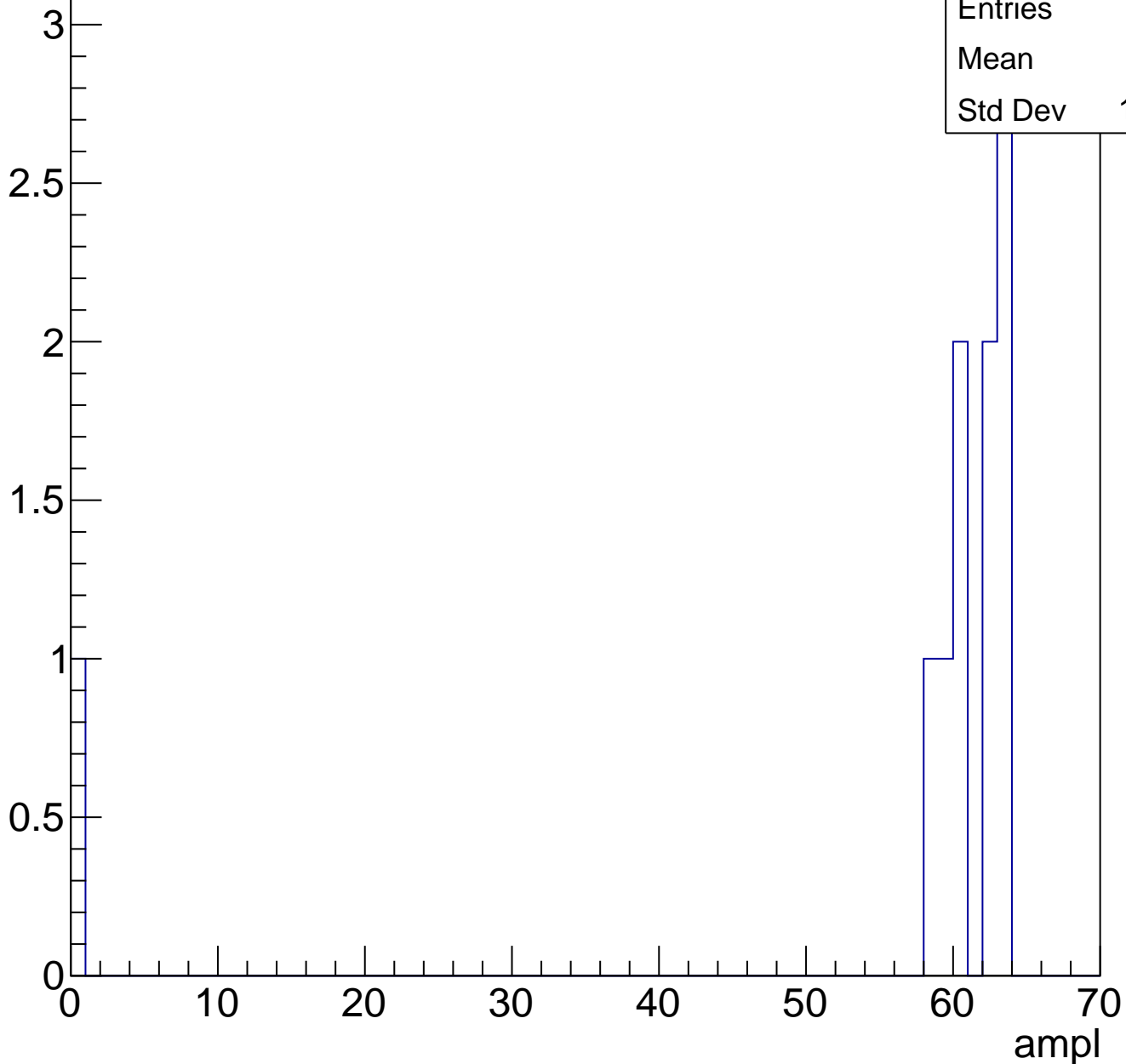
Entries	56
Mean	59.04
Std Dev	3.19



# B1L003S, U22-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	28.87
Std Dev	4.752

**Gaus mean : 29.5641**

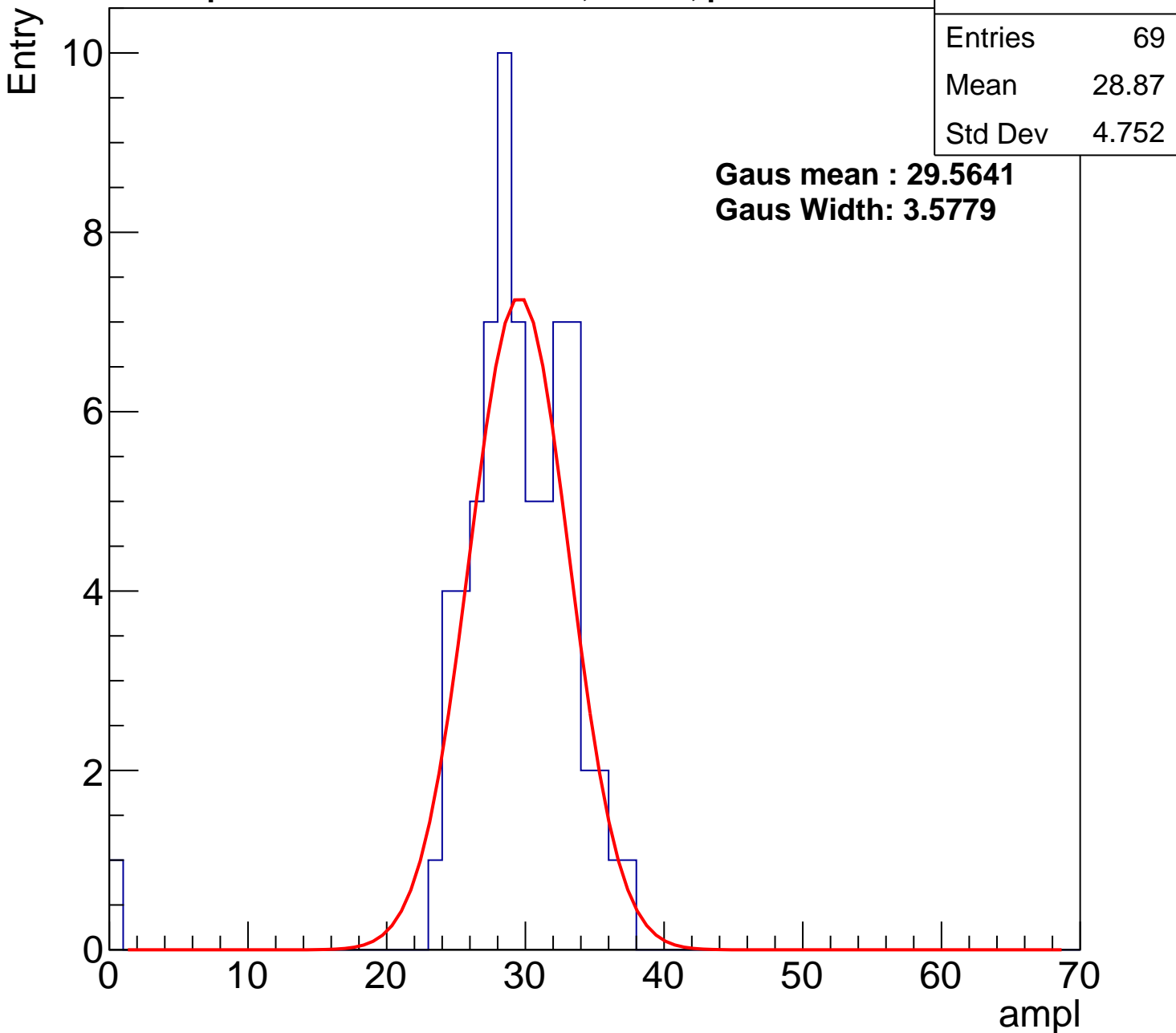
**Gaus Width: 3.5779**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



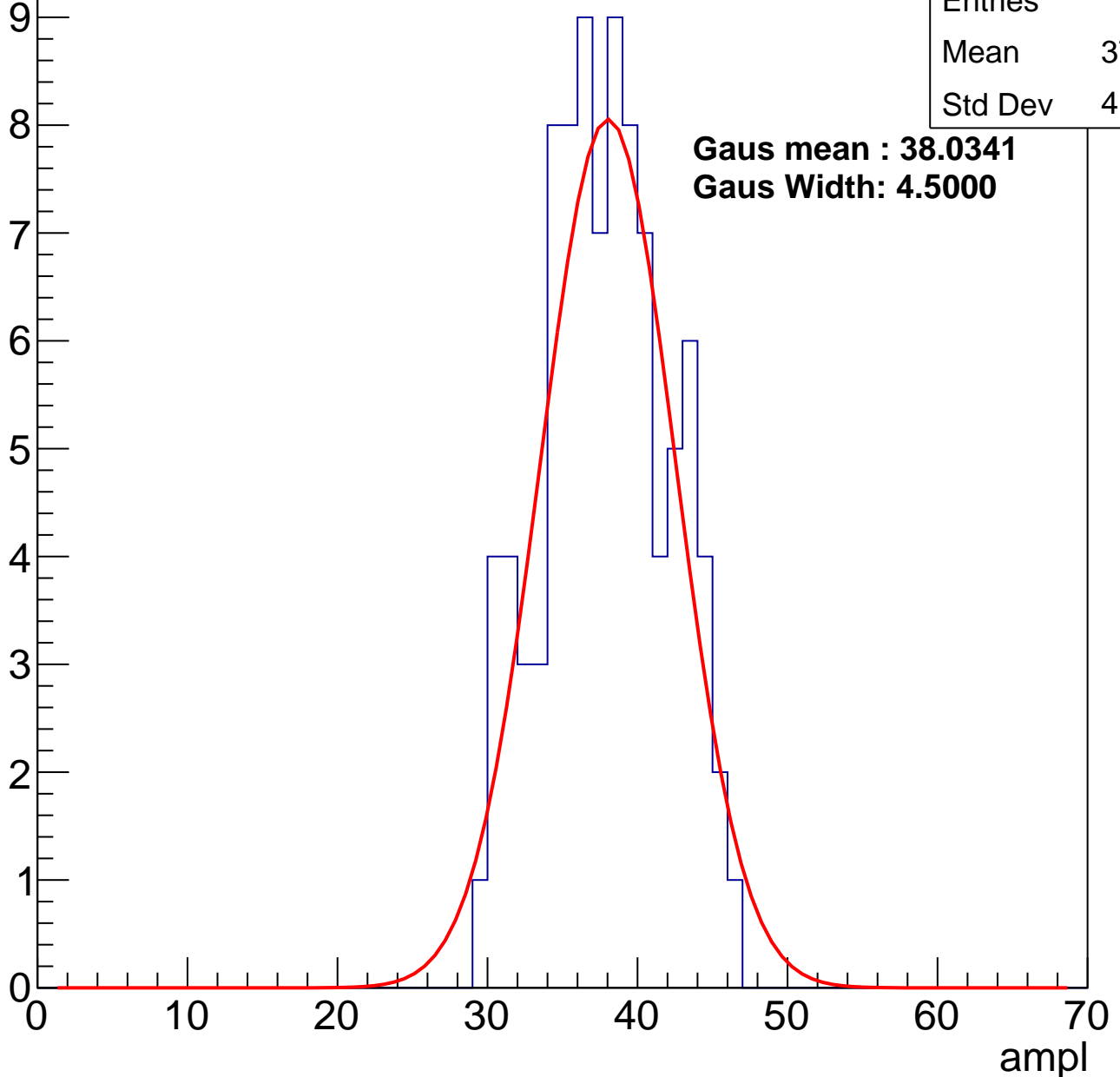
# B1L003S, U22-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	93
Mean	37.43
Std Dev	4.065

**Gaus mean : 38.0341**  
**Gaus Width: 4.5000**



# B1L003S, U22-ch65, adc2

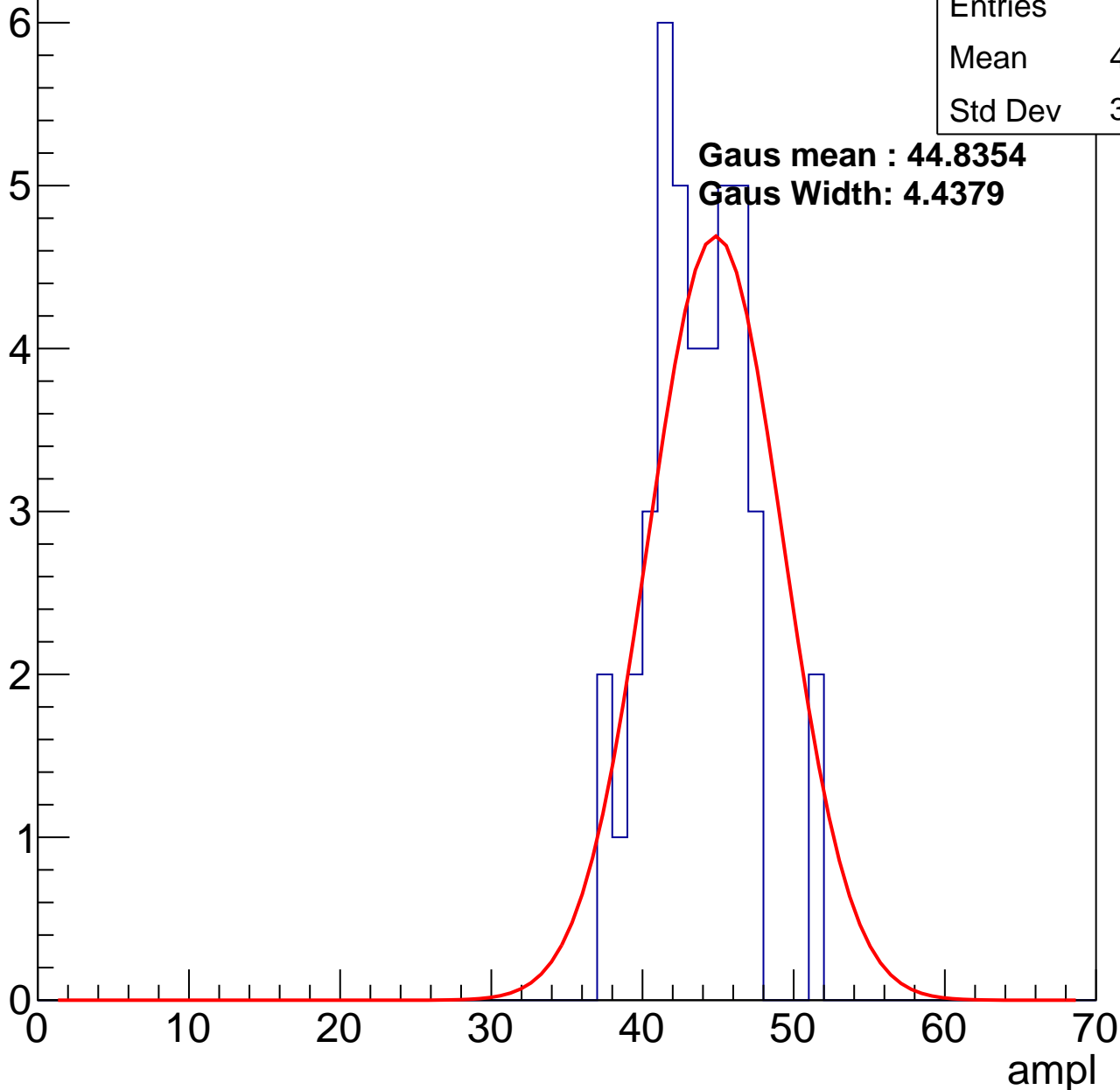
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	43.14
Std Dev	3.197

**Gaus mean : 44.8354**

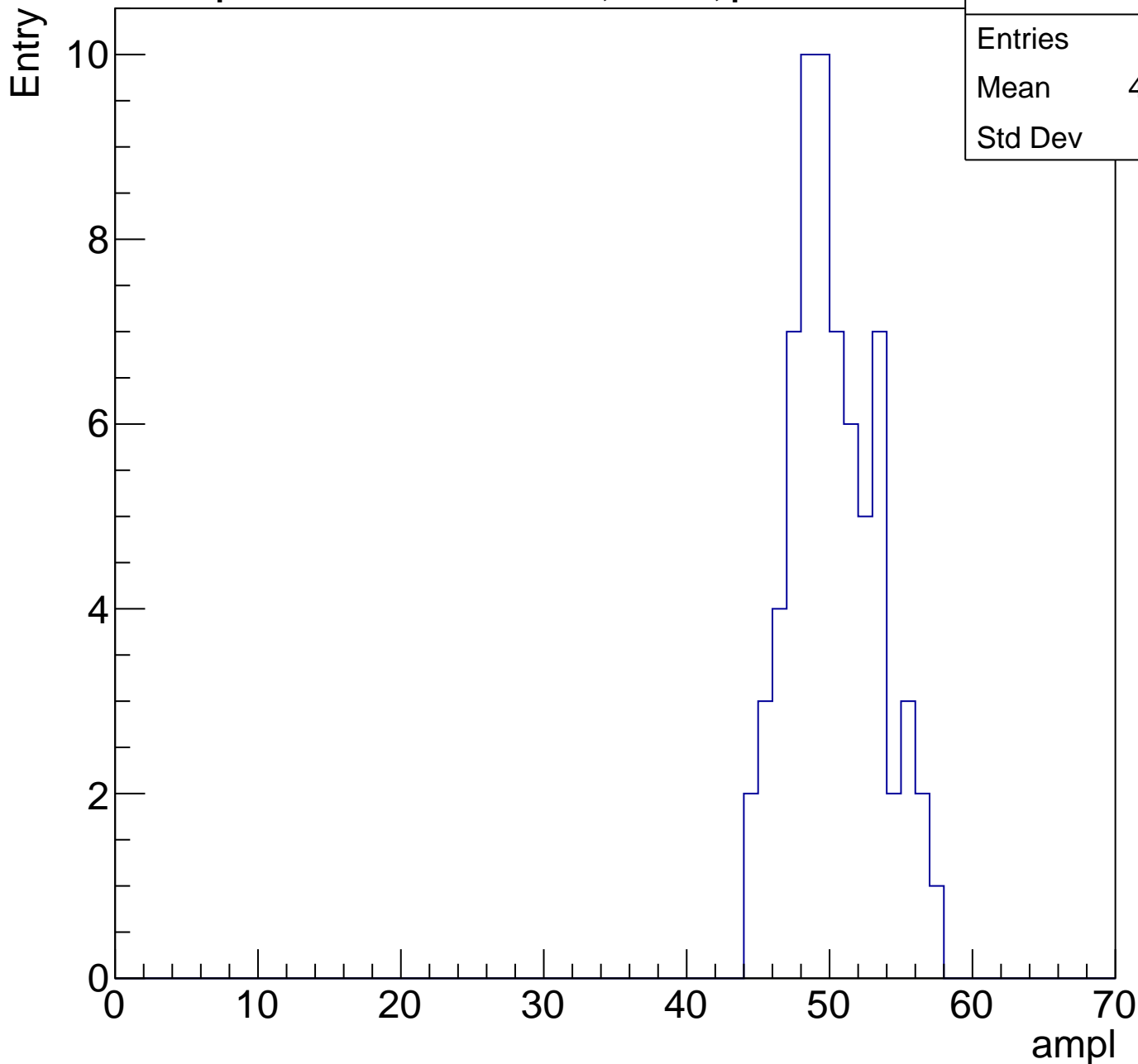
**Gaus Width: 4.4379**



# B1L003S, U22-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	49.78
Std Dev	3.04

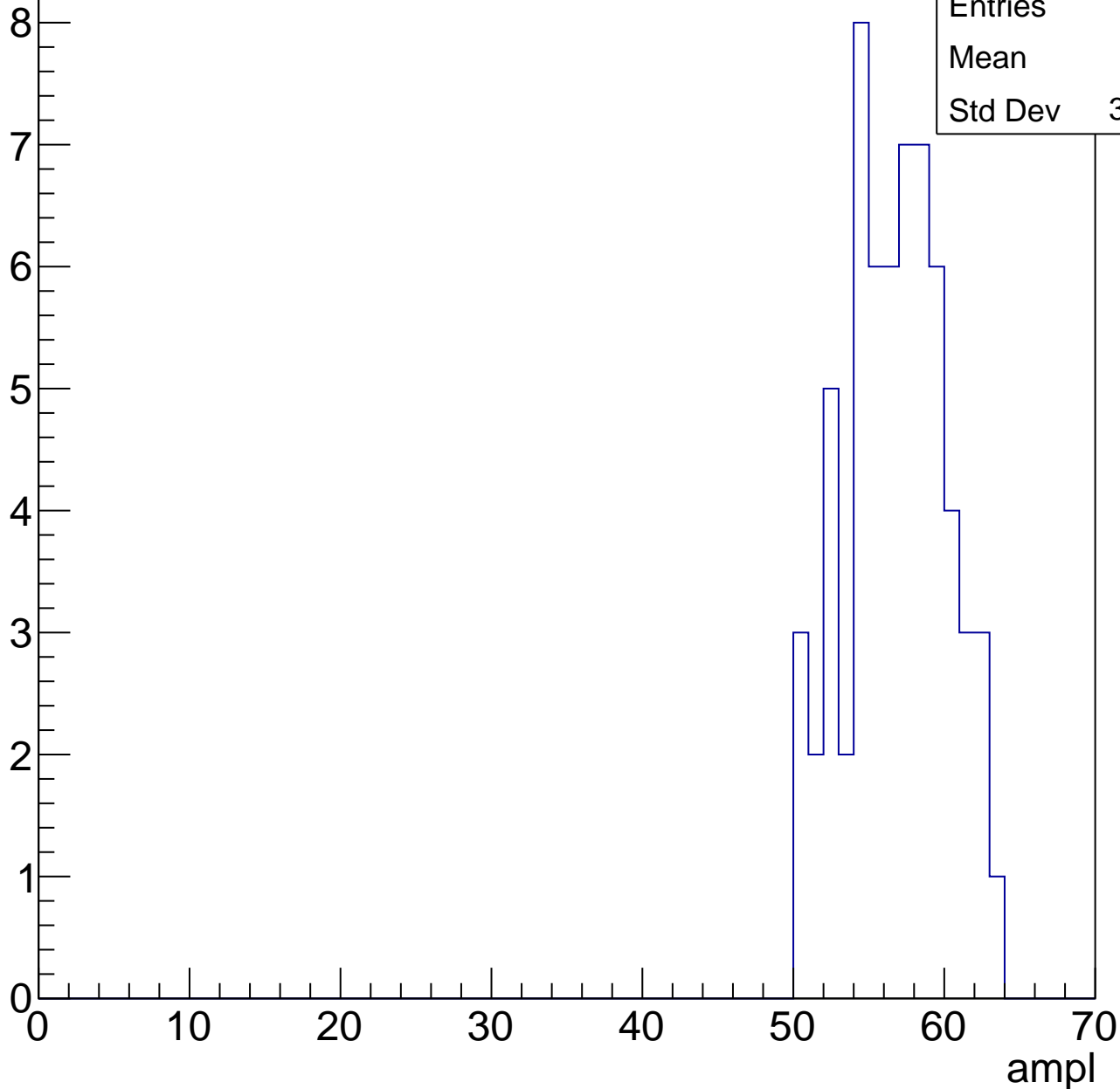


# B1L003S, U22-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	56.3
Std Dev	3.269



# B1L003S, U22-ch65, adc5

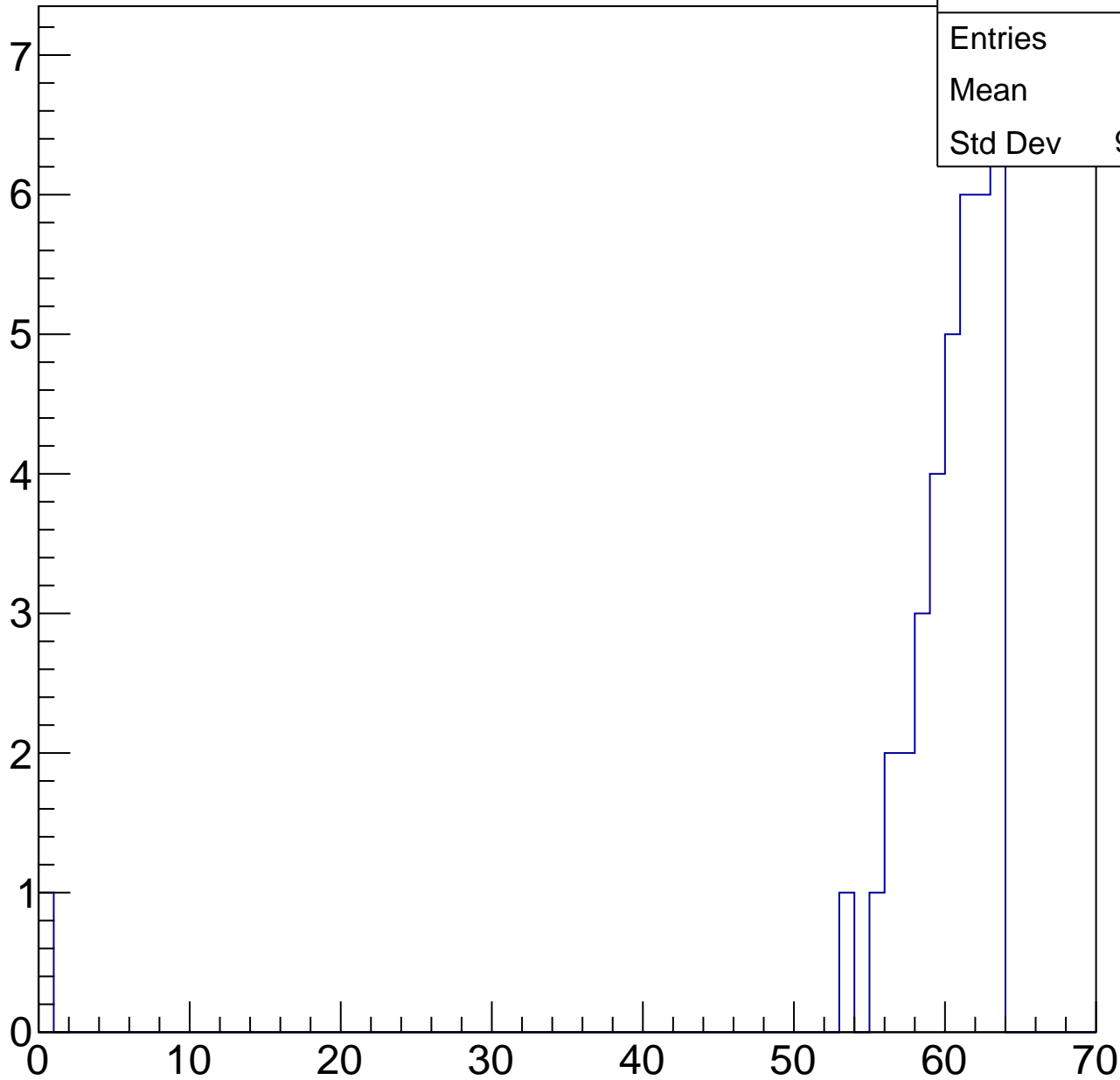
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	58.5
Std Dev	9.931

ampl



# B1L003S, U22-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch66, adc0

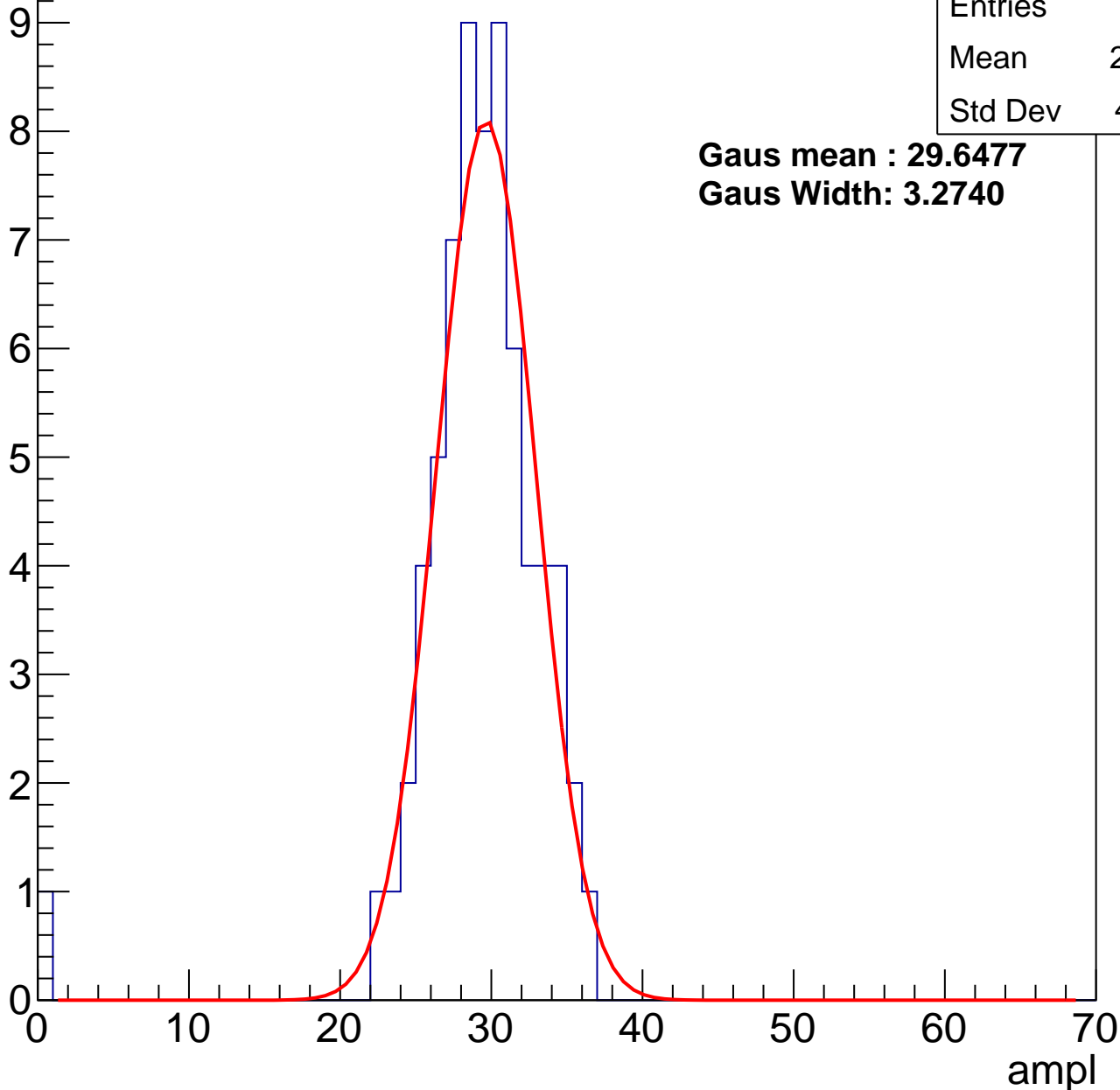
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.74
Std Dev	4.651

**Gaus mean : 29.6477**

**Gaus Width: 3.2740**



# B1L003S, U22-ch66, adc1

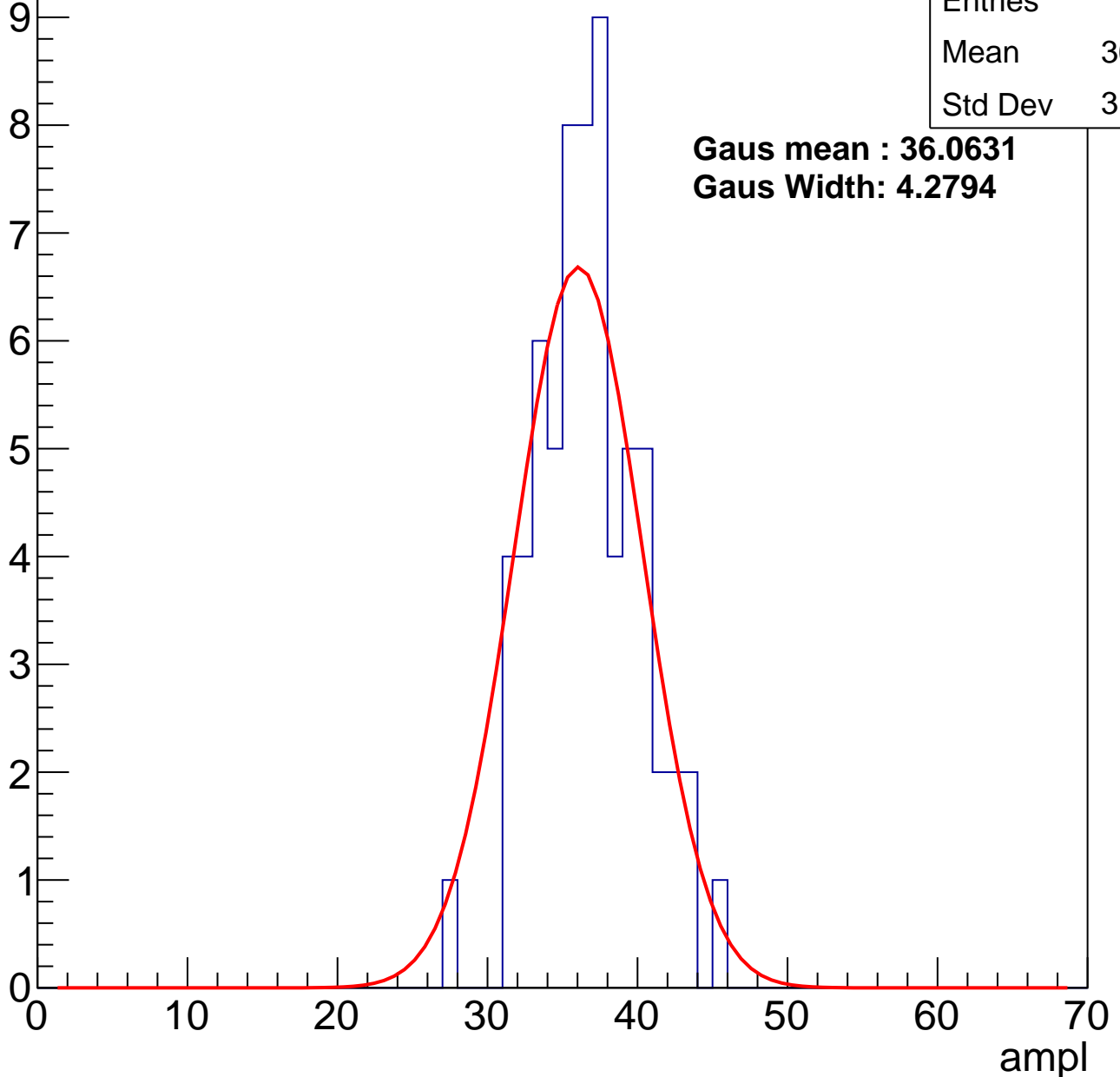
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.24
Std Dev	3.425

**Gaus mean : 36.0631**

**Gaus Width: 4.2794**



# B1L003S, U22-ch66, adc2

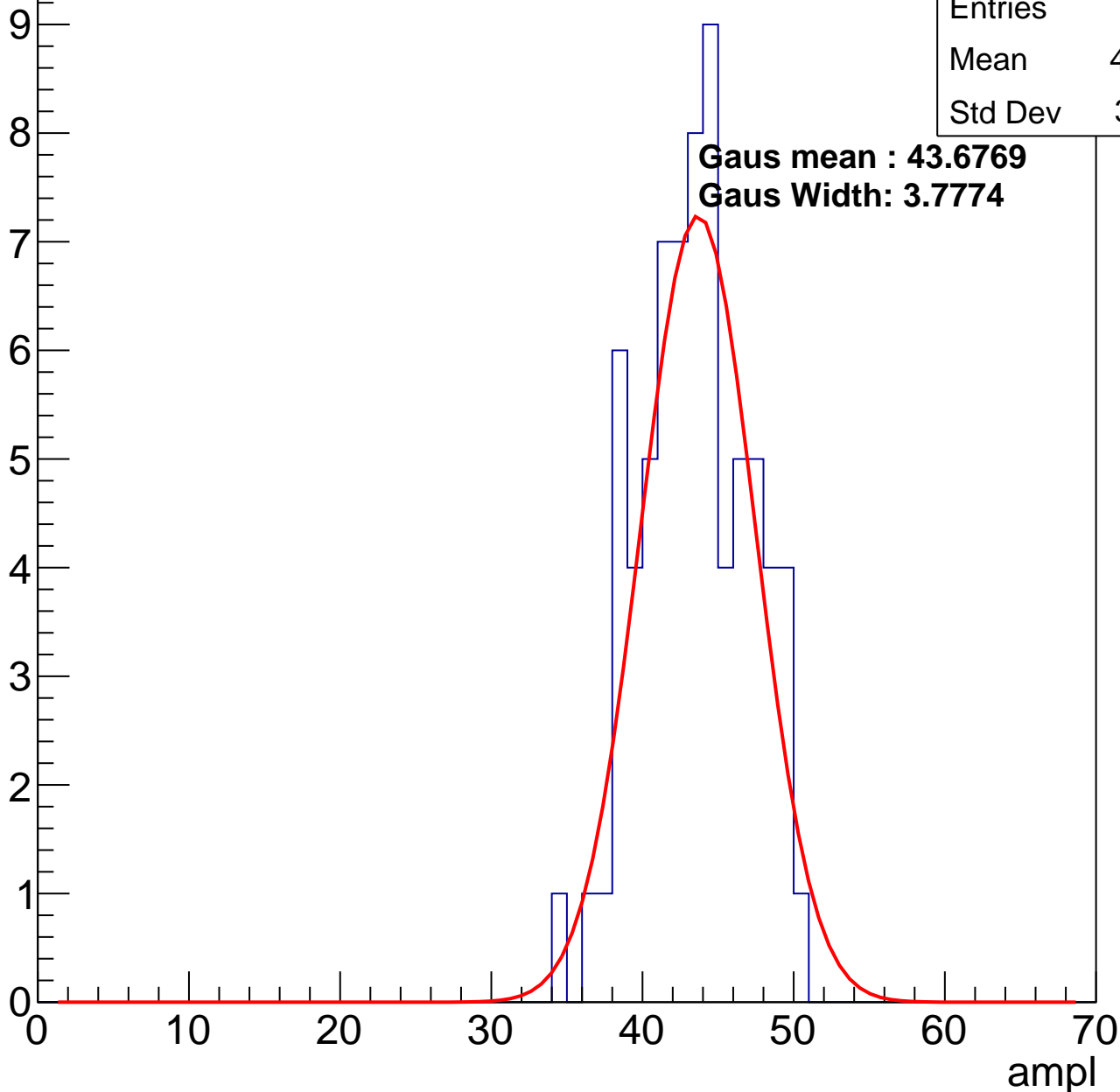
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	42.99
Std Dev	3.541

**Gaus mean : 43.6769**

**Gaus Width: 3.7774**

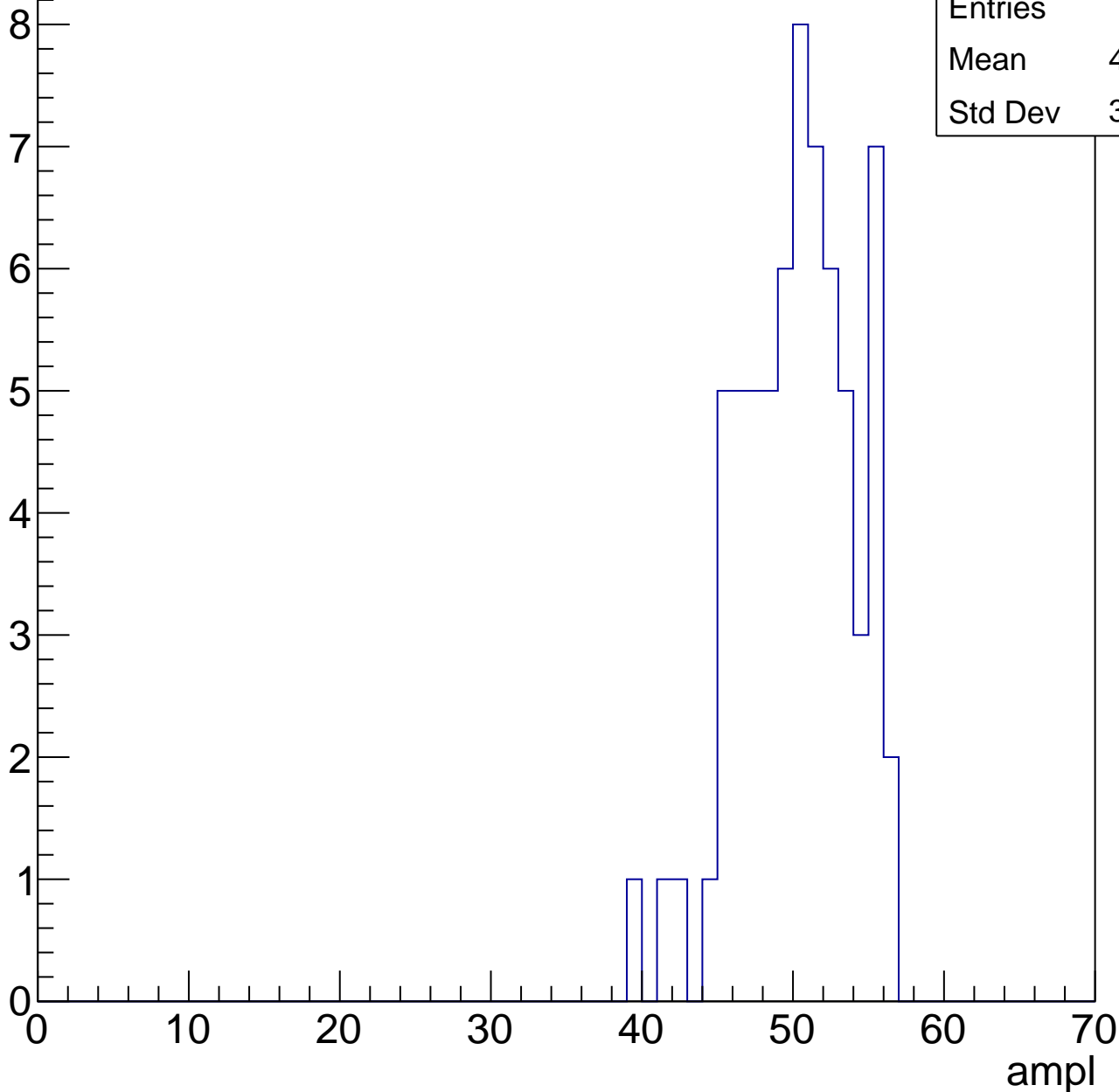


# B1L003S, U22-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

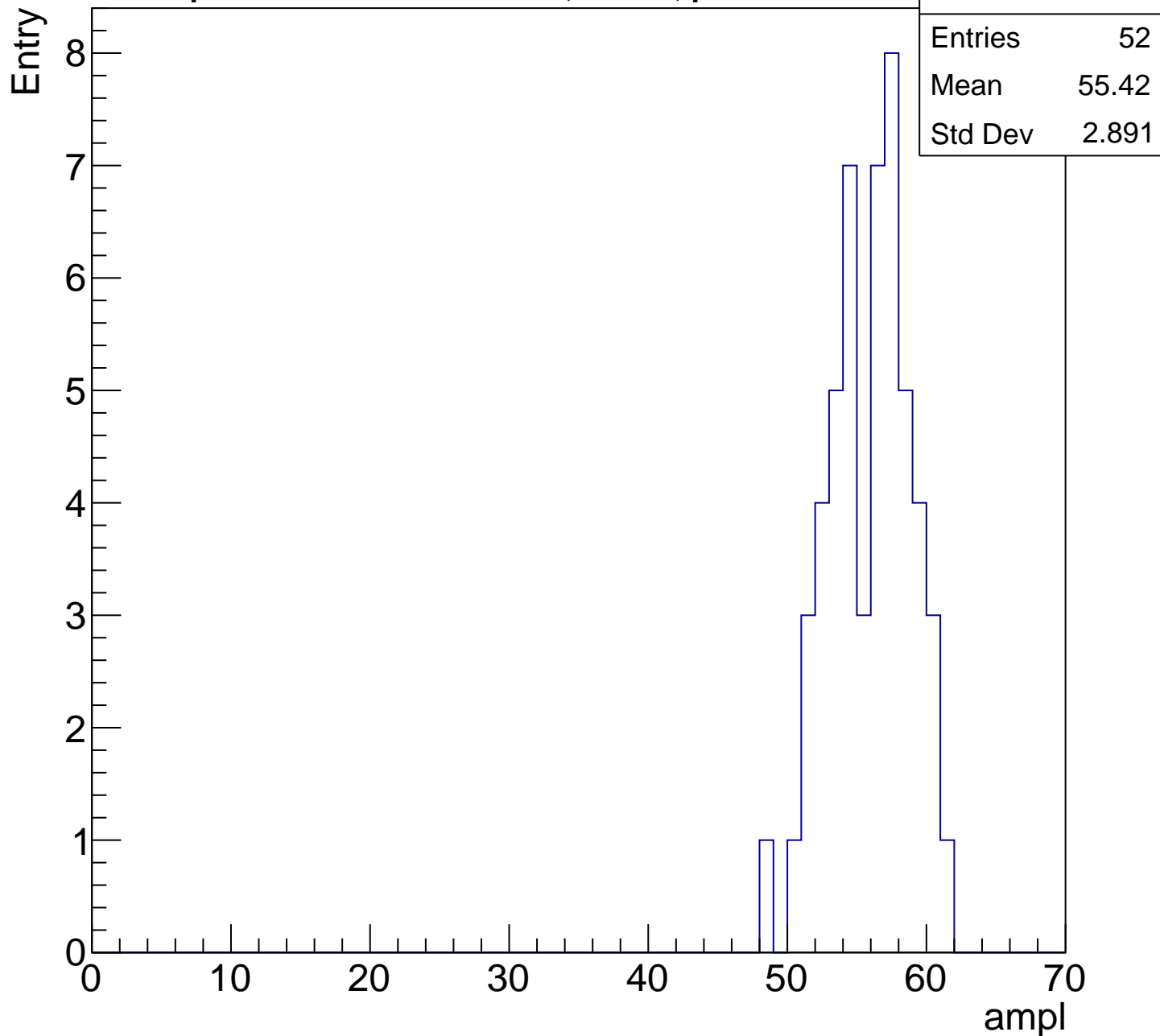
Entry

Entries	68
Mean	49.75
Std Dev	3.727



# B1L003S, U22-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

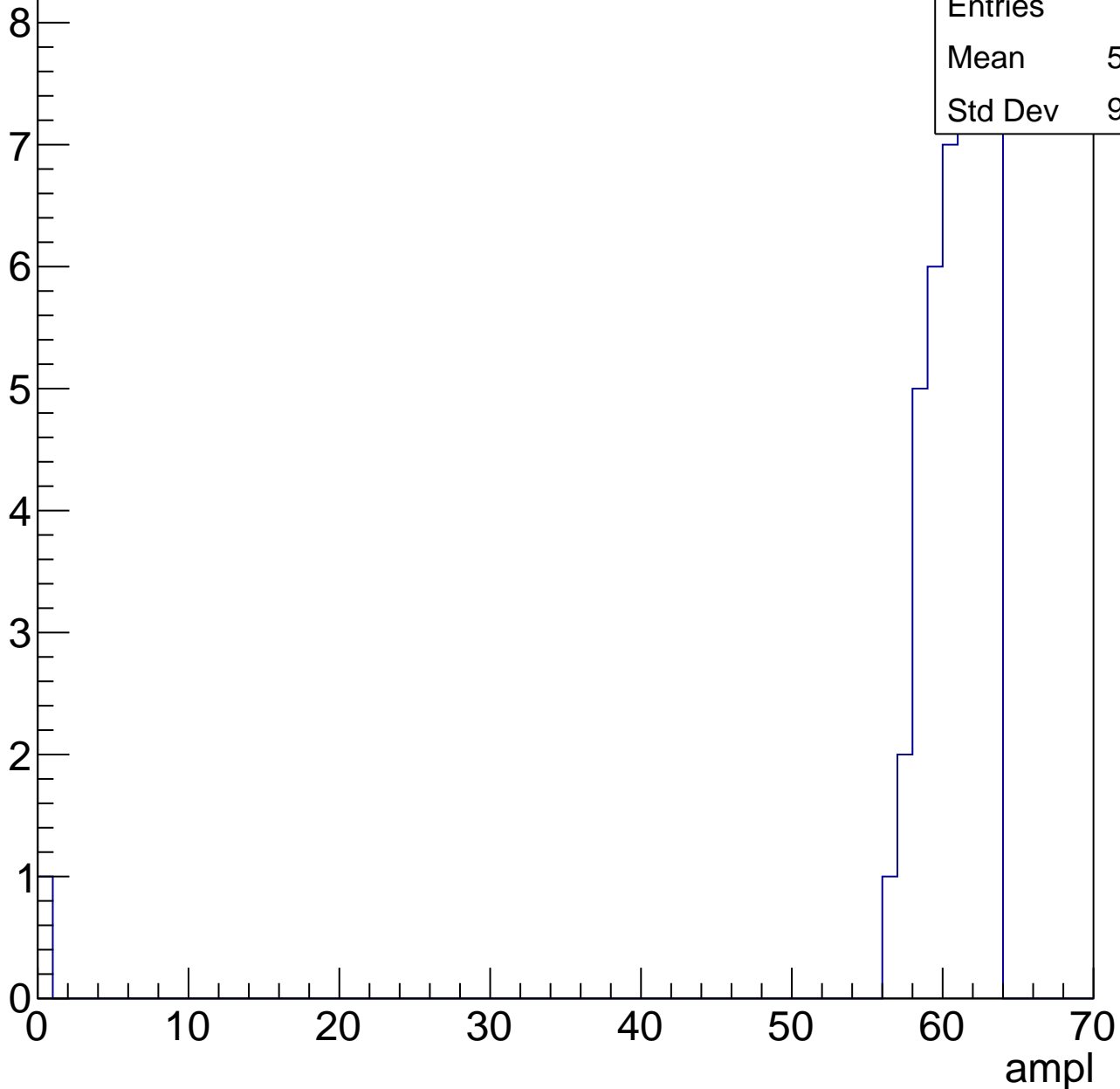


# B1L003S, U22-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	59.17
Std Dev	9.018



# B1L003S, U22-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

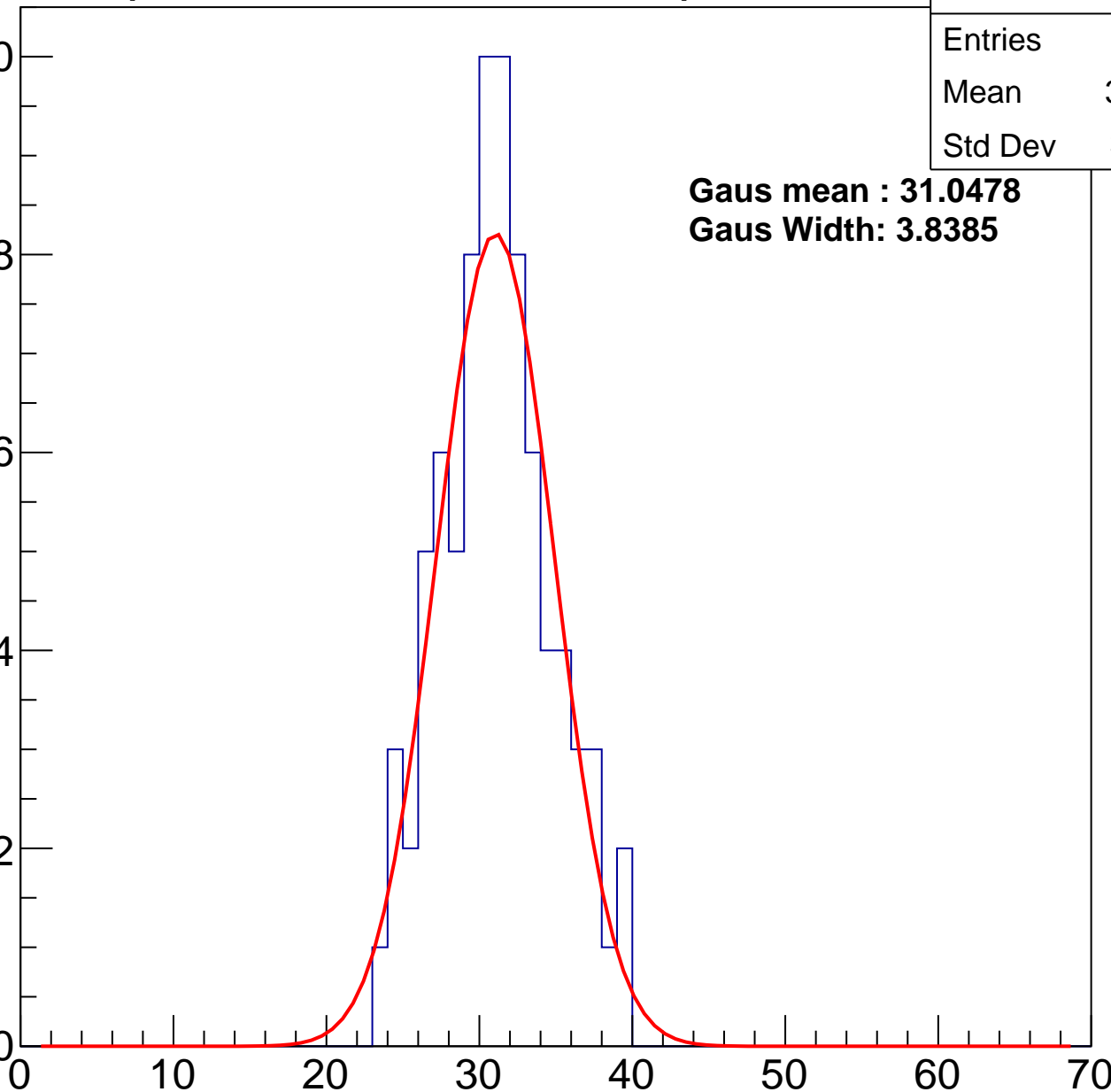
Entries	81
Mean	30.67
Std Dev	3.641

**Gaus mean : 31.0478**

**Gaus Width: 3.8385**

10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch67, adc1

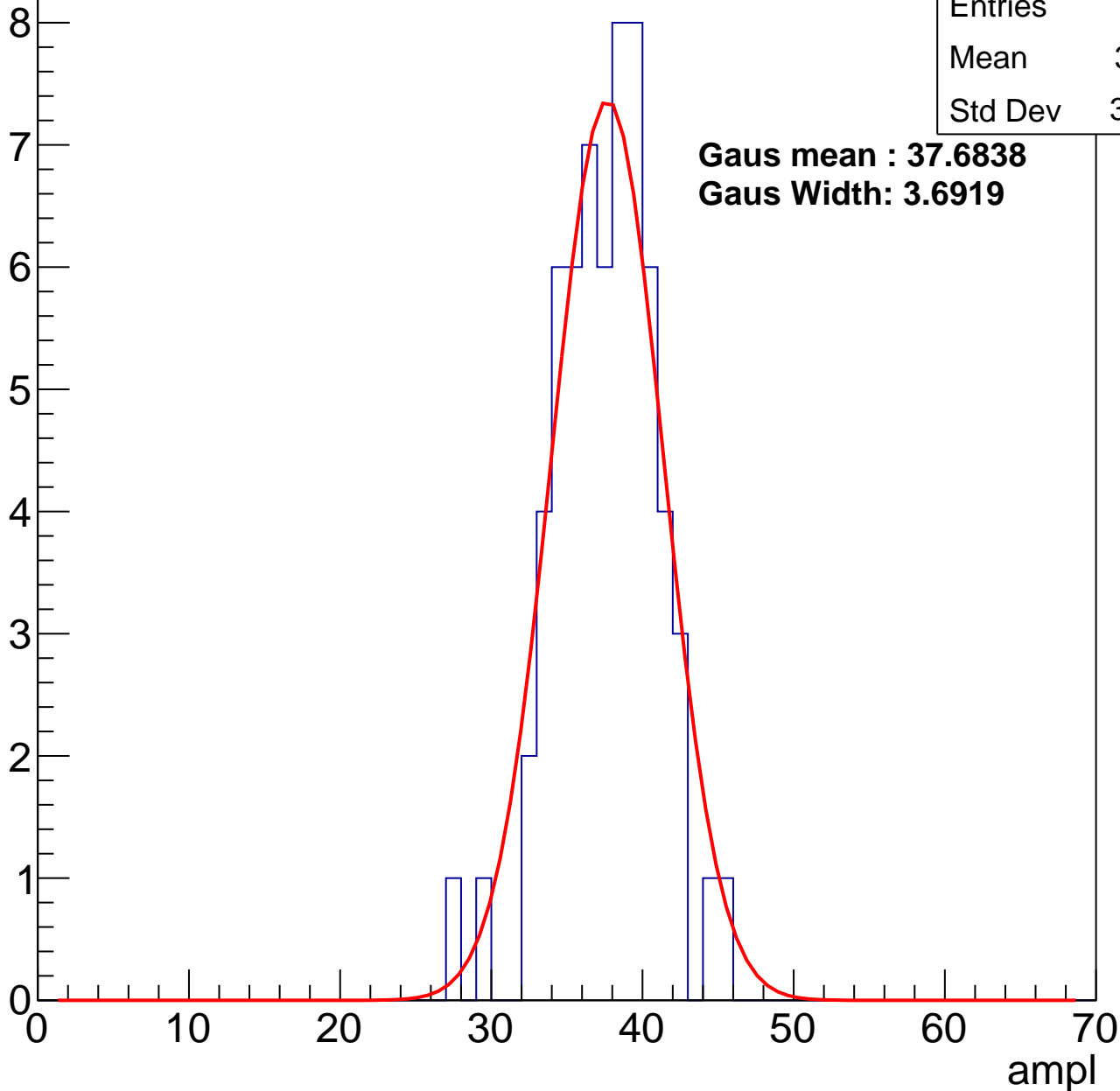
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.11
Std Dev	3.327

**Gaus mean : 37.6838**

**Gaus Width: 3.6919**



# B1L003S, U22-ch67, adc2

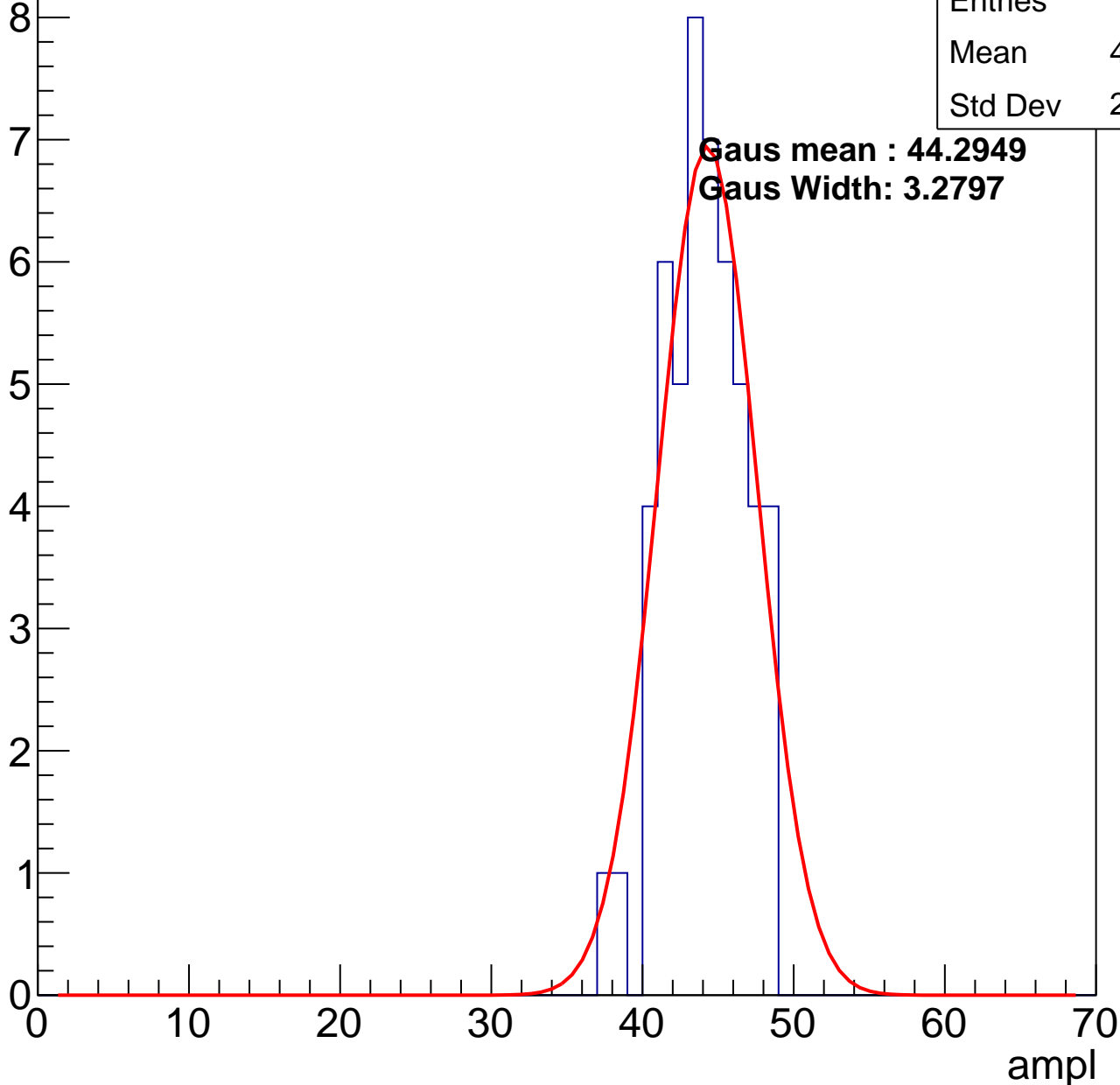
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	43.59
Std Dev	2.614

**Gaus mean : 44.2949**

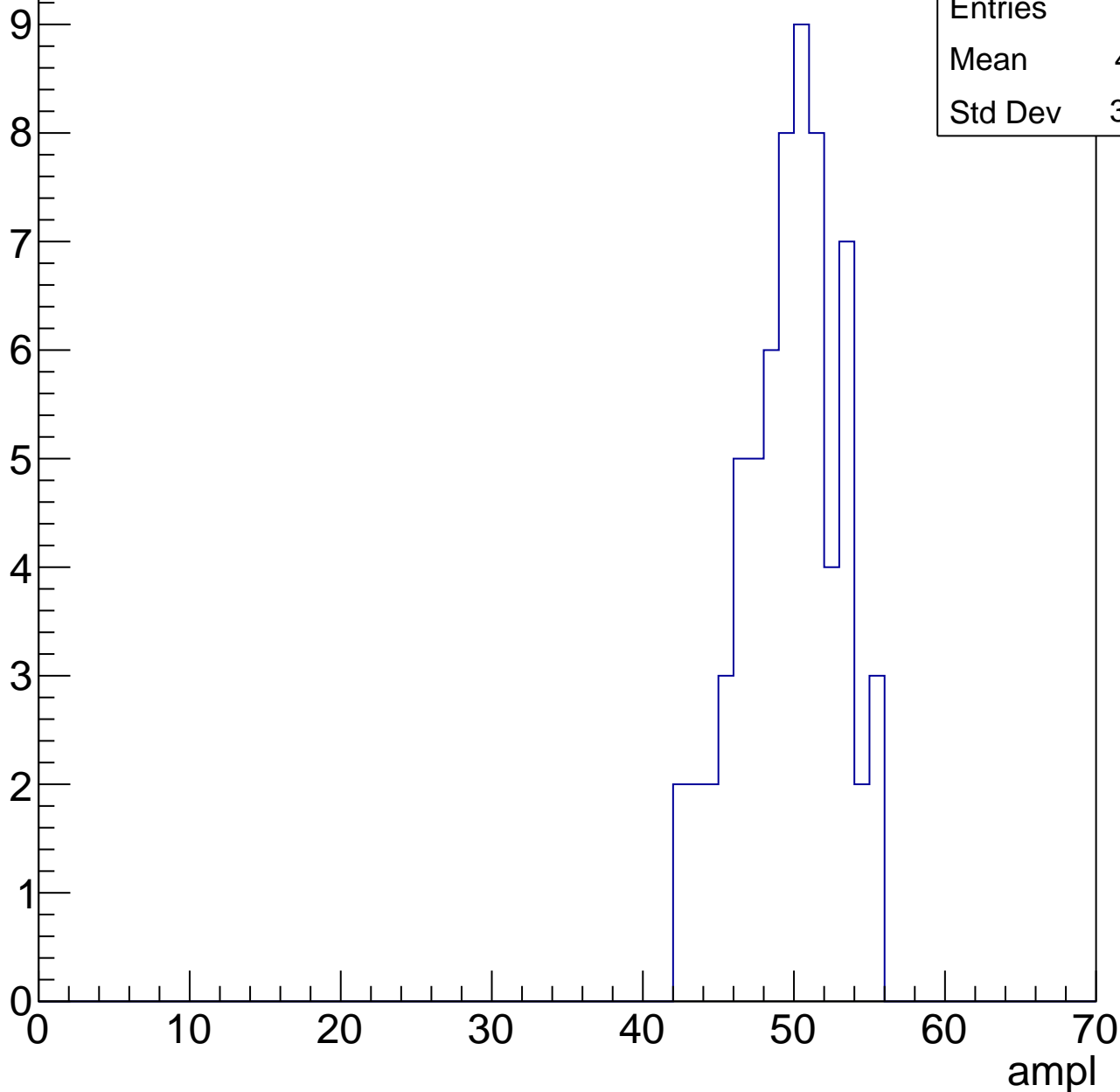
**Gaus Width: 3.2797**



# B1L003S, U22-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

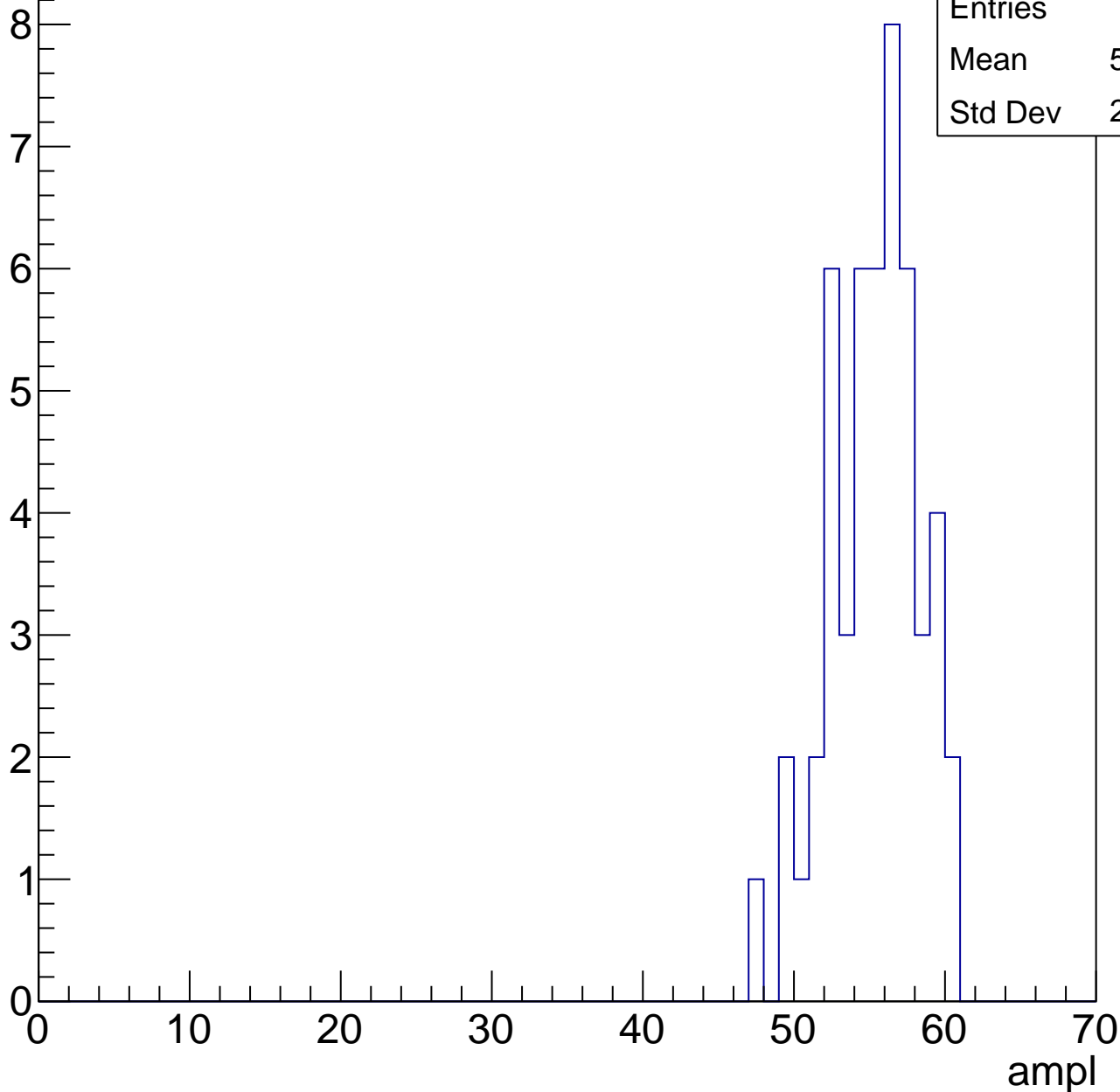


# B1L003S, U22-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	54.84
Std Dev	2.955

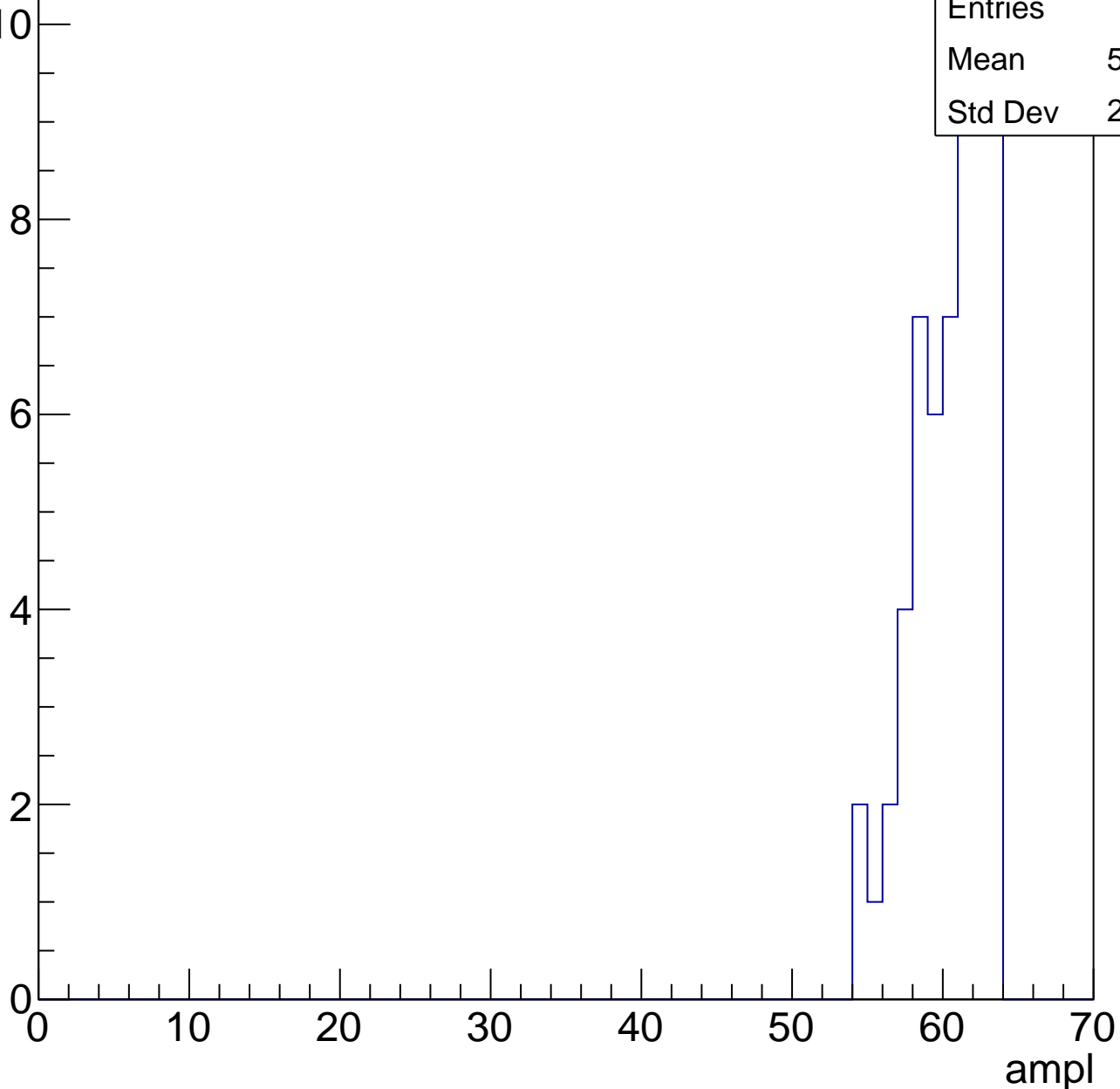


# B1L003S, U22-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

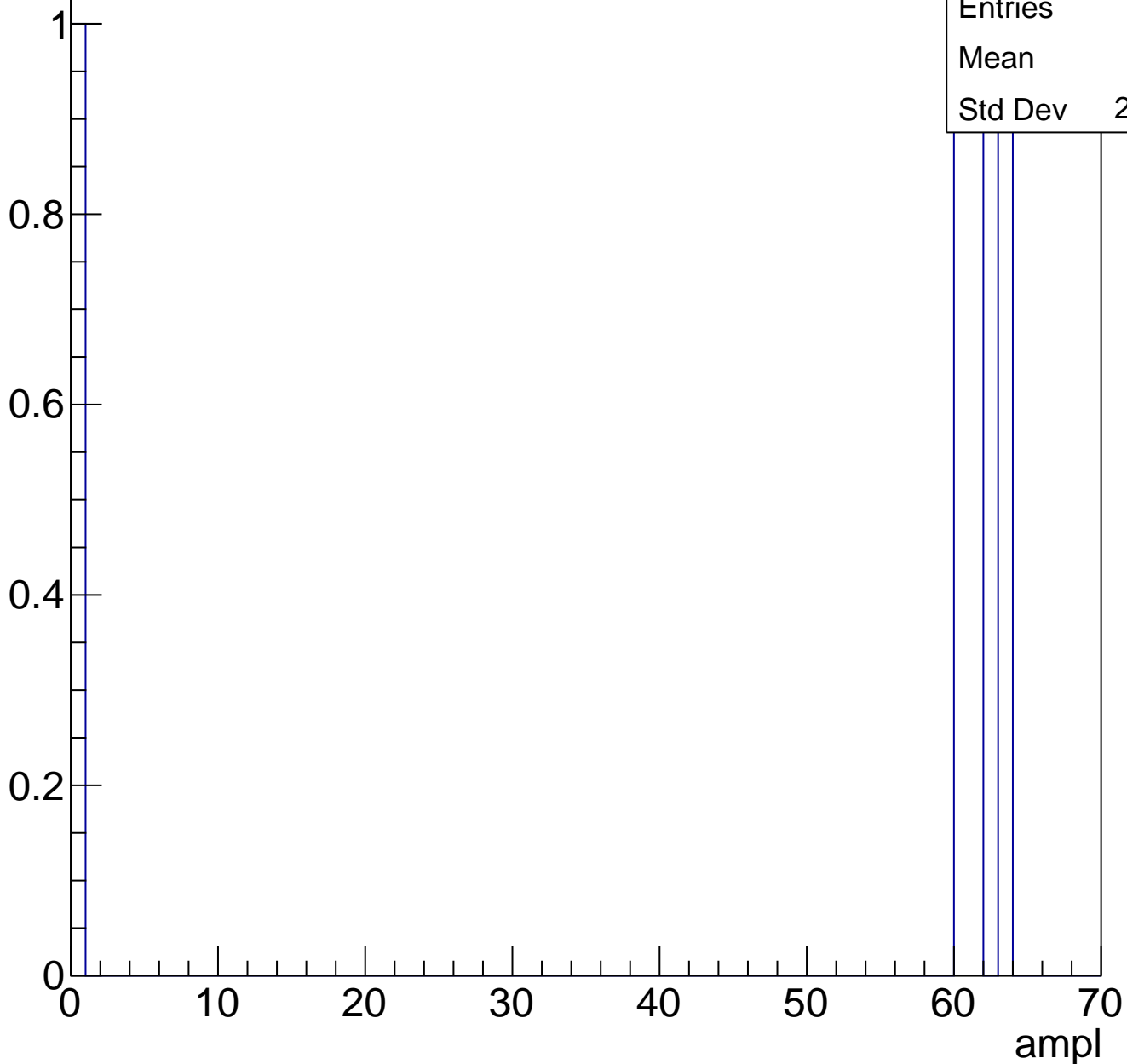
Entries	57
Mean	59.98
Std Dev	2.402



# B1L003S, U22-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch68, adc0

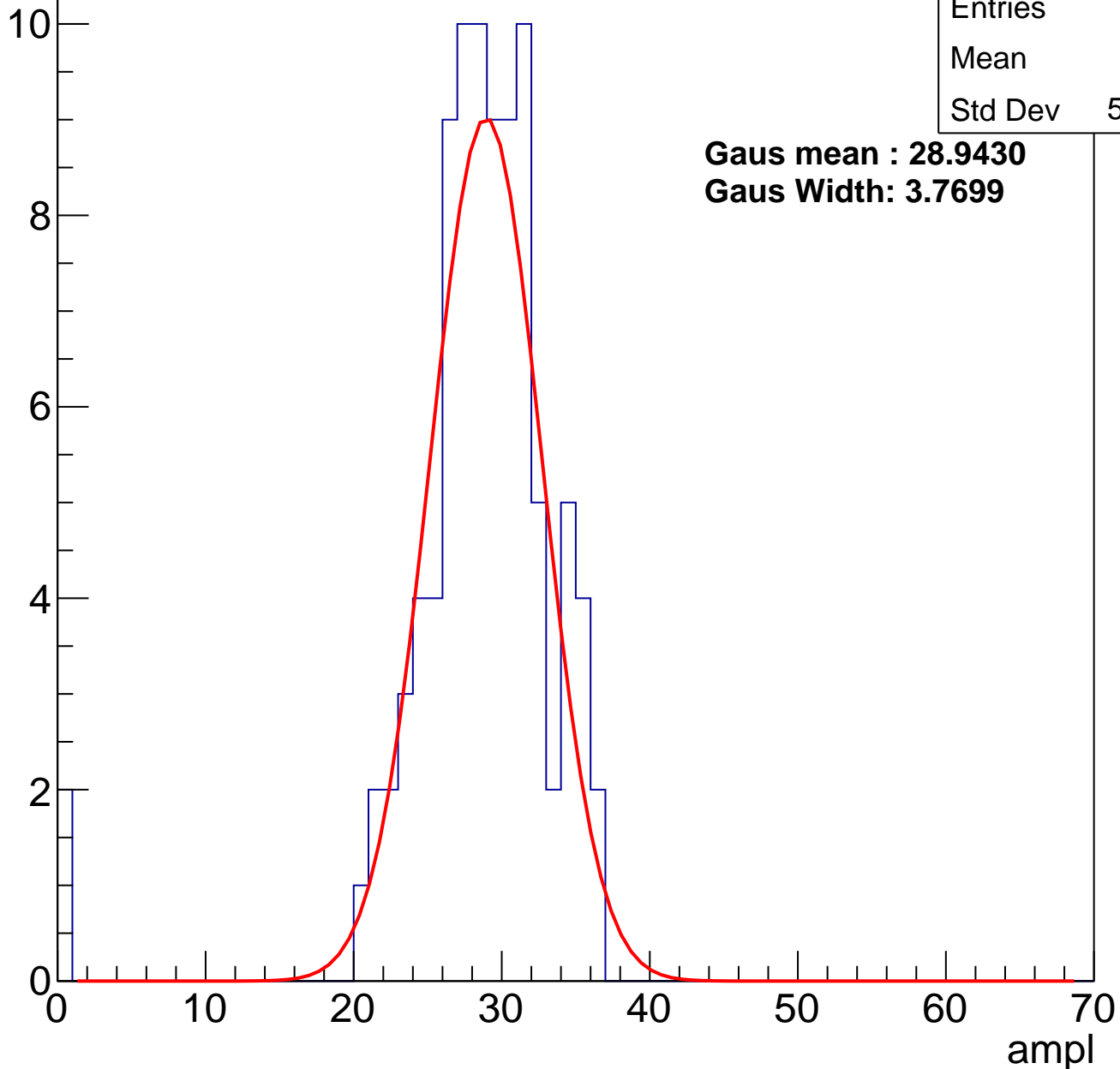
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	93
Mean	28
Std Dev	5.479

**Gaus mean : 28.9430**

**Gaus Width: 3.7699**

Entry



# B1L003S, U22-ch68, adc1

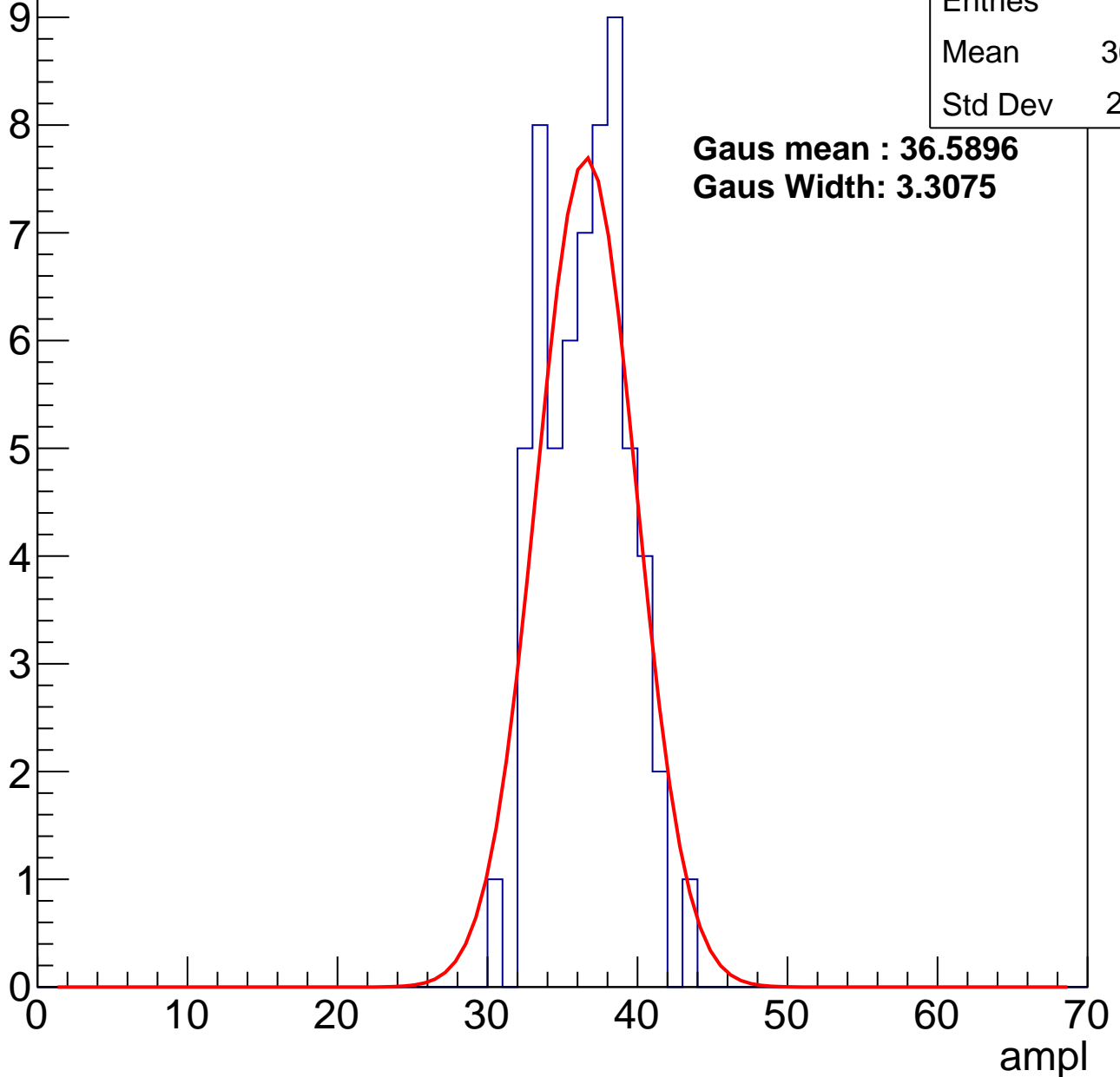
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.13
Std Dev	2.761

**Gaus mean : 36.5896**

**Gaus Width: 3.3075**



# B1L003S, U22-ch68, adc2

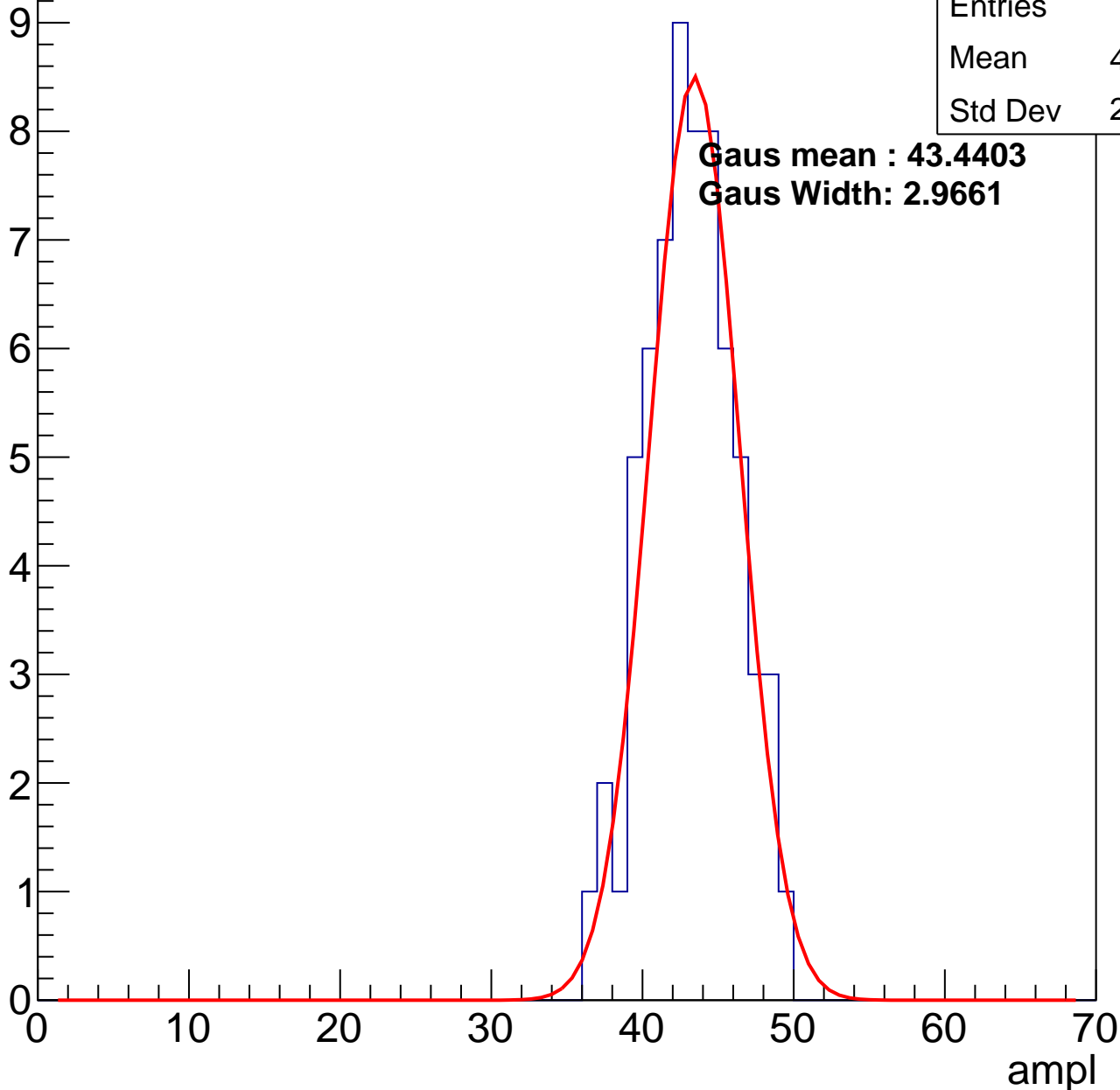
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.74
Std Dev	2.905

**Gaus mean : 43.4403**

**Gaus Width: 2.9661**

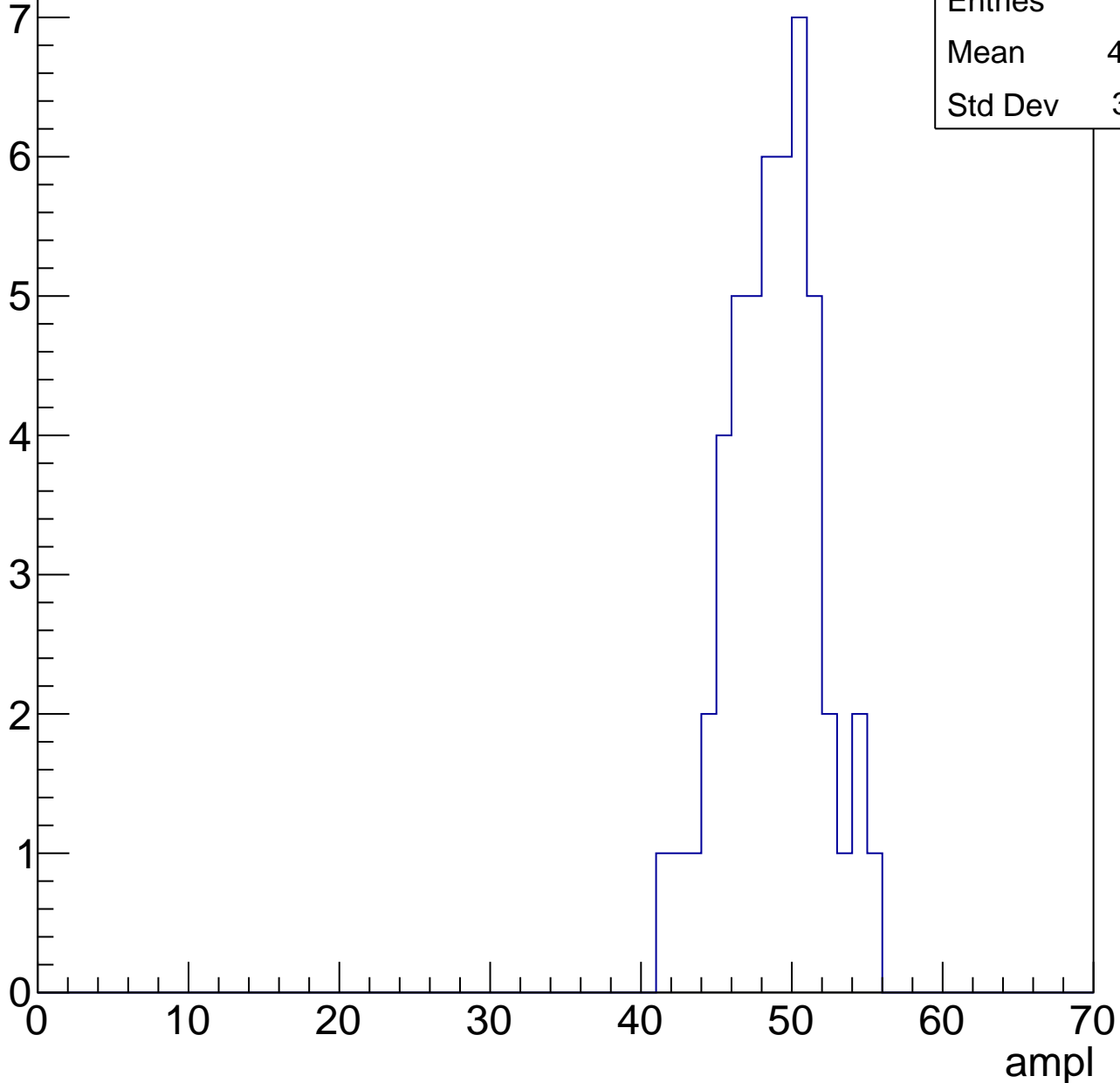


# B1L003S, U22-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	48.29
Std Dev	3.051

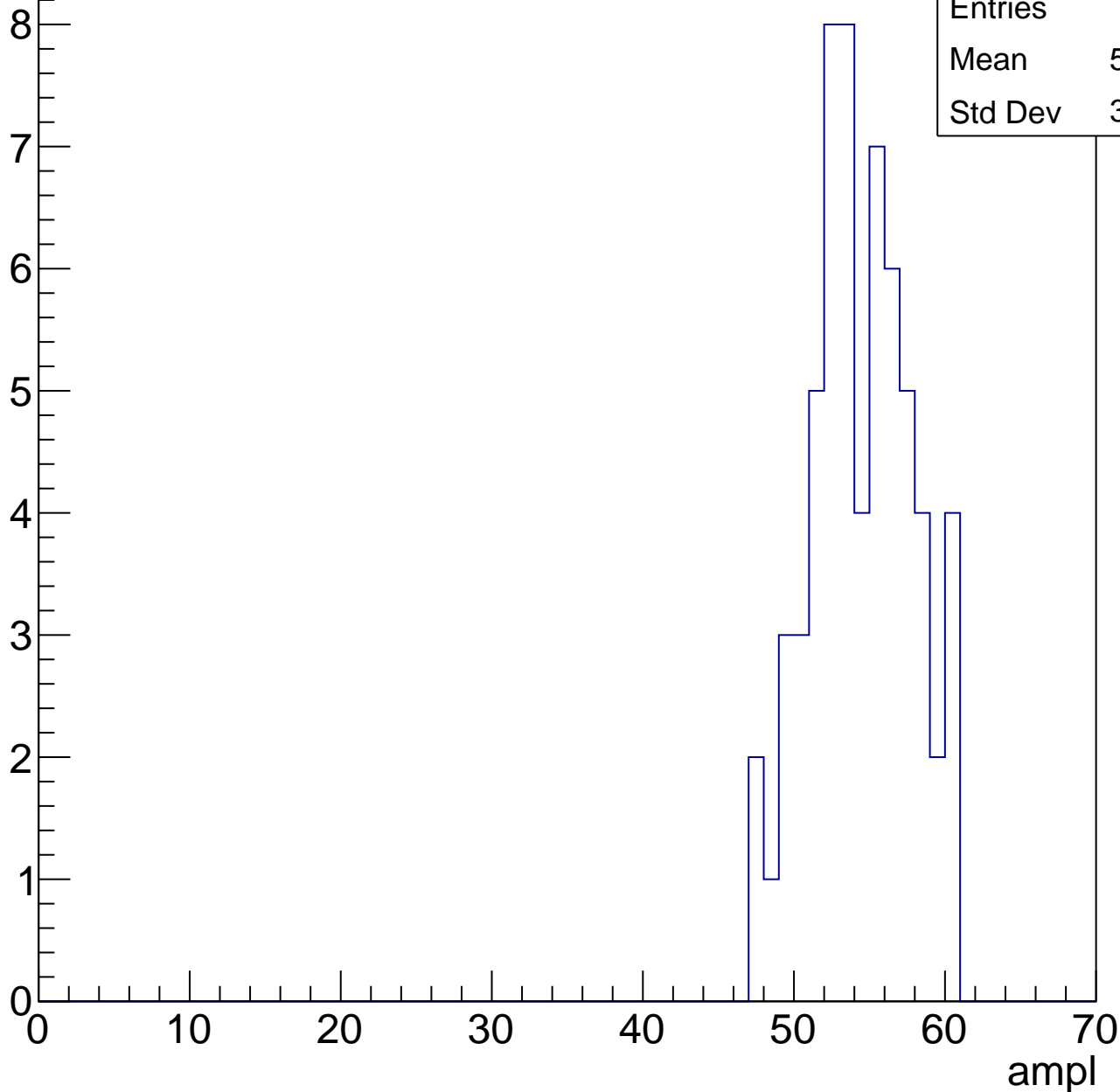


# B1L003S, U22-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

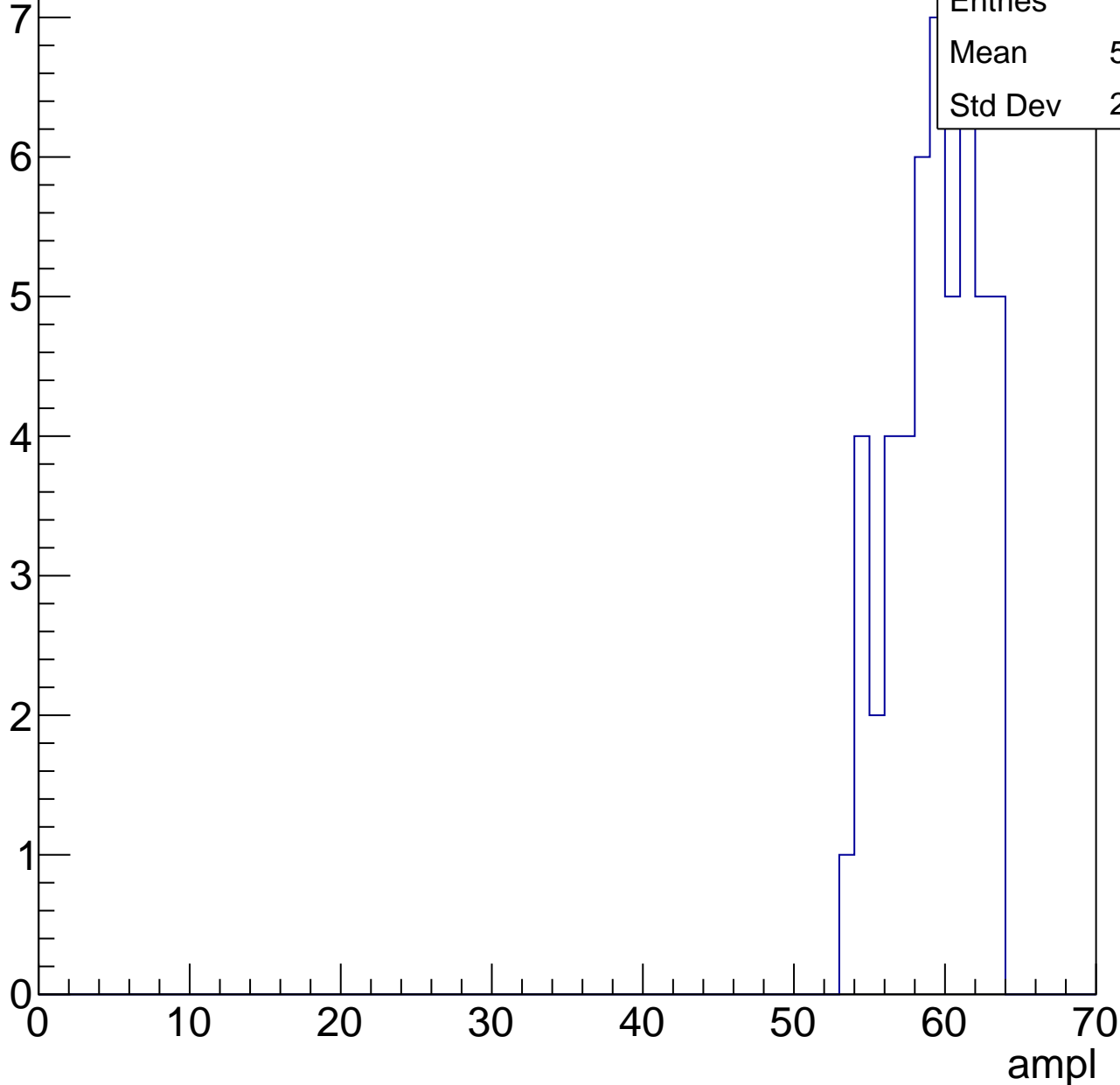
Entries	62
Mean	53.97
Std Dev	3.302



# B1L003S, U22-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



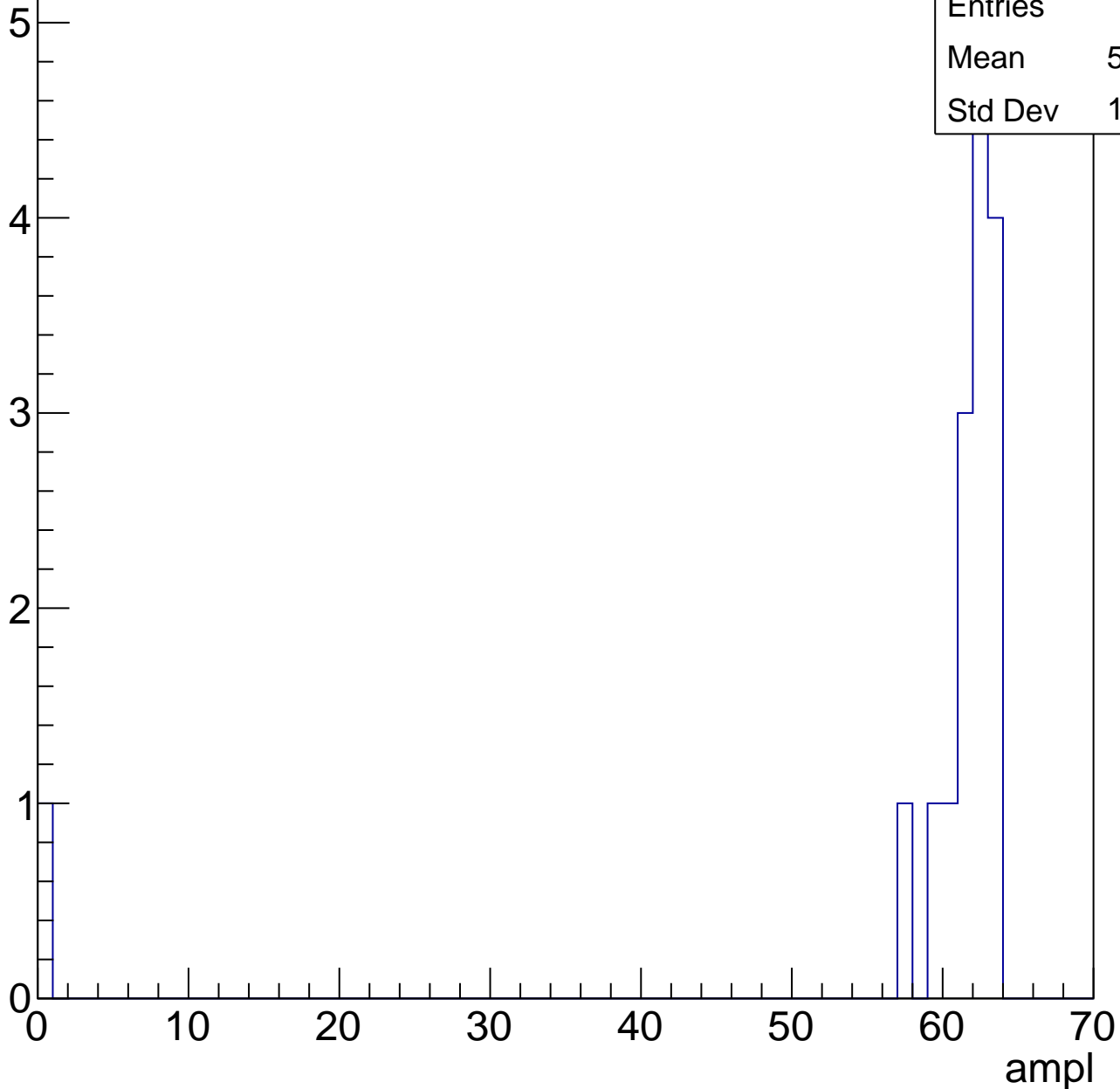
Entries	50
Mean	58.88
Std Dev	2.769

# B1L003S, U22-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.56
Std Dev	14.95





# B1L003S, U22-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	30.96
Std Dev	3.501

**Gaus mean : 31.3760**

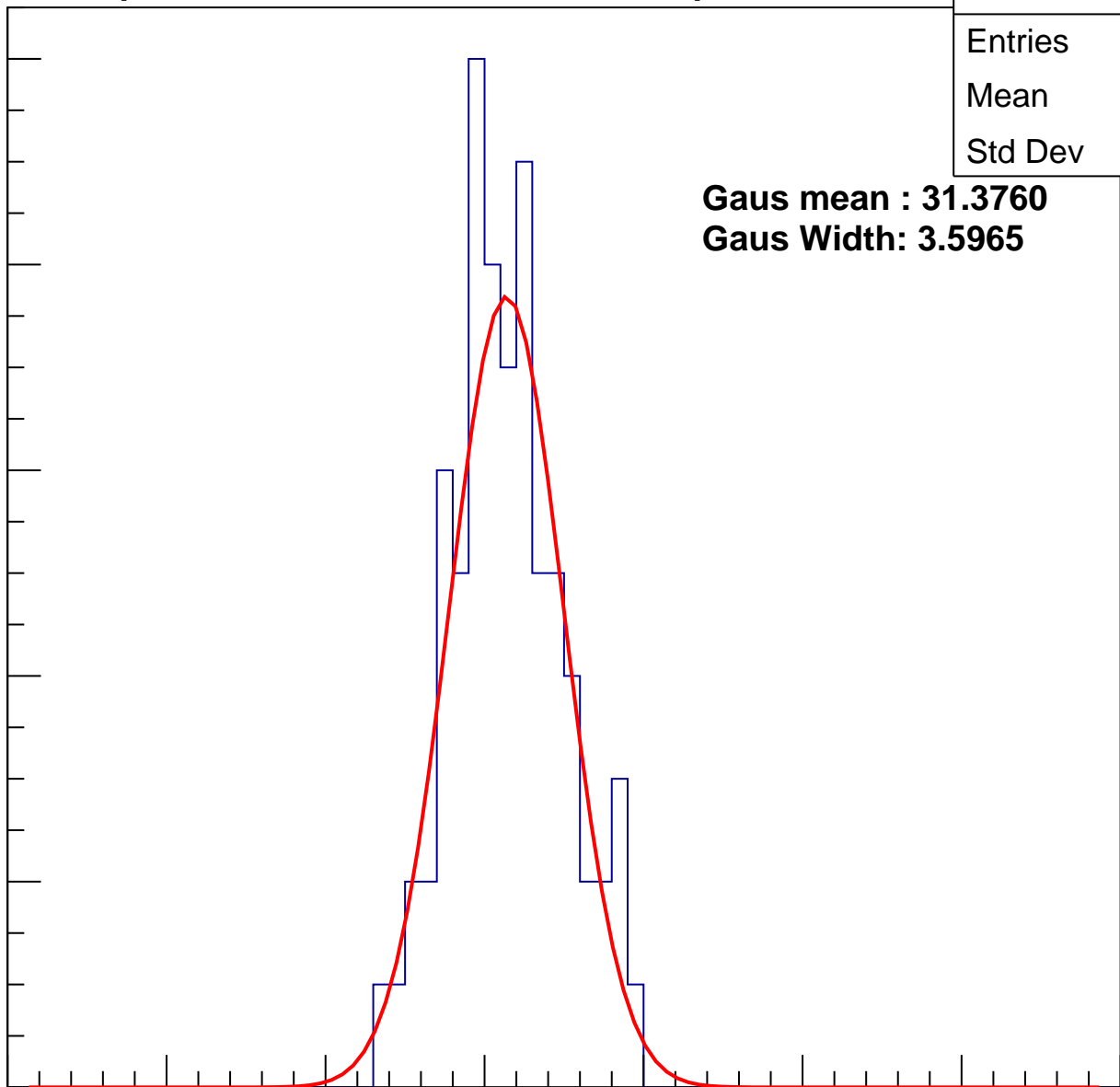
**Gaus Width: 3.5965**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch69, adc1

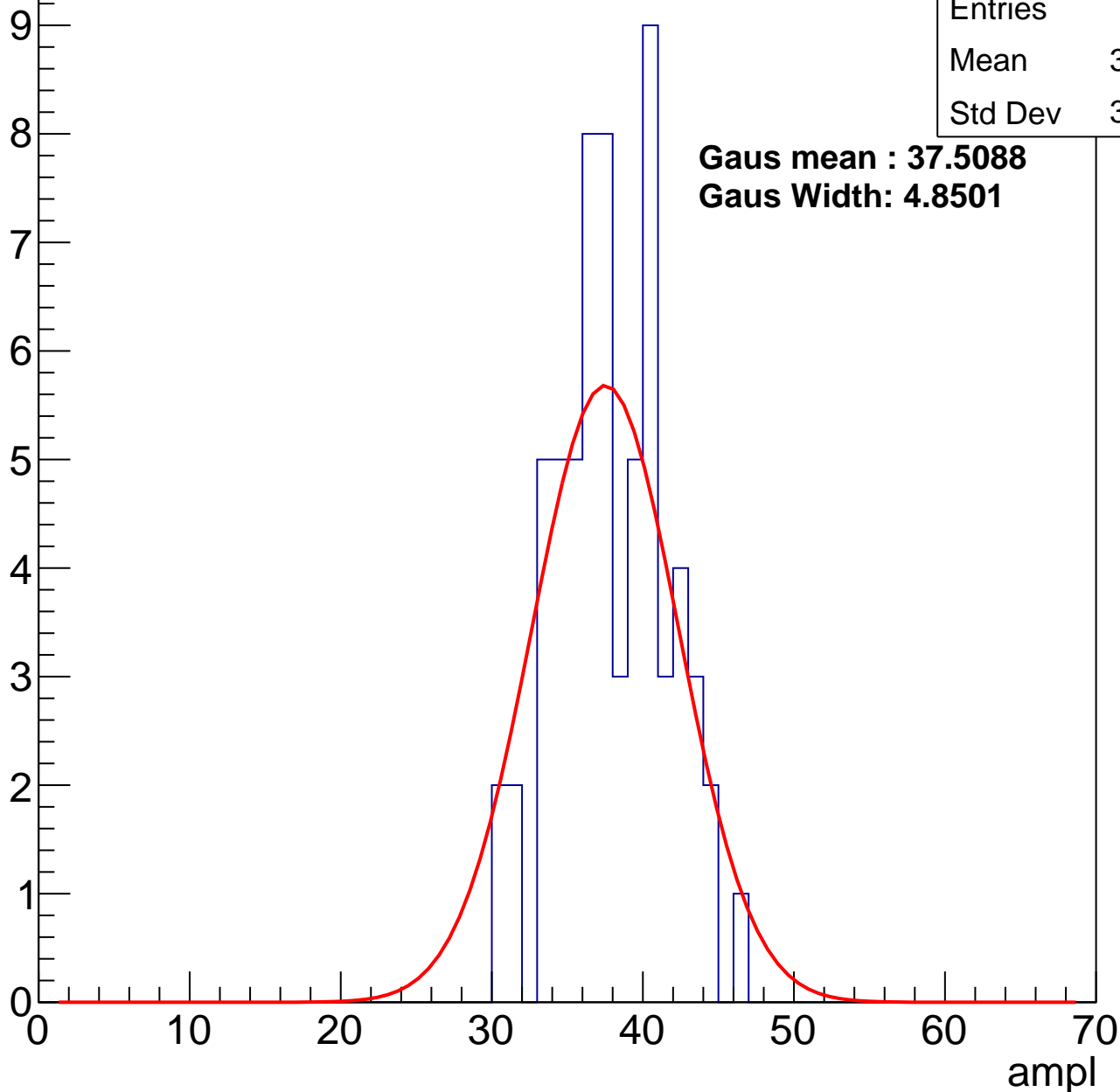
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	37.52
Std Dev	3.604

**Gaus mean : 37.5088**

**Gaus Width: 4.8501**



# B1L003S, U22-ch69, adc2

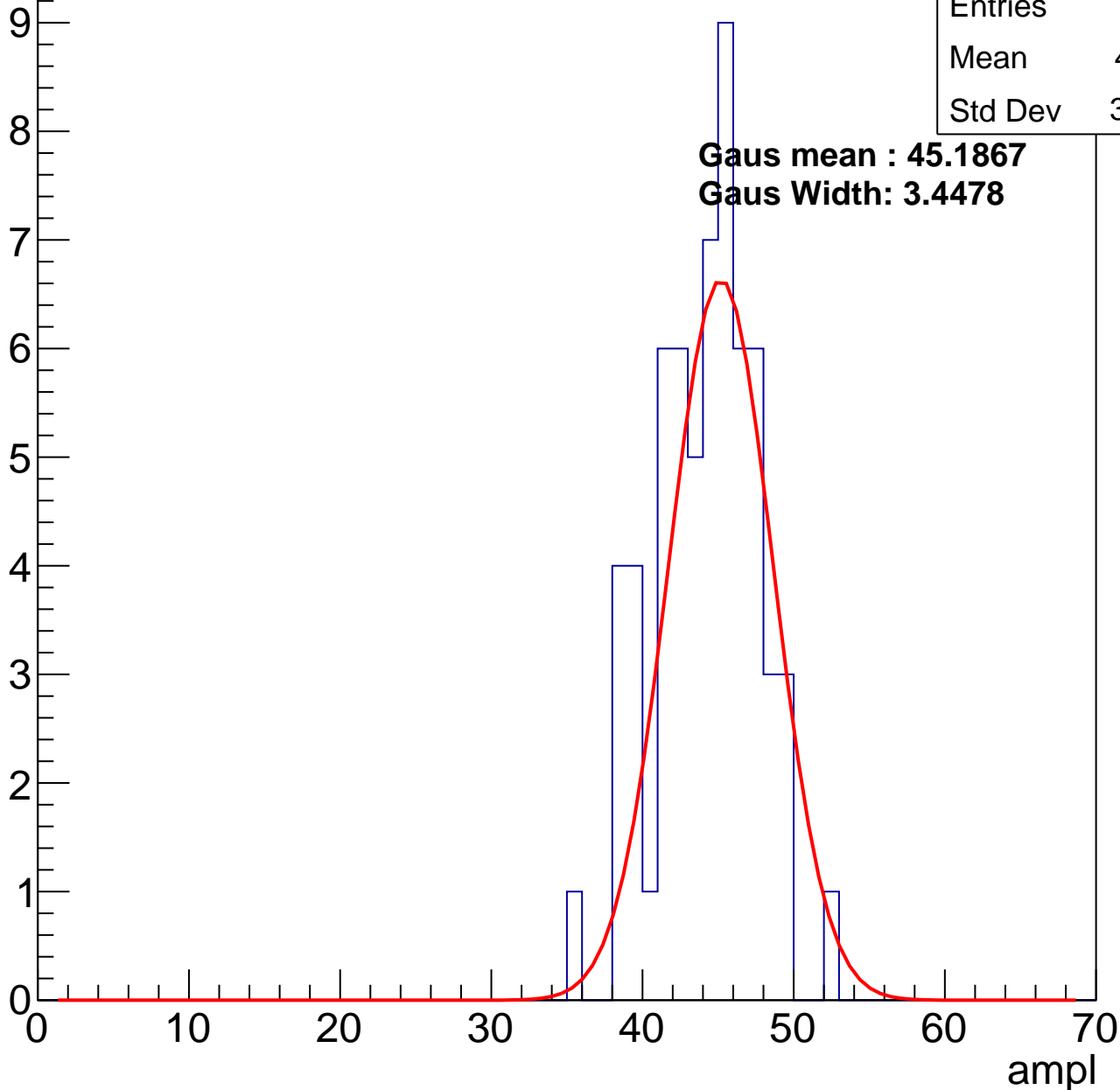
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.71
Std Dev	3.343

**Gaus mean : 45.1867**

**Gaus Width: 3.4478**

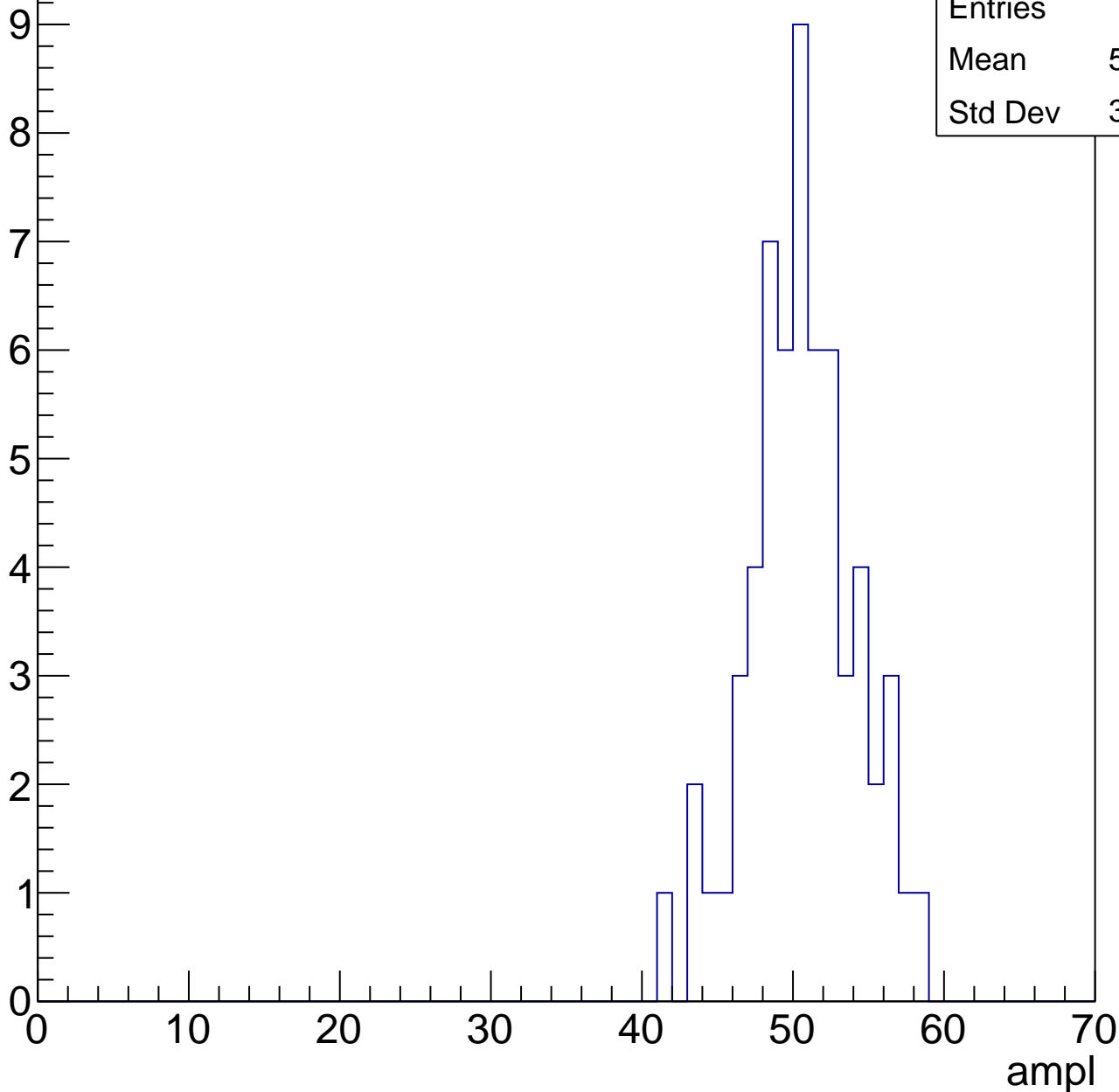


# B1L003S, U22-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

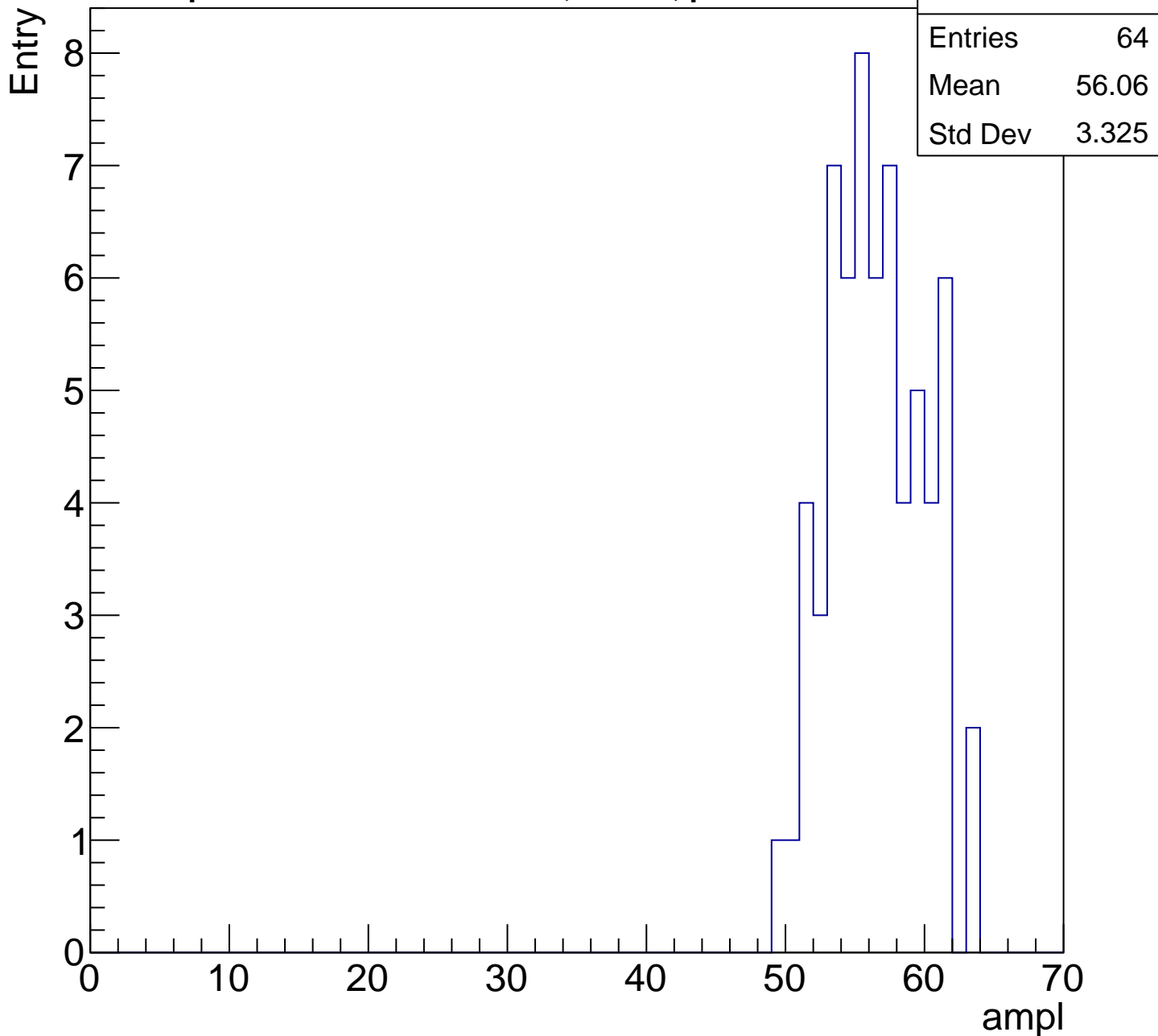
Entry

Entries	60
Mean	50.13
Std Dev	3.533



# B1L003S, U22-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

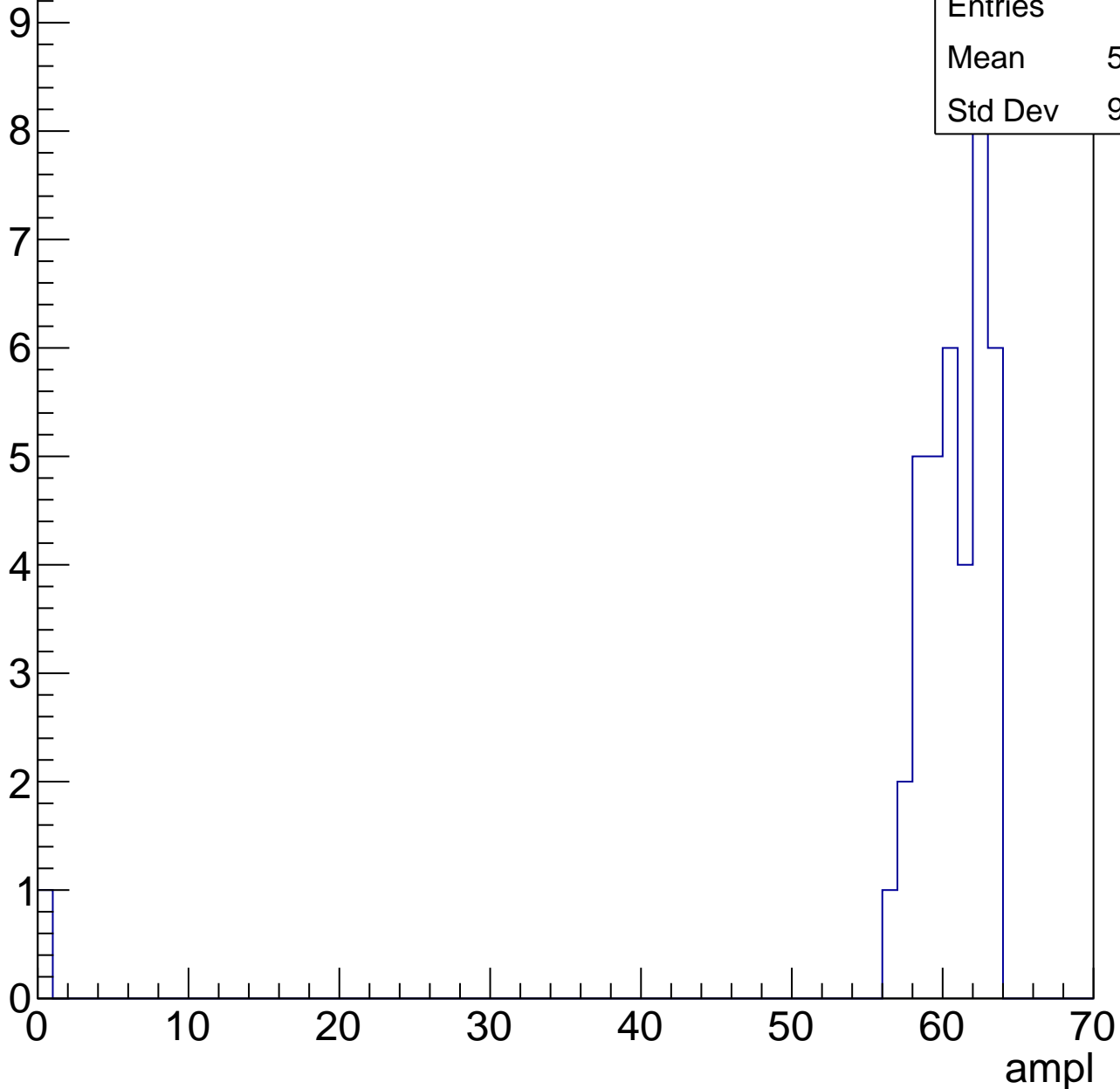


# B1L003S, U22-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.85
Std Dev	9.742



# B1L003S, U22-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch70, adc0

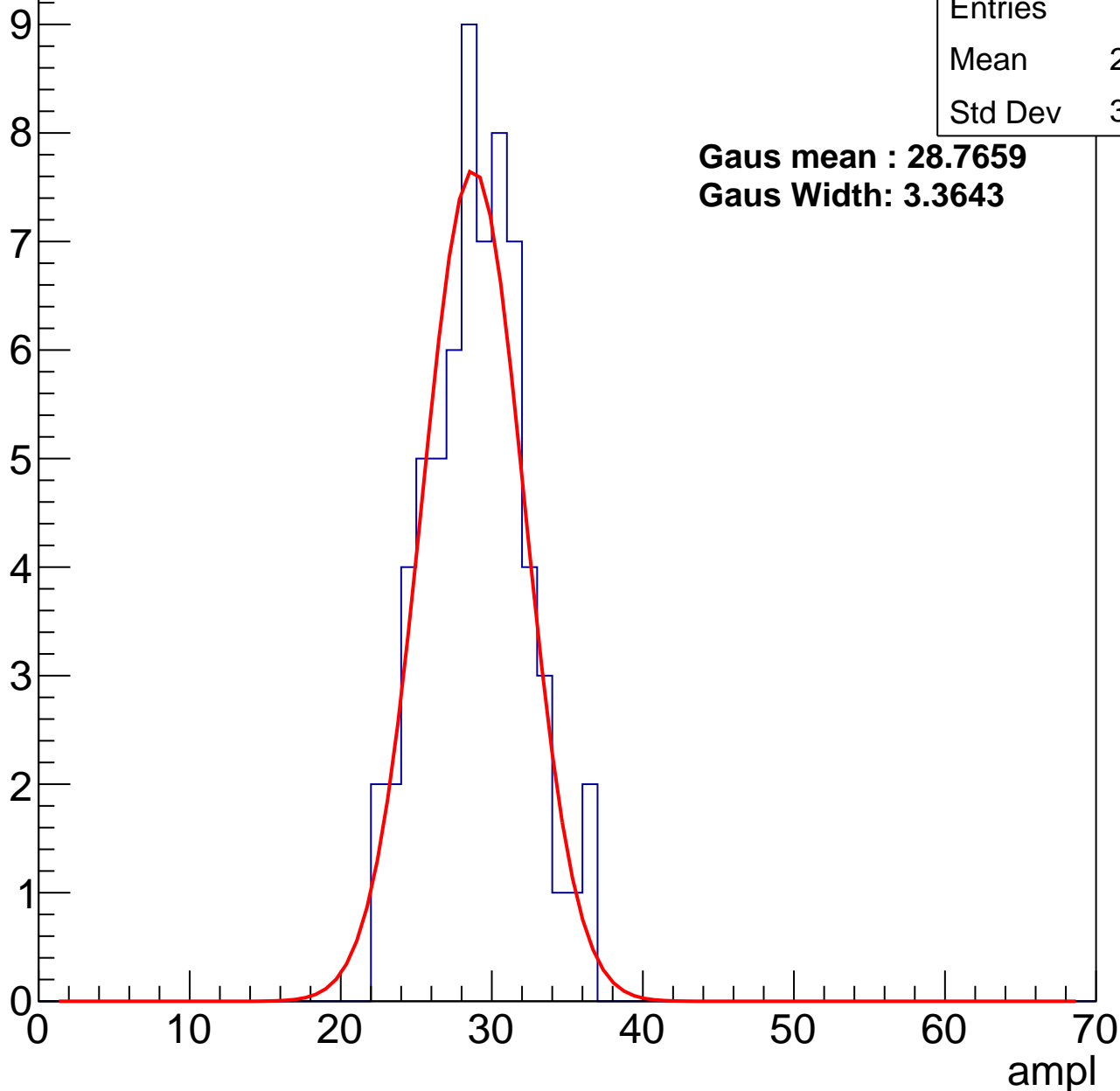
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	28.53
Std Dev	3.239

**Gaus mean : 28.7659**

**Gaus Width: 3.3643**



# B1L003S, U22-ch70, adc1

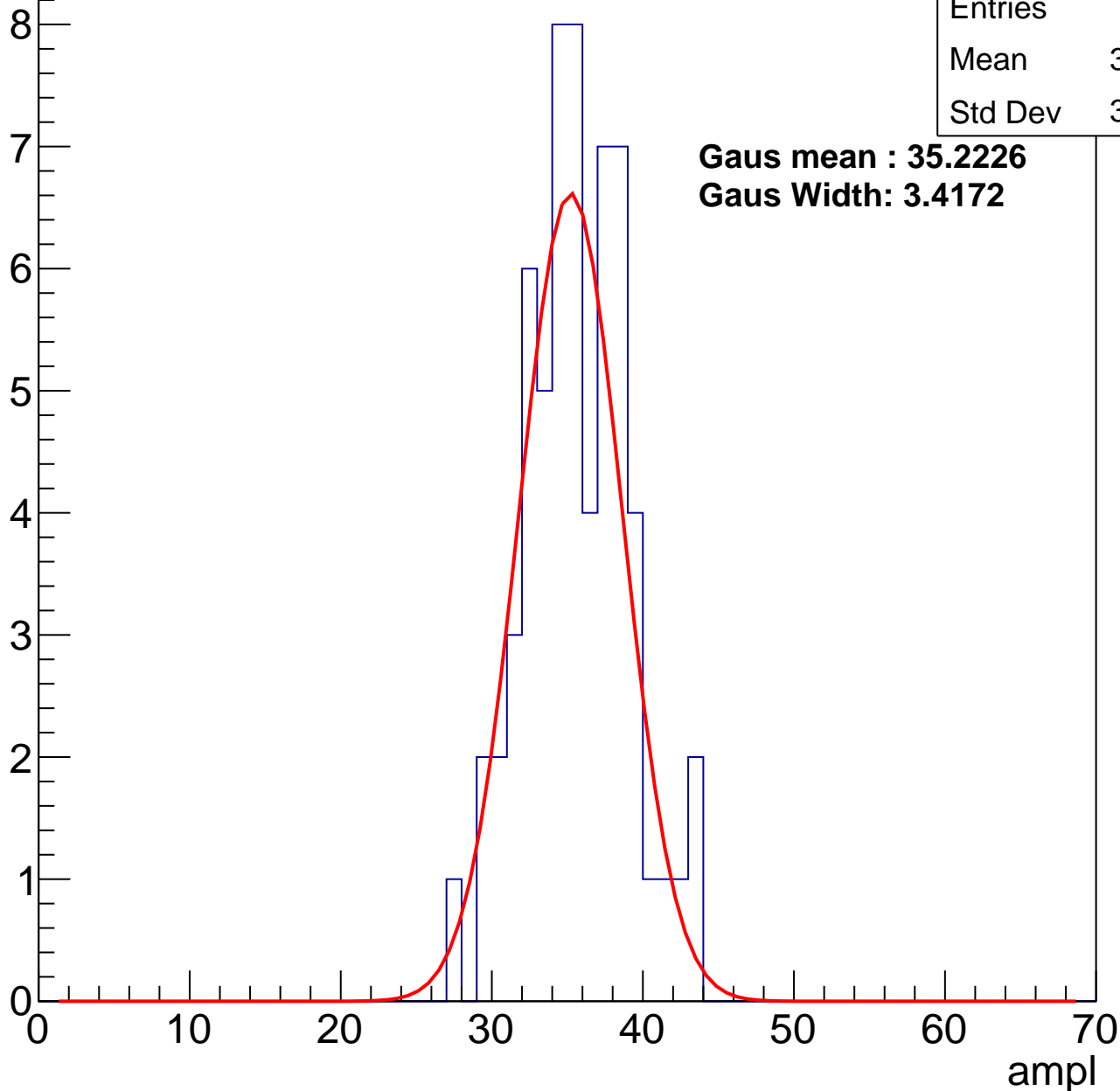
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.18
Std Dev	3.387

**Gaus mean : 35.2226**

**Gaus Width: 3.4172**



# B1L003S, U22-ch70, adc2

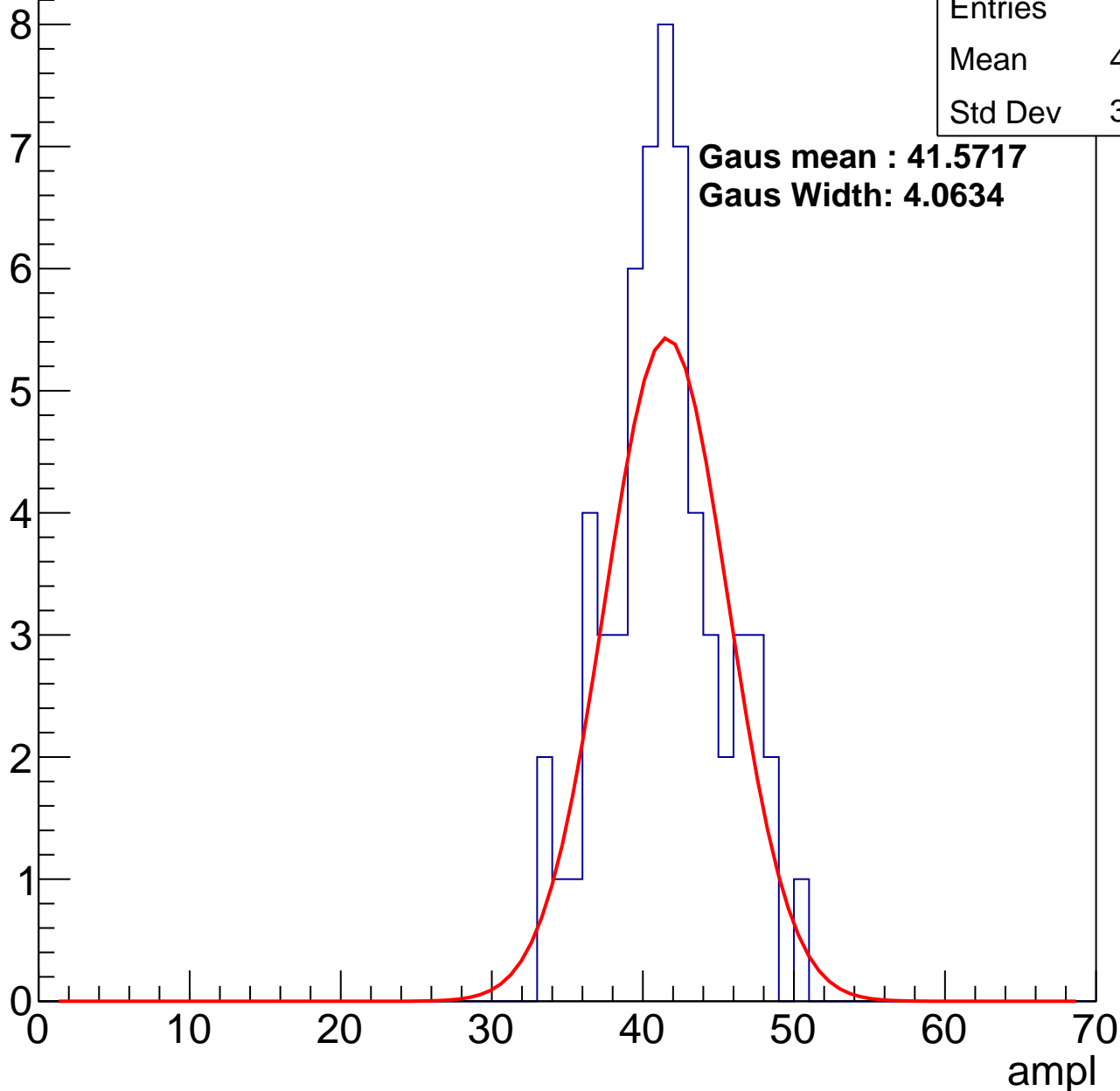
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	40.98
Std Dev	3.792

**Gaus mean : 41.5717**

**Gaus Width: 4.0634**

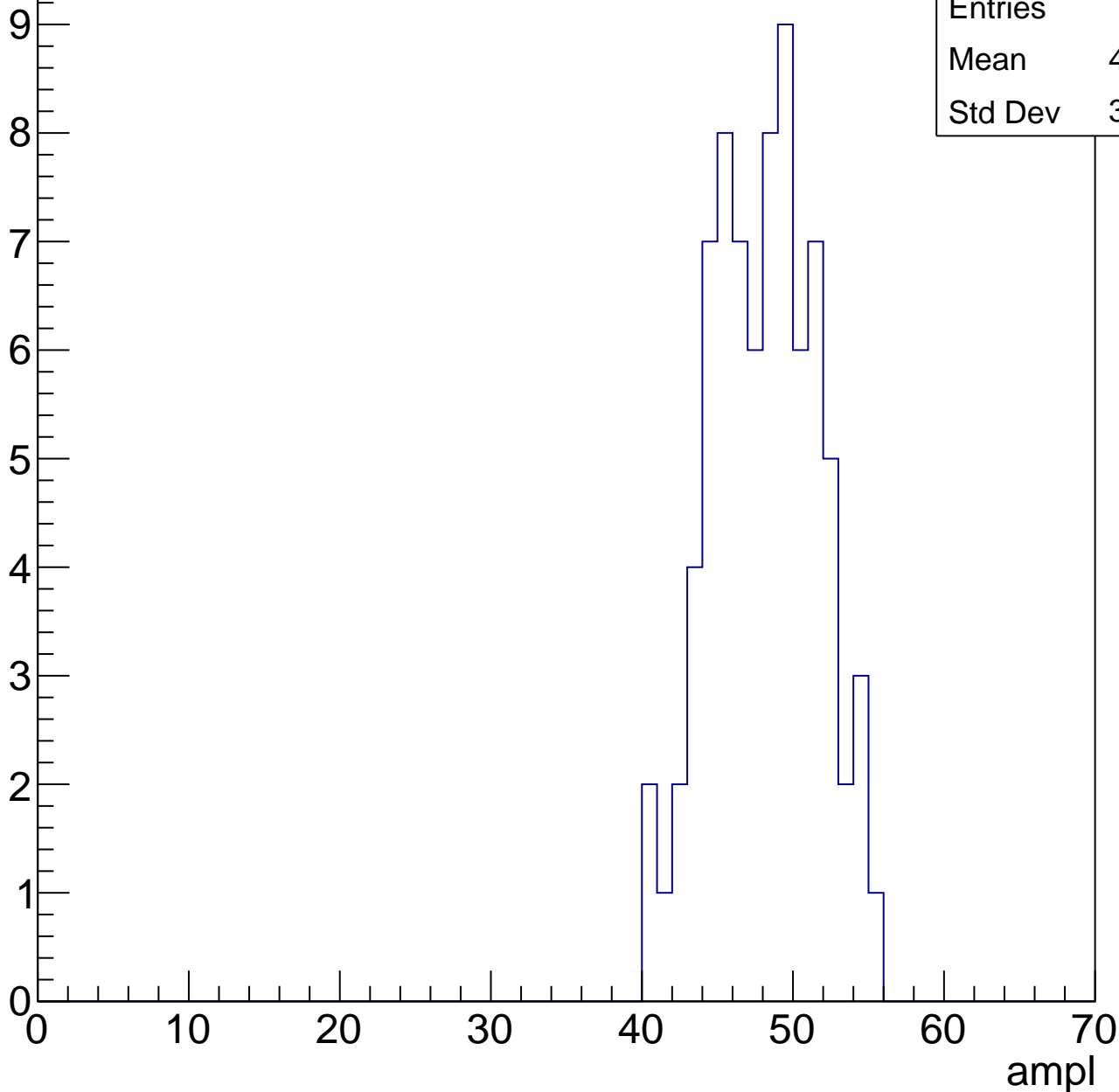


# B1L003S, U22-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	47.62
Std Dev	3.469

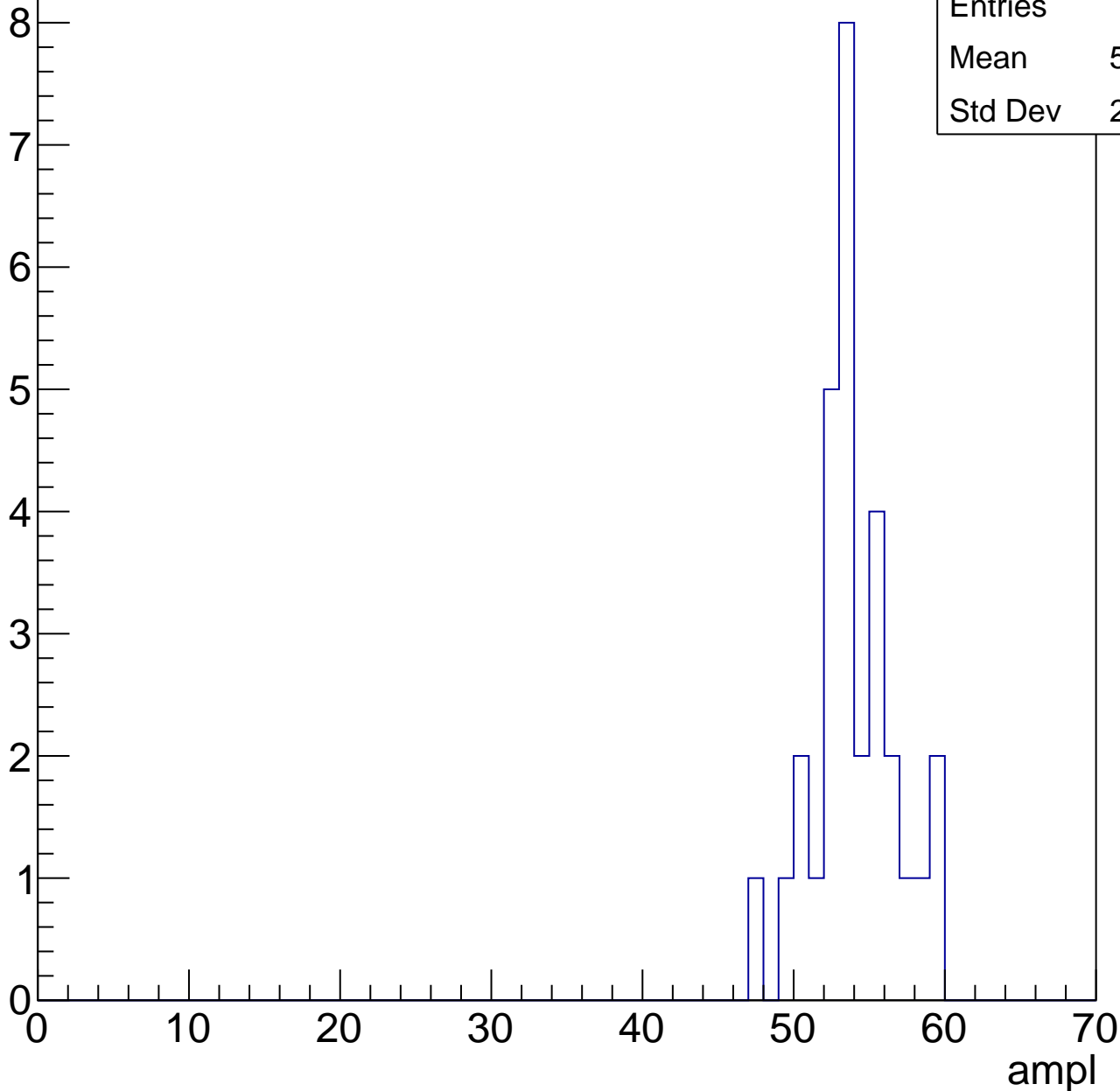


# B1L003S, U22-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

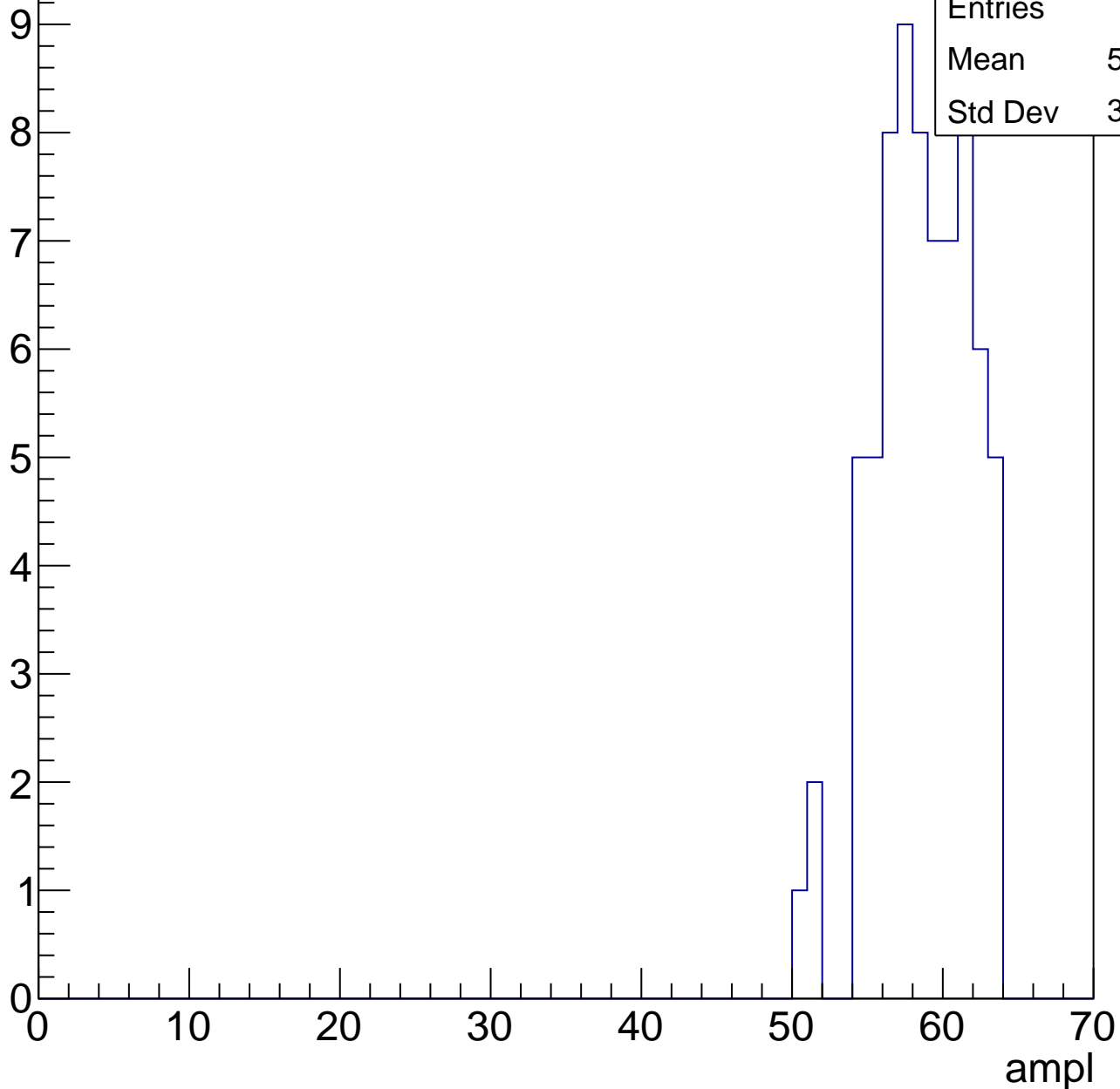
Entries	30
Mean	53.47
Std Dev	2.717



# B1L003S, U22-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

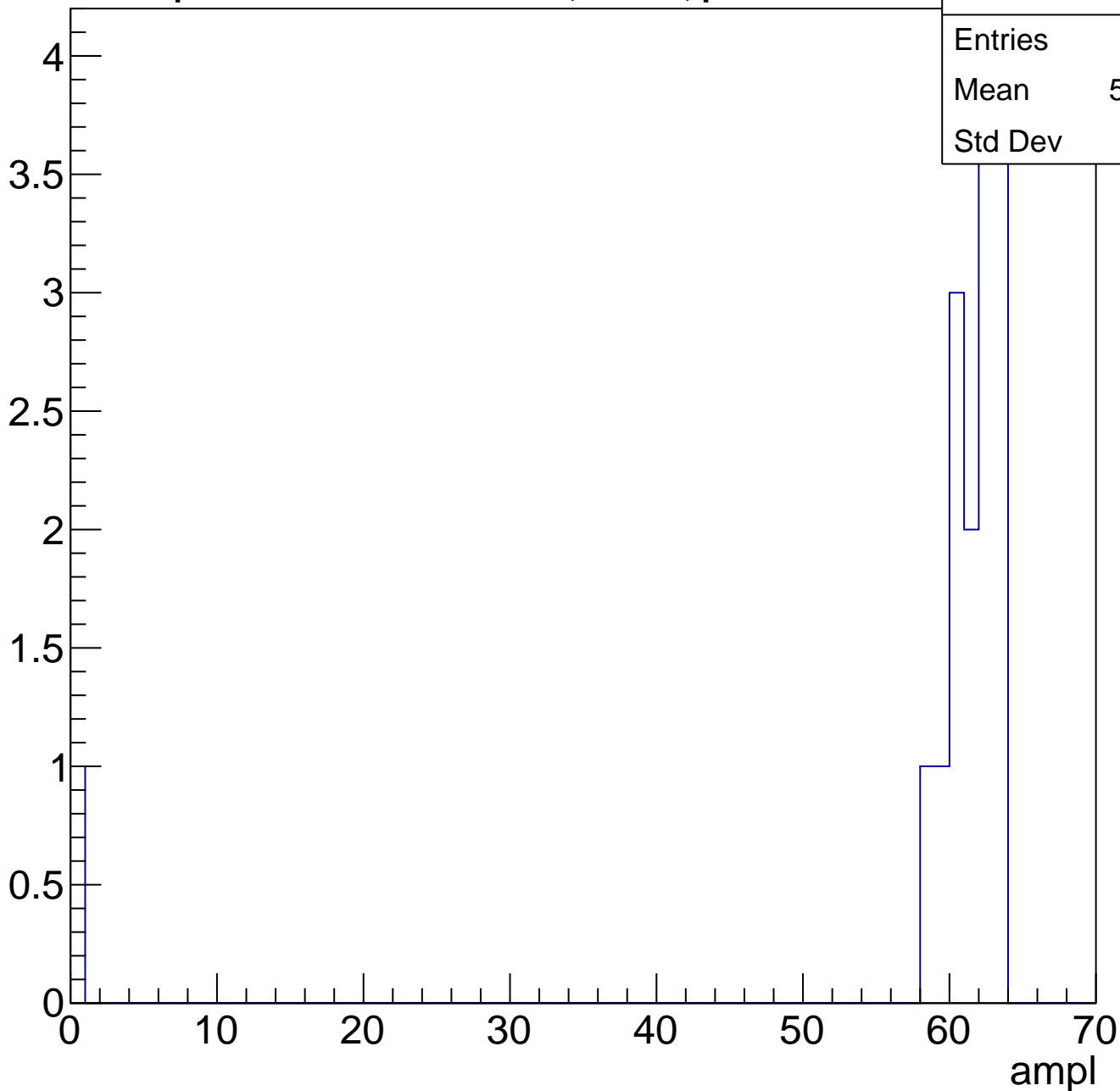


Entries	71
Mean	58.17
Std Dev	3.035

# B1L003S, U22-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch71, adc0

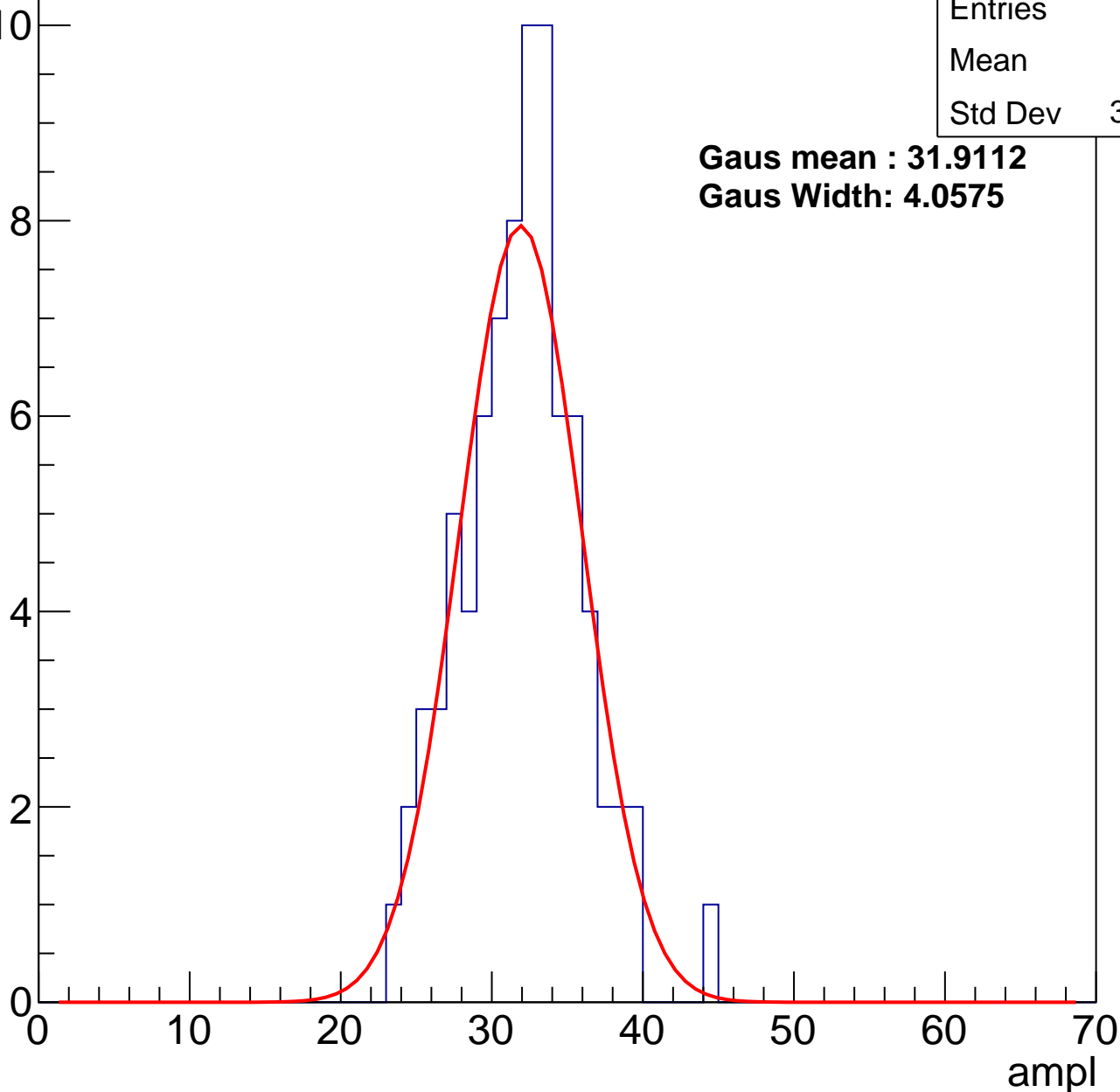
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	82
Mean	31.5
Std Dev	3.886

**Gaus mean : 31.9112**

**Gaus Width: 4.0575**



# B1L003S, U22-ch71, adc1

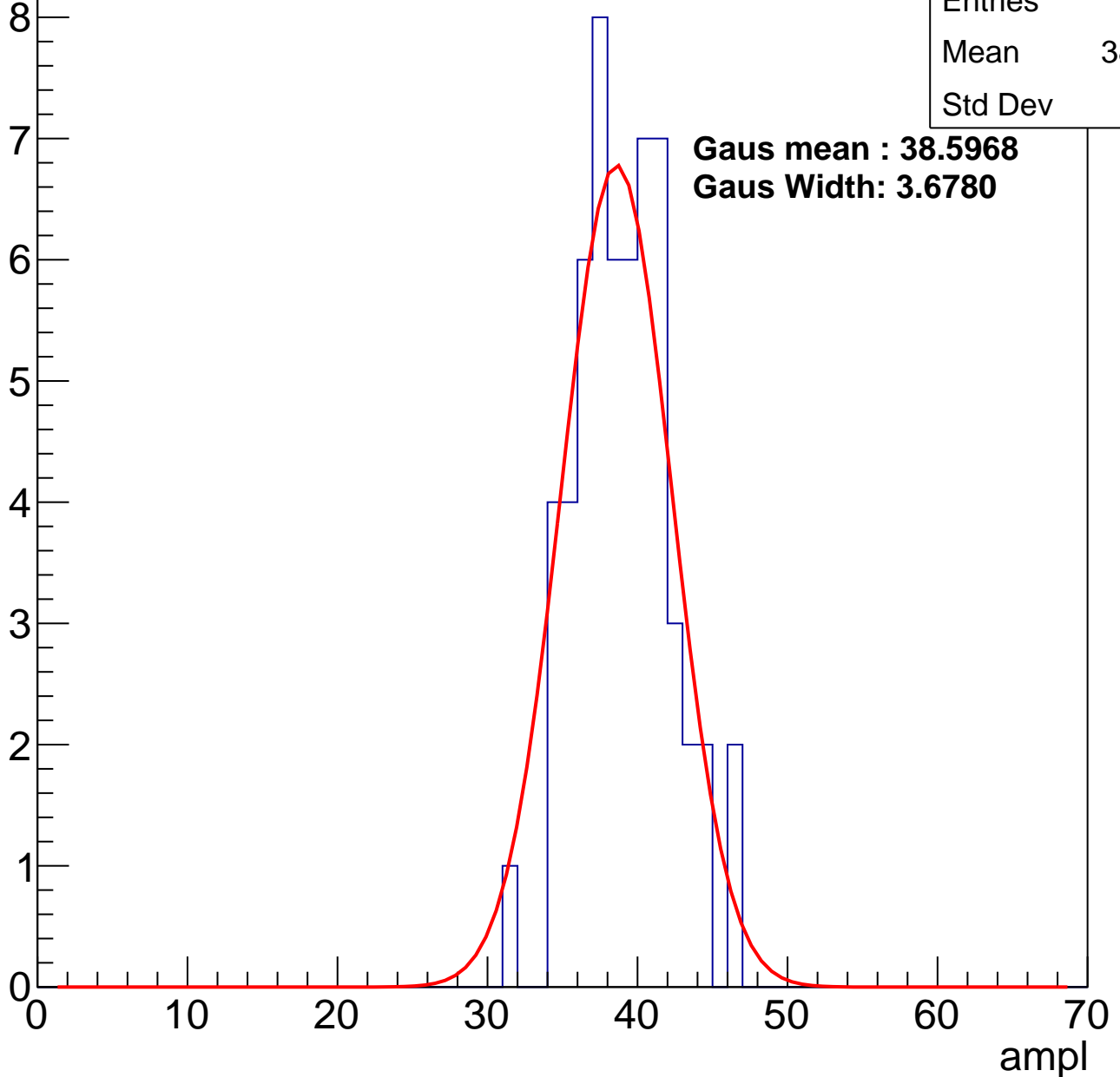
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	38.62
Std Dev	3.09

**Gaus mean : 38.5968**

**Gaus Width: 3.6780**



# B1L003S, U22-ch71, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	57
Mean	44.61
Std Dev	3.172

**Gaus mean : 45.6541**

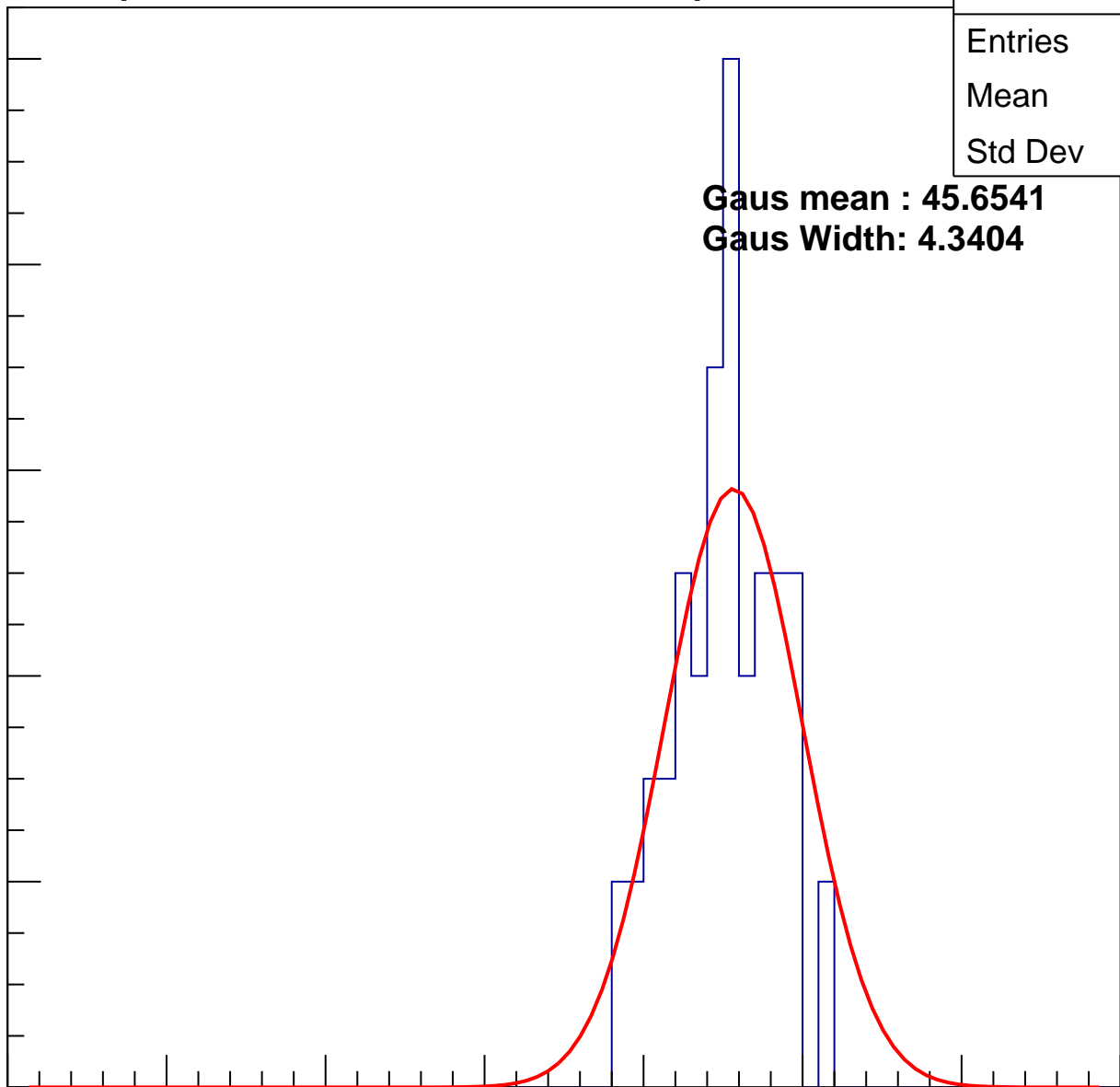
**Gaus Width: 4.3404**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	67
Mean	50.13
Std Dev	3.599

Entry

10

8

6

4

2

0

0

10

20

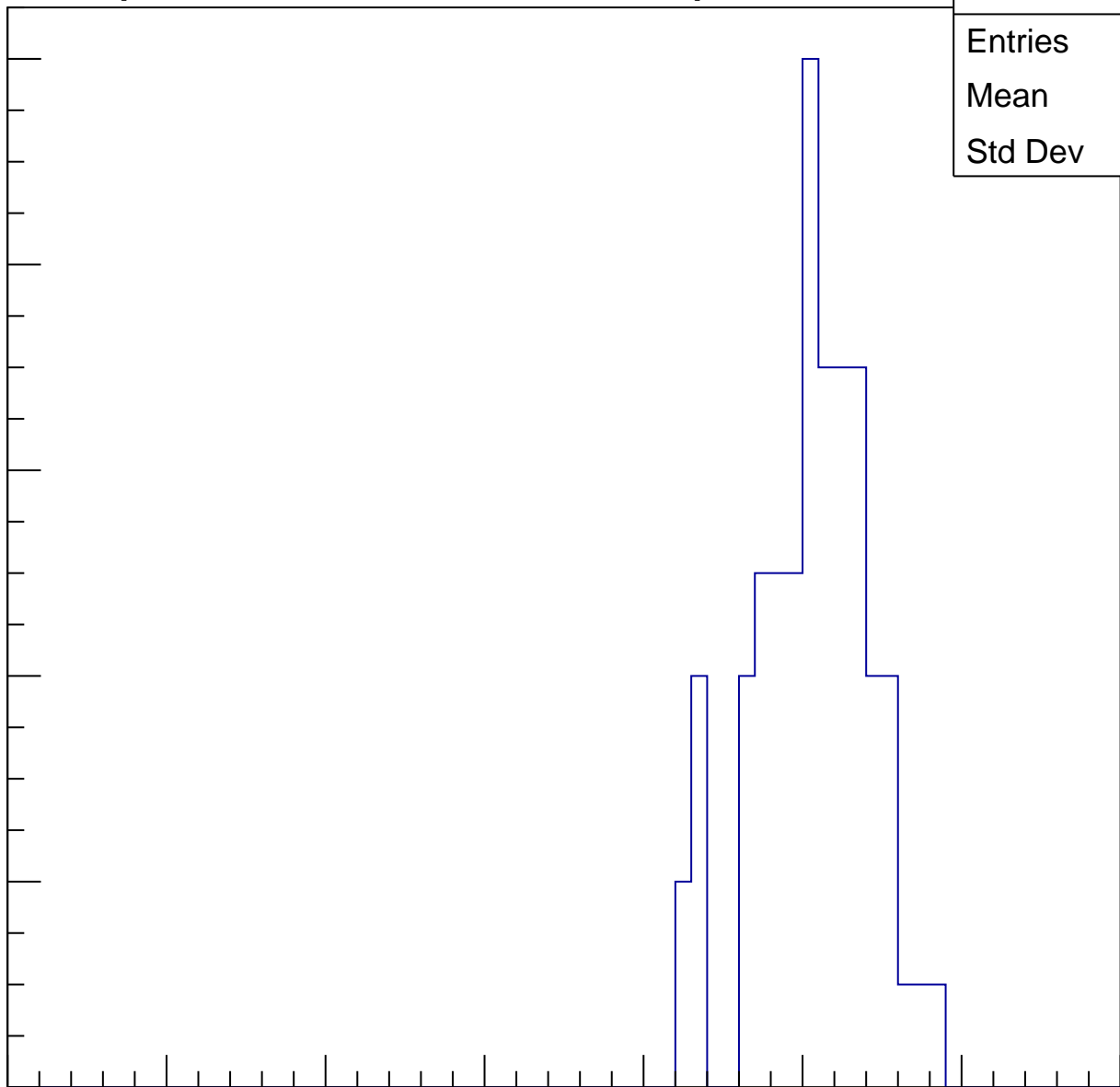
30

40

50

60

ampl

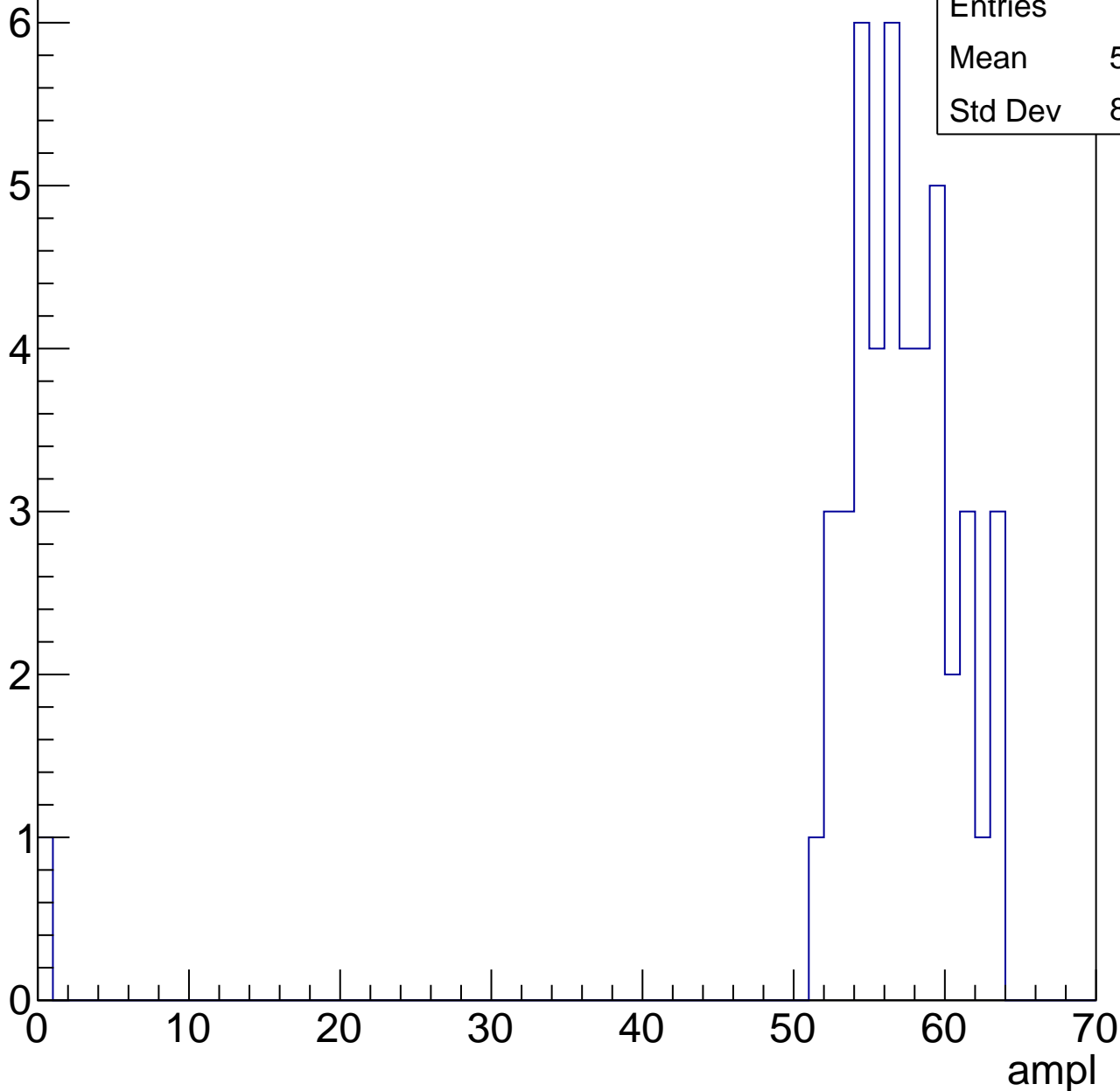


# B1L003S, U22-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

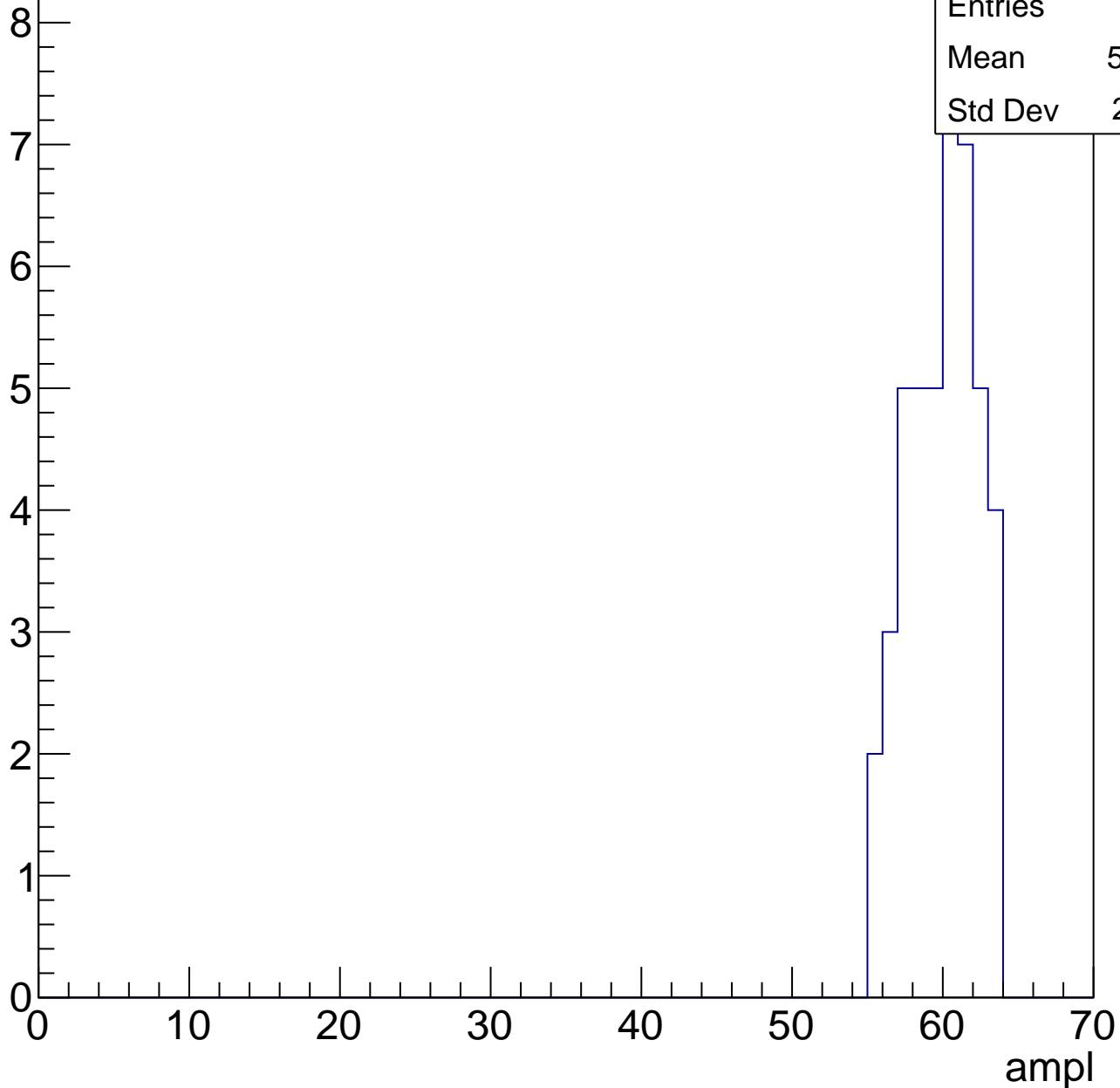
Entries	46
Mean	55.54
Std Dev	8.858



# B1L003S, U22-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

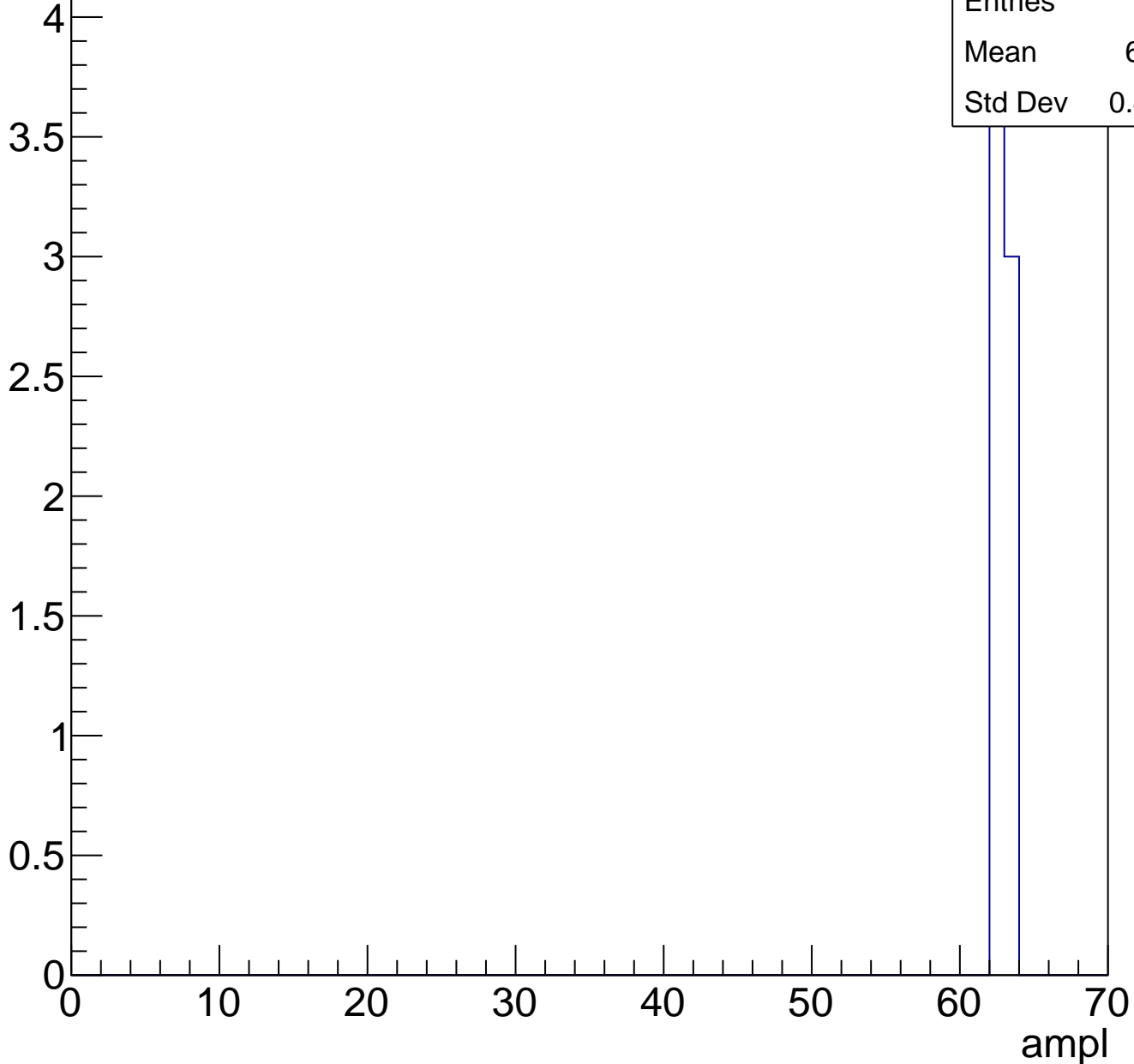


Entries	44
Mean	59.48
Std Dev	2.231

# B1L003S, U22-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	7
Mean	62.43
Std Dev	0.4949



# B1L003S, U22-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch72, adc0

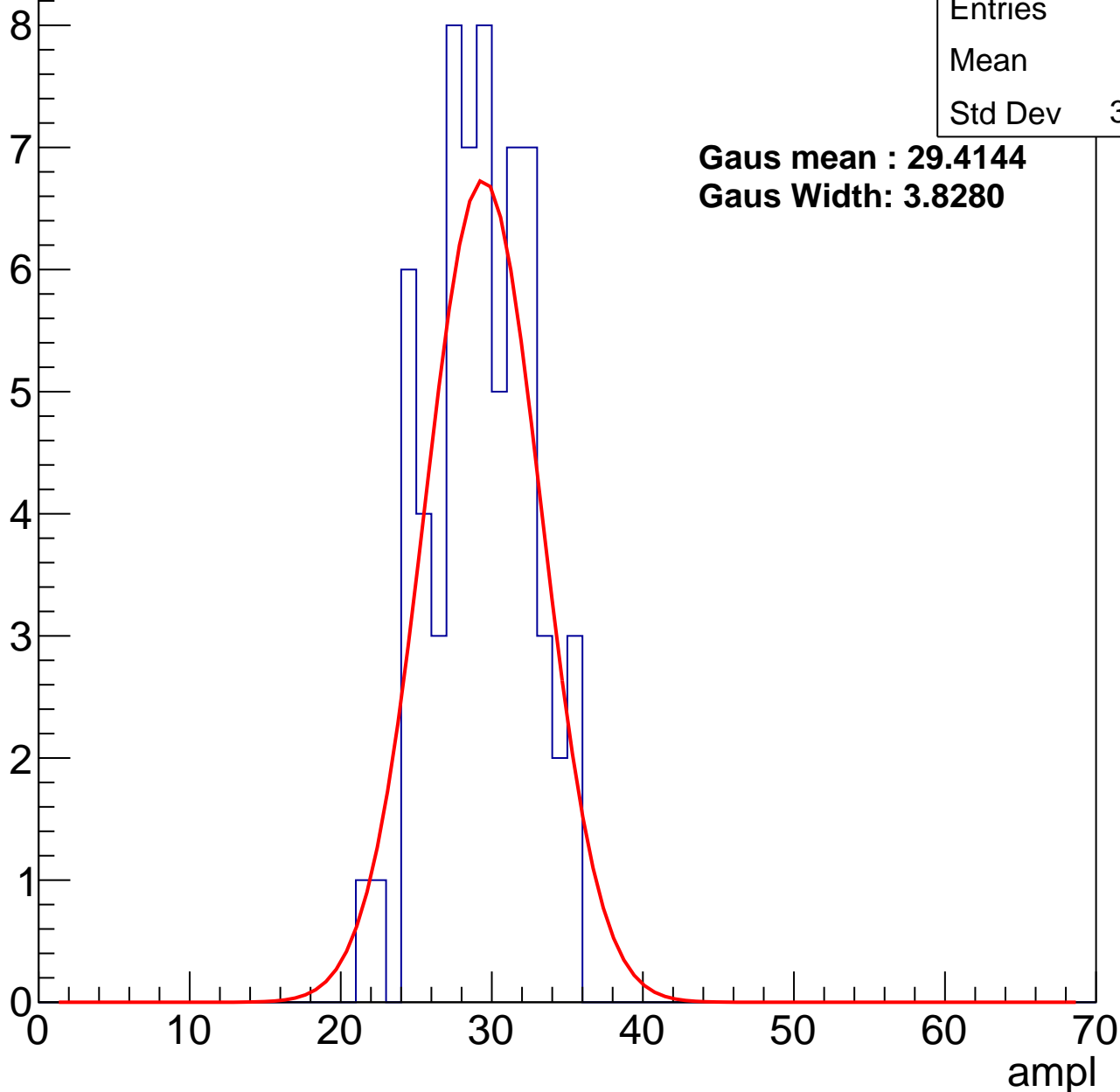
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.8
Std Dev	3.264

**Gaus mean : 29.4144**

**Gaus Width: 3.8280**



# B1L003S, U22-ch72, adc1

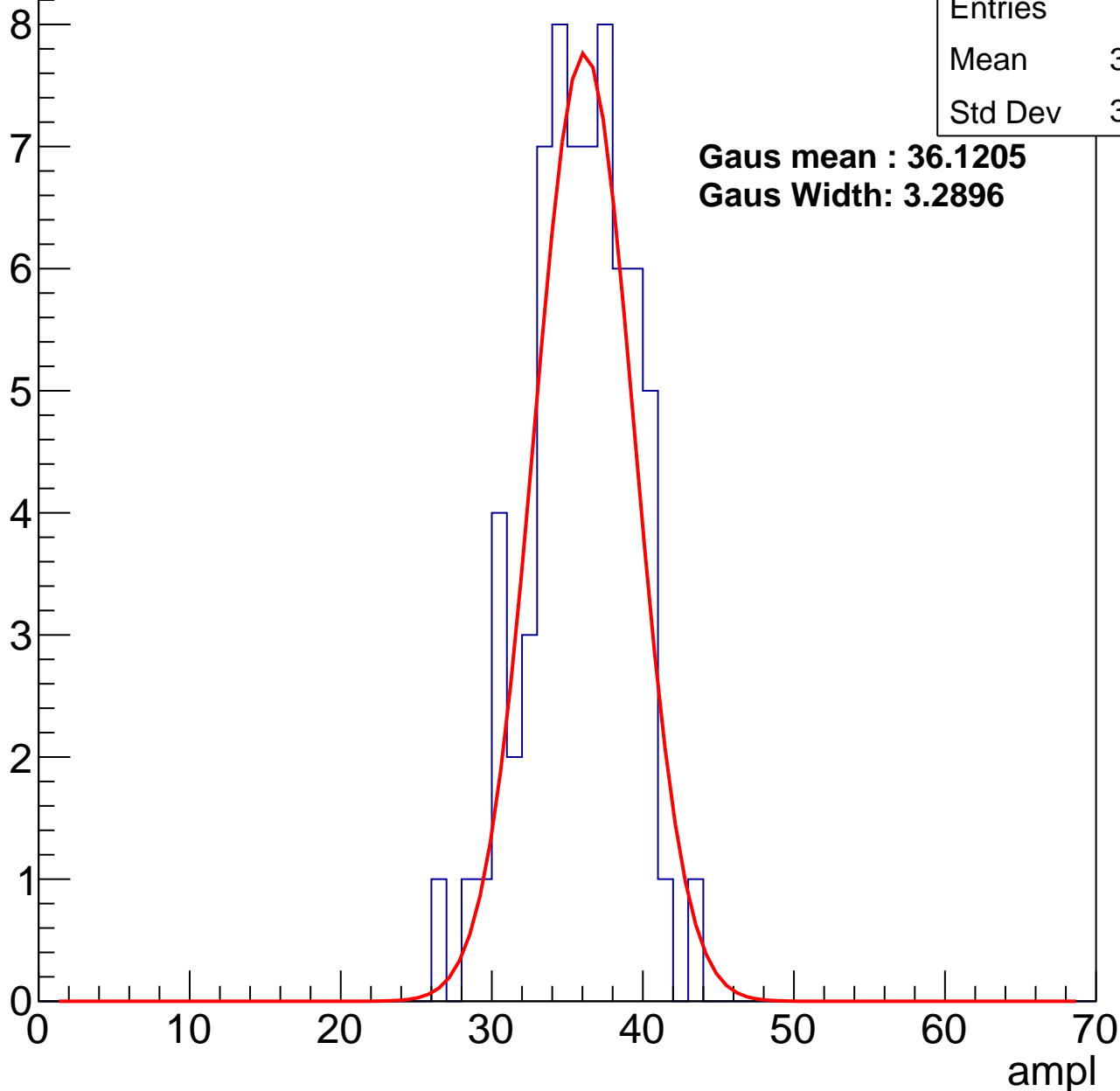
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.34
Std Dev	3.359

**Gaus mean : 36.1205**

**Gaus Width: 3.2896**



# B1L003S, U22-ch72, adc2

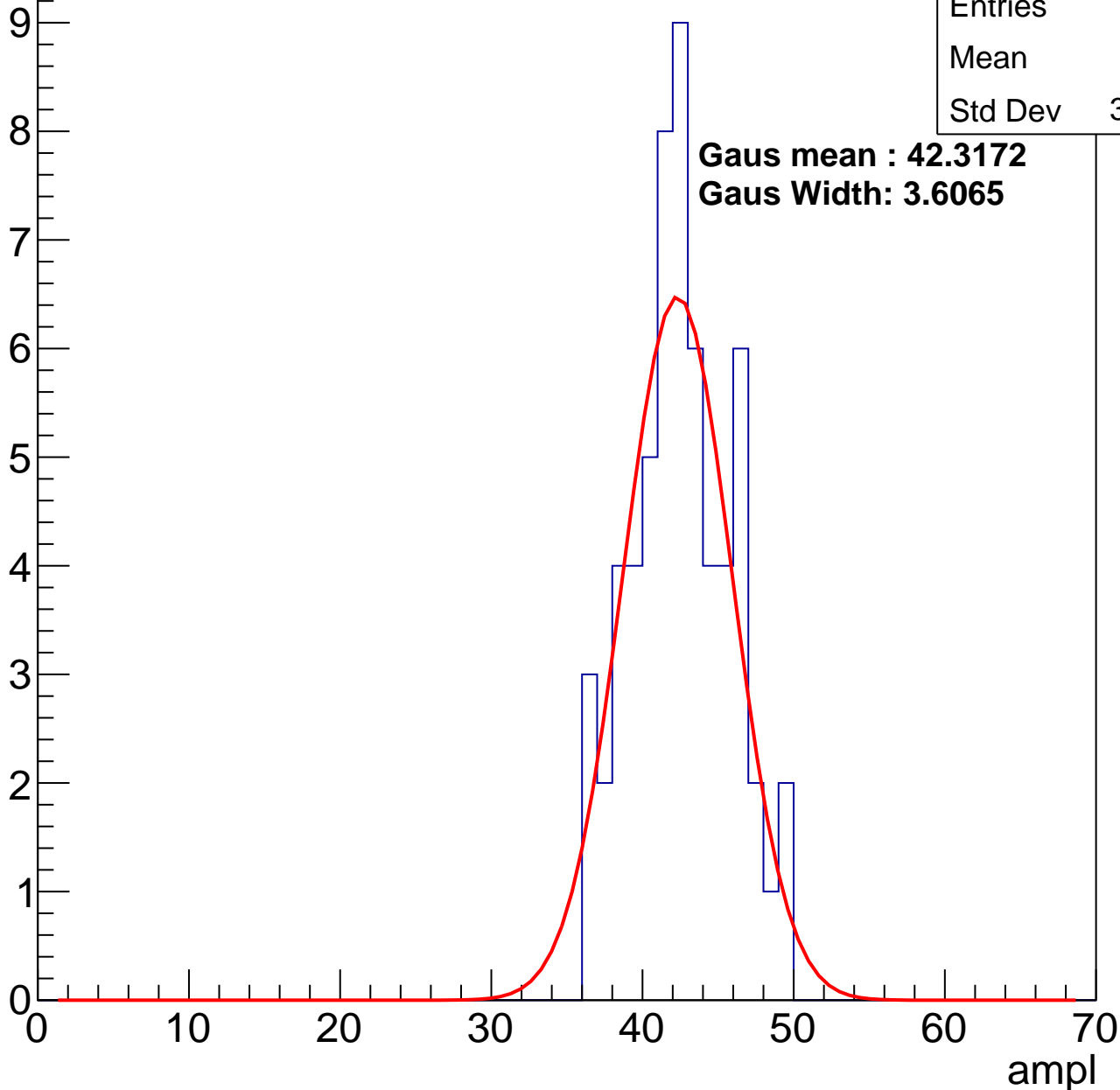
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.1
Std Dev	3.223

**Gaus mean : 42.3172**

**Gaus Width: 3.6065**

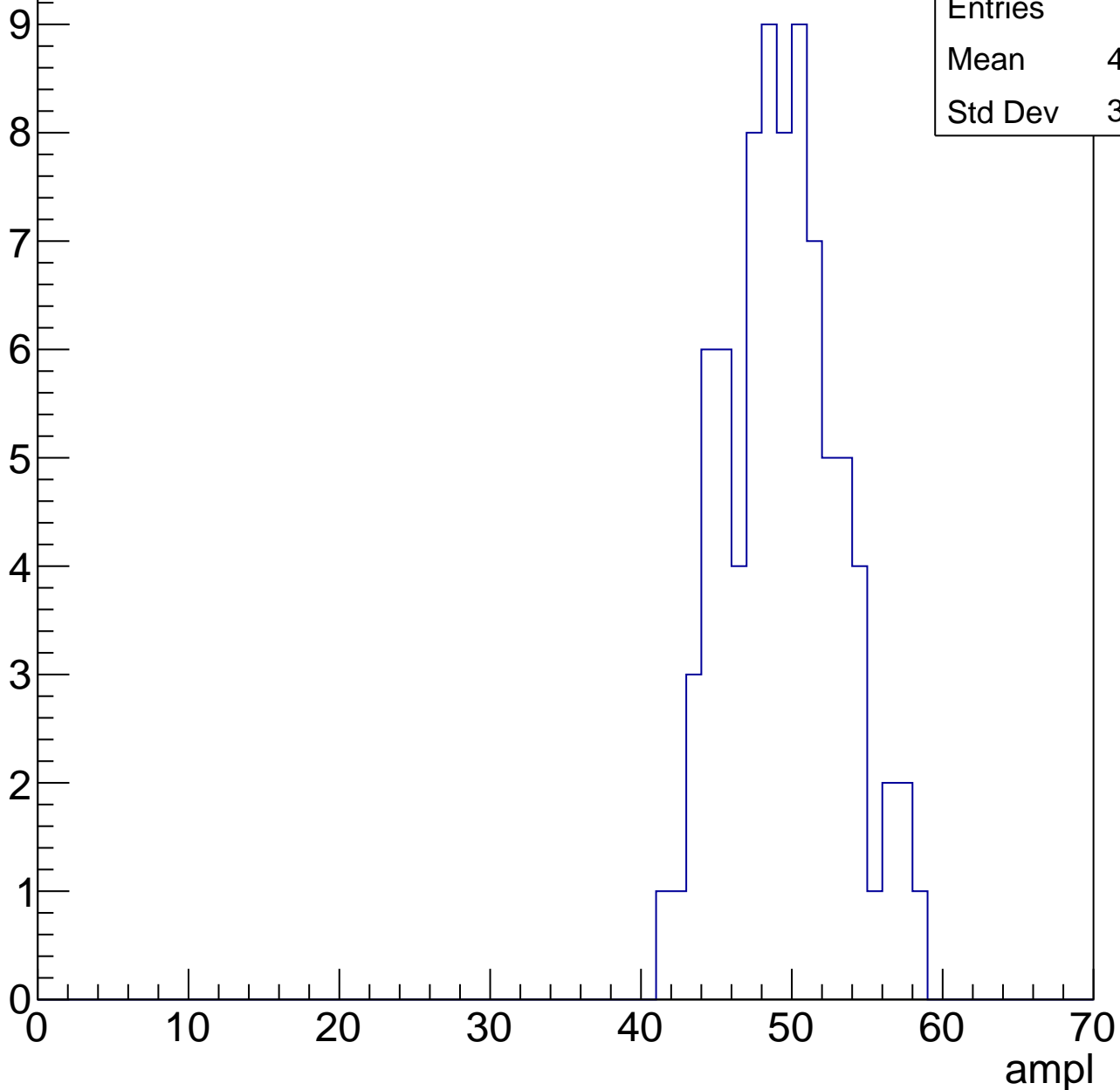


# B1L003S, U22-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	82
Mean	48.99
Std Dev	3.743

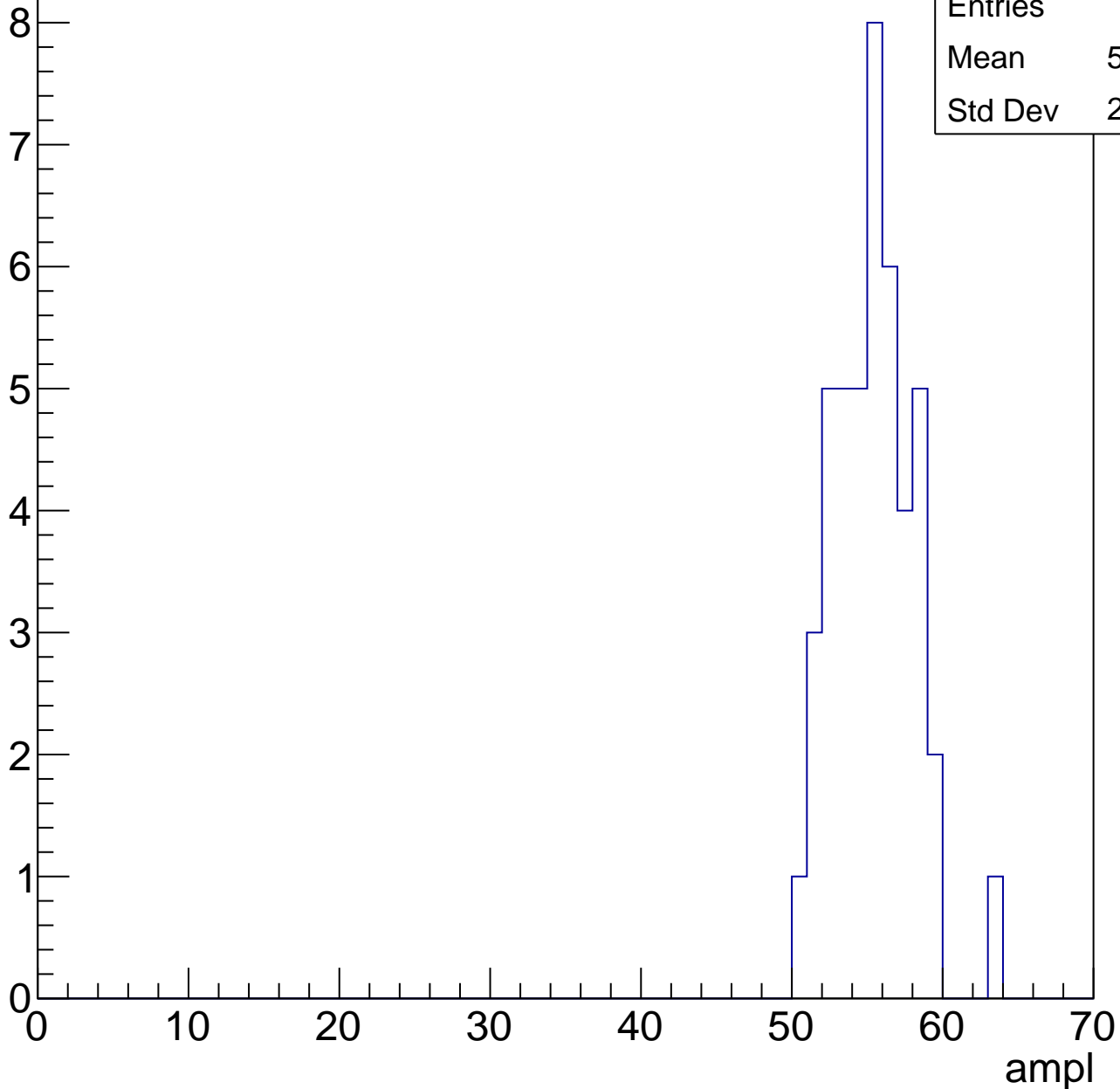


# B1L003S, U22-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	54.96
Std Dev	2.607

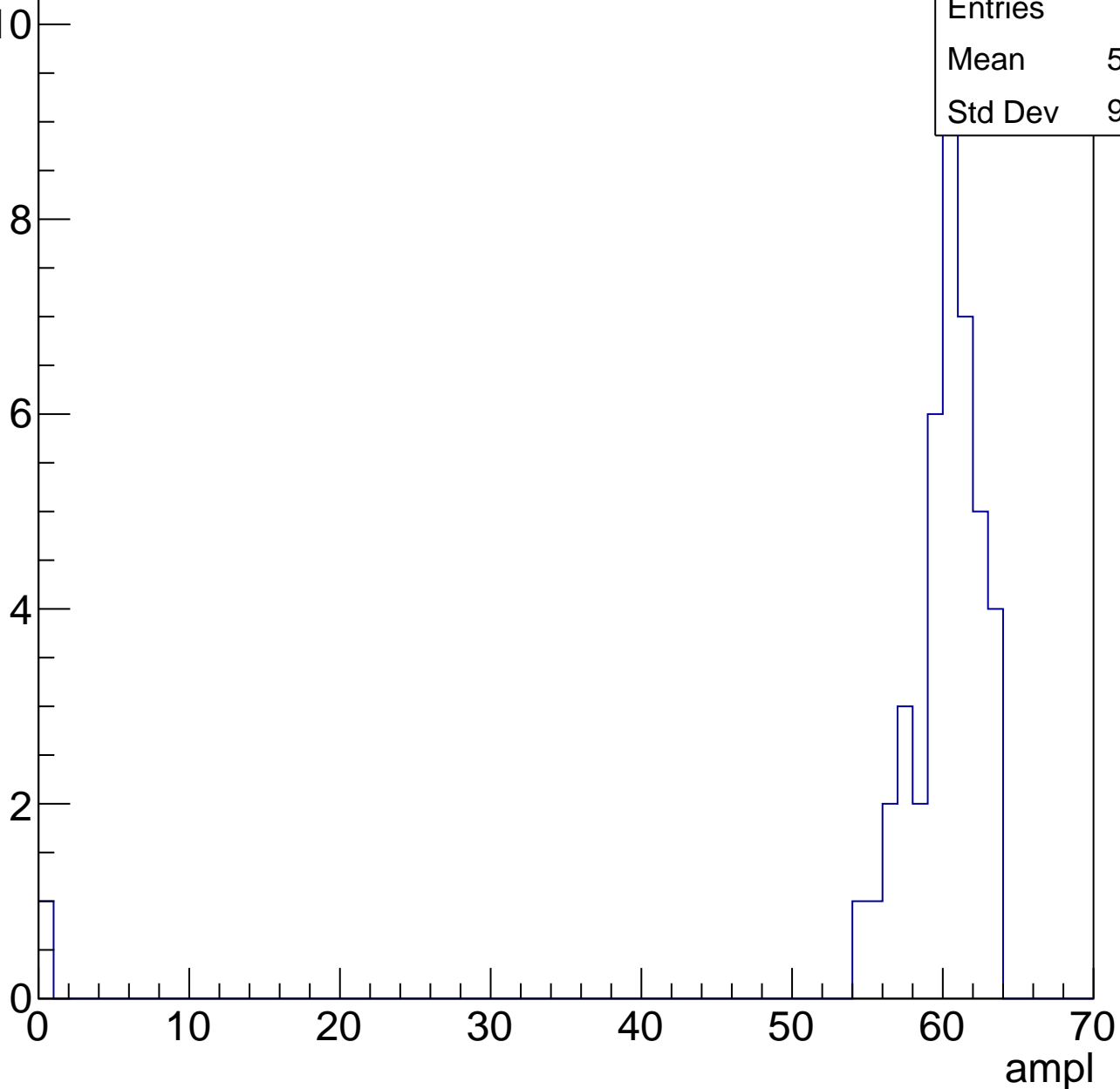


# B1L003S, U22-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.36
Std Dev	9.365

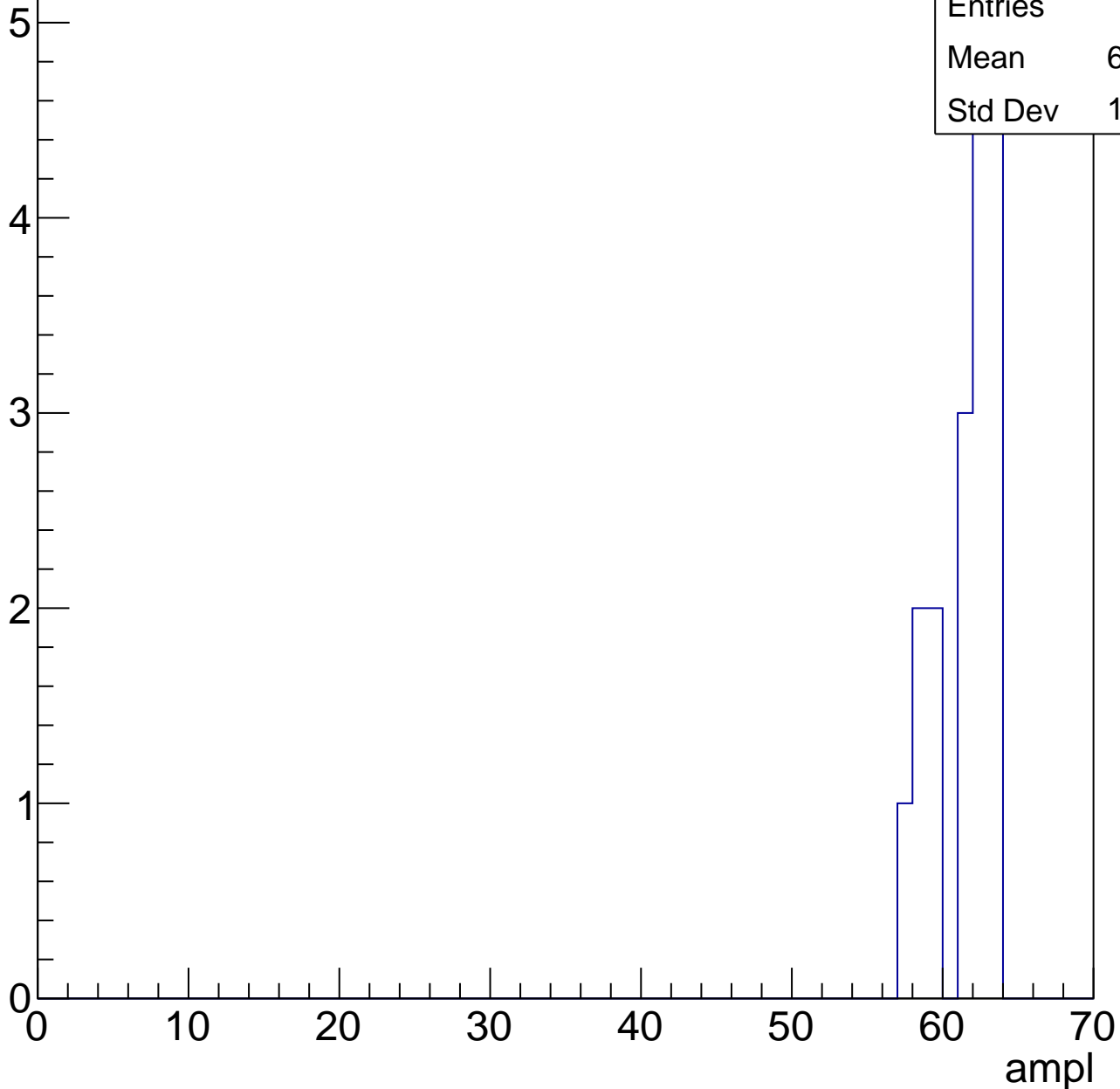


# B1L003S, U22-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	18
Mean	61.06
Std Dev	1.929





# B1L003S, U22-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch73, adc0

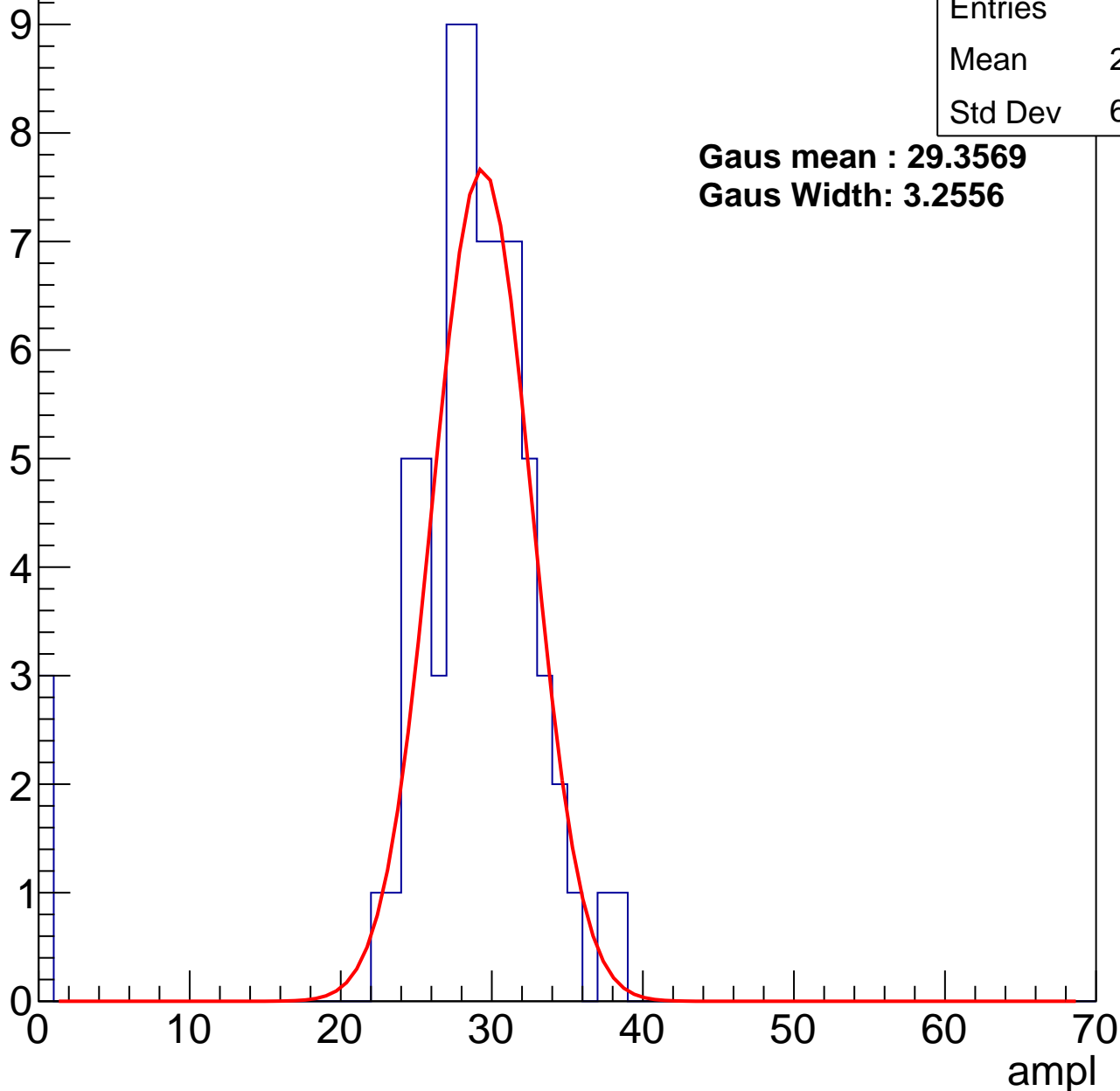
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	27.57
Std Dev	6.656

**Gaus mean : 29.3569**

**Gaus Width: 3.2556**



# B1L003S, U22-ch73, adc1

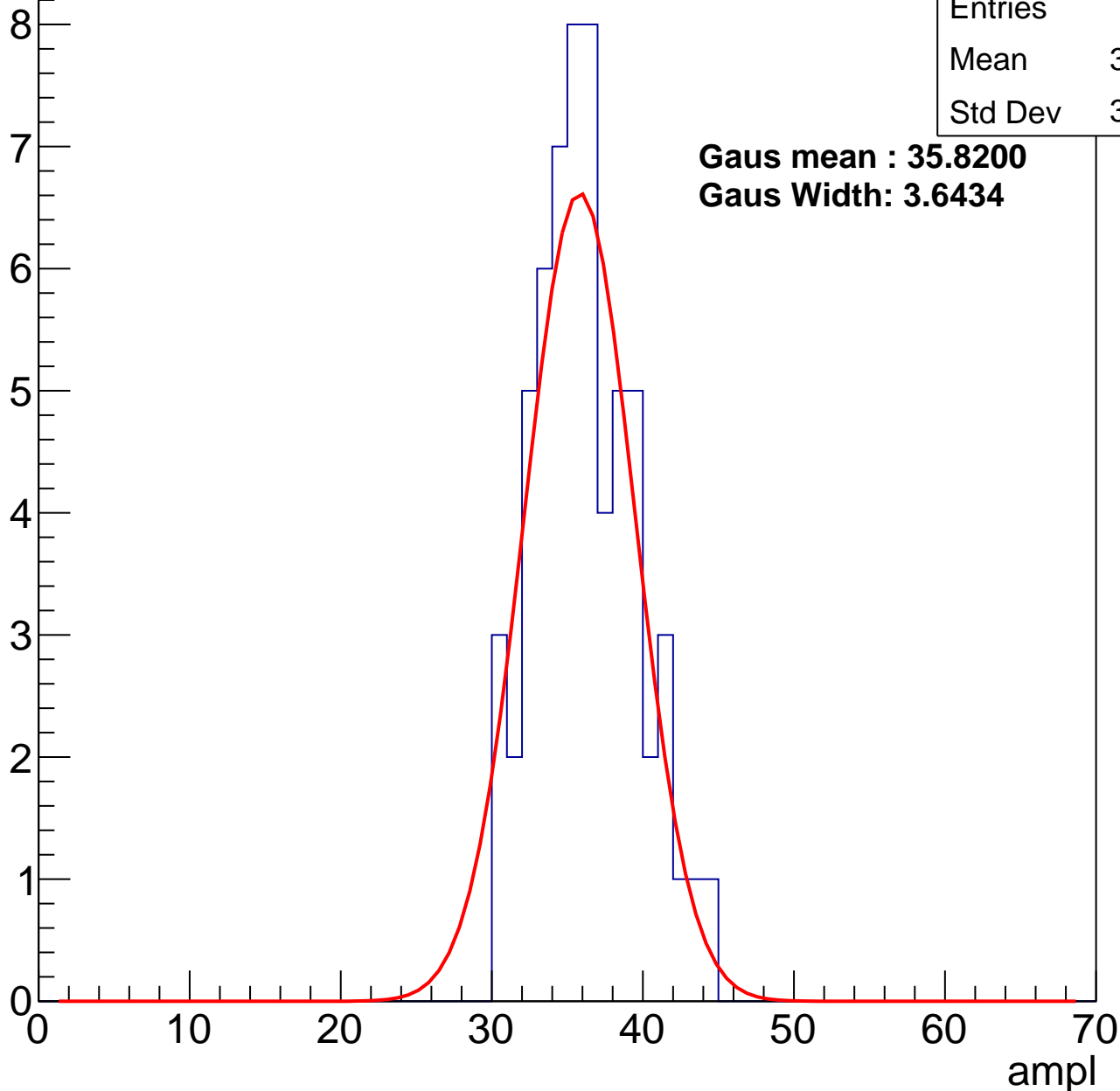
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.75
Std Dev	3.263

**Gaus mean : 35.8200**

**Gaus Width: 3.6434**



# B1L003S, U22-ch73, adc2

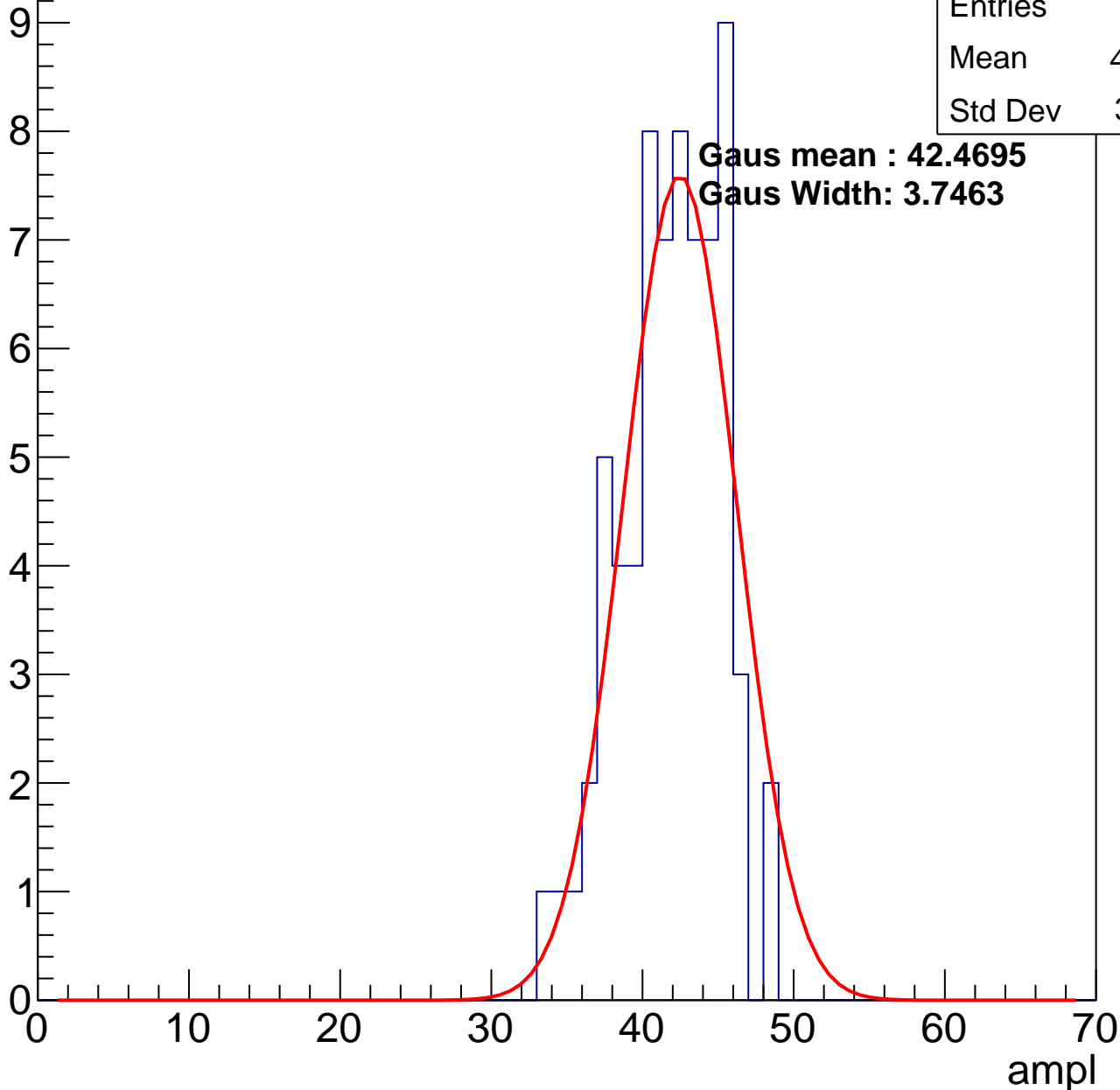
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	41.42
Std Dev	3.281

**Gaus mean : 42.4695**

**Gaus Width: 3.7463**

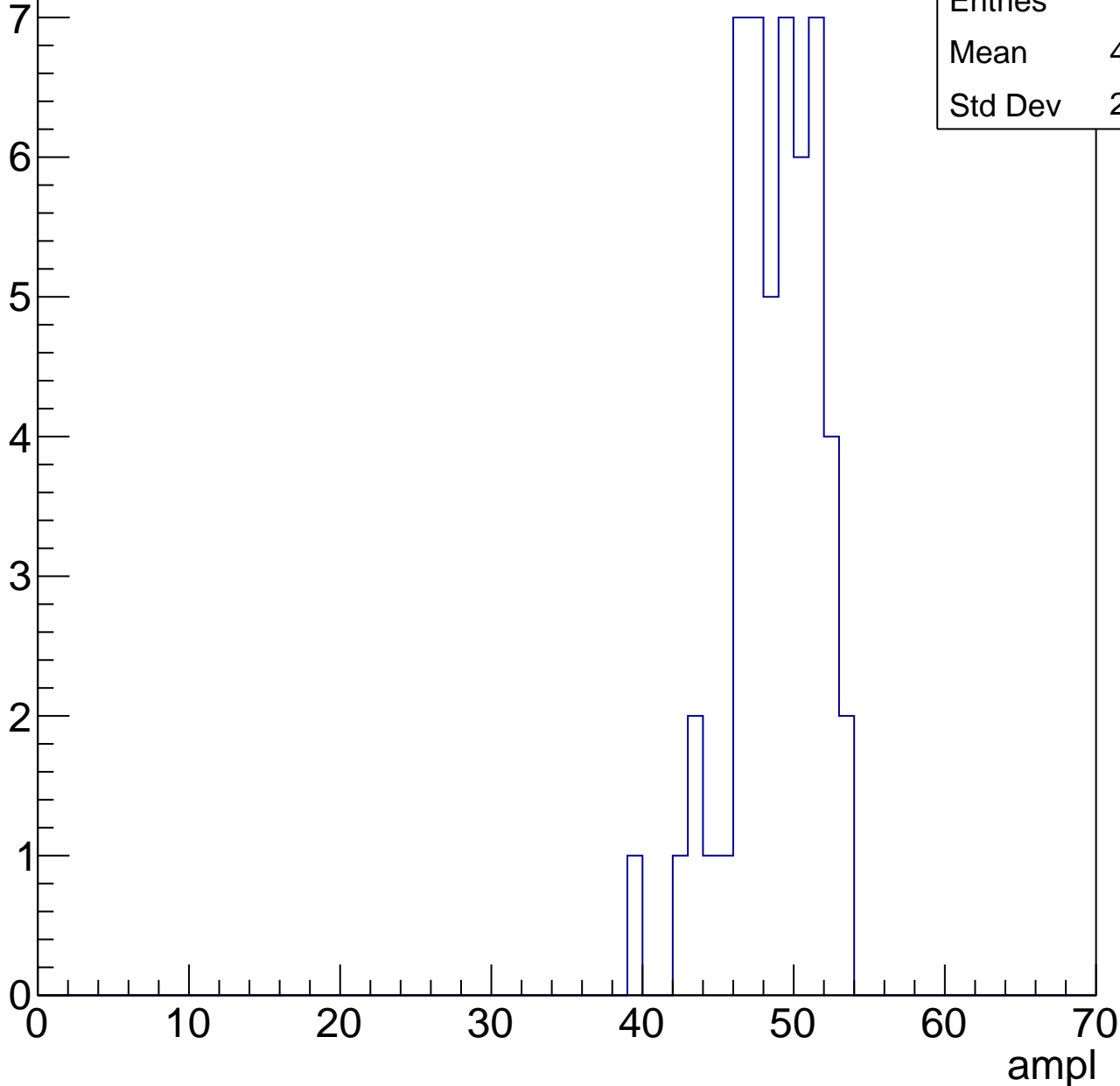


# B1L003S, U22-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	48.25
Std Dev	2.909

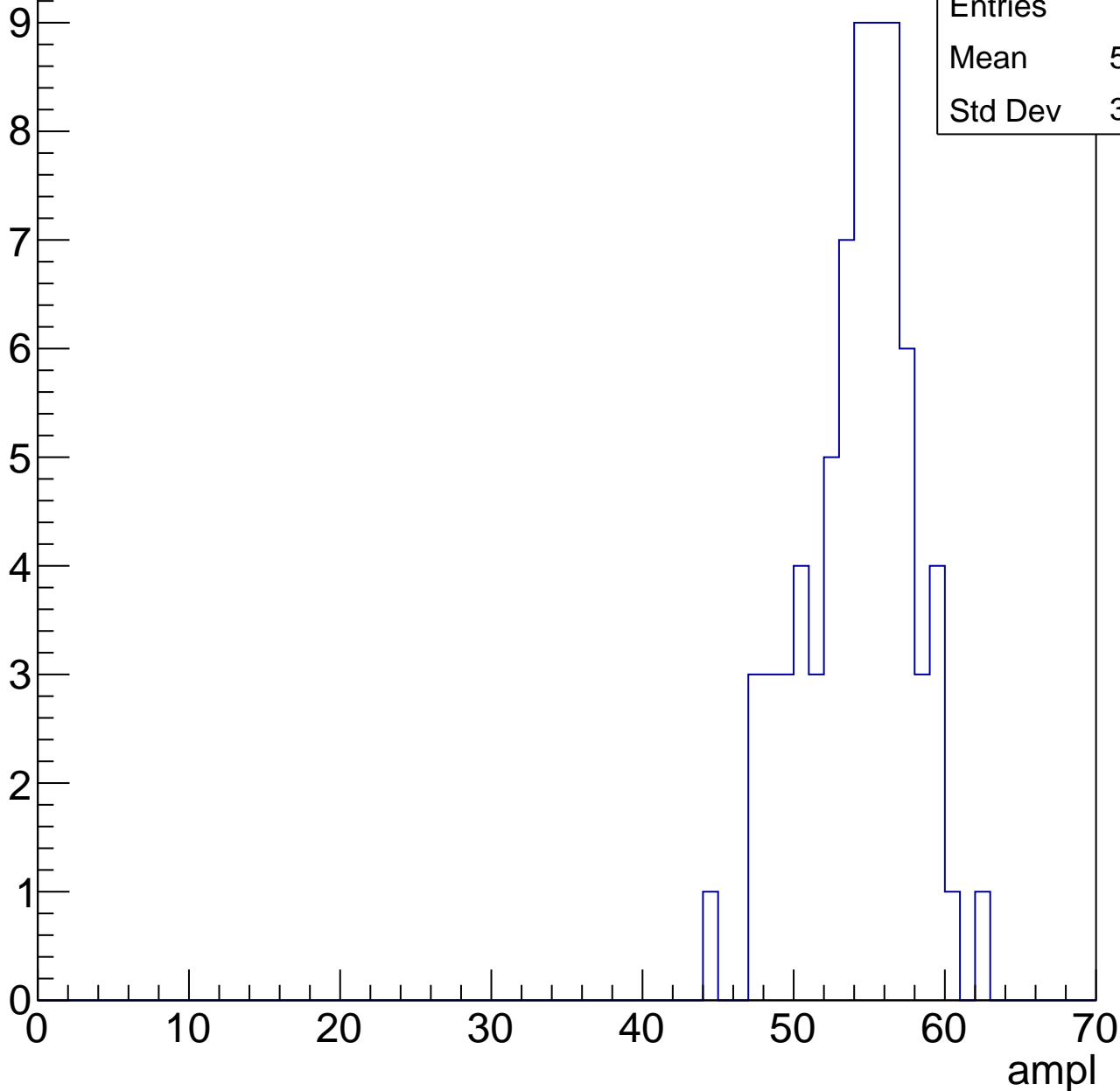


# B1L003S, U22-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	53.79
Std Dev	3.544

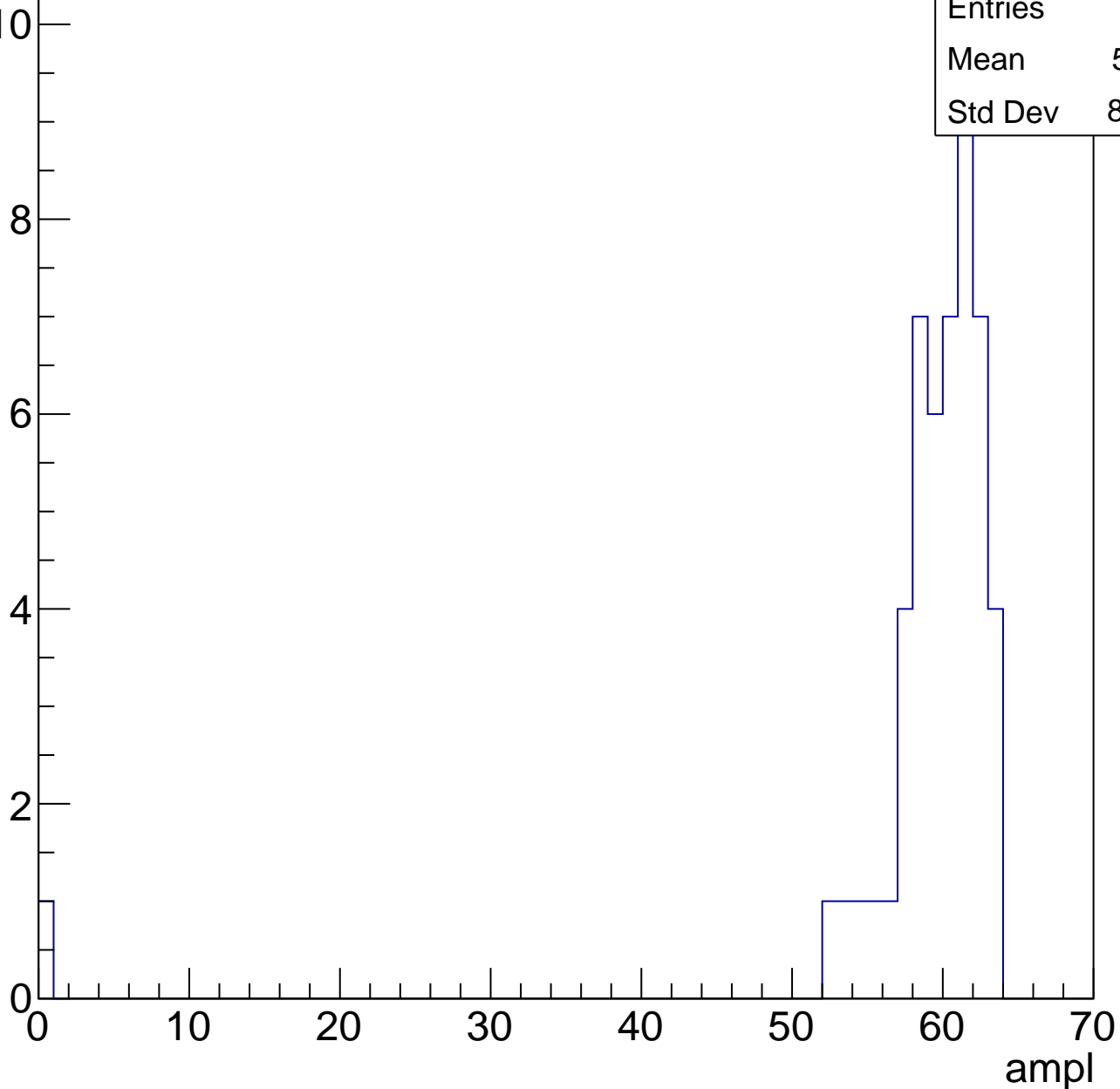


# B1L003S, U22-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	58.31
Std Dev	8.619

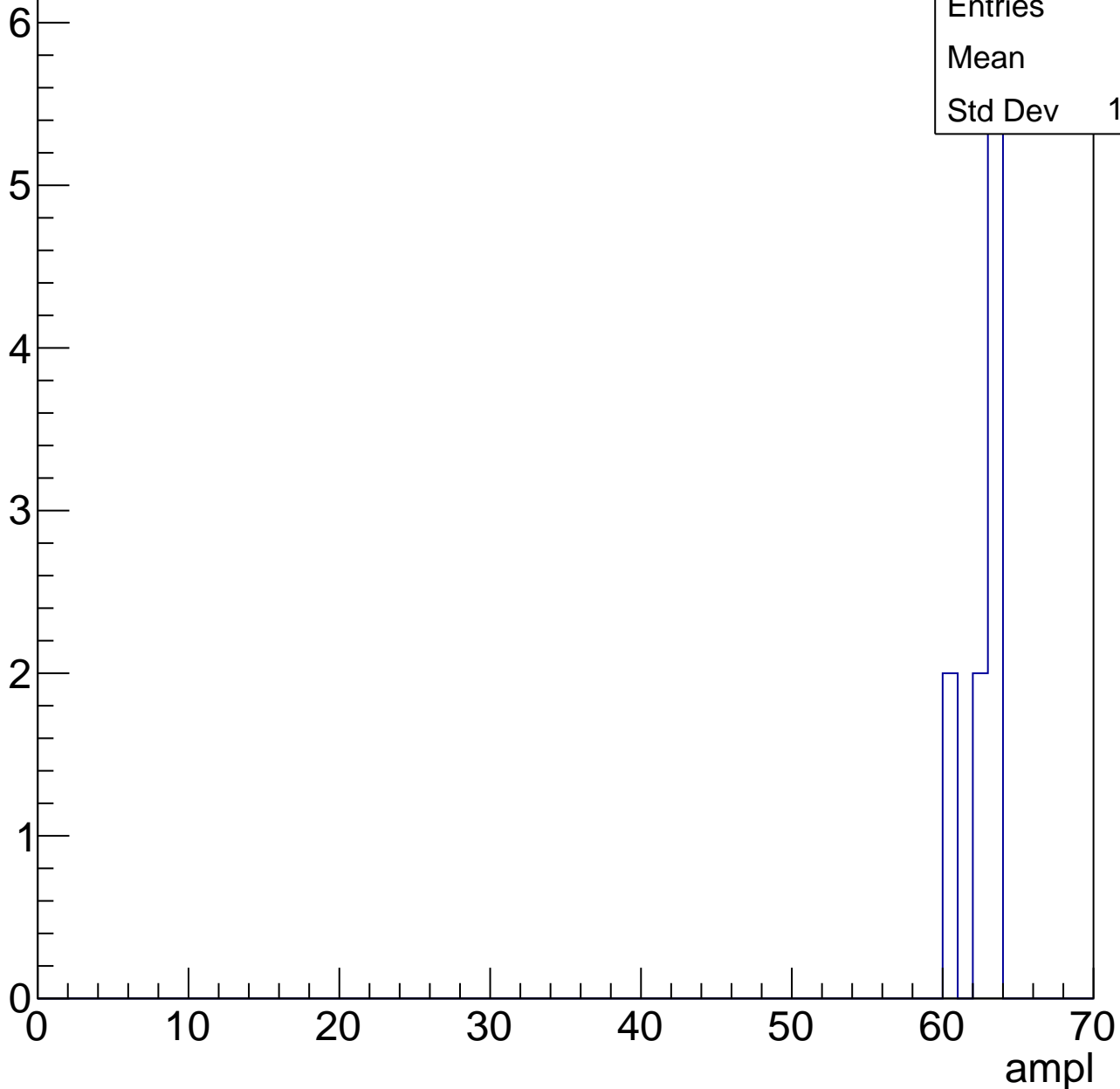


# B1L003S, U22-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	10
Mean	62.2
Std Dev	1.166





# B1L003S, U22-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch74, adc0

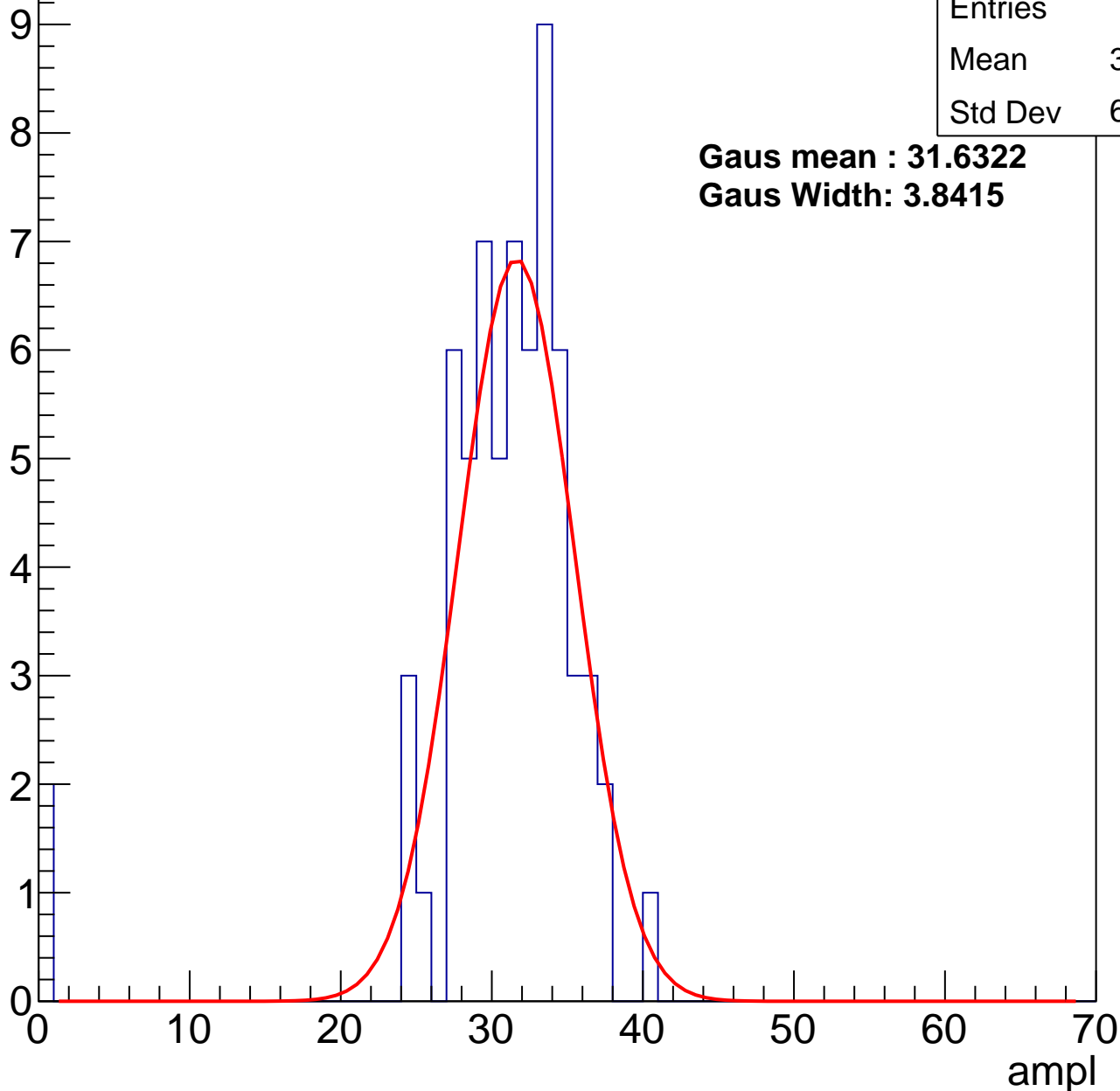
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.14
Std Dev	6.279

**Gaus mean : 31.6322**

**Gaus Width: 3.8415**



# B1L003S, U22-ch74, adc1

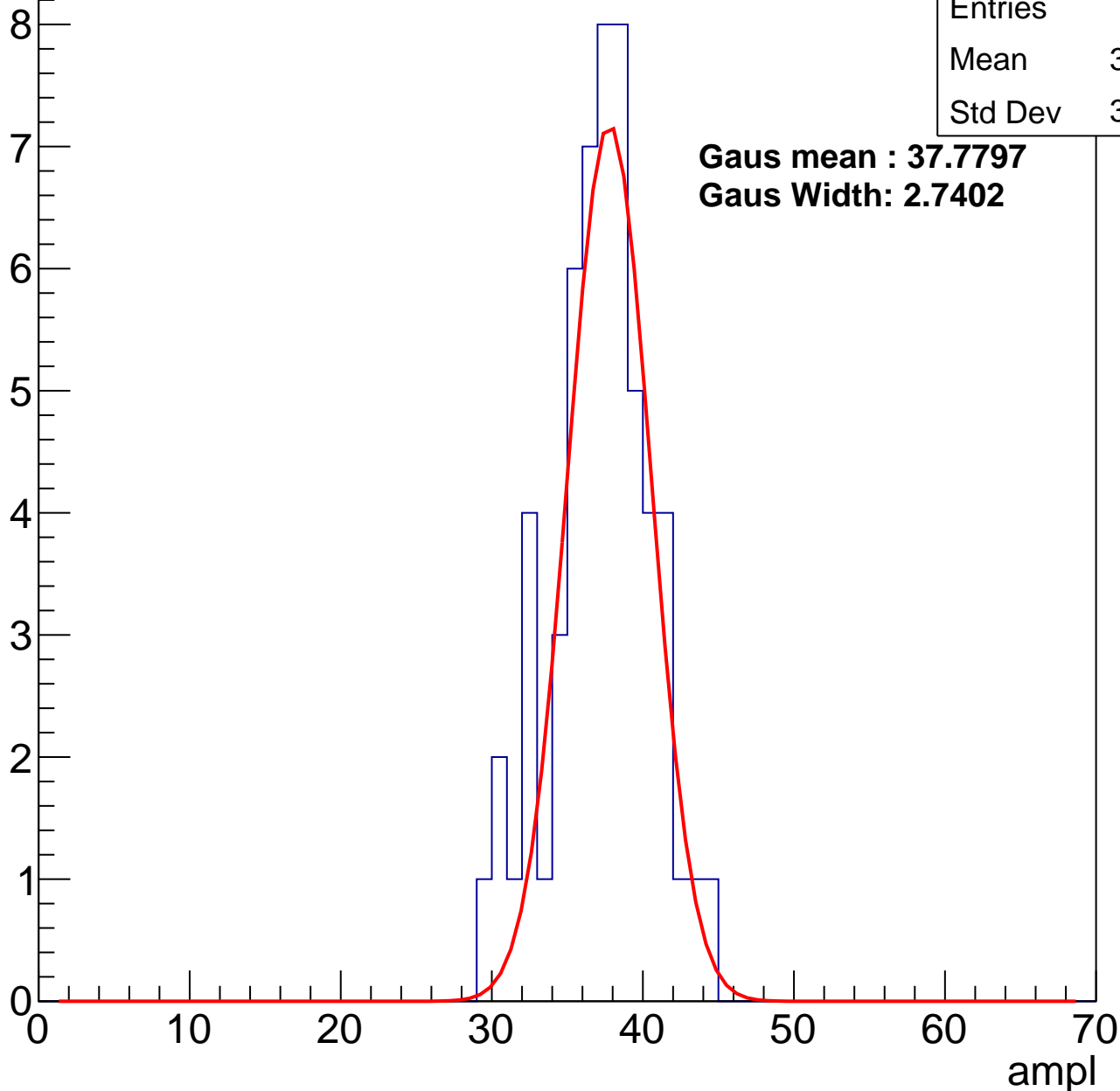
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.72
Std Dev	3.259

**Gaus mean : 37.7797**

**Gaus Width: 2.7402**



# B1L003S, U22-ch74, adc2

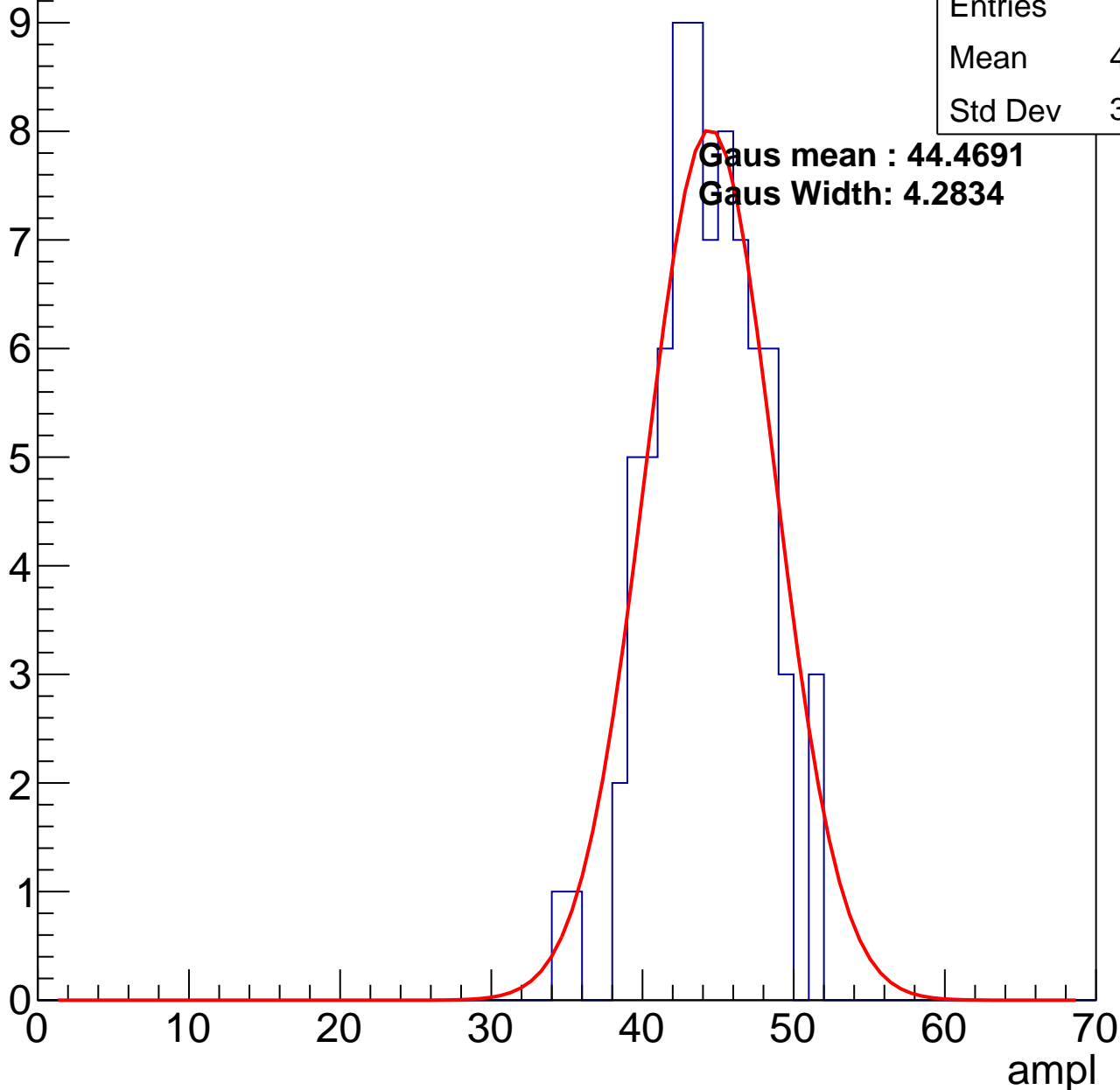
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	43.73
Std Dev	3.507

**Gaus mean : 44.4691**

**Gaus Width: 4.2834**

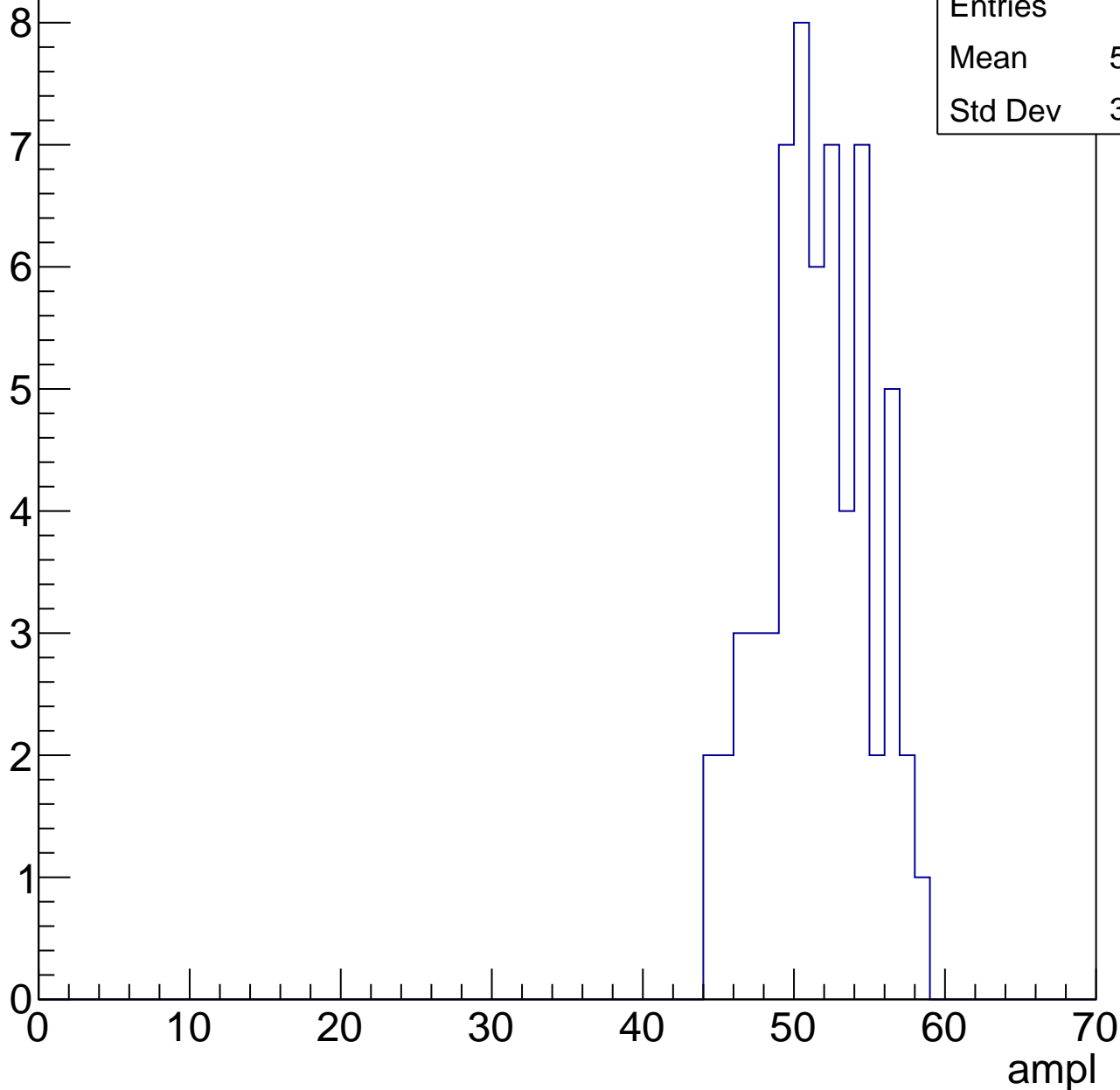


# B1L003S, U22-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	51.06
Std Dev	3.407

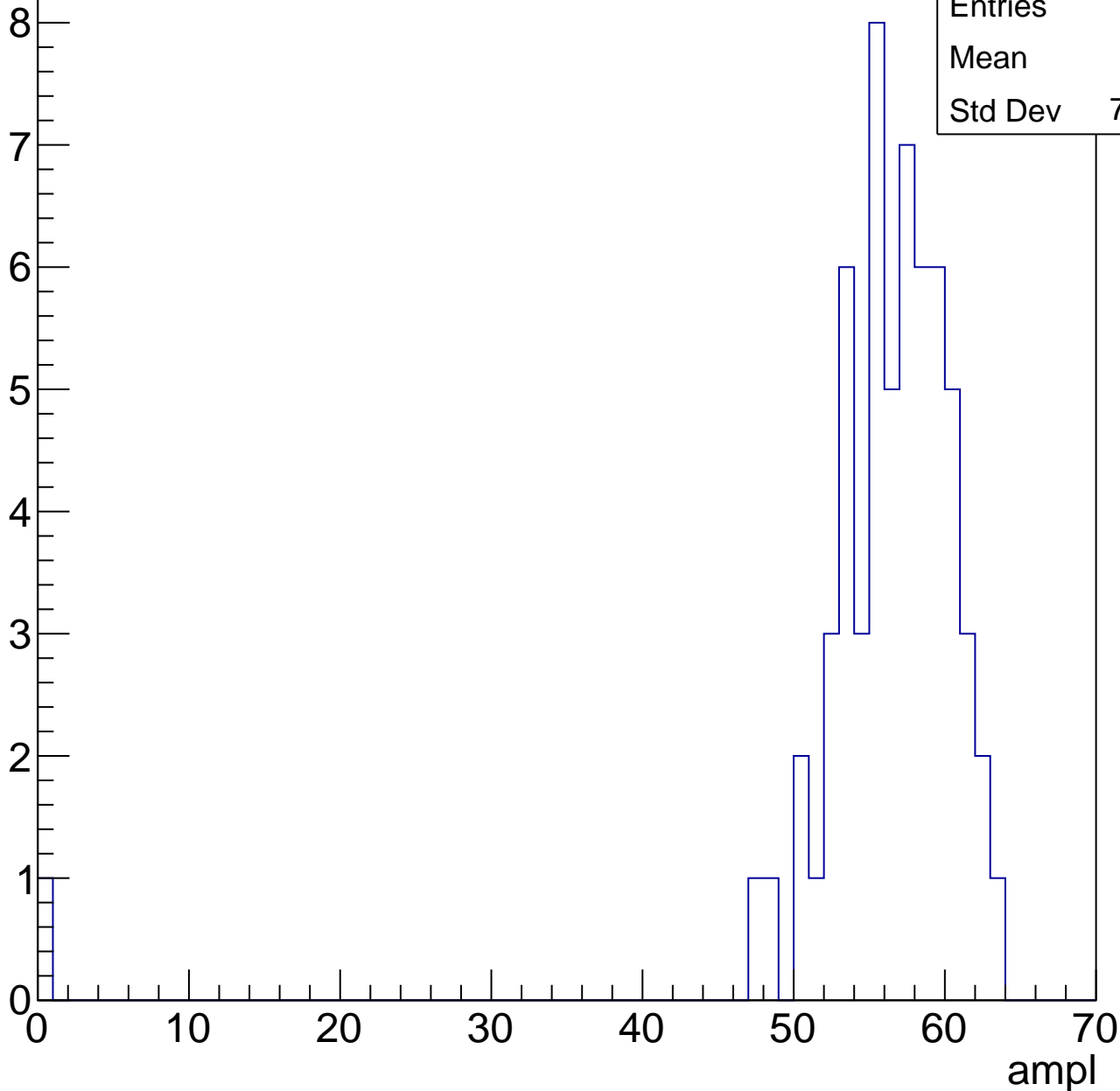


# B1L003S, U22-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	55.3
Std Dev	7.925

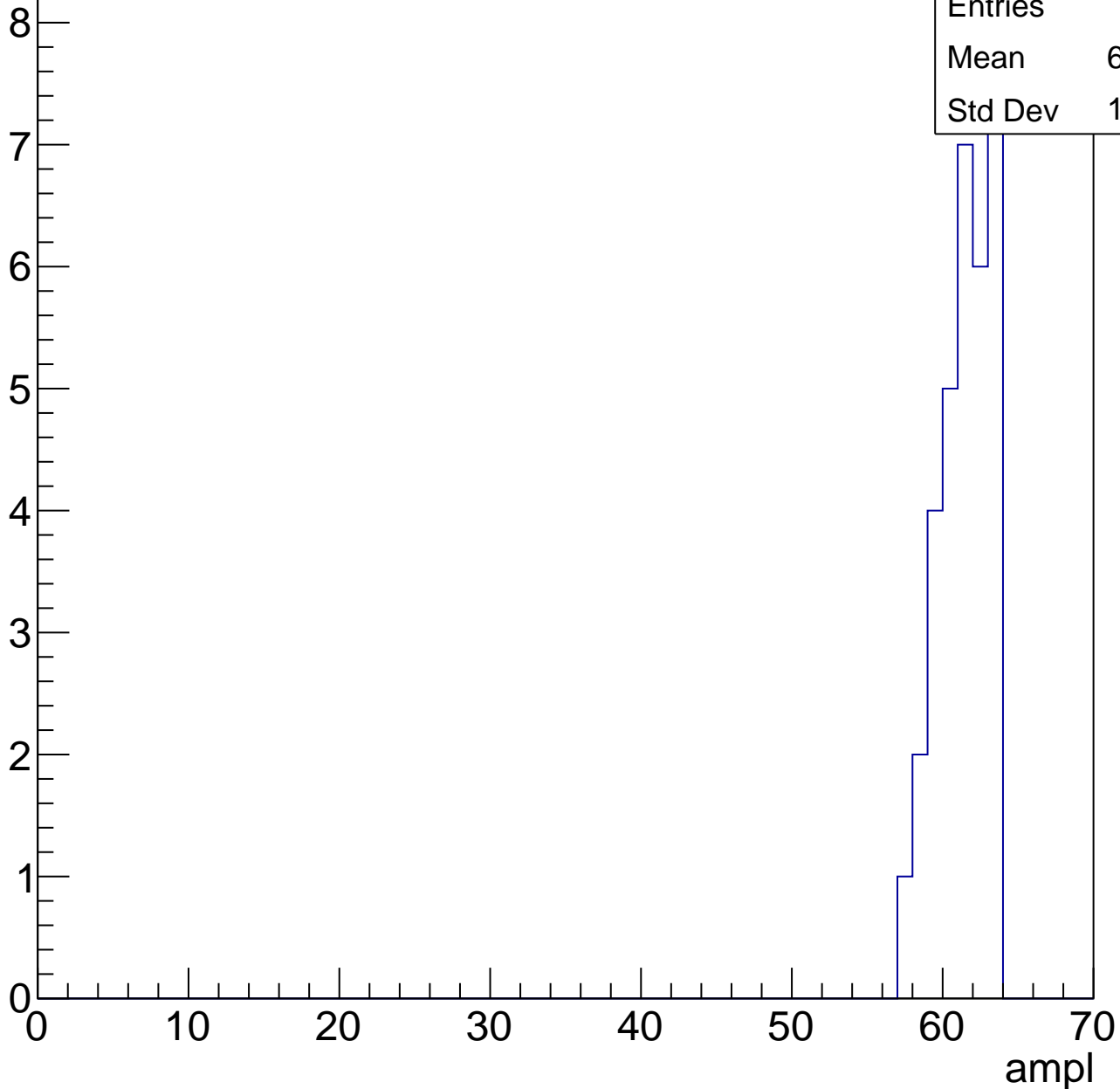


# B1L003S, U22-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

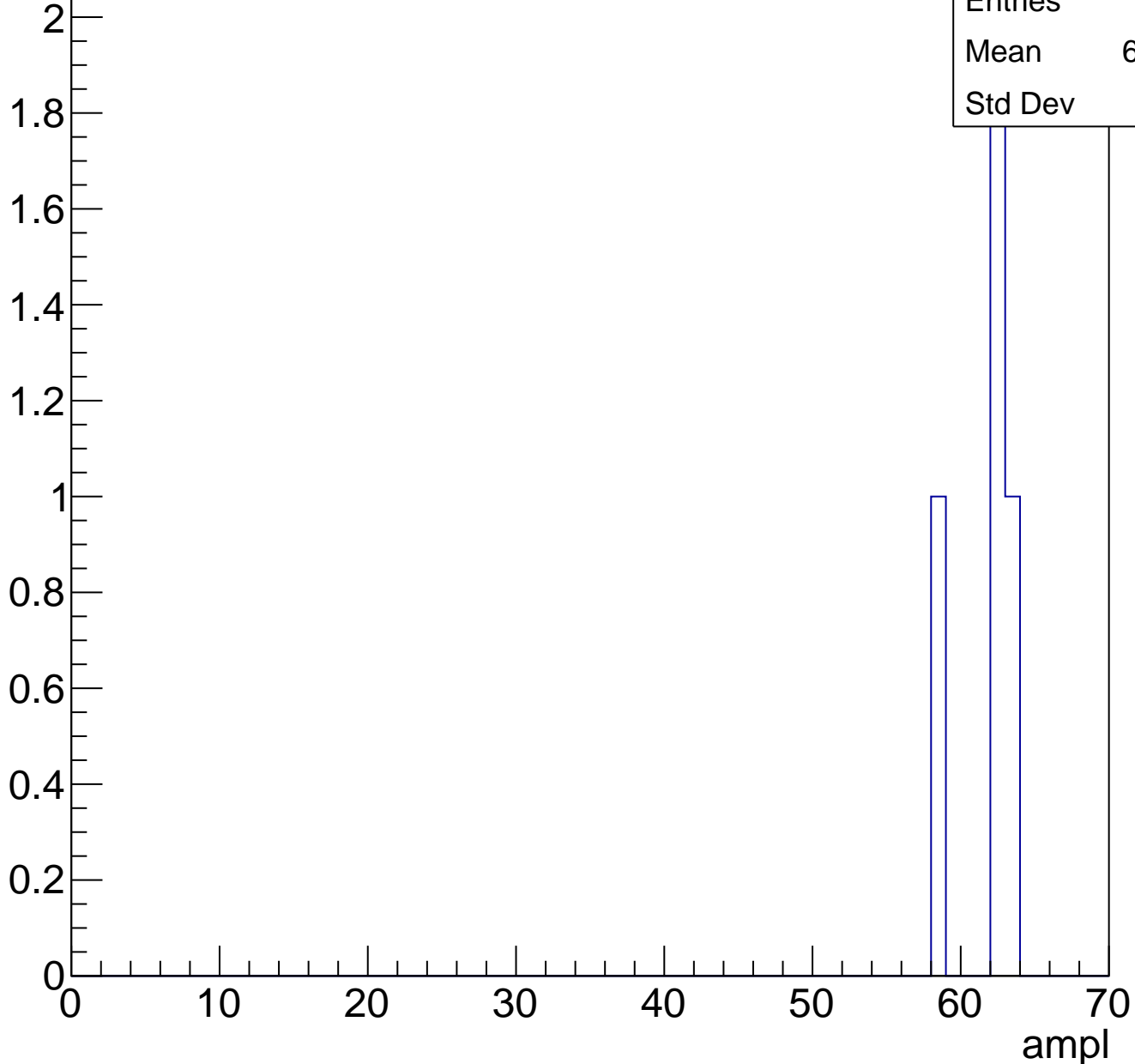
Entries	33
Mean	60.97
Std Dev	1.678



# B1L003S, U22-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch75, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	71
Mean	30.08
Std Dev	2.862

**Gaus mean : 30.2877**

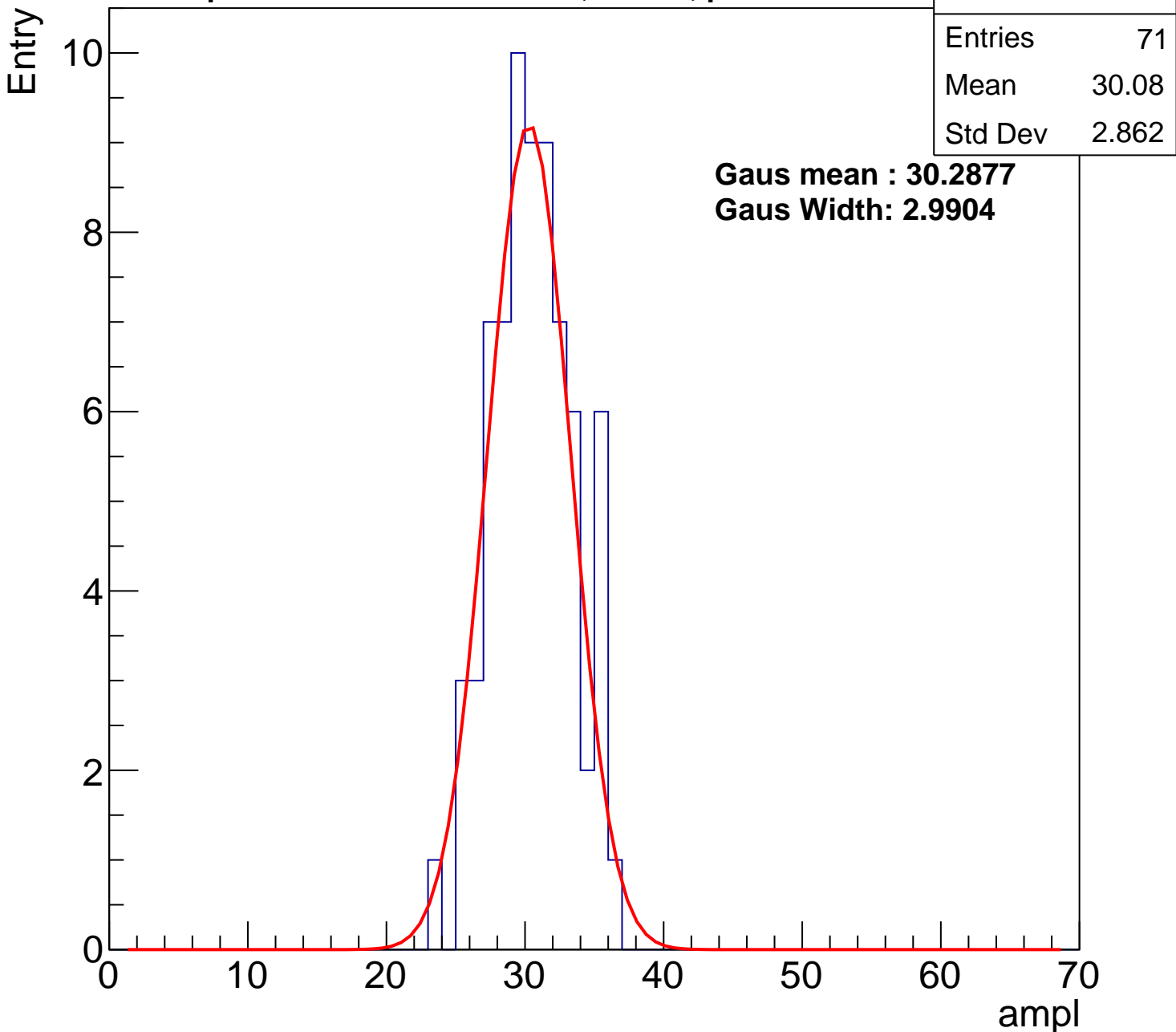
**Gaus Width: 2.9904**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



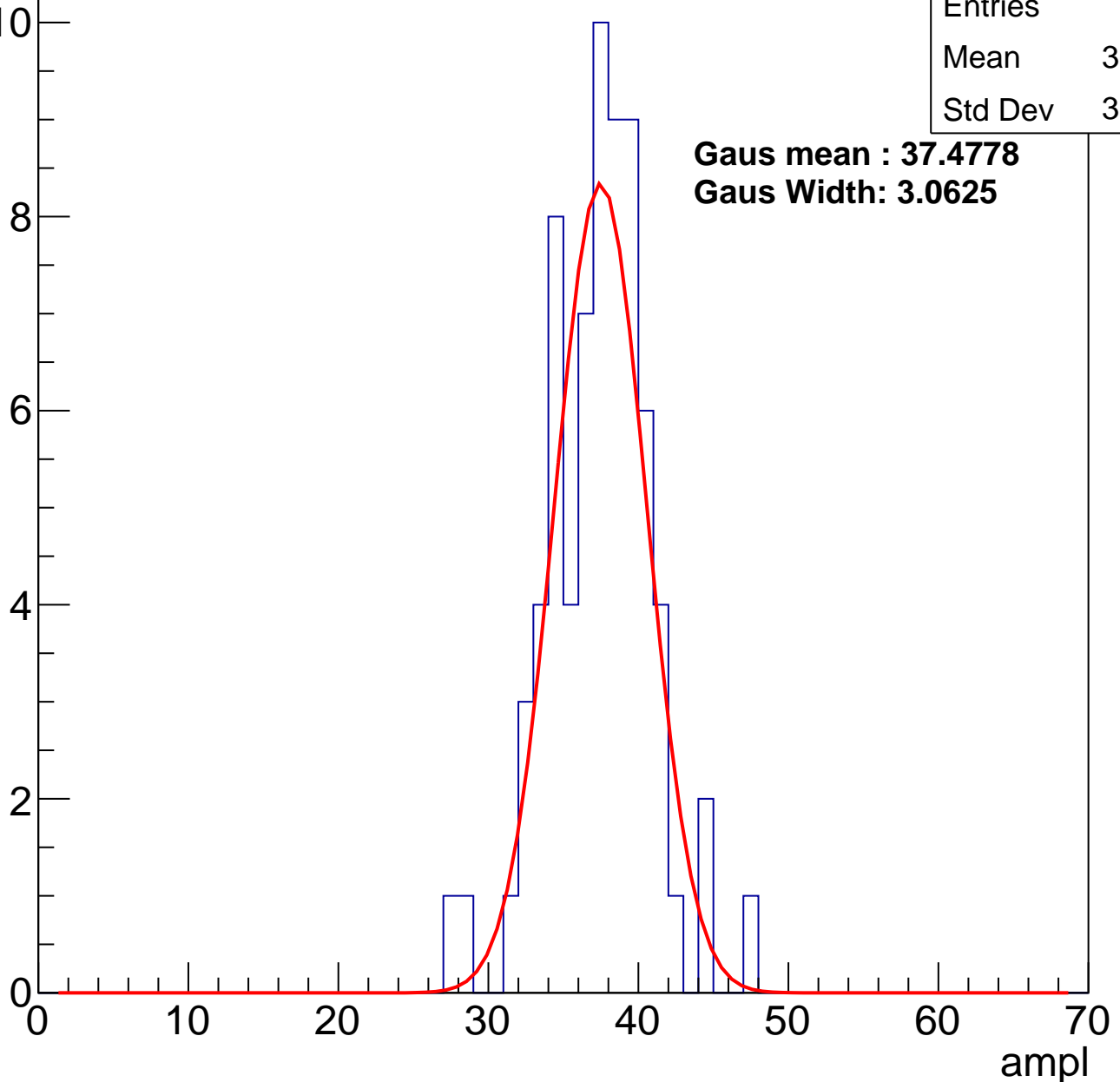
# B1L003S, U22-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.93
Std Dev	3.433

**Gaus mean : 37.4778**  
**Gaus Width: 3.0625**



# B1L003S, U22-ch75, adc2

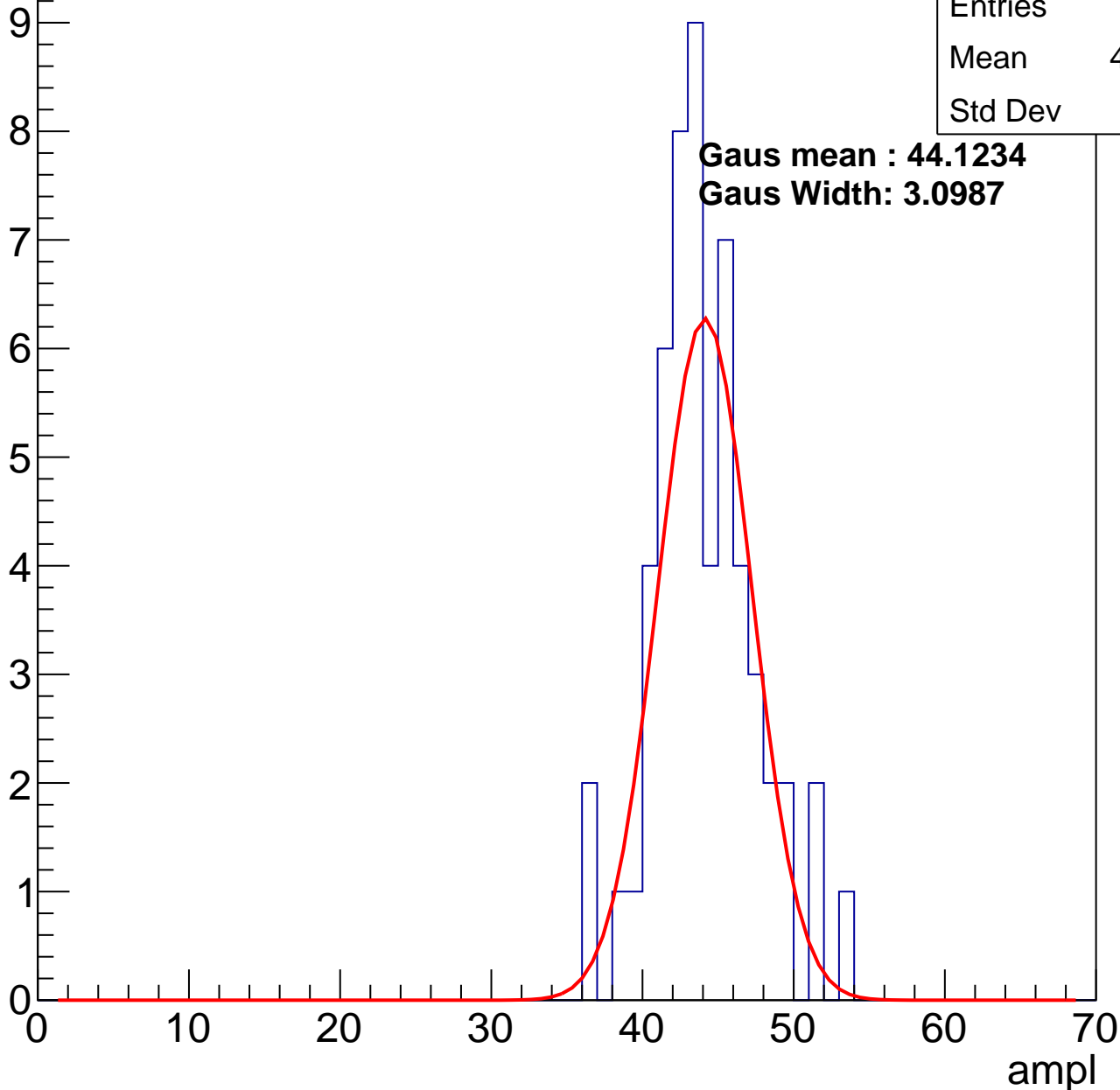
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.62
Std Dev	3.41

**Gaus mean : 44.1234**

**Gaus Width: 3.0987**

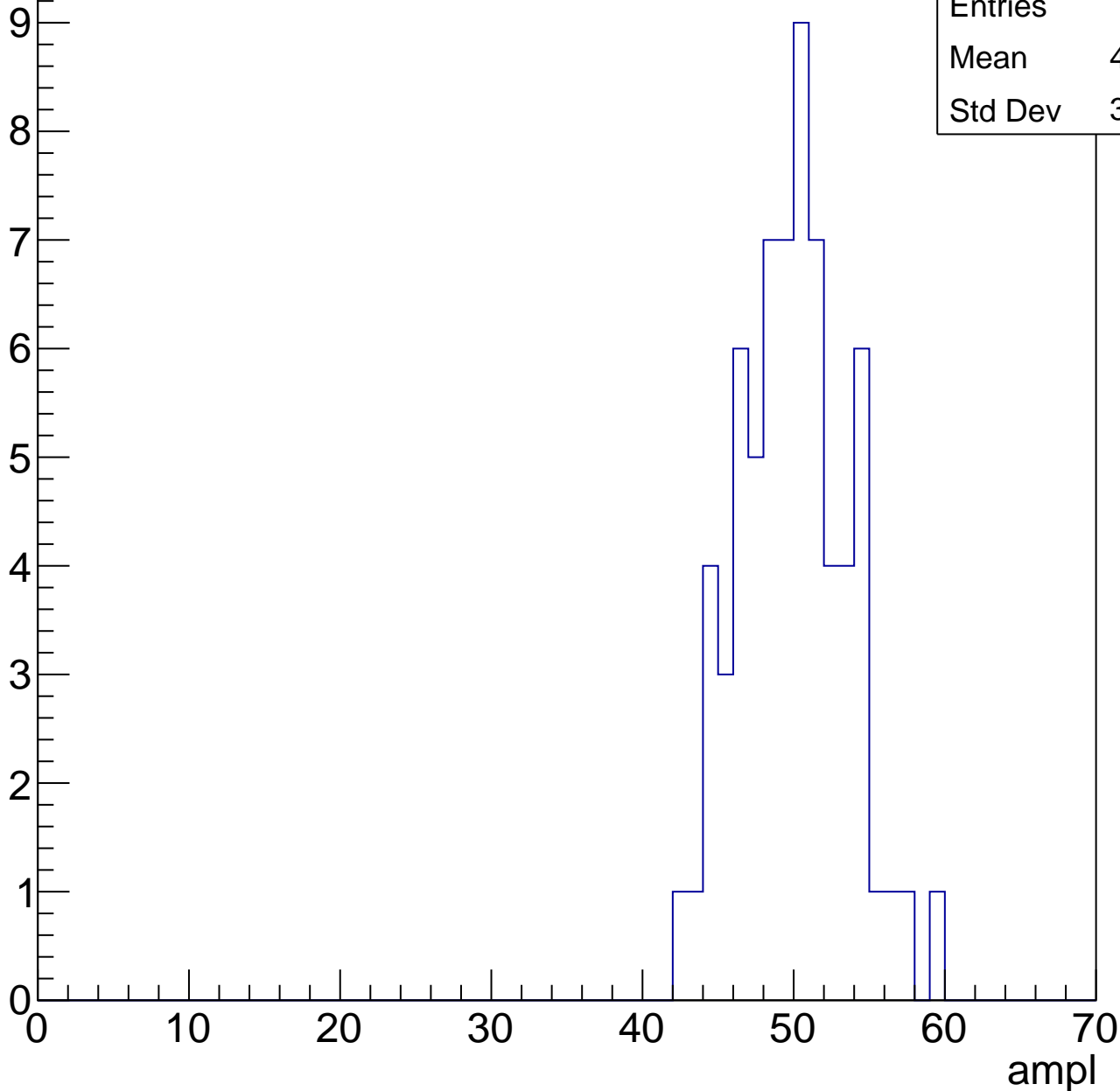


# B1L003S, U22-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

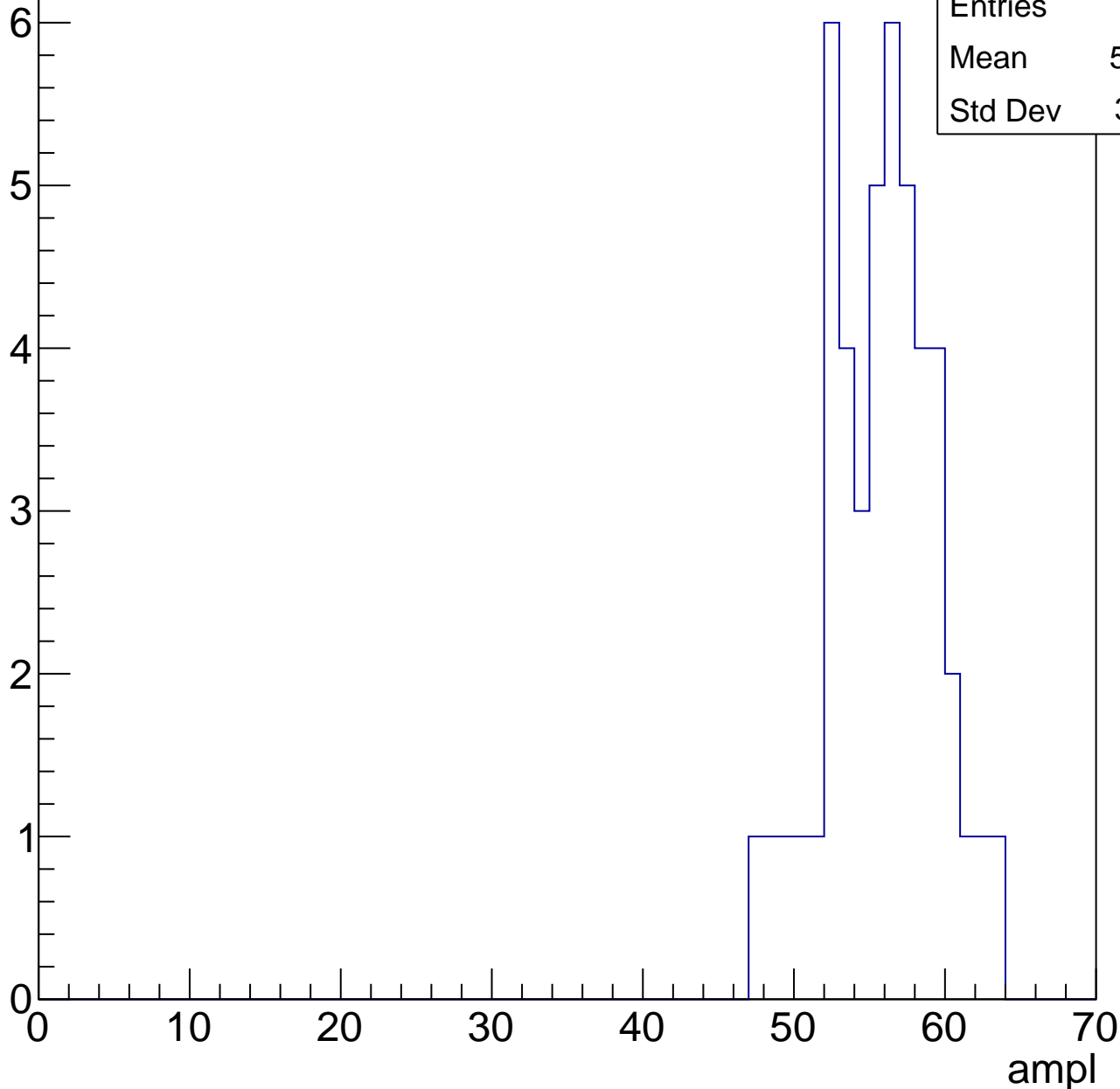
Entries	68
Mean	49.47
Std Dev	3.504



# B1L003S, U22-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



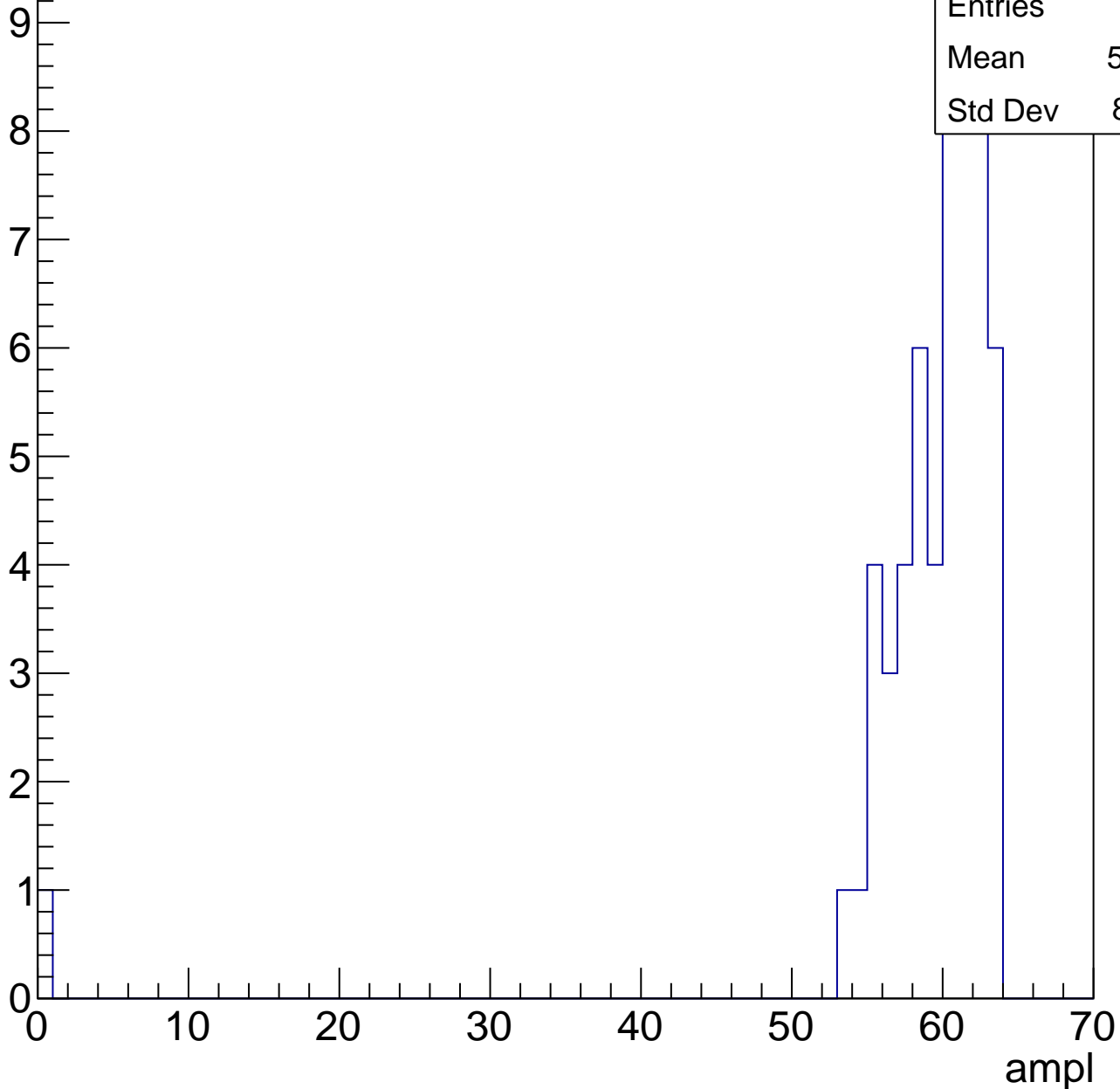
Entries	47
Mean	55.34
Std Dev	3.521

# B1L003S, U22-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	58.36
Std Dev	8.361



# B1L003S, U22-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

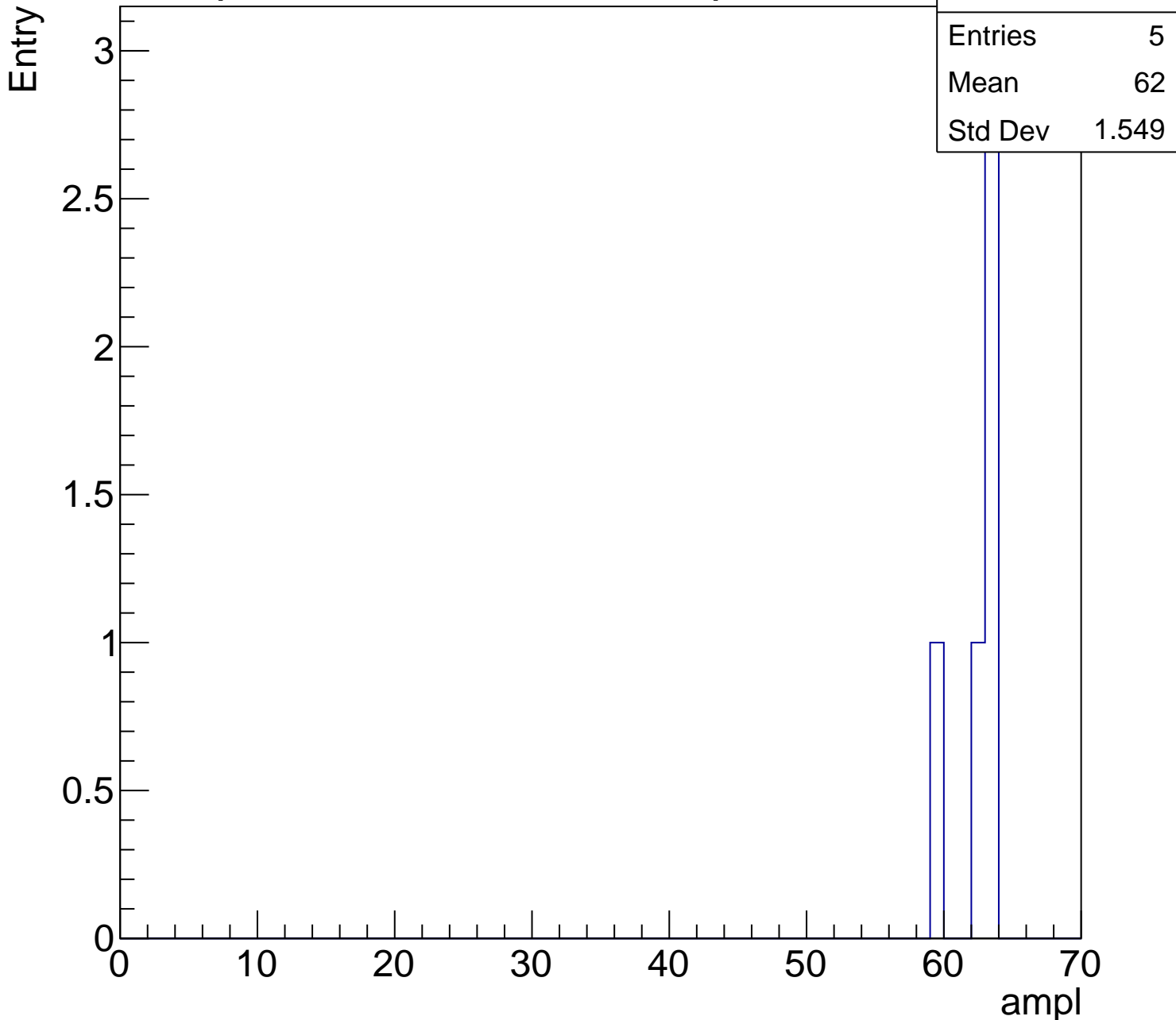
5

Mean

62

Std Dev

1.549





# B1L003S, U22-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch76, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	61
Mean	31.21
Std Dev	3.031

**Gaus mean : 31.0513**

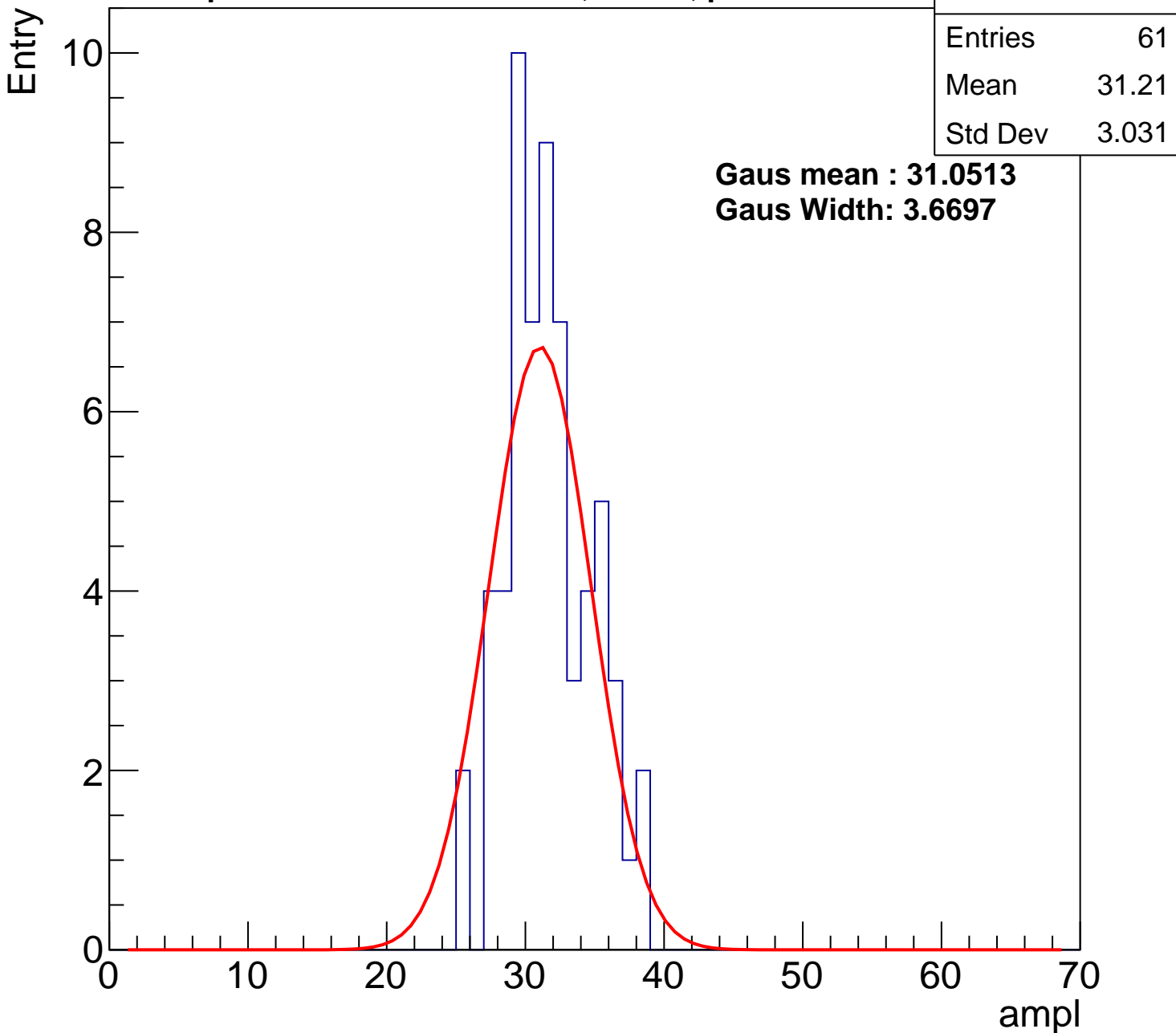
**Gaus Width: 3.6697**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



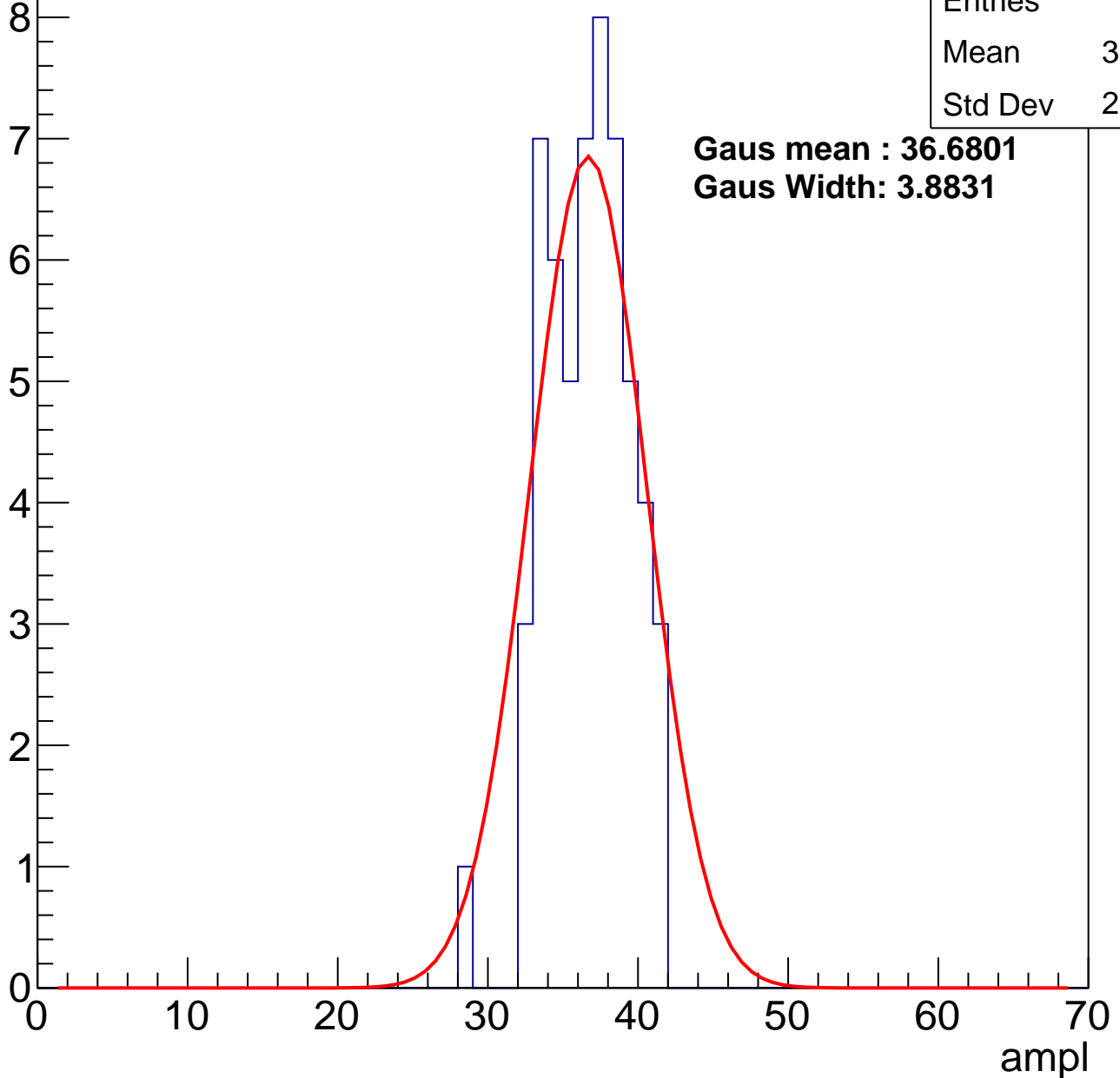
# B1L003S, U22-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.18
Std Dev	2.746

**Gaus mean : 36.6801**  
**Gaus Width: 3.8831**



# B1L003S, U22-ch76, adc2

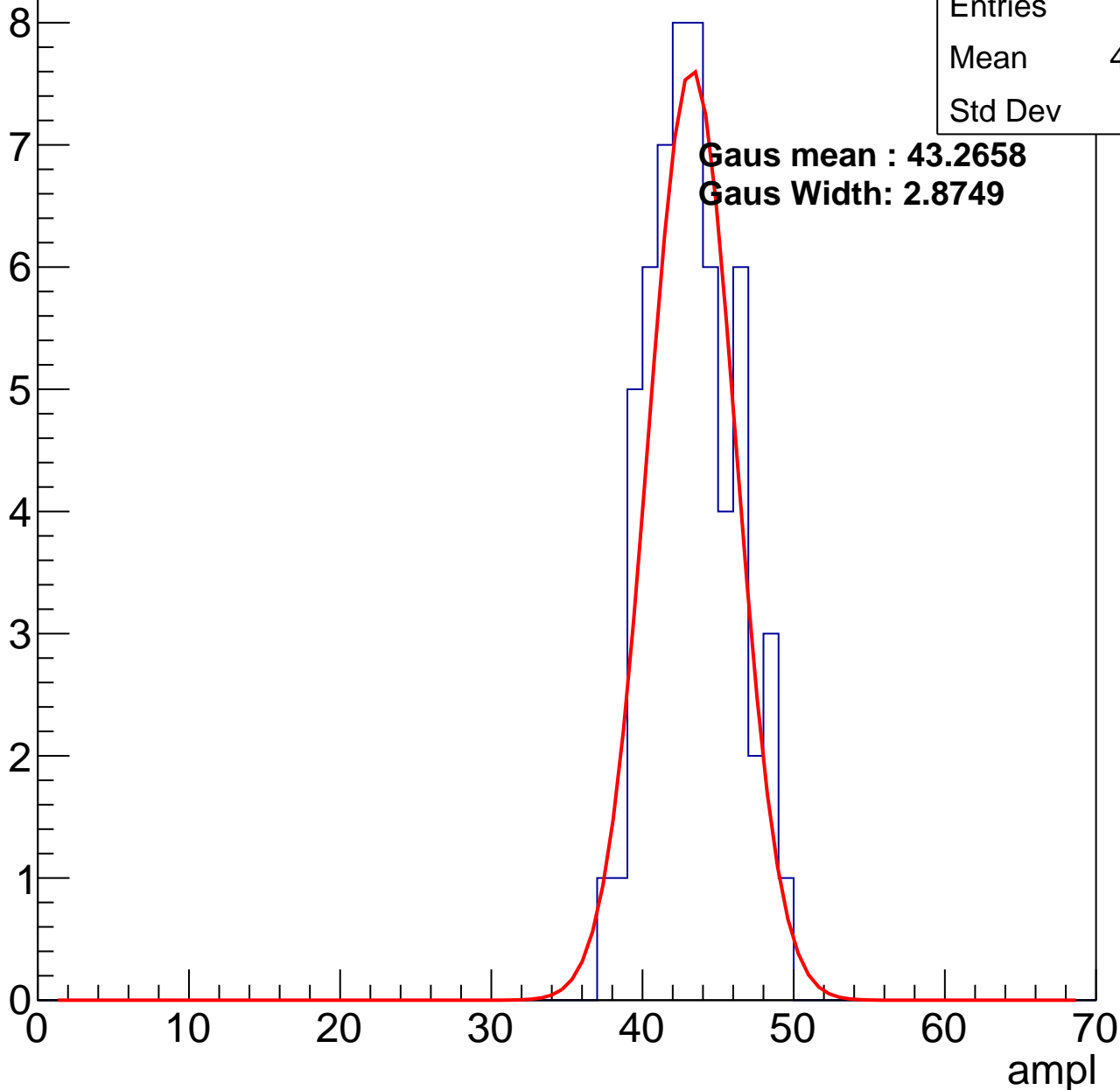
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.83
Std Dev	2.78

**Gaus mean : 43.2658**

**Gaus Width: 2.8749**

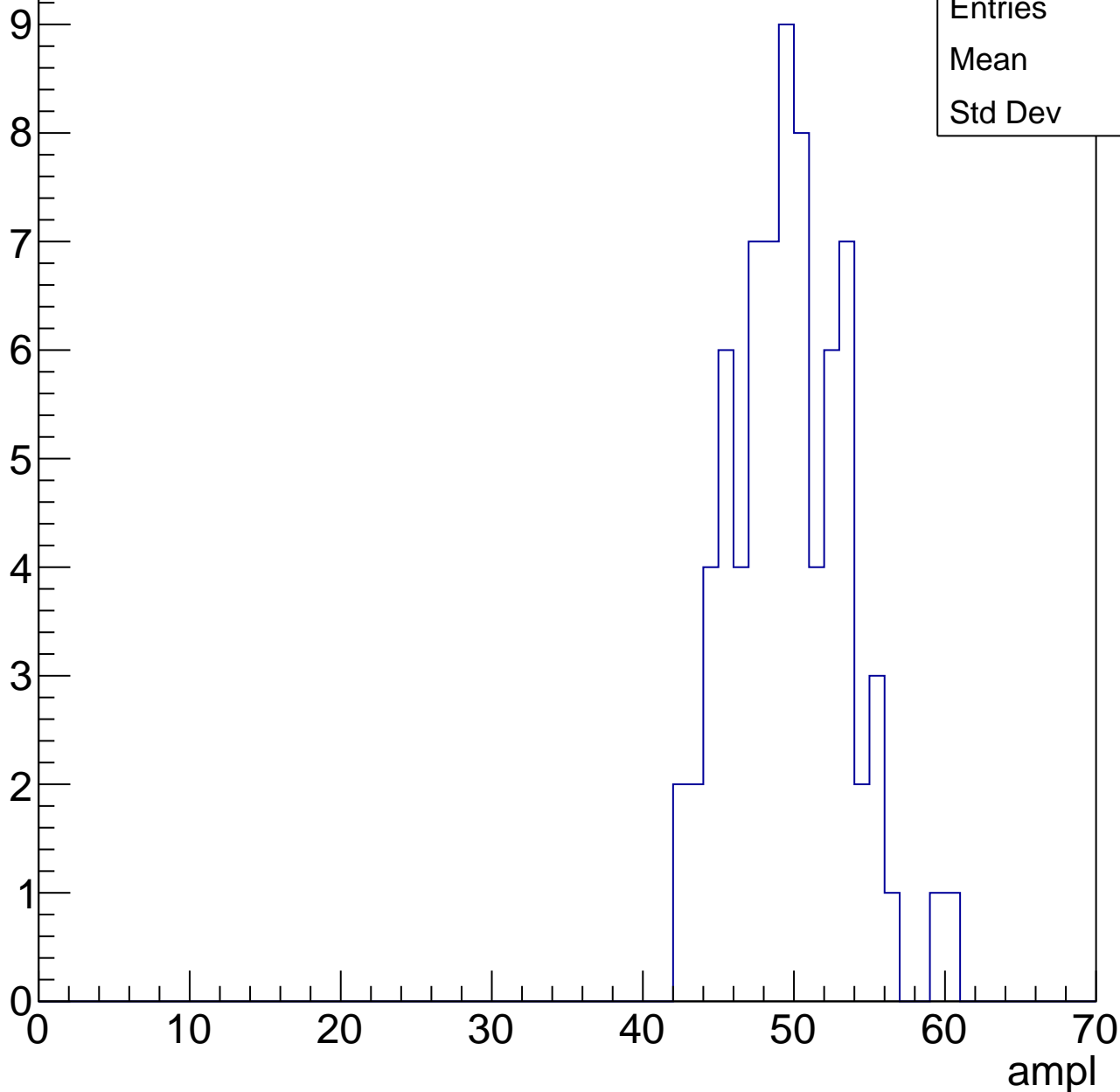


# B1L003S, U22-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.2
Std Dev	3.77

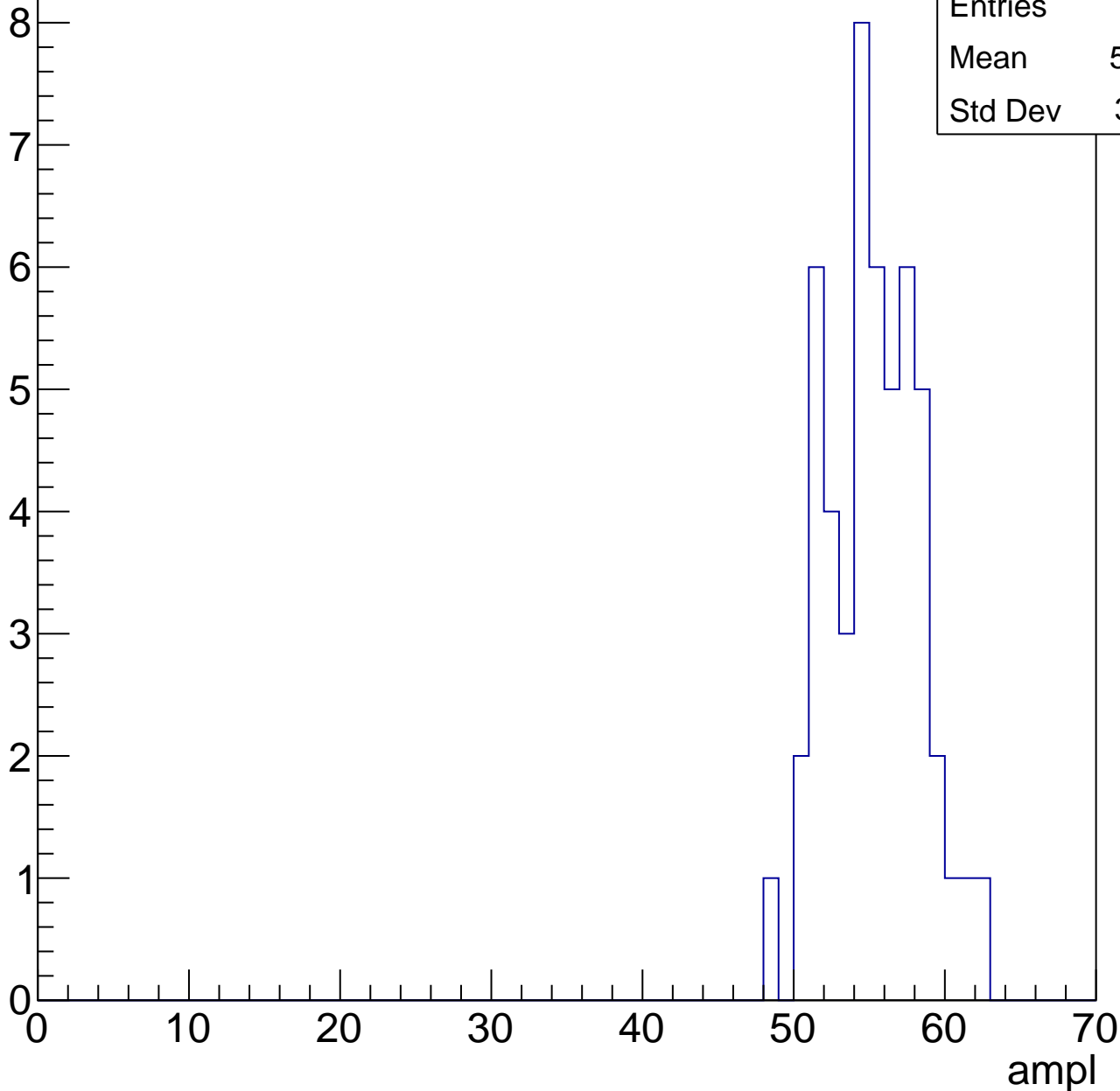


# B1L003S, U22-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.82
Std Dev	3.021

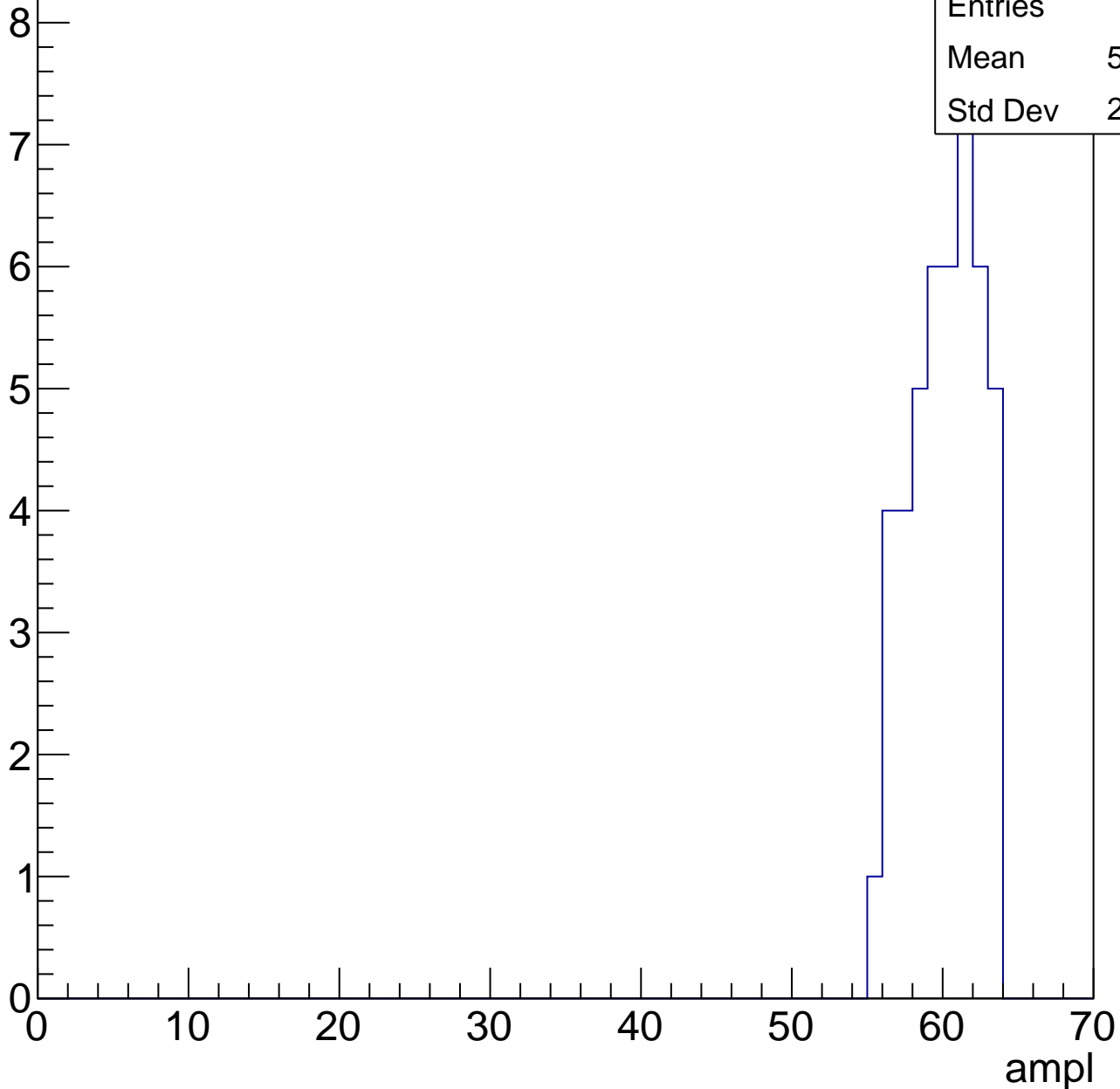


# B1L003S, U22-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.69
Std Dev	2.229

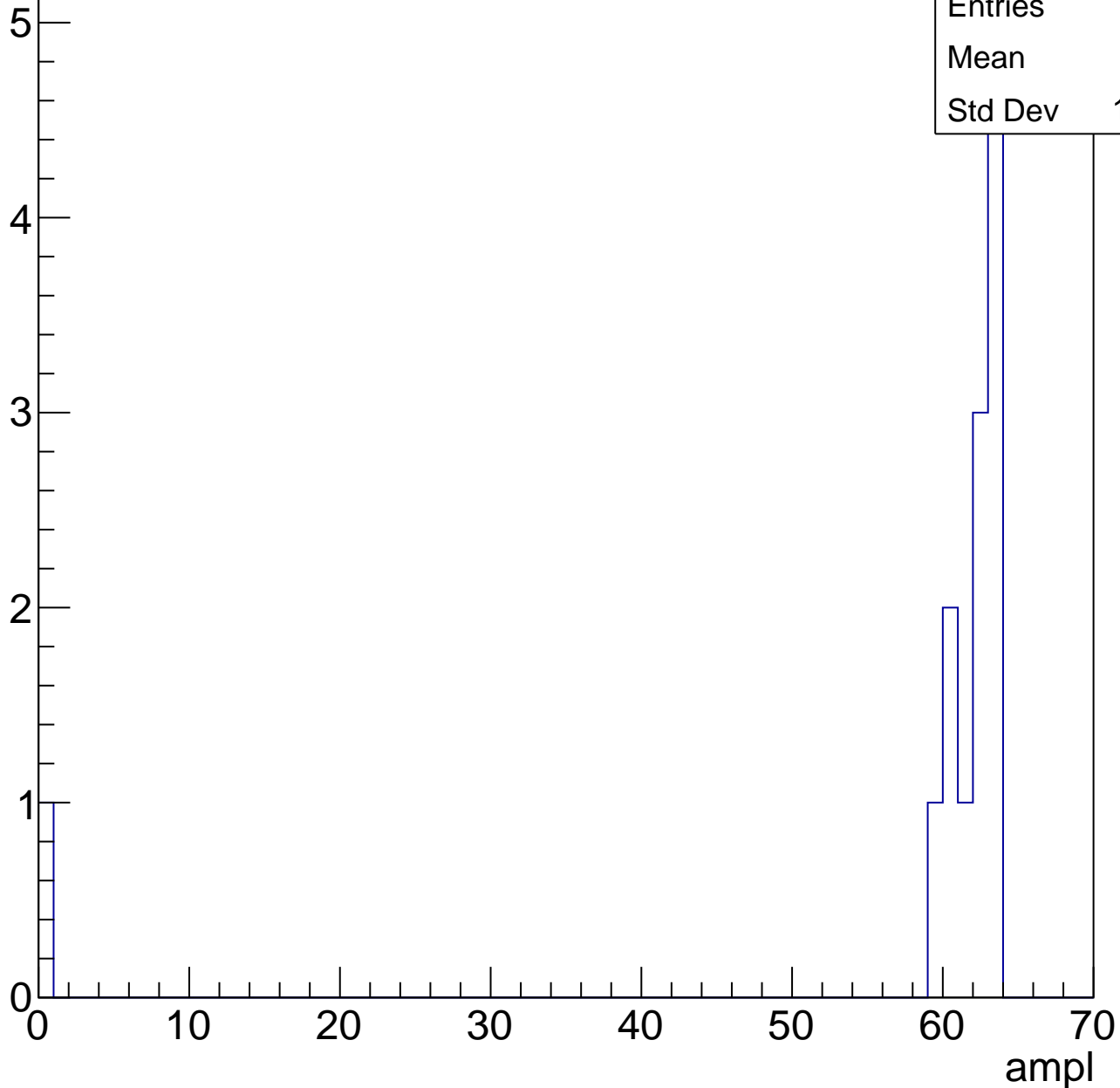


# B1L003S, U22-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	13
Mean	57
Std Dev	16.51





# B1L003S, U22-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	82
Mean	29.41
Std Dev	3.523

**Gaus mean : 29.6218**

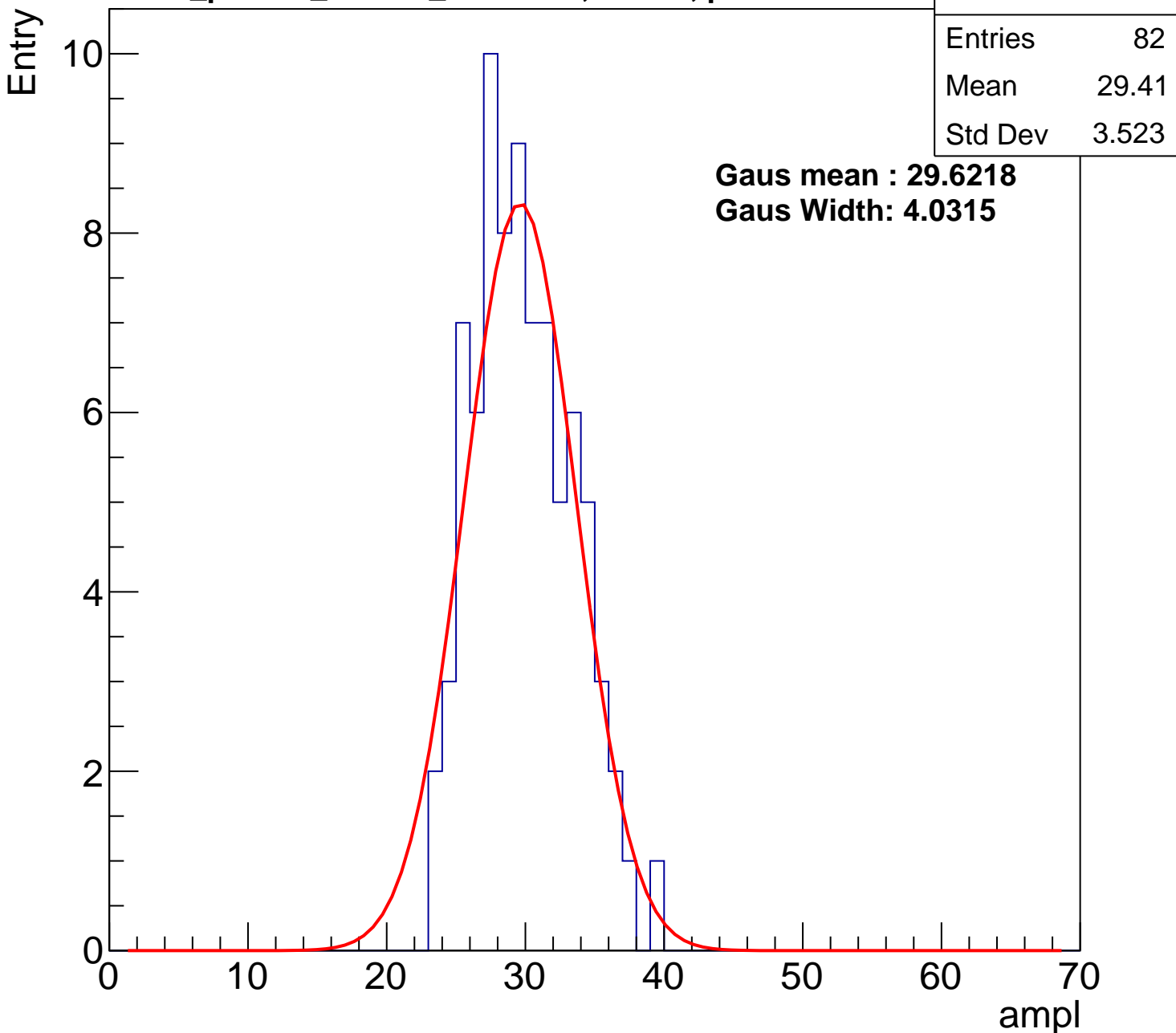
**Gaus Width: 4.0315**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch77, adc1

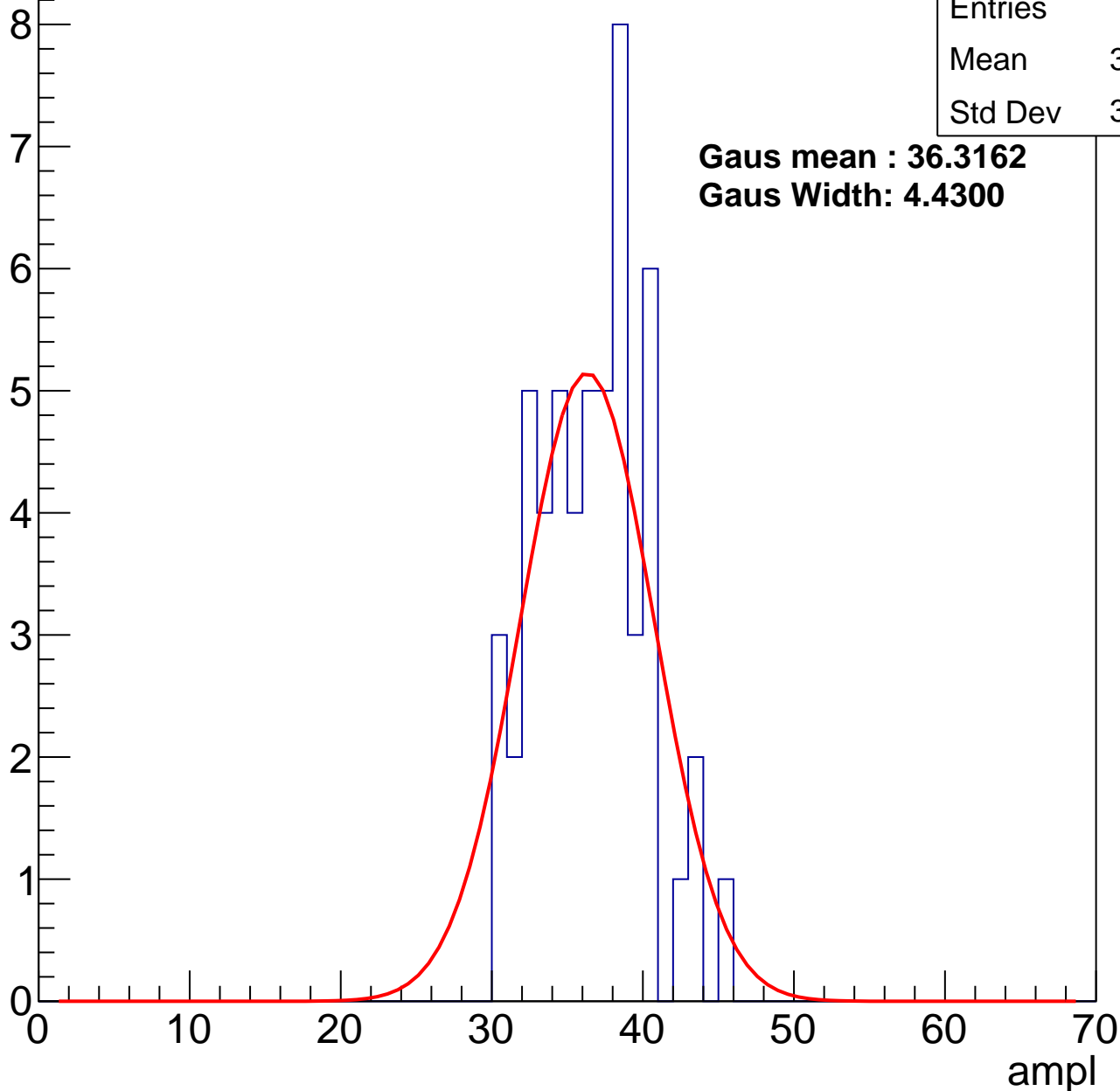
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.17
Std Dev	3.526

**Gaus mean : 36.3162**

**Gaus Width: 4.4300**



# B1L003S, U22-ch77, adc2

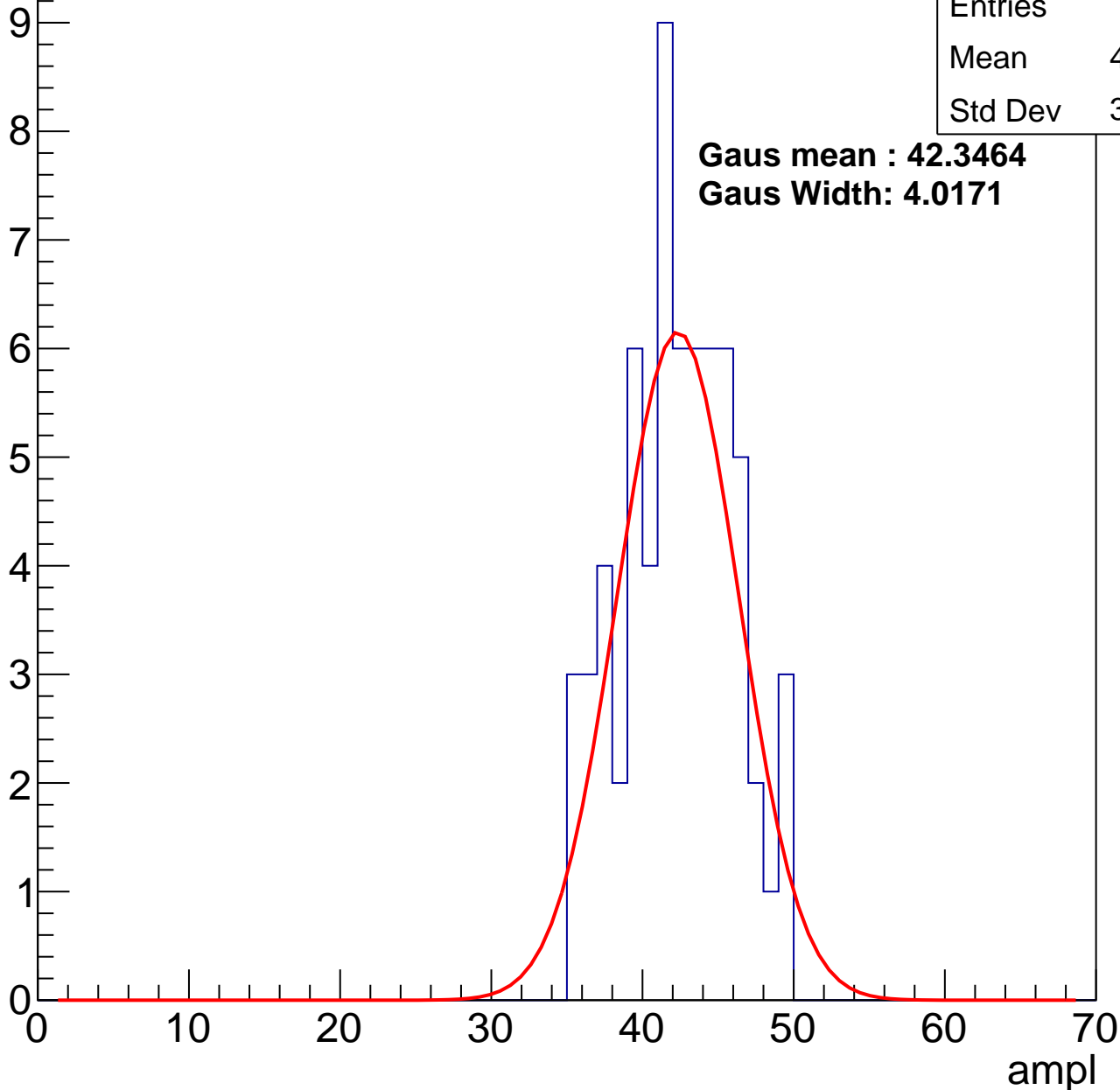
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.86
Std Dev	3.613

**Gaus mean : 42.3464**

**Gaus Width: 4.0171**

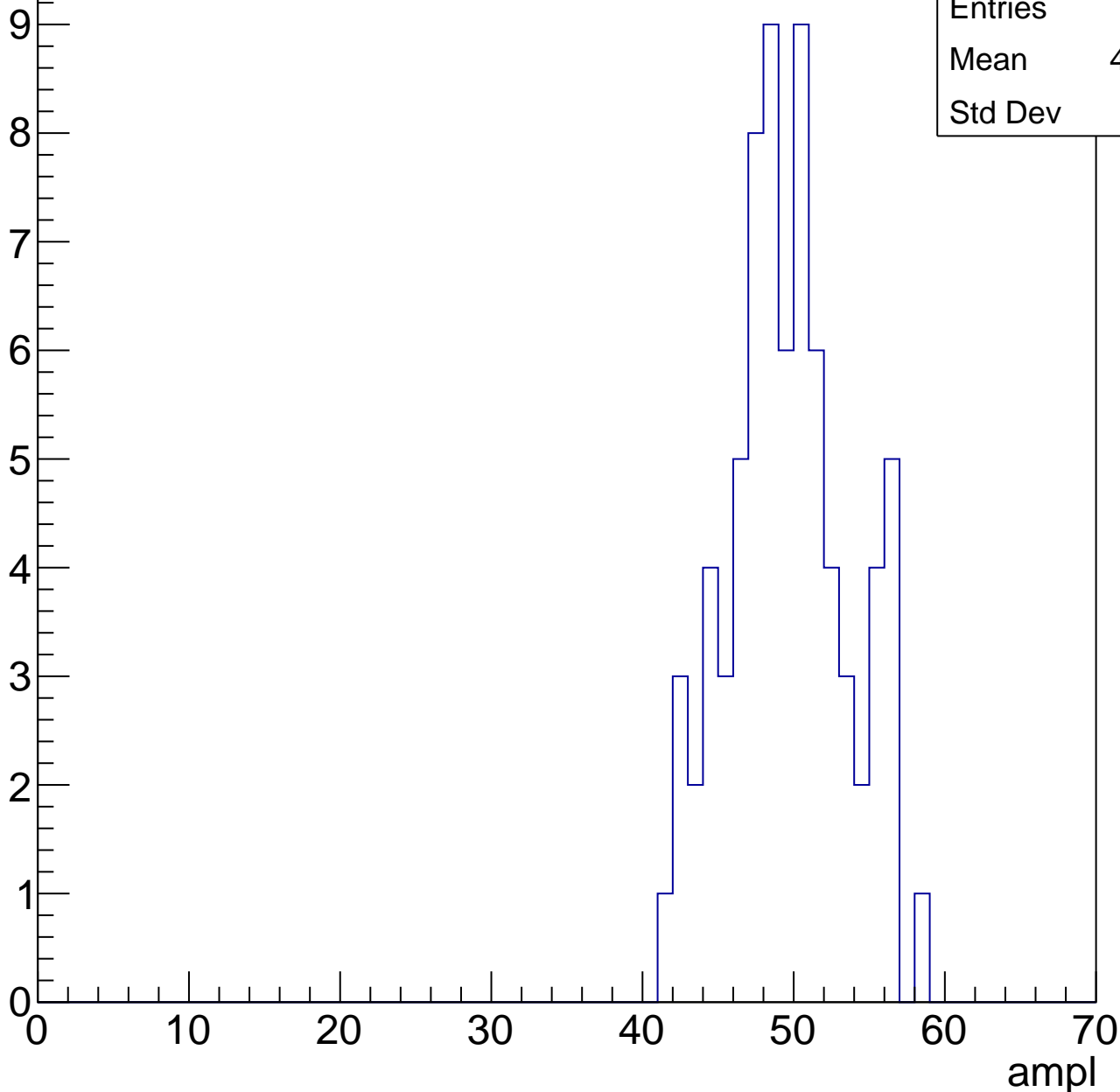


# B1L003S, U22-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	49.13
Std Dev	3.92

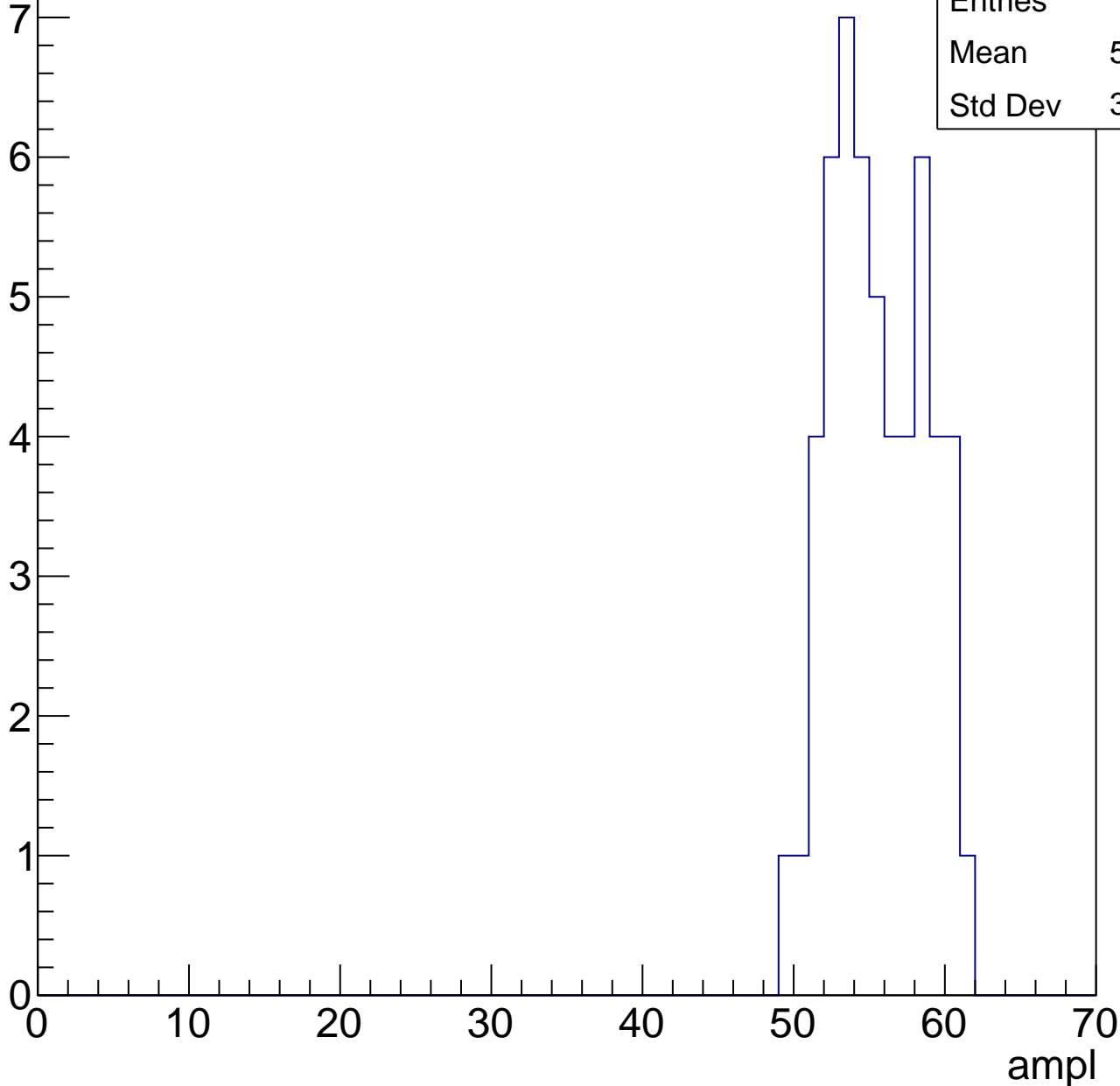


# B1L003S, U22-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.13
Std Dev	3.028

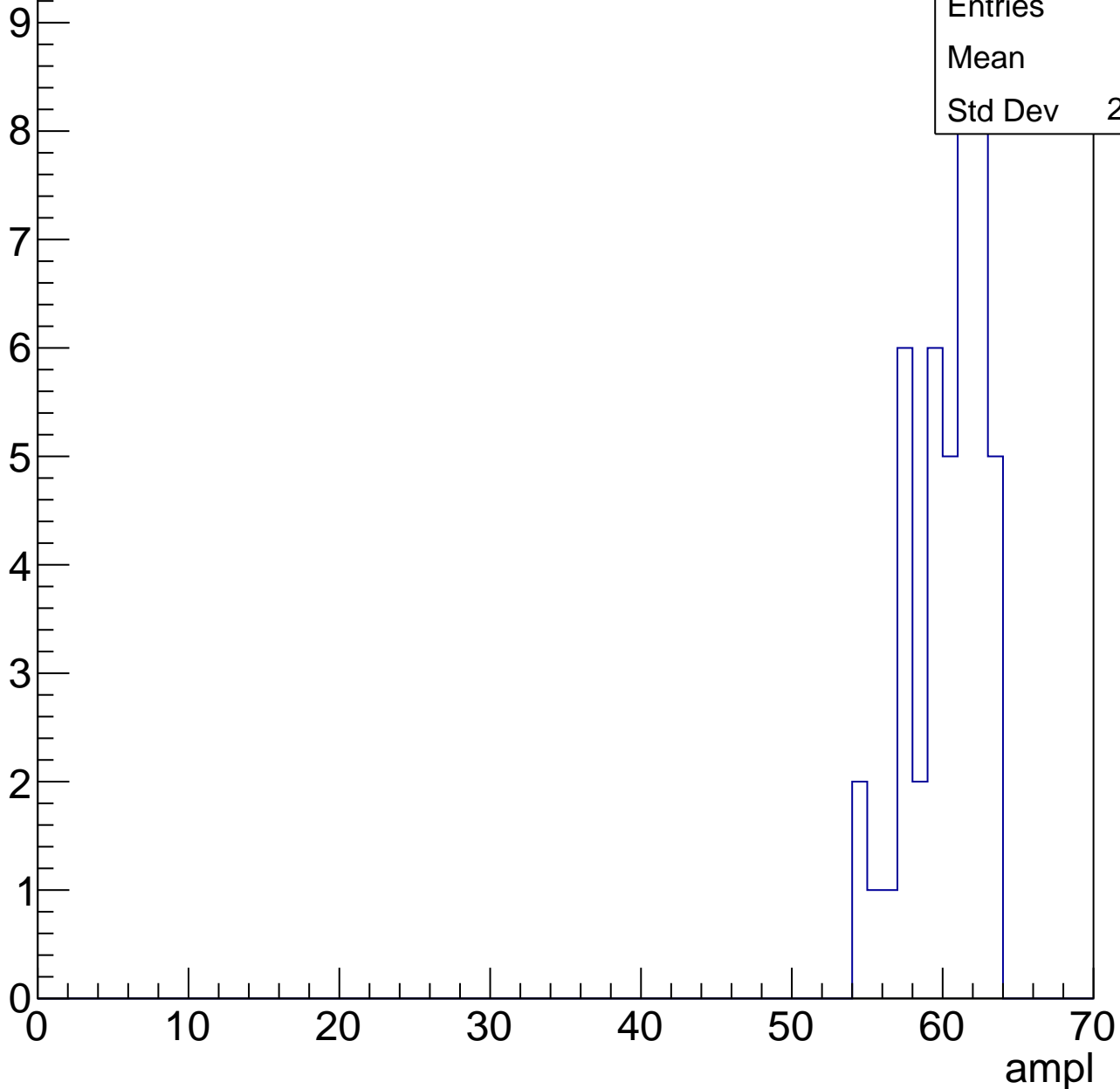


# B1L003S, U22-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

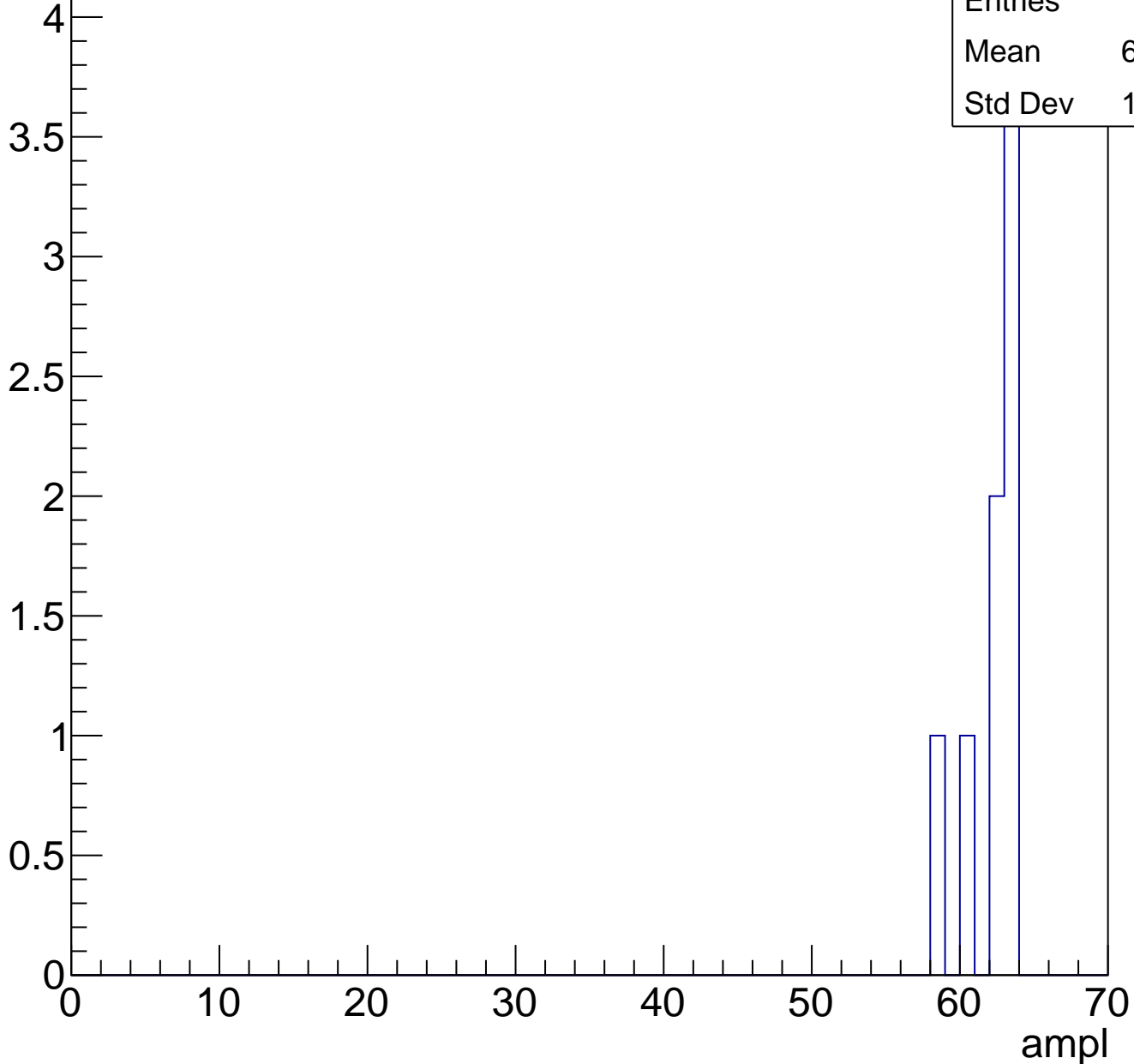
Entries	45
Mean	59.8
Std Dev	2.428



# B1L003S, U22-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

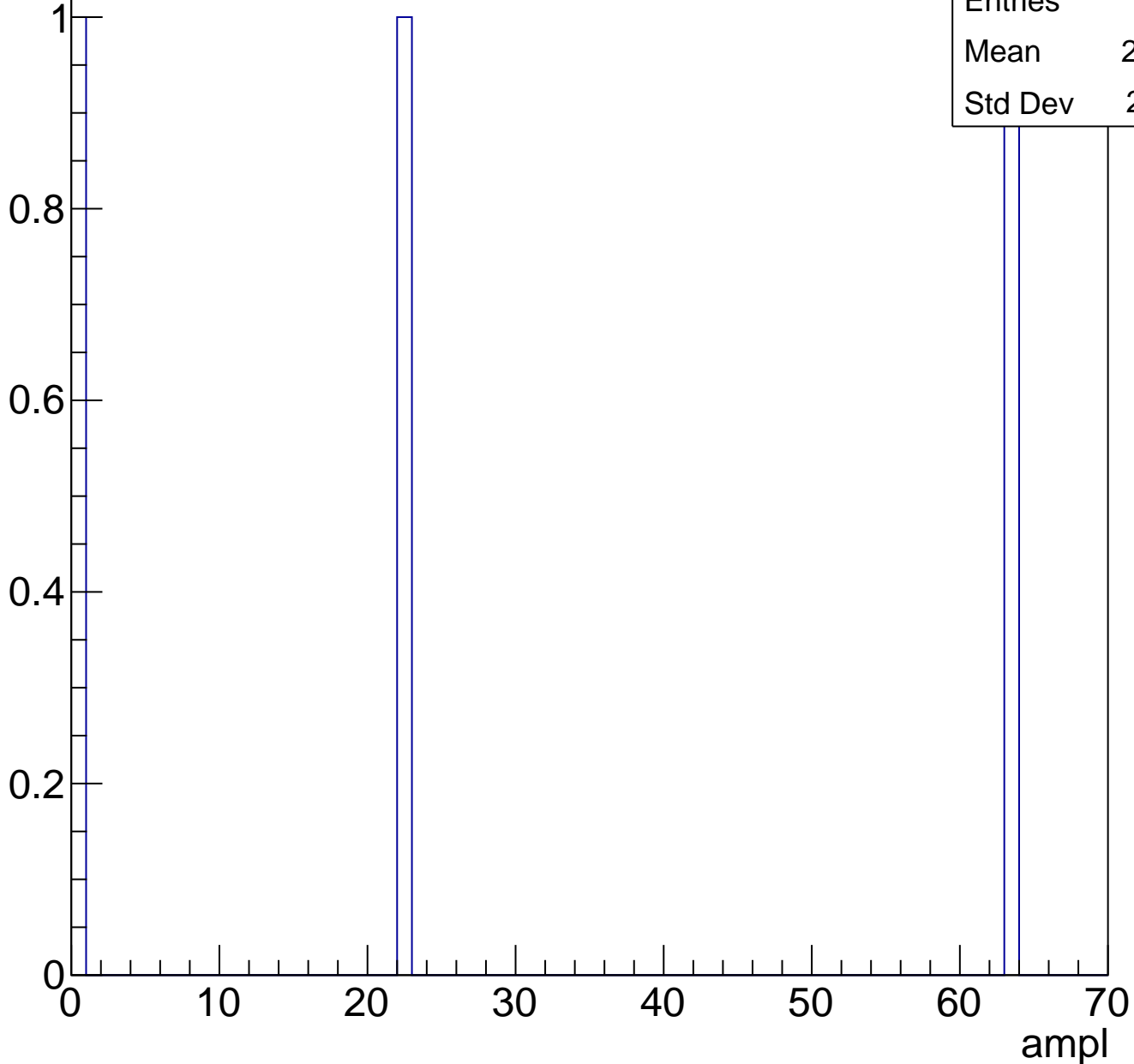




# B1L003S, U22-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L003S, U22-ch78, adc0

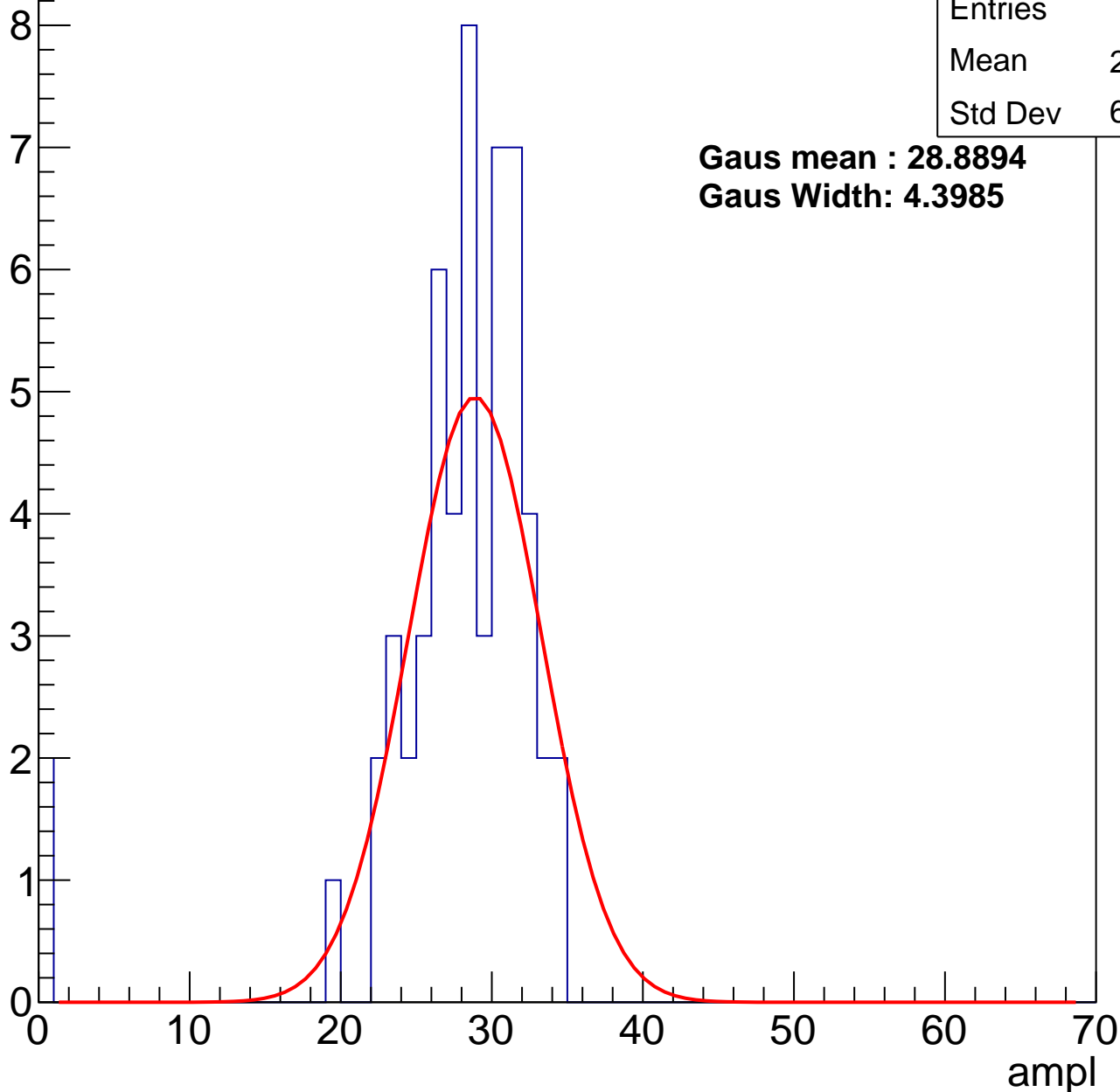
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	27.12
Std Dev	6.153

**Gaus mean : 28.8894**

**Gaus Width: 4.3985**



# B1L003S, U22-ch78, adc1

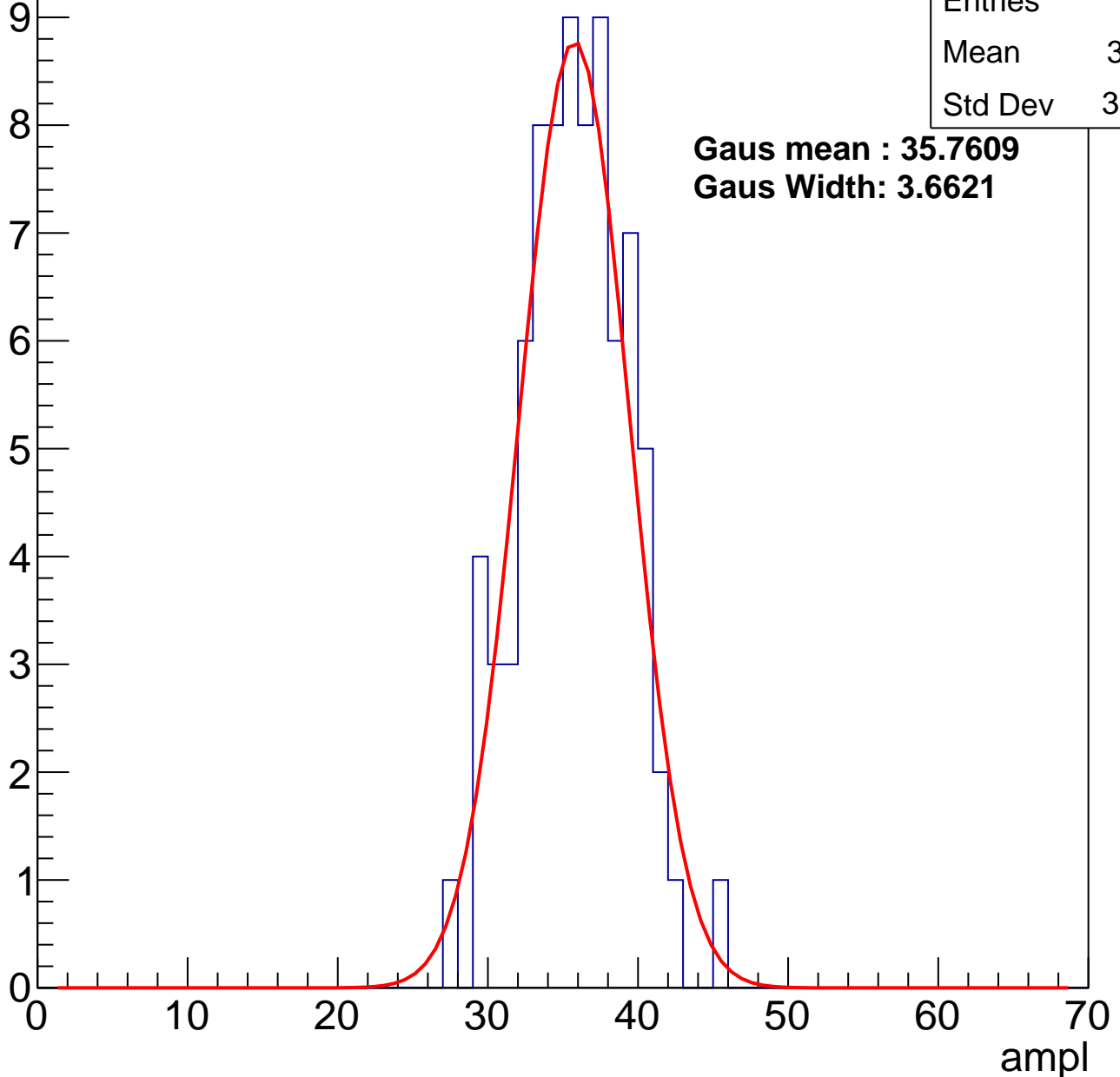
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	81
Mean	35.31
Std Dev	3.466

**Gaus mean : 35.7609**

**Gaus Width: 3.6621**



# B1L003S, U22-ch78, adc2

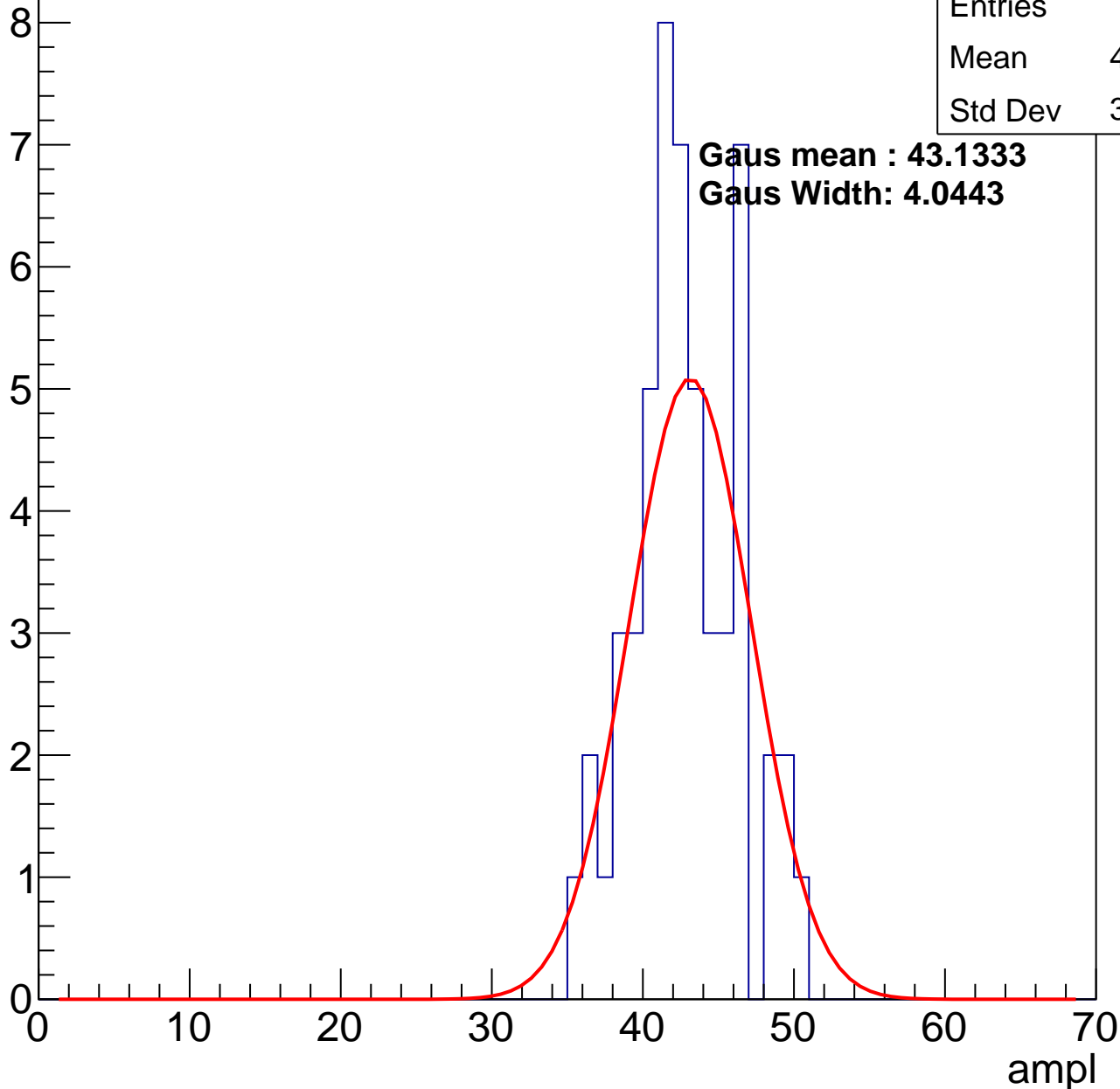
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.36
Std Dev	3.454

**Gaus mean : 43.1333**

**Gaus Width: 4.0443**

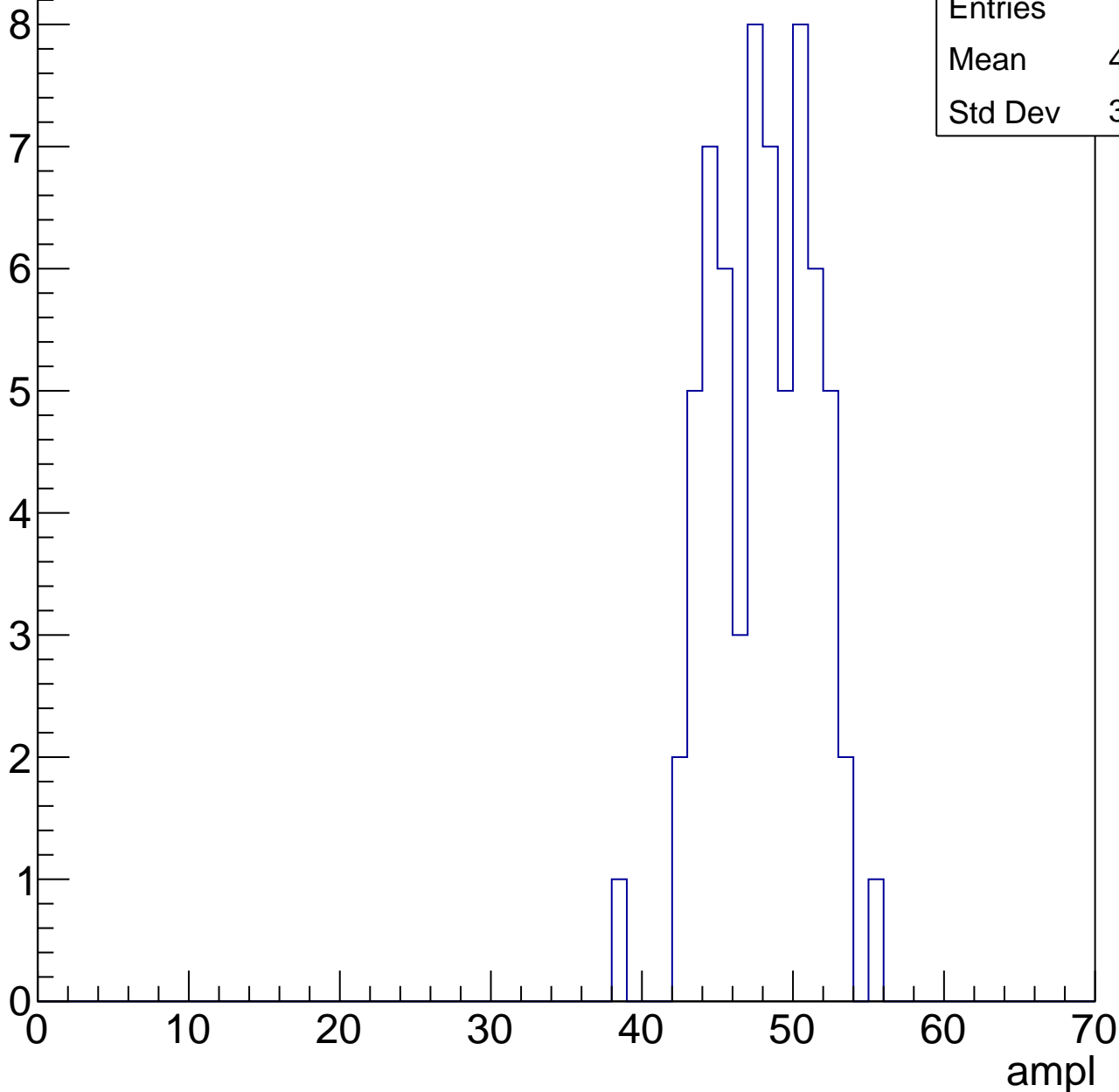


# B1L003S, U22-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	47.53
Std Dev	3.345

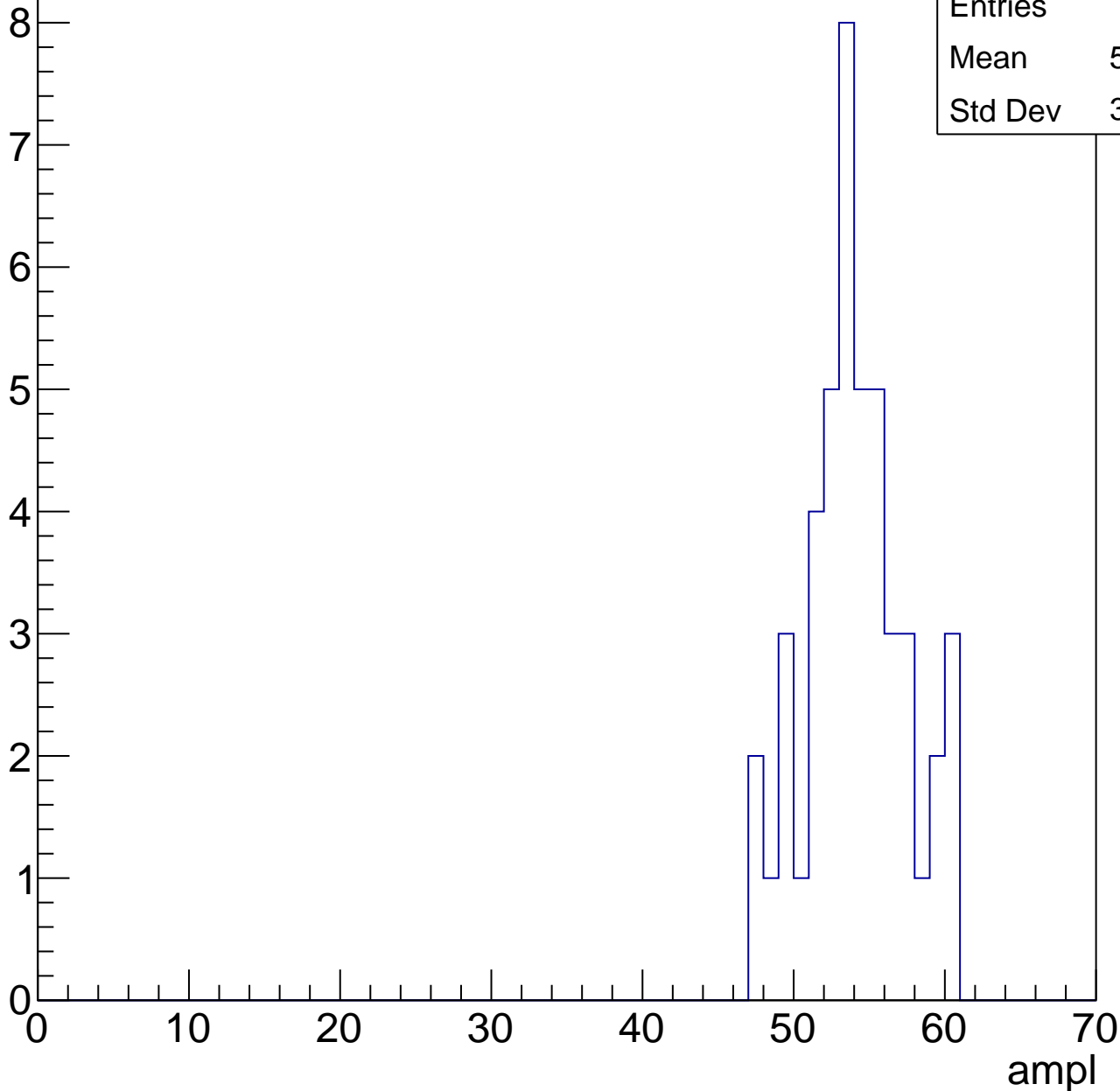


# B1L003S, U22-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	53.63
Std Dev	3.299

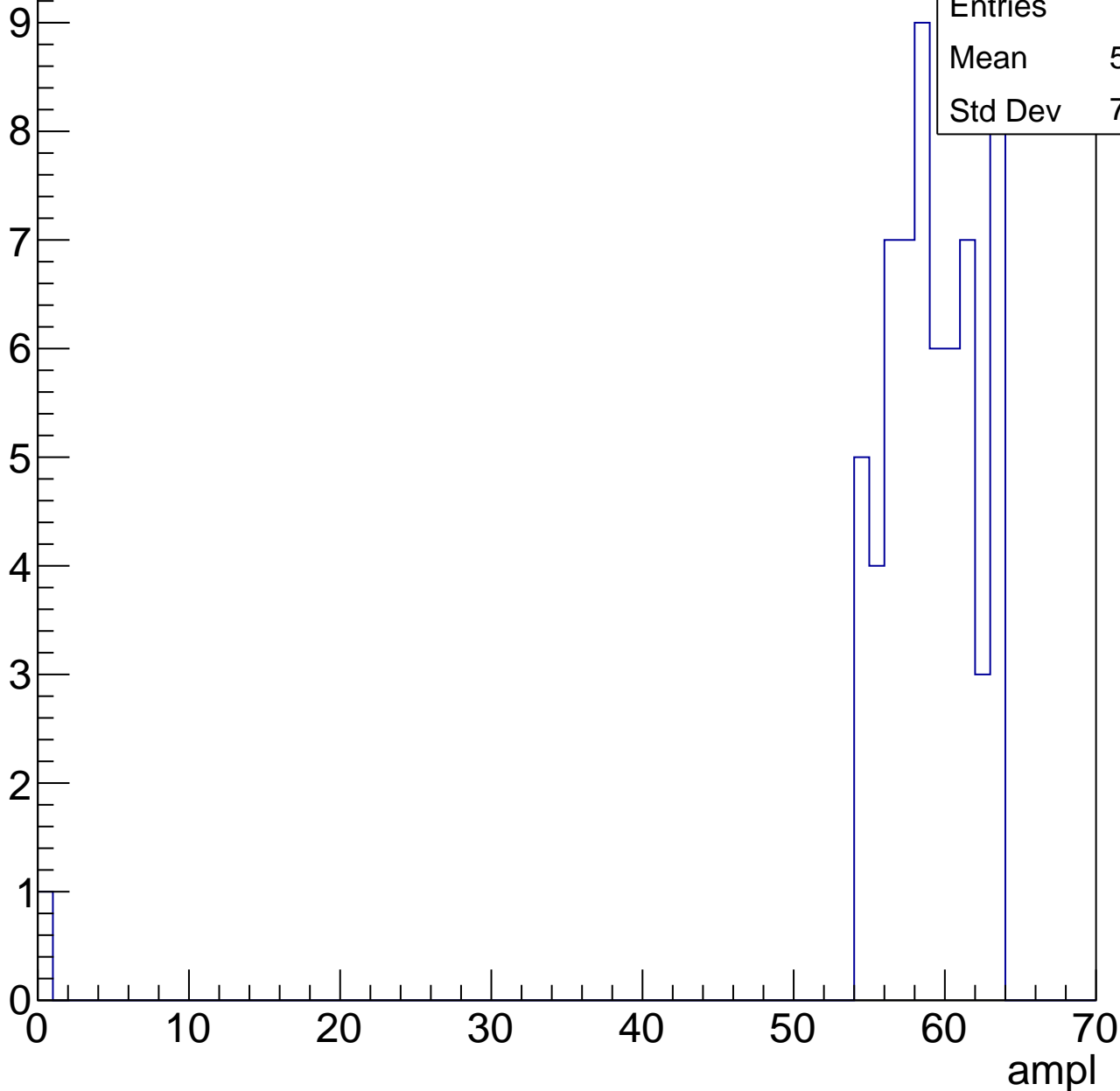


# B1L003S, U22-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	57.68
Std Dev	7.817

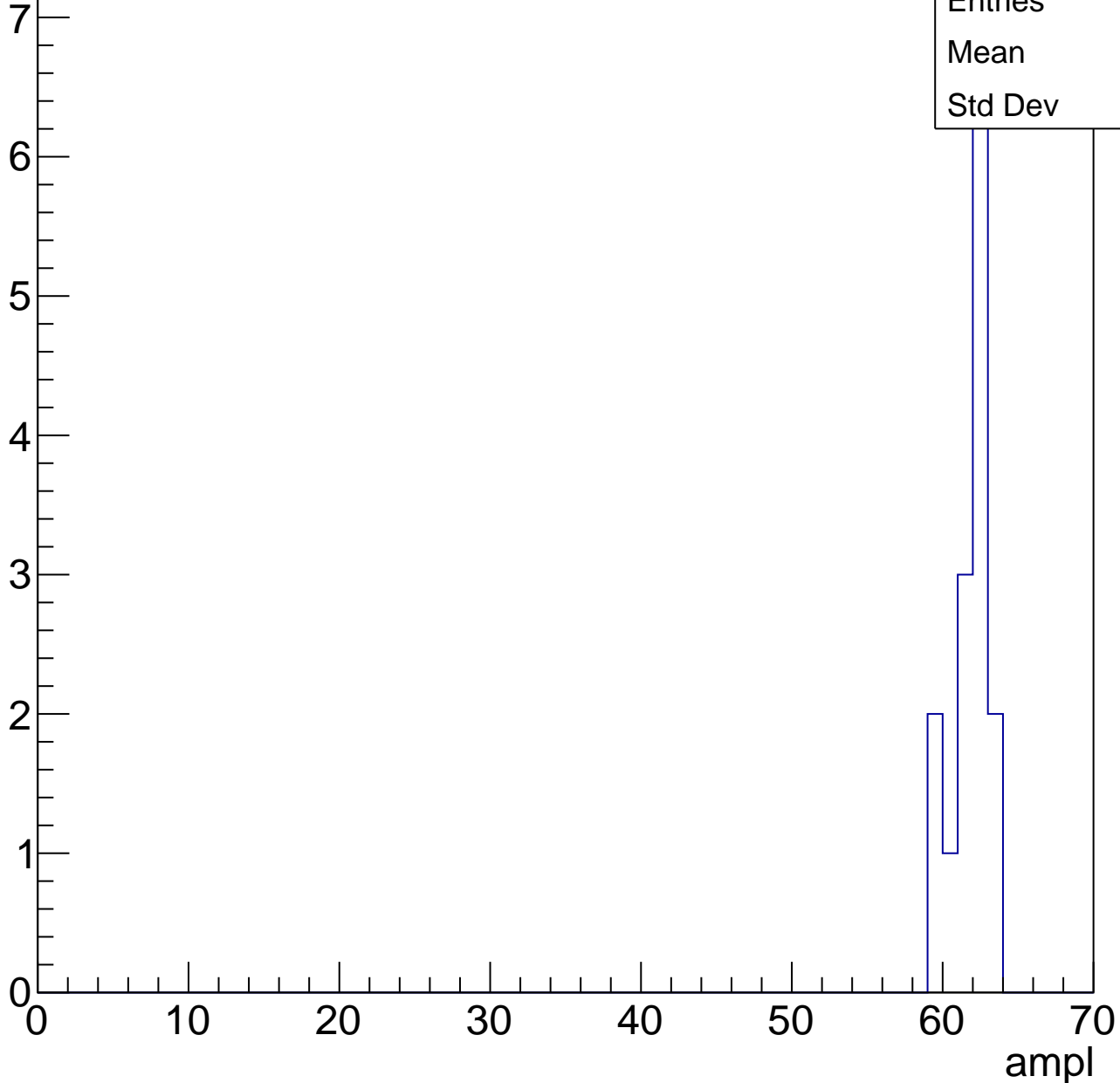


# B1L003S, U22-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	15
Mean	61.4
Std Dev	1.2





# B1L003S, U22-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch79, adc0

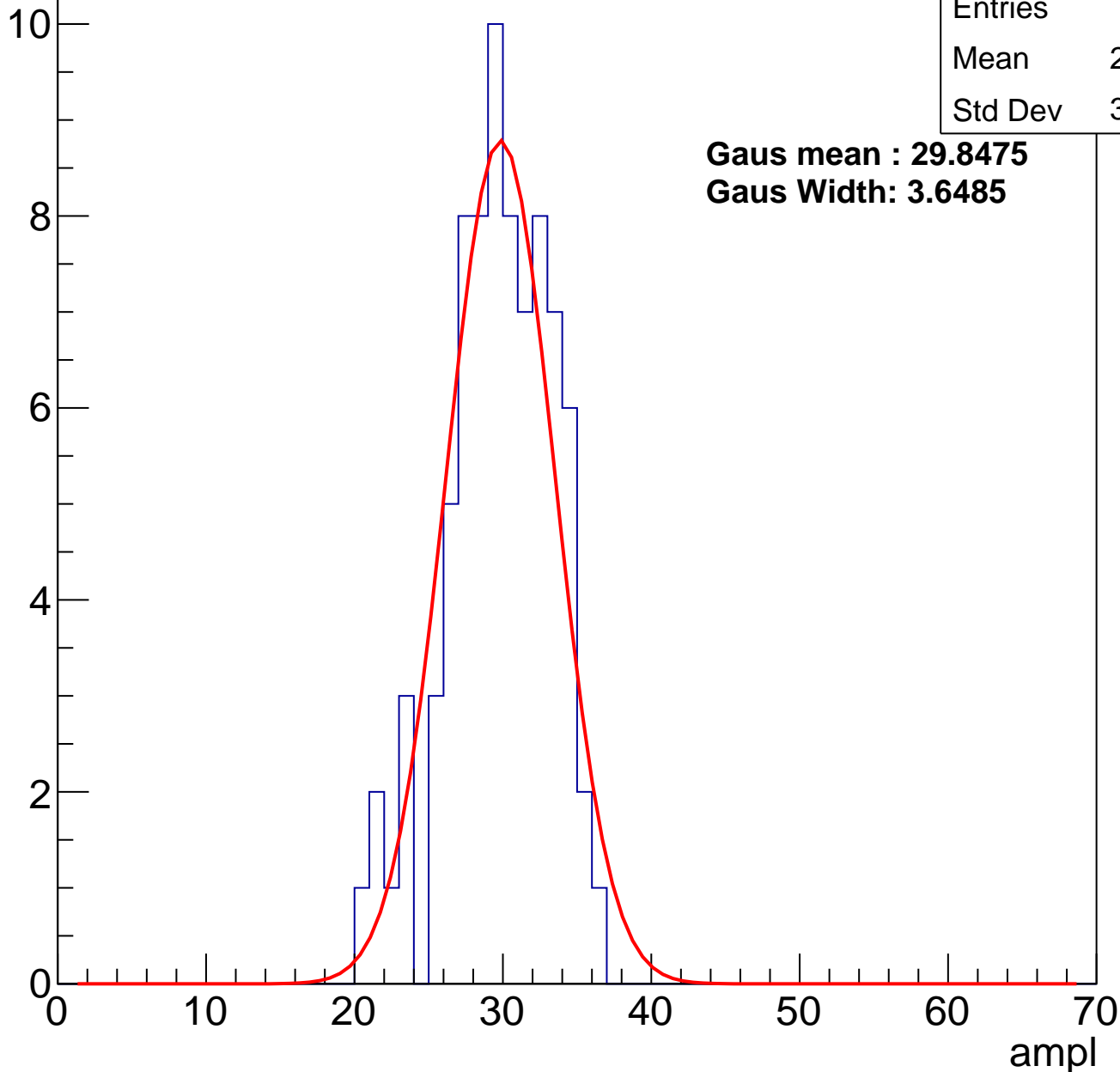
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	29.27
Std Dev	3.507

**Gaus mean : 29.8475**

**Gaus Width: 3.6485**

Entry



# B1L003S, U22-ch79, adc1

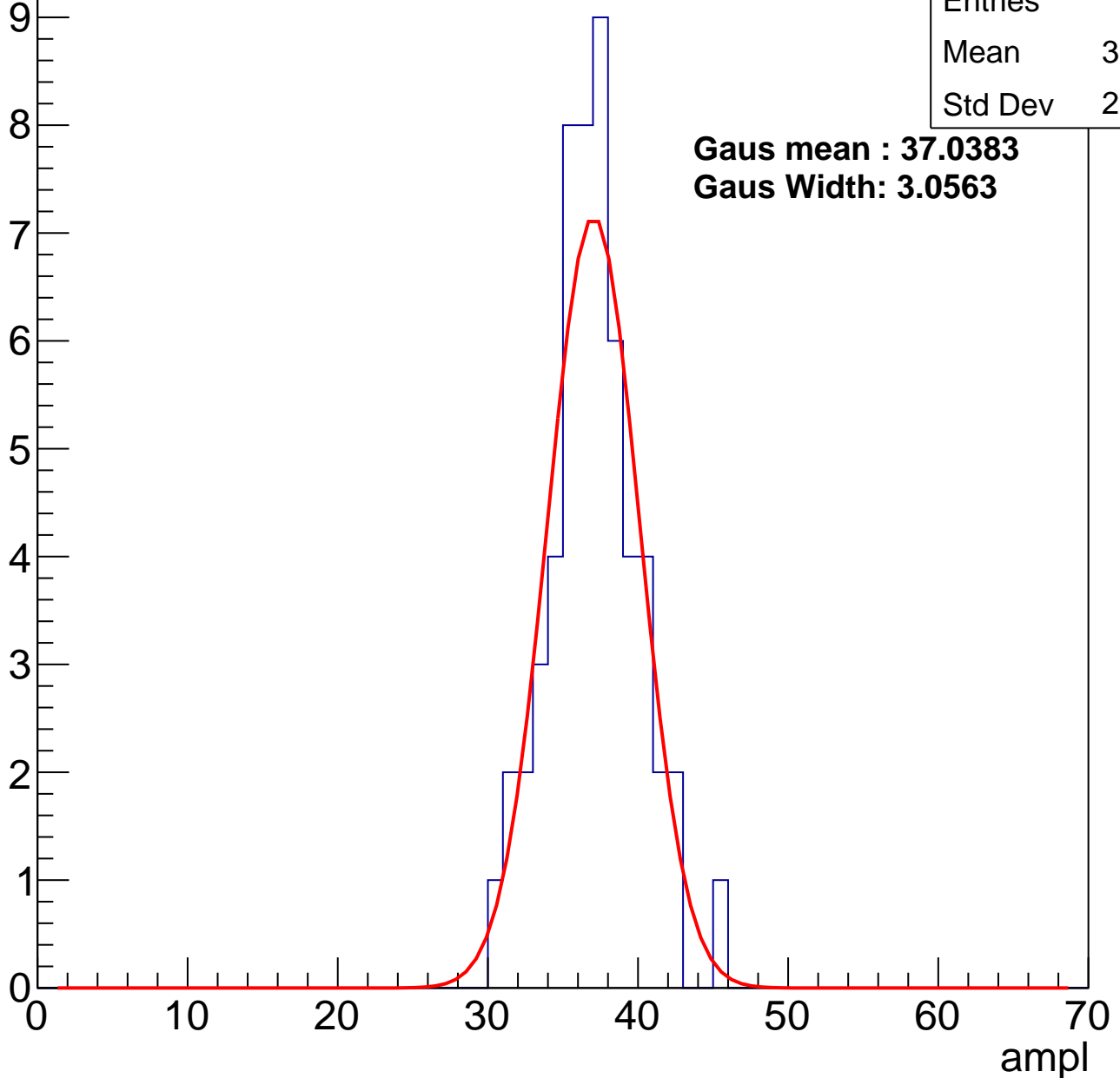
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	36.55
Std Dev	2.952

**Gaus mean : 37.0383**

**Gaus Width: 3.0563**



# B1L003S, U22-ch79, adc2

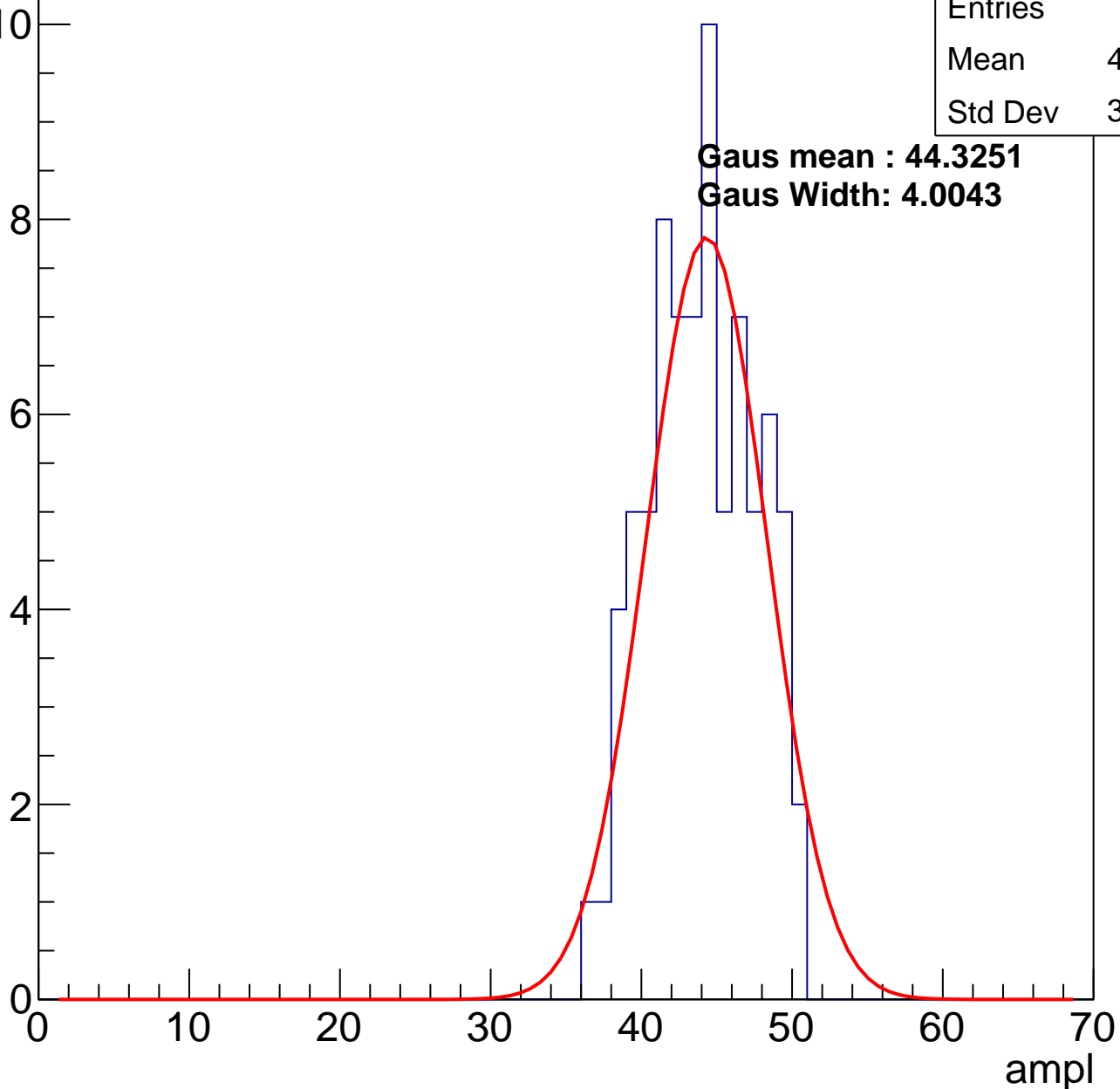
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	43.56
Std Dev	3.444

**Gaus mean : 44.3251**

**Gaus Width: 4.0043**

Entry

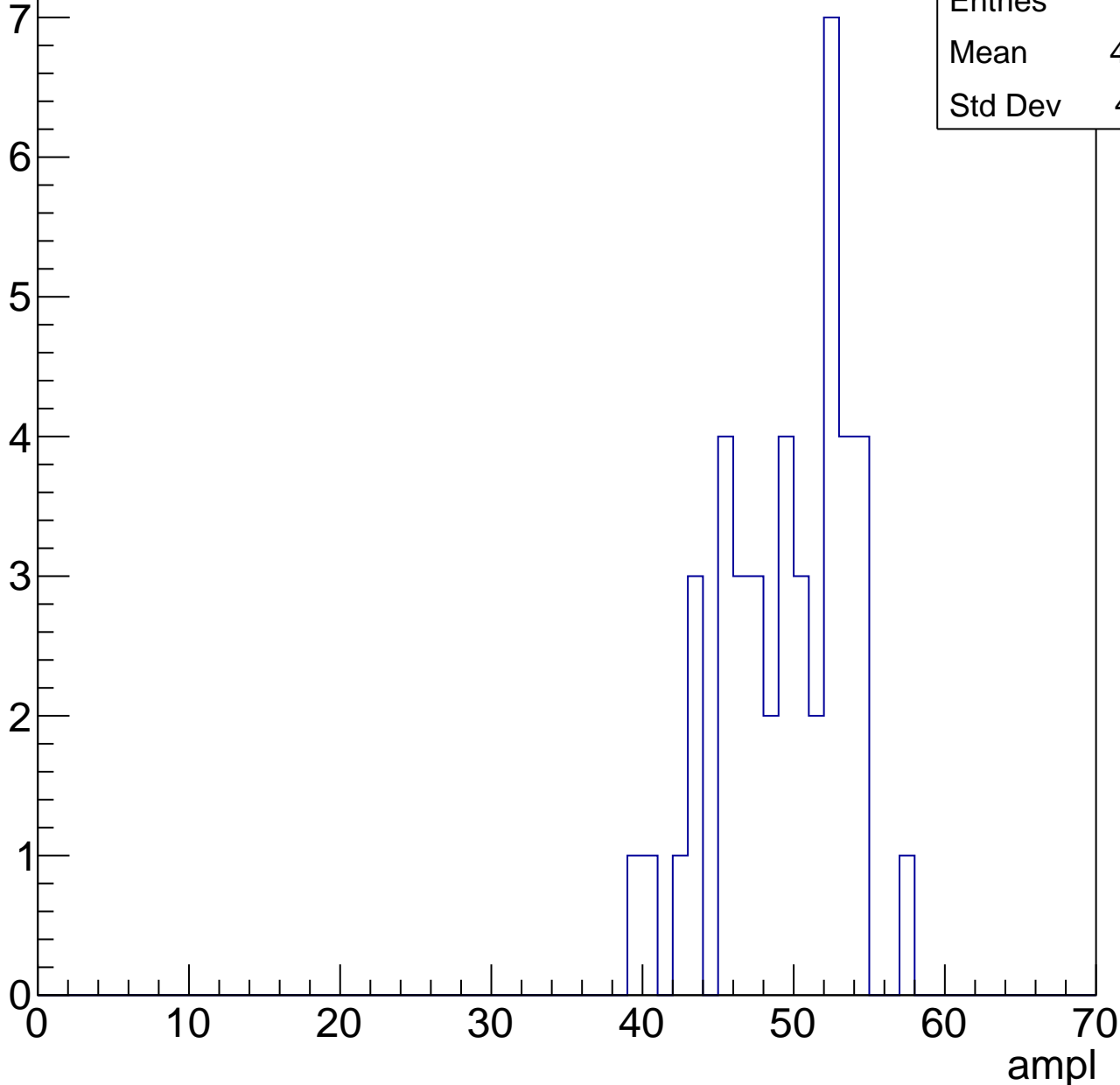


# B1L003S, U22-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

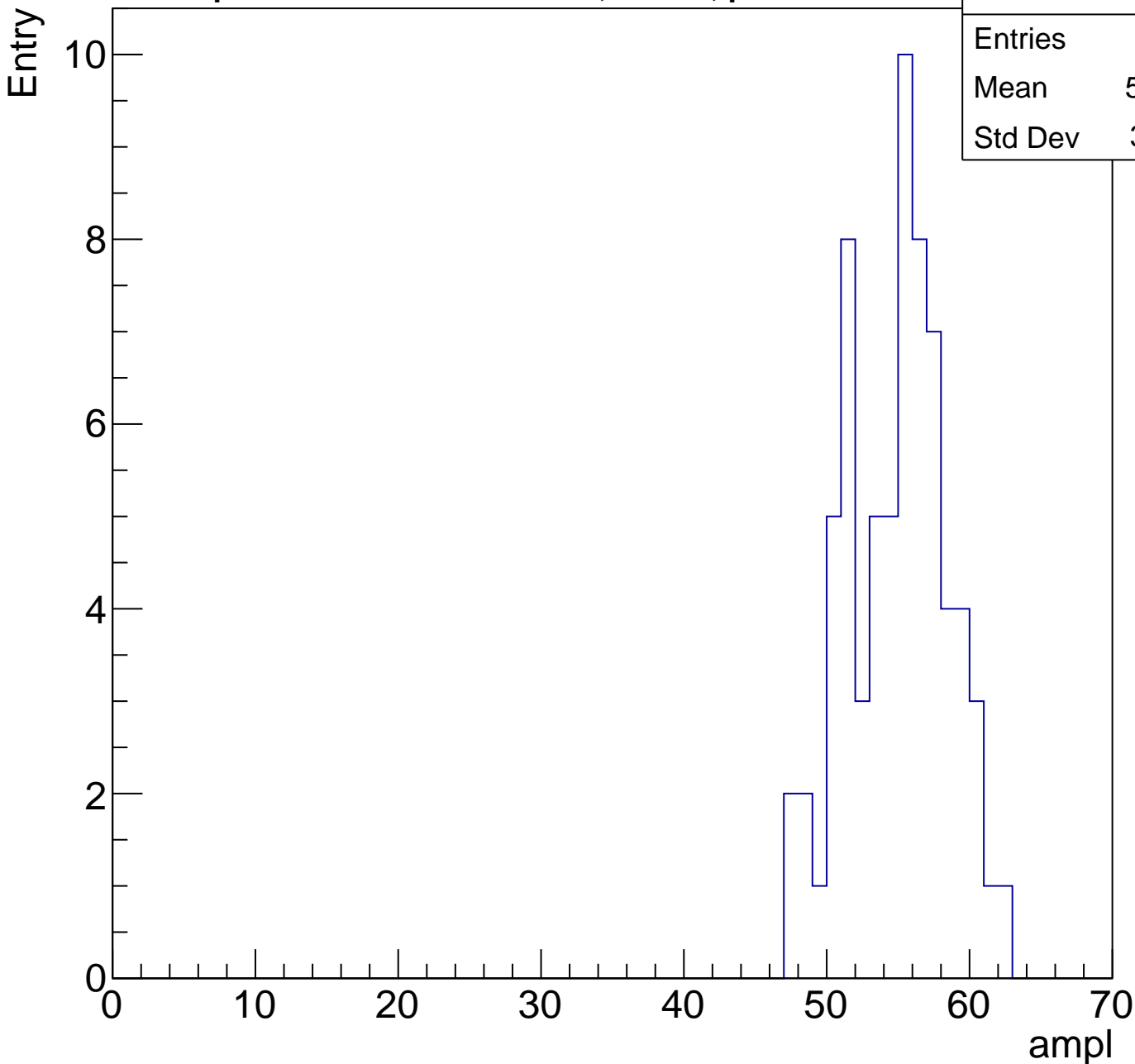
Entries	43
Mean	48.88
Std Dev	4.161



# B1L003S, U22-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	54.43
Std Dev	3.491

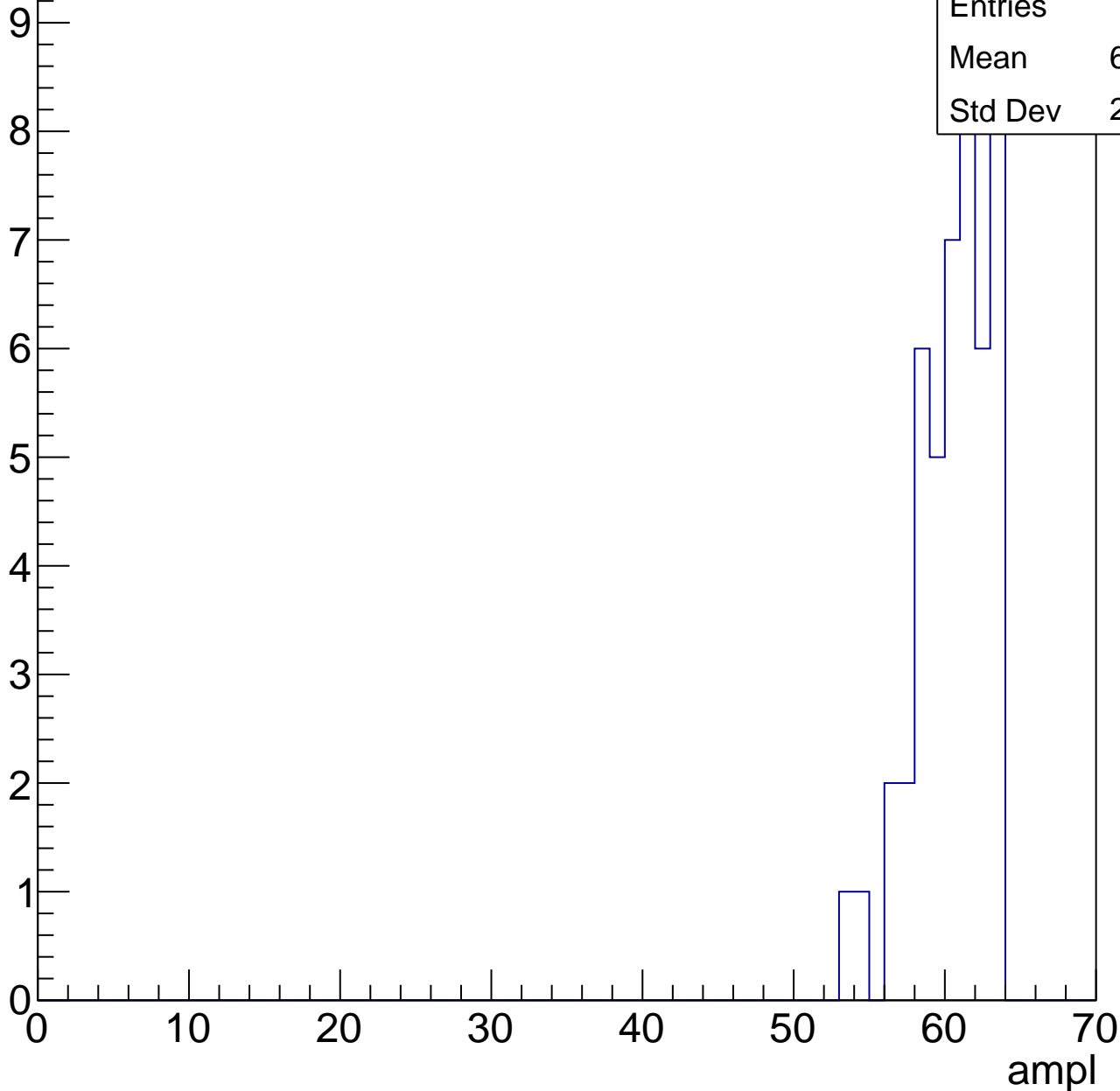


# B1L003S, U22-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

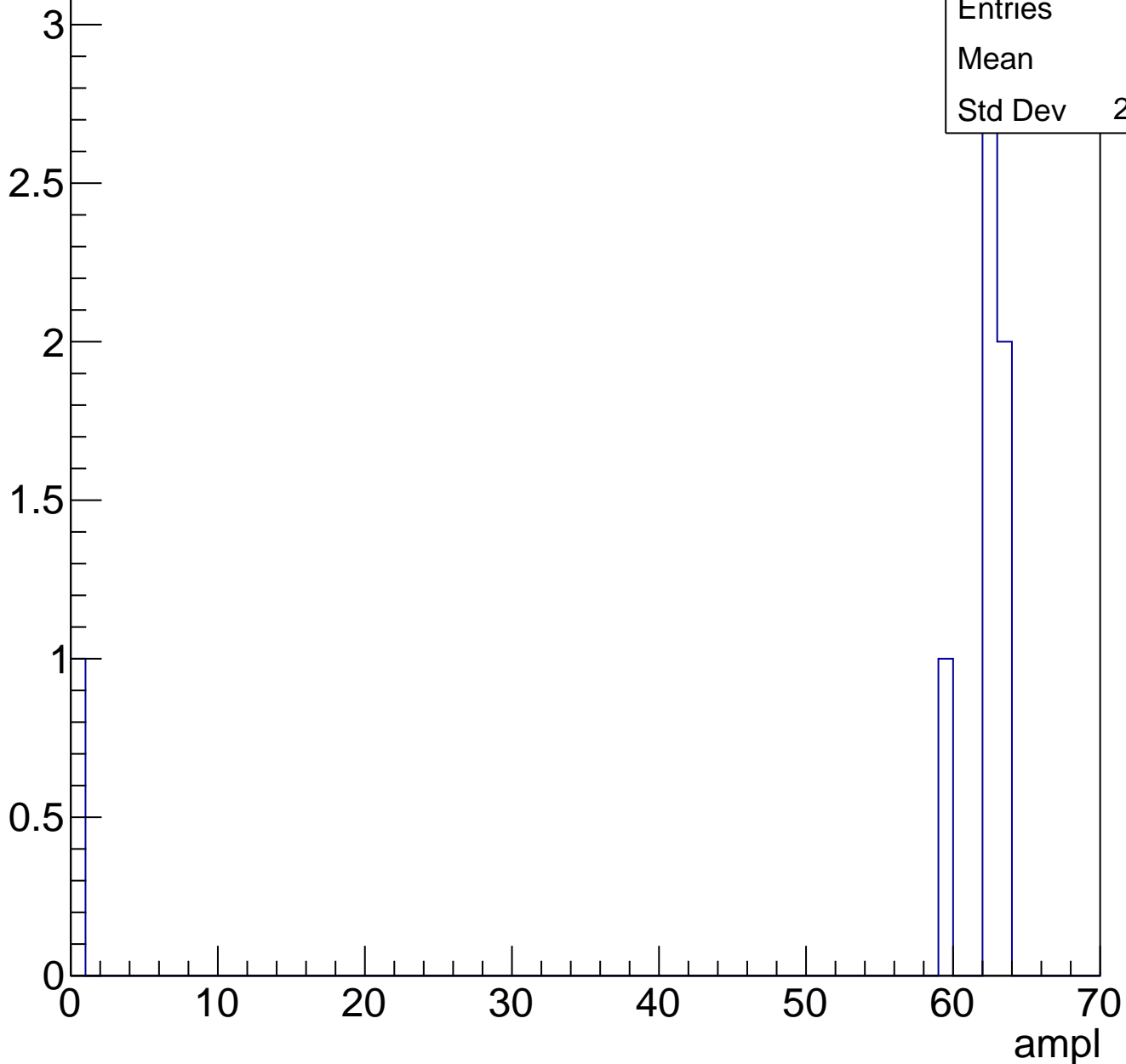
Entries	47
Mean	60.02
Std Dev	2.392



# B1L003S, U22-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch80, adc0

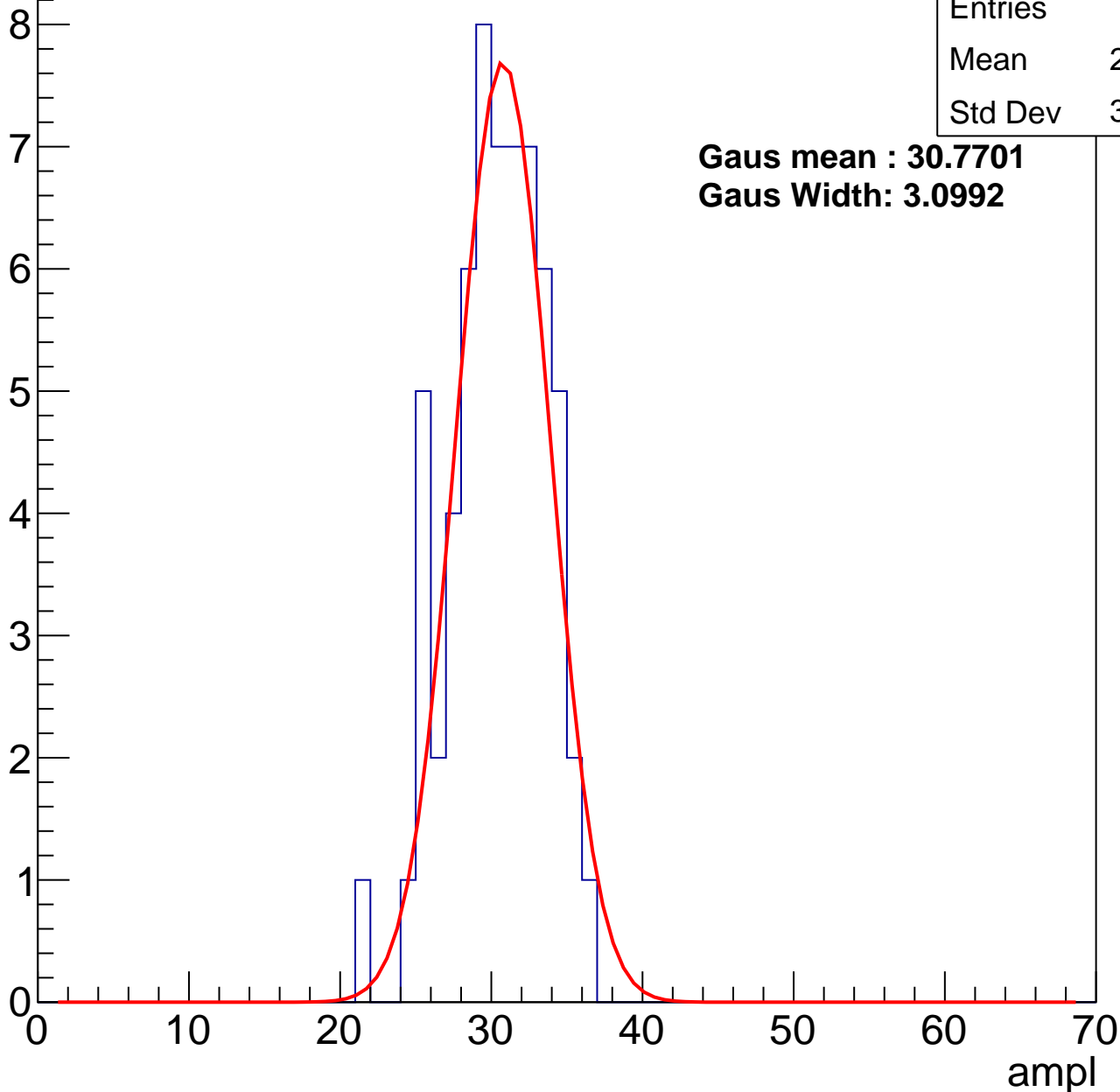
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	29.92
Std Dev	3.102

**Gaus mean : 30.7701**

**Gaus Width: 3.0992**



# B1L003S, U22-ch80, adc1

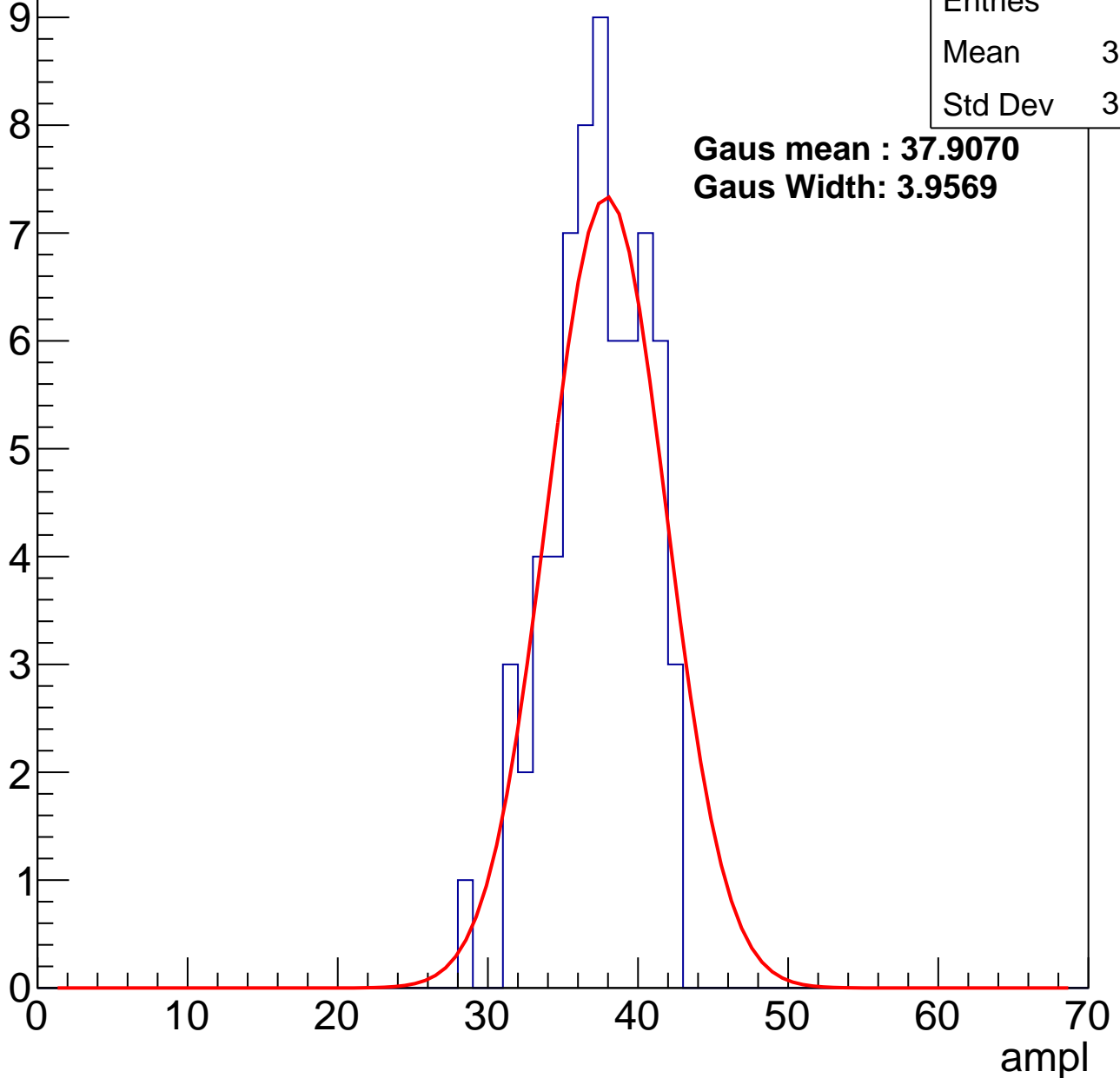
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.86
Std Dev	3.109

**Gaus mean : 37.9070**

**Gaus Width: 3.9569**



# B1L003S, U22-ch80, adc2

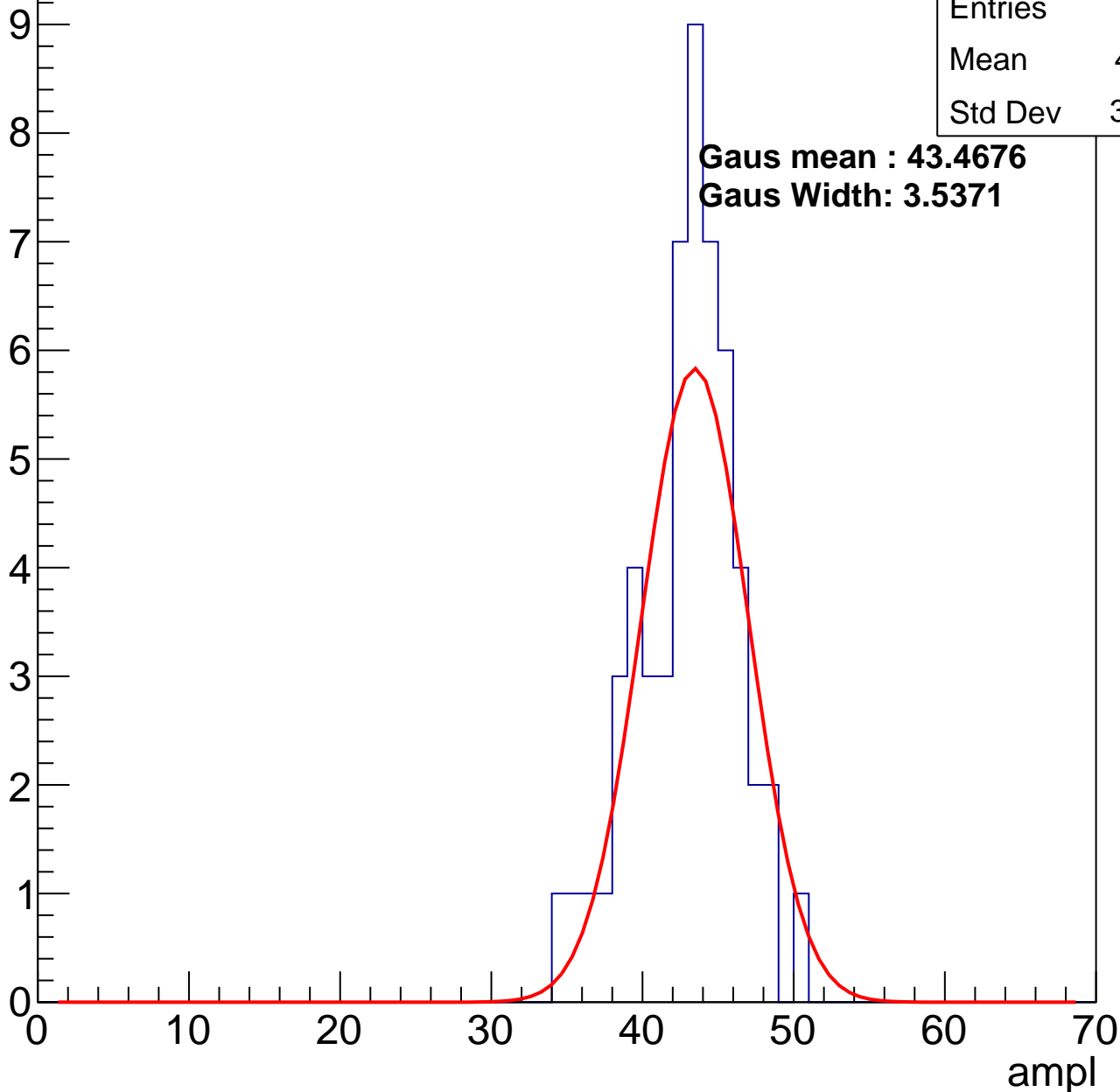
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.51
Std Dev	3.302

**Gaus mean : 43.4676**

**Gaus Width: 3.5371**

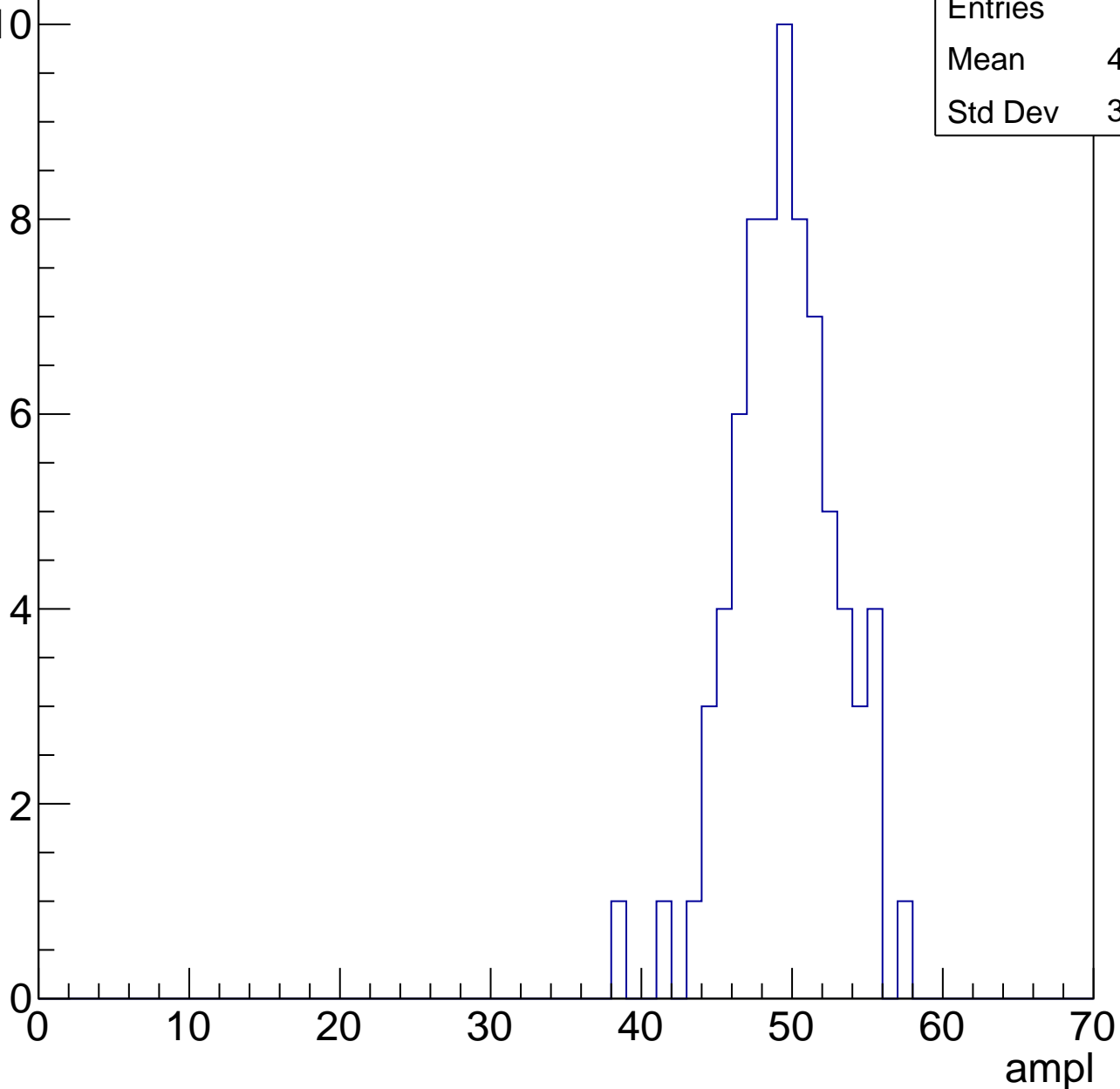


# B1L003S, U22-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	49.03
Std Dev	3.452

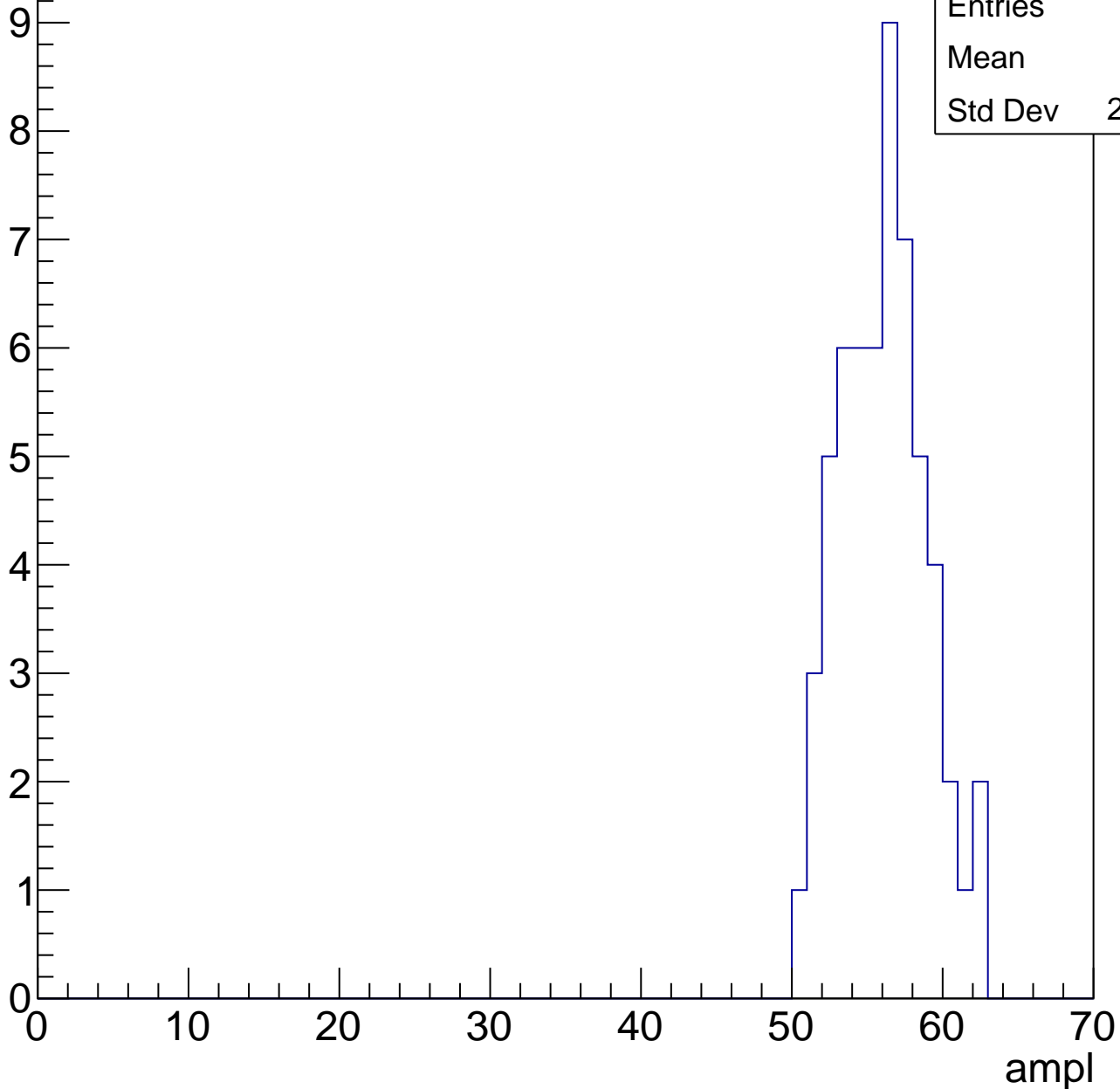


# B1L003S, U22-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	55.6
Std Dev	2.834

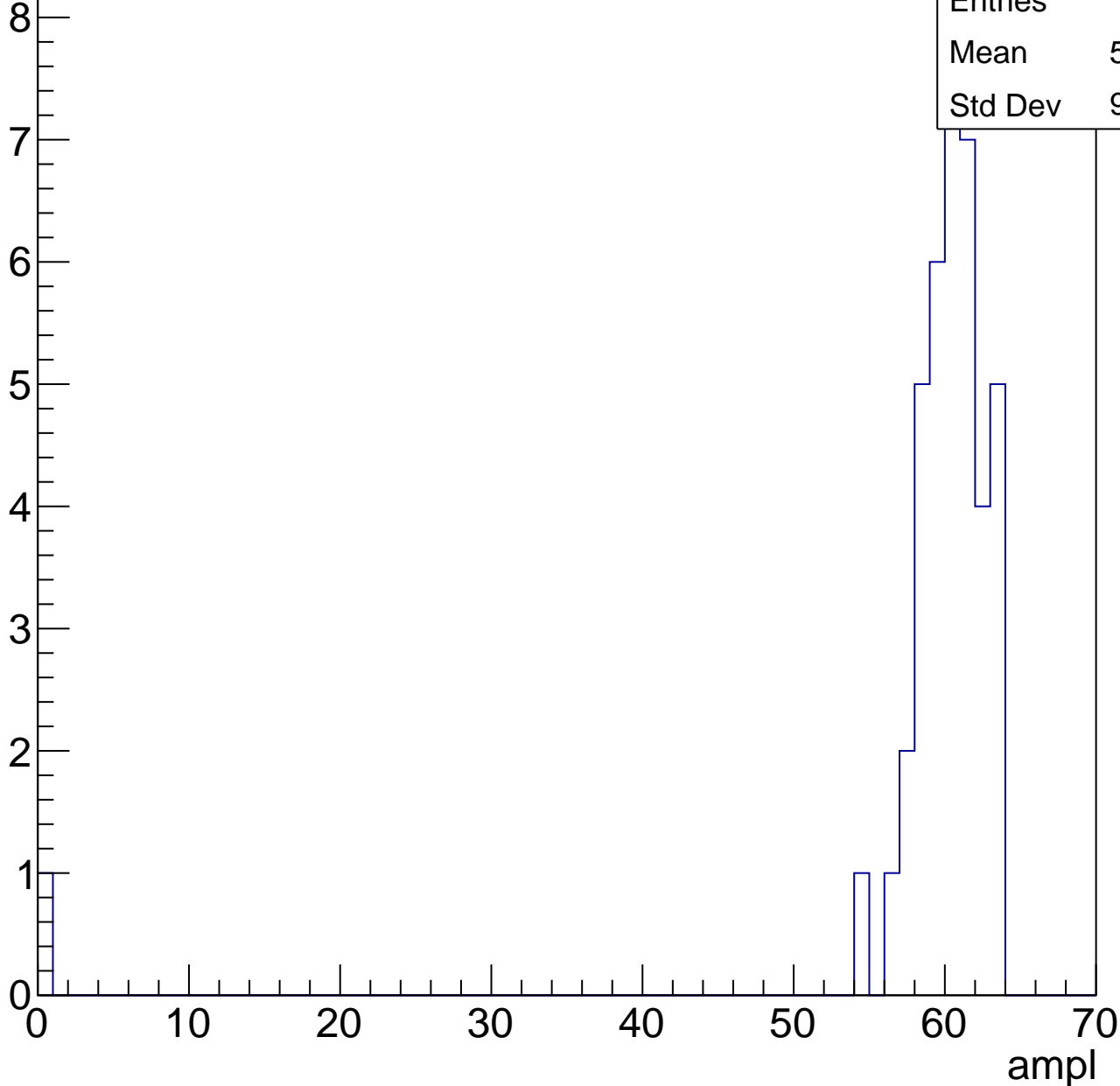


# B1L003S, U22-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

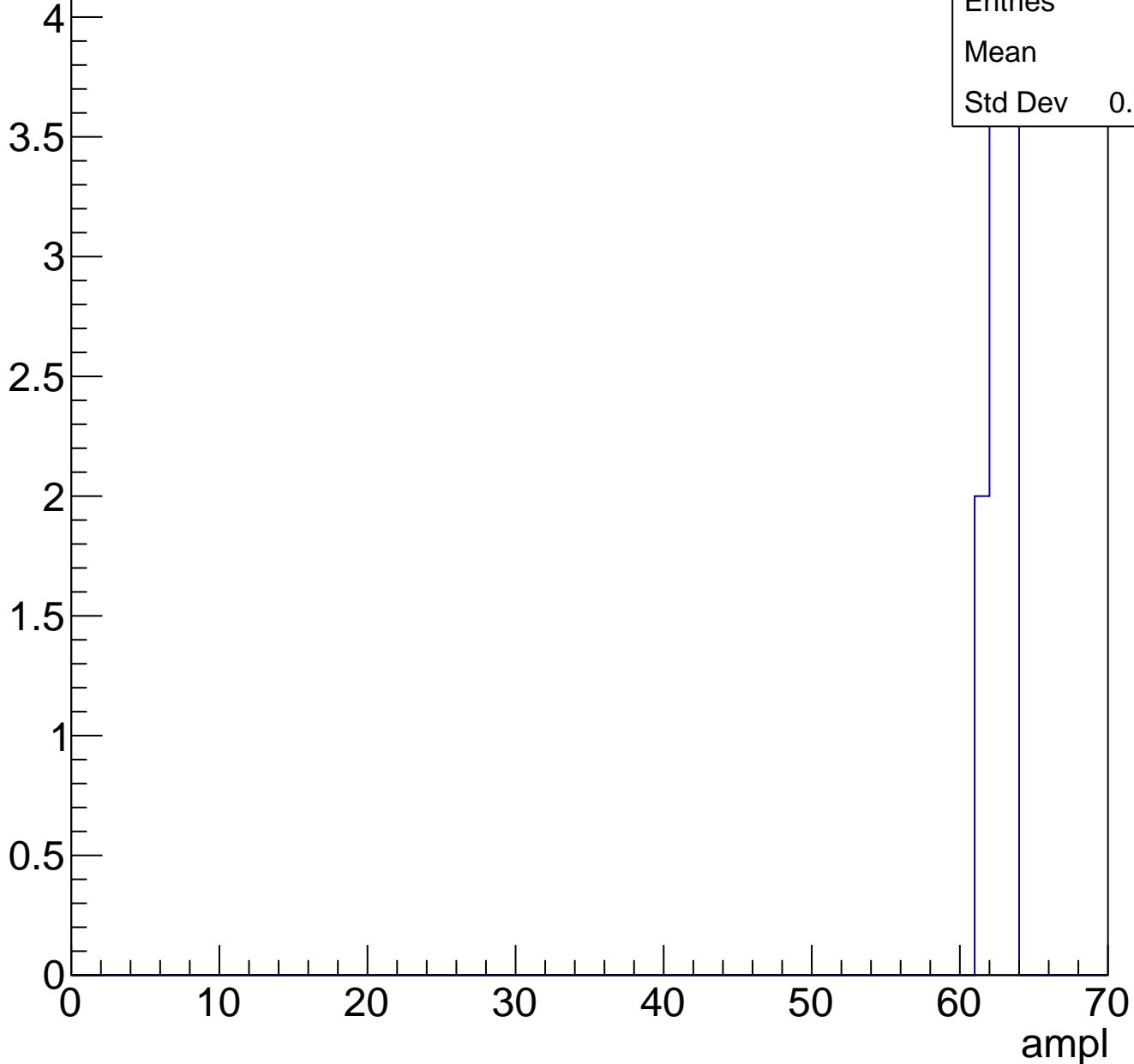
Entries	40
Mean	58.45
Std Dev	9.576



# B1L003S, U22-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

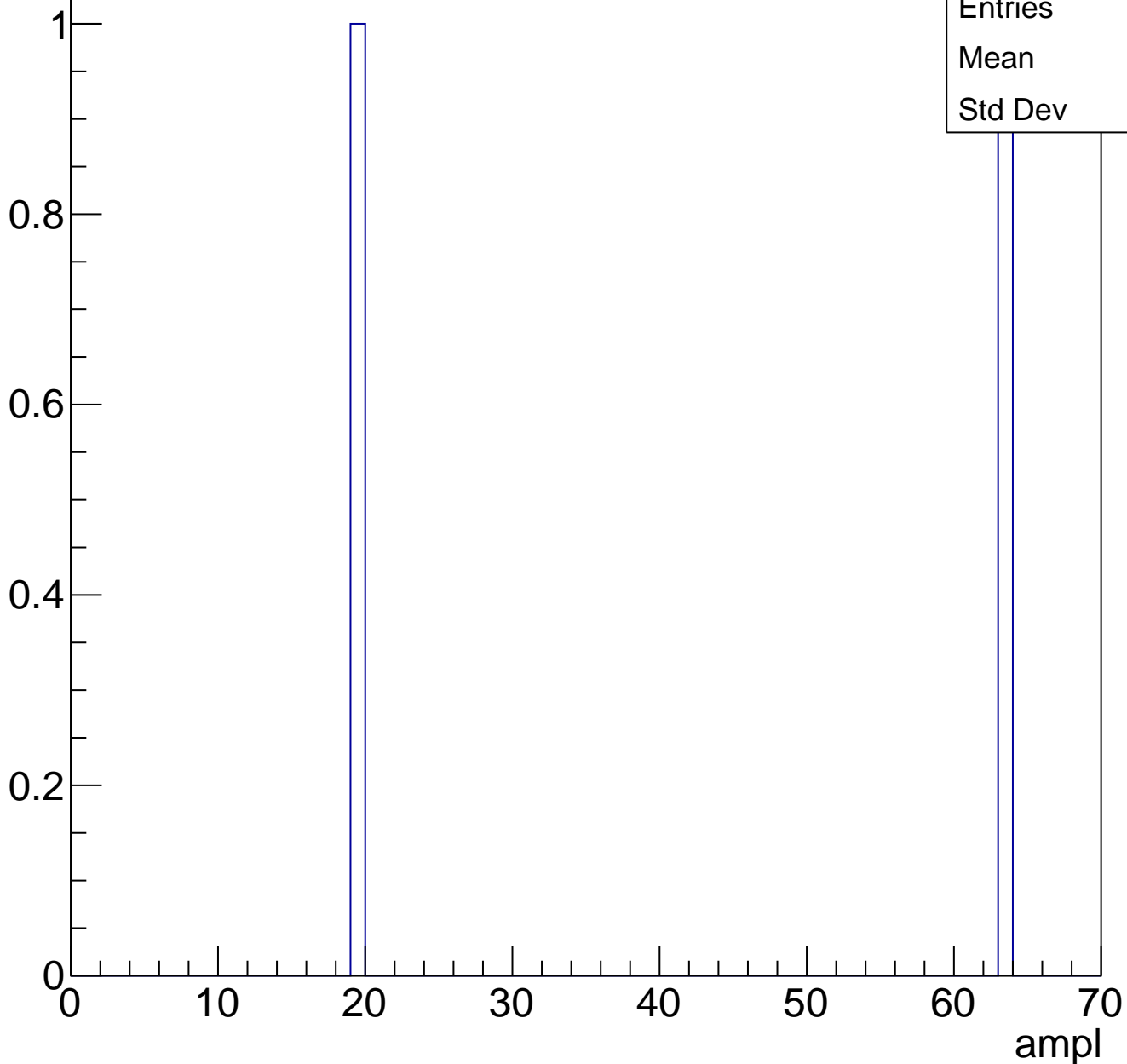




# B1L003S, U22-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch81, adc0

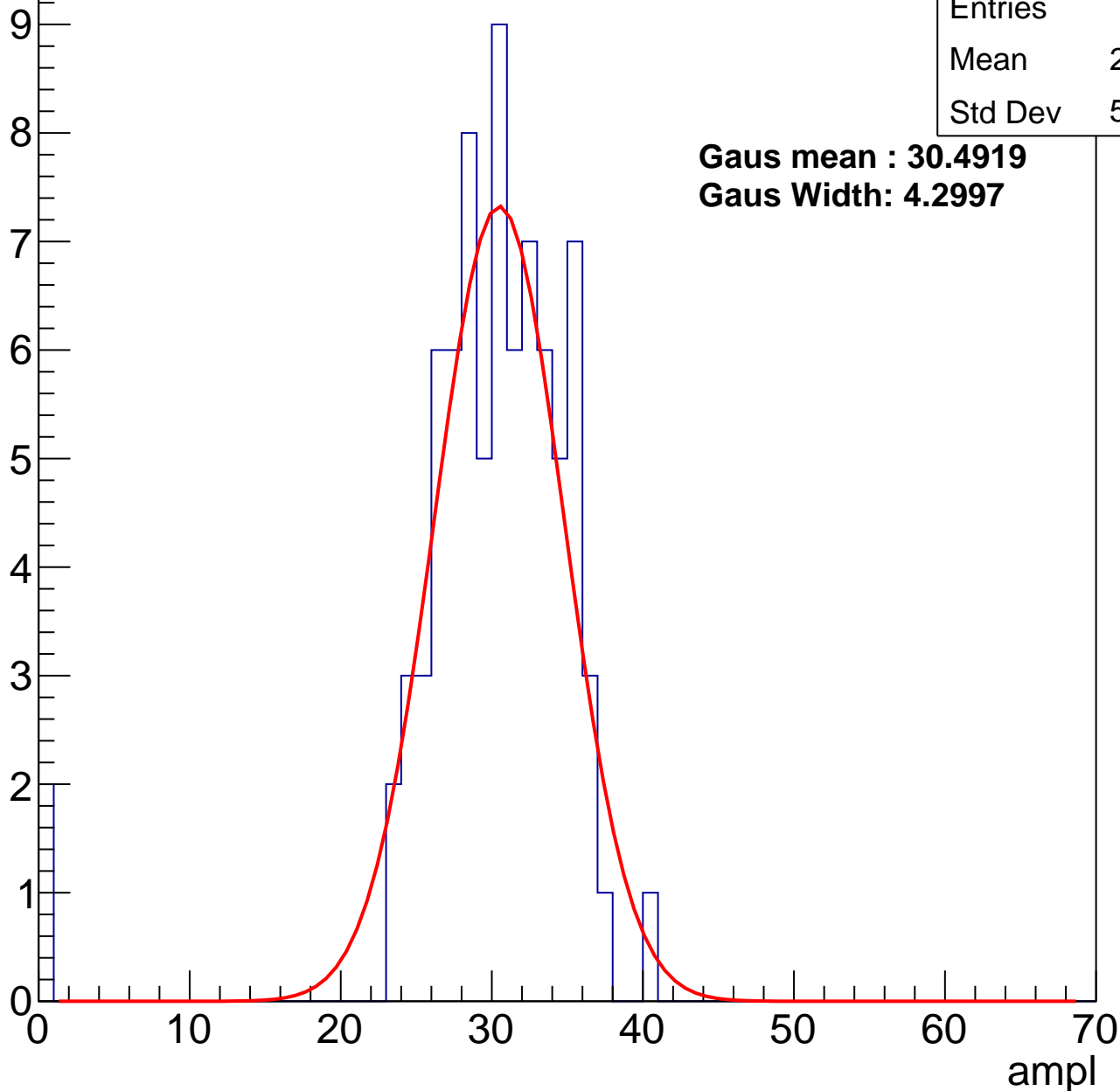
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.48
Std Dev	5.964

**Gaus mean : 30.4919**

**Gaus Width: 4.2997**



# B1L003S, U22-ch81, adc1

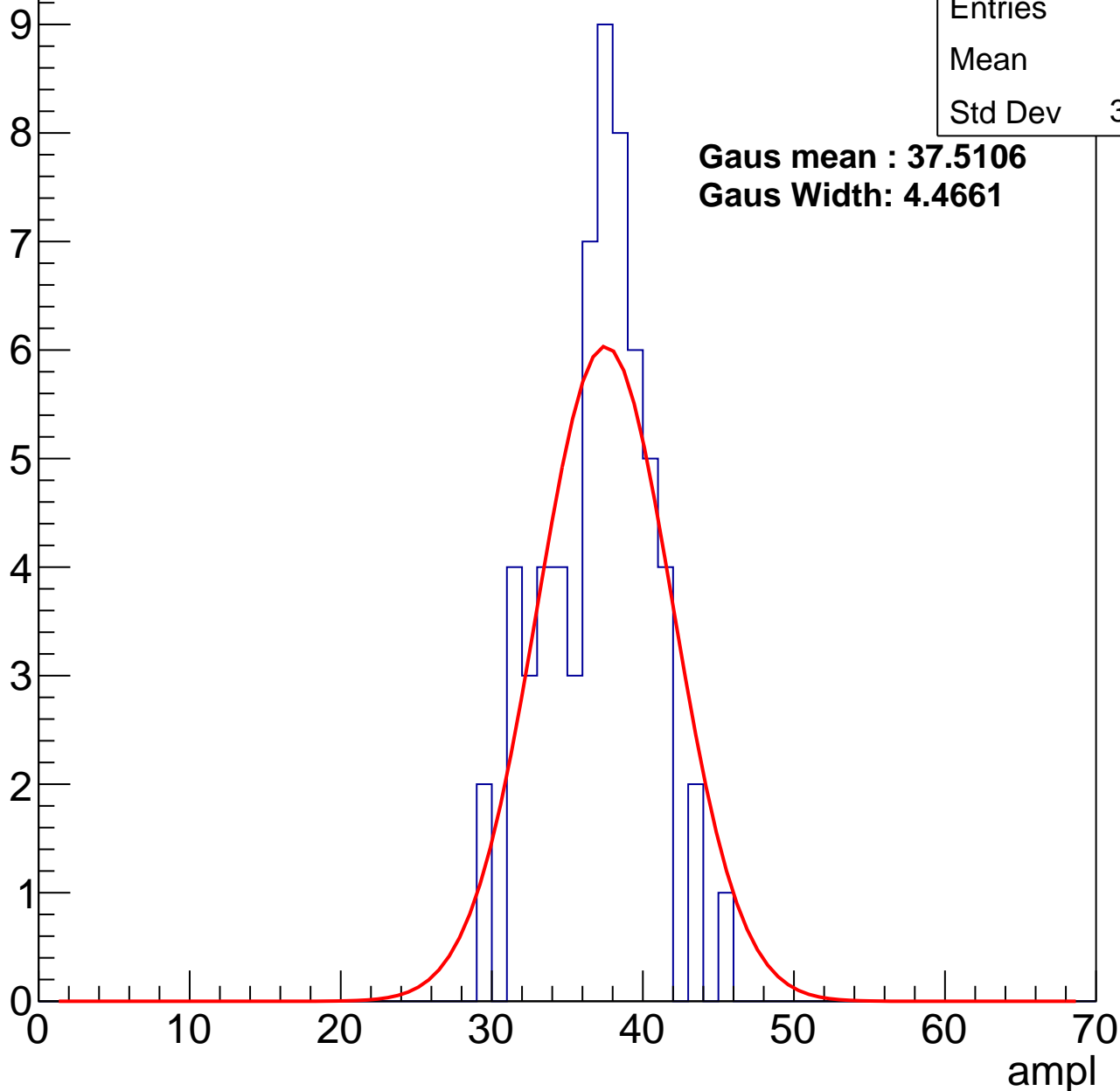
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.6
Std Dev	3.438

**Gaus mean : 37.5106**

**Gaus Width: 4.4661**



# B1L003S, U22-ch81, adc2

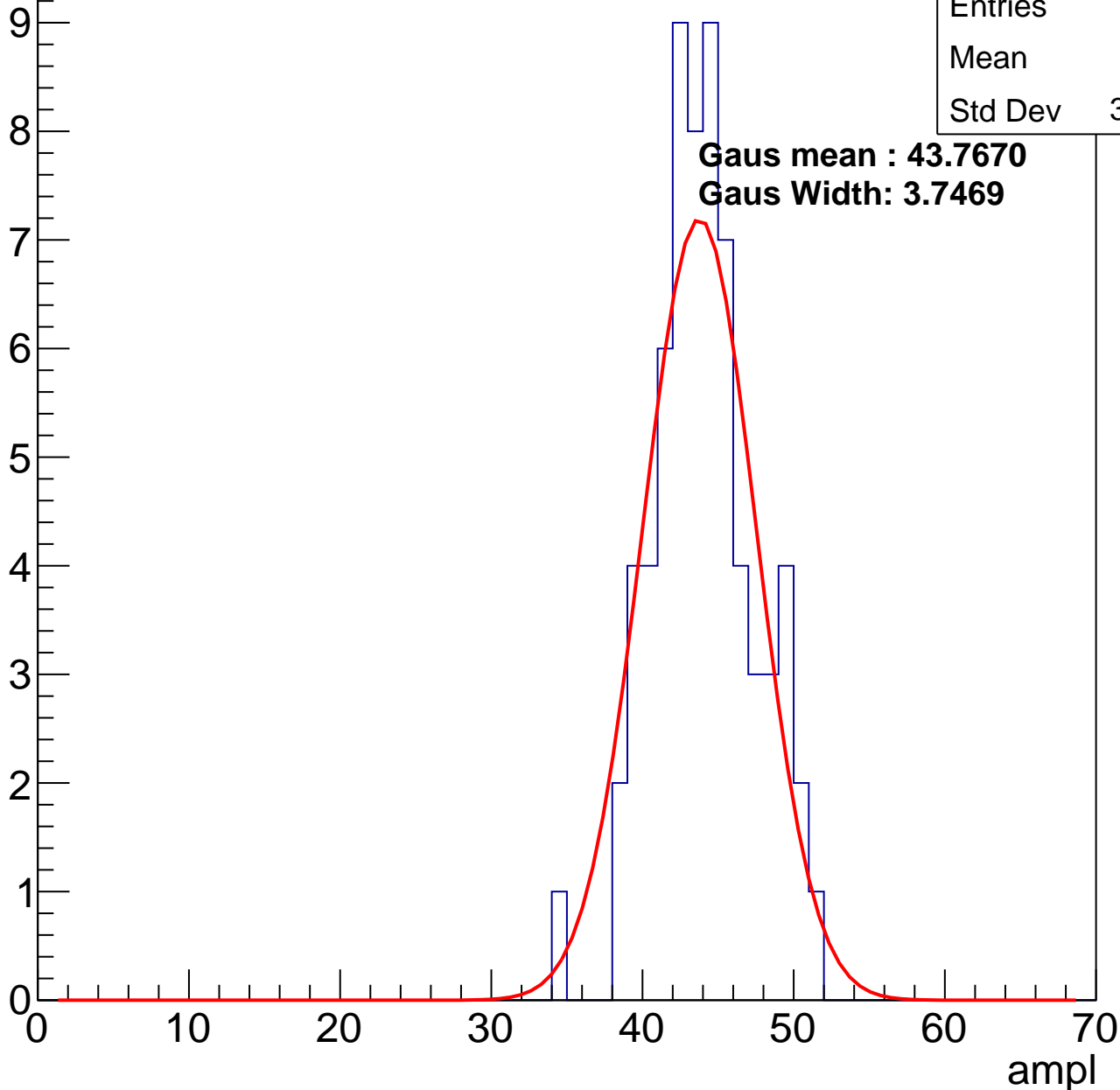
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.6
Std Dev	3.328

**Gaus mean : 43.7670**

**Gaus Width: 3.7469**

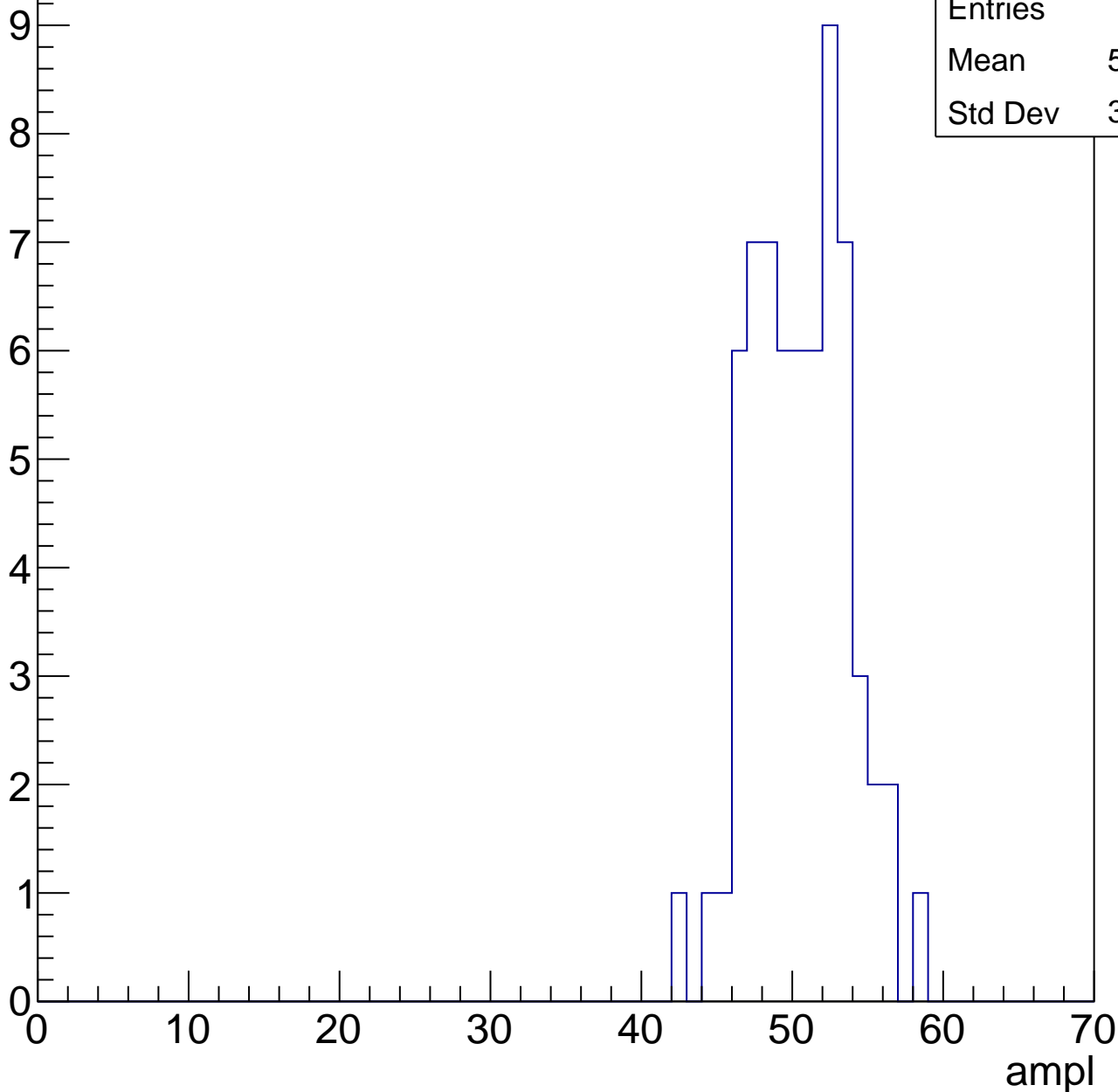


# B1L003S, U22-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	50.05
Std Dev	3.179

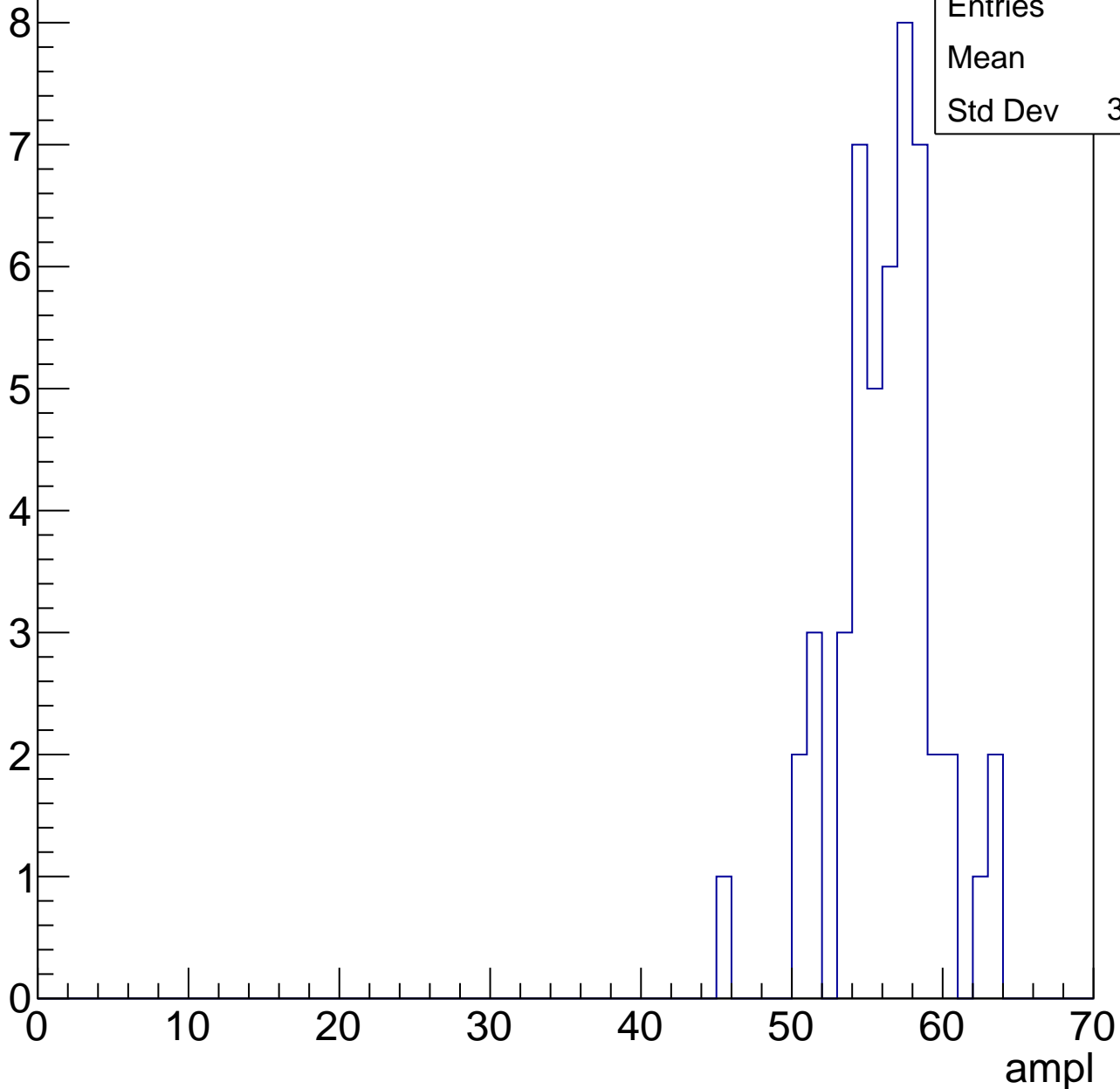


# B1L003S, U22-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55.8
Std Dev	3.338



# B1L003S, U22-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	47
Mean	58.43
Std Dev	8.948

Entry

10

8

6

4

2

0

0

10

20

30

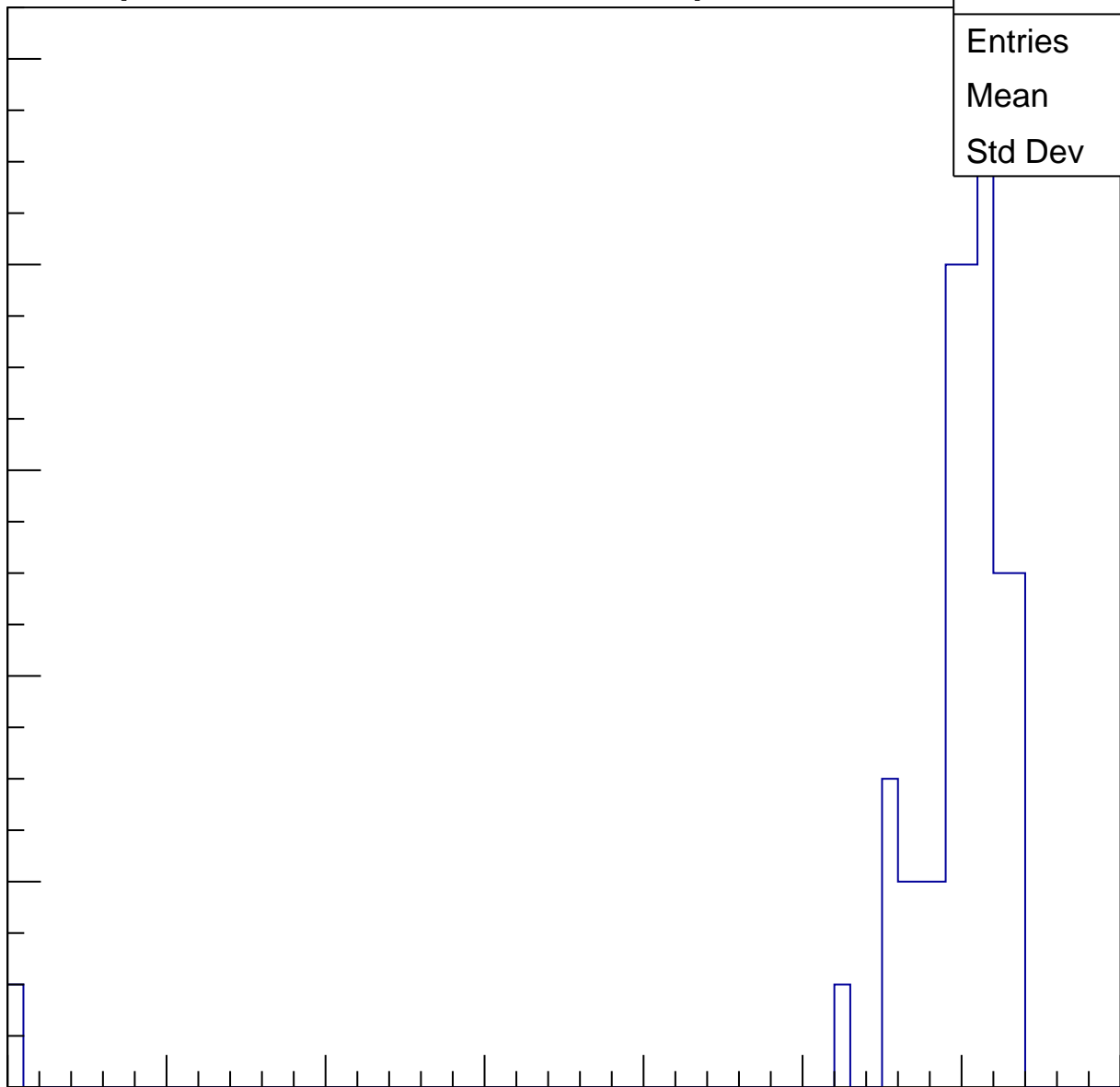
40

50

60

70

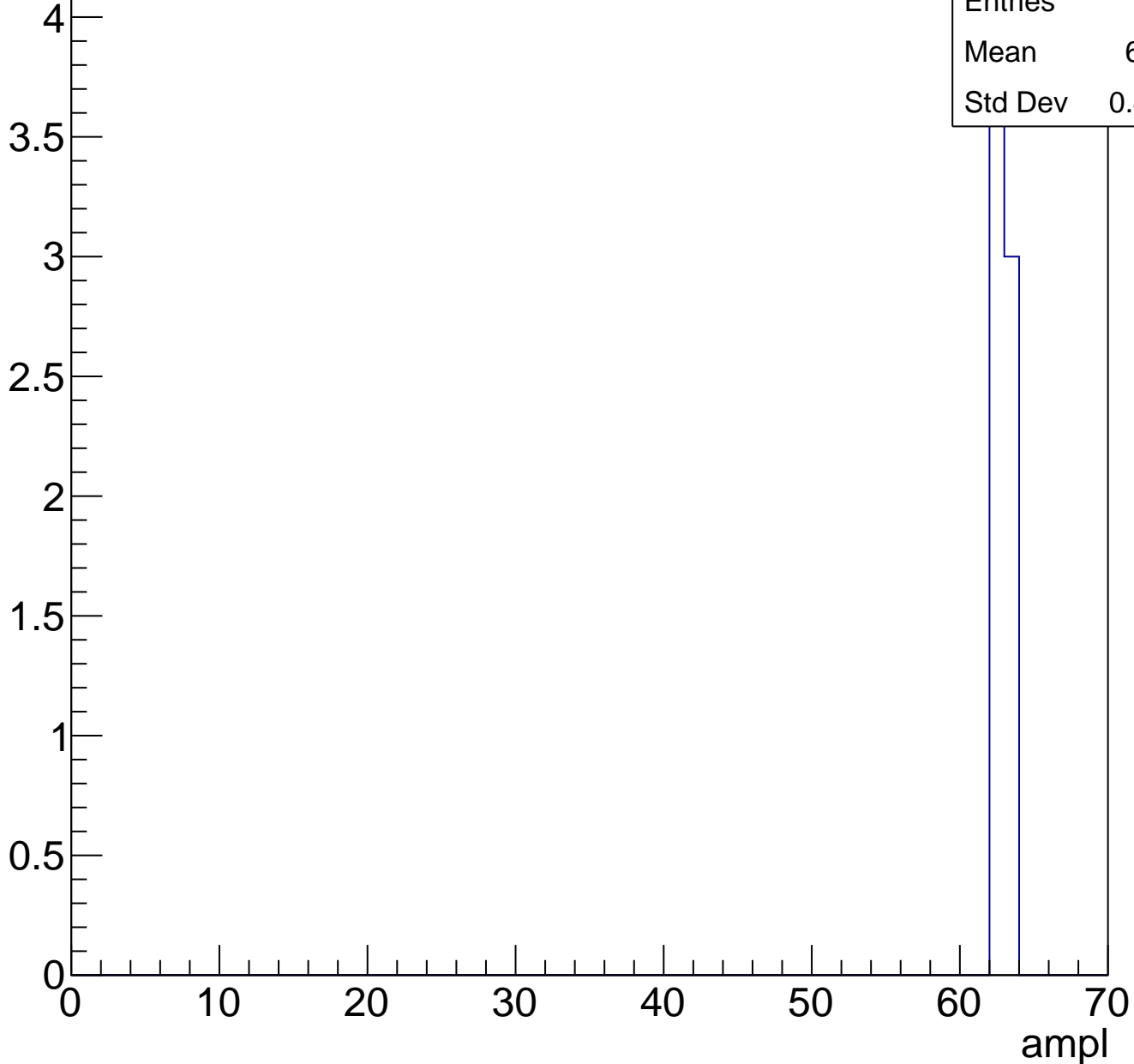
ampl



# B1L003S, U22-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch82, adc0

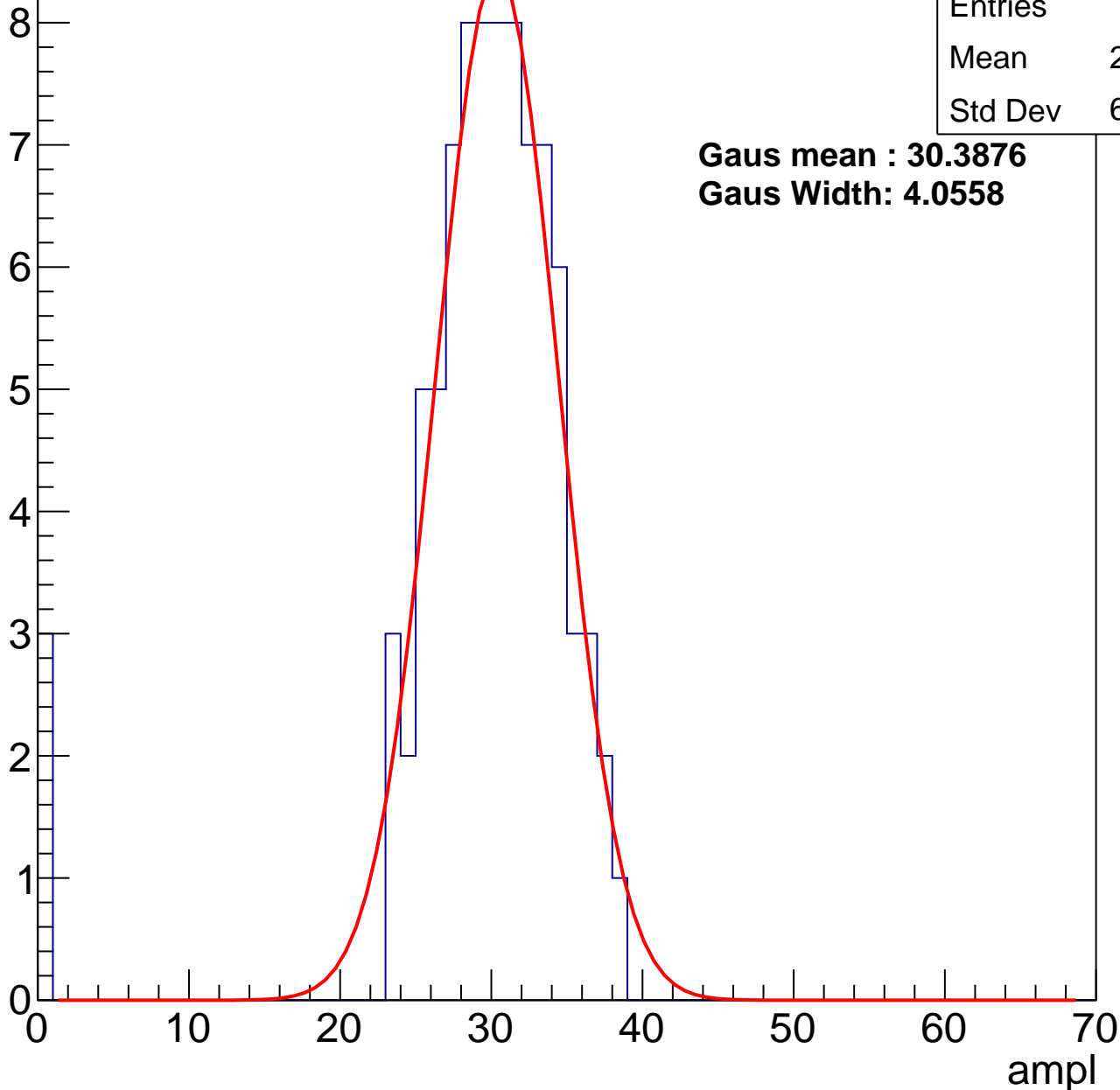
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	86
Mean	28.94
Std Dev	6.533

**Gaus mean : 30.3876**

**Gaus Width: 4.0558**



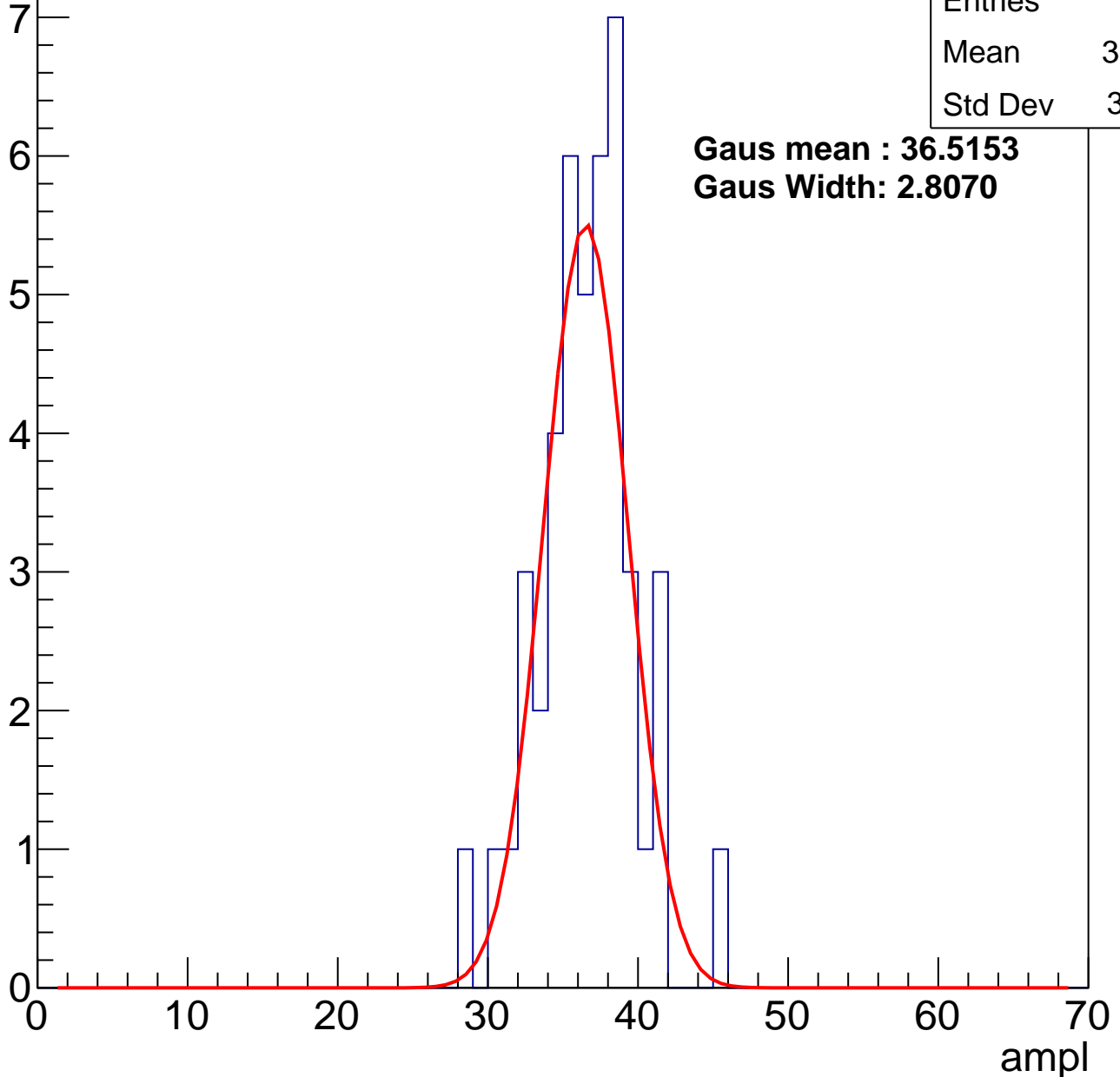
# B1L003S, U22-ch82, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	36.14
Std Dev	3.181

**Gaus mean : 36.5153**  
**Gaus Width: 2.8070**



# B1L003S, U22-ch82, adc2

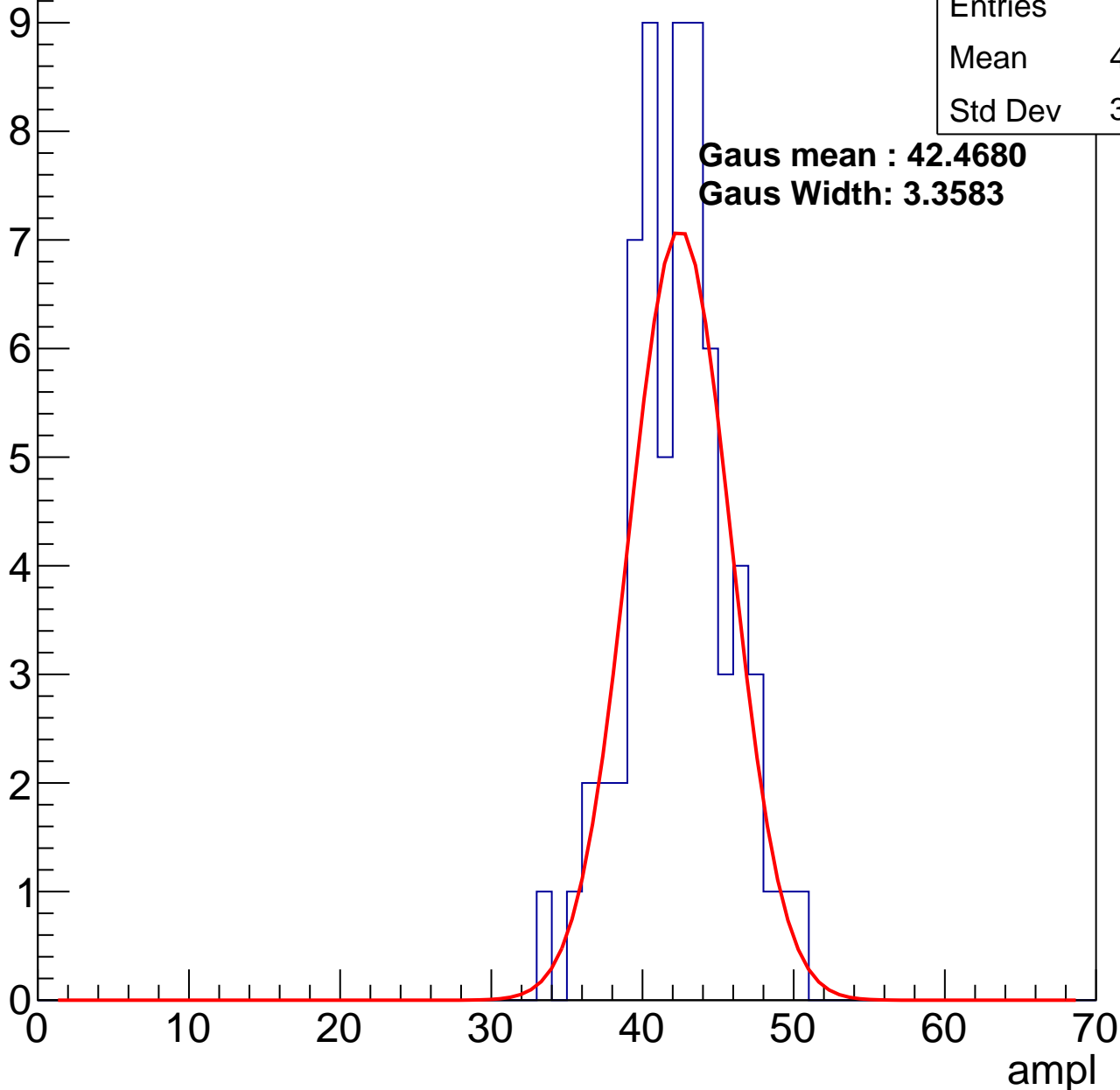
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	41.88
Std Dev	3.337

**Gaus mean : 42.4680**

**Gaus Width: 3.3583**

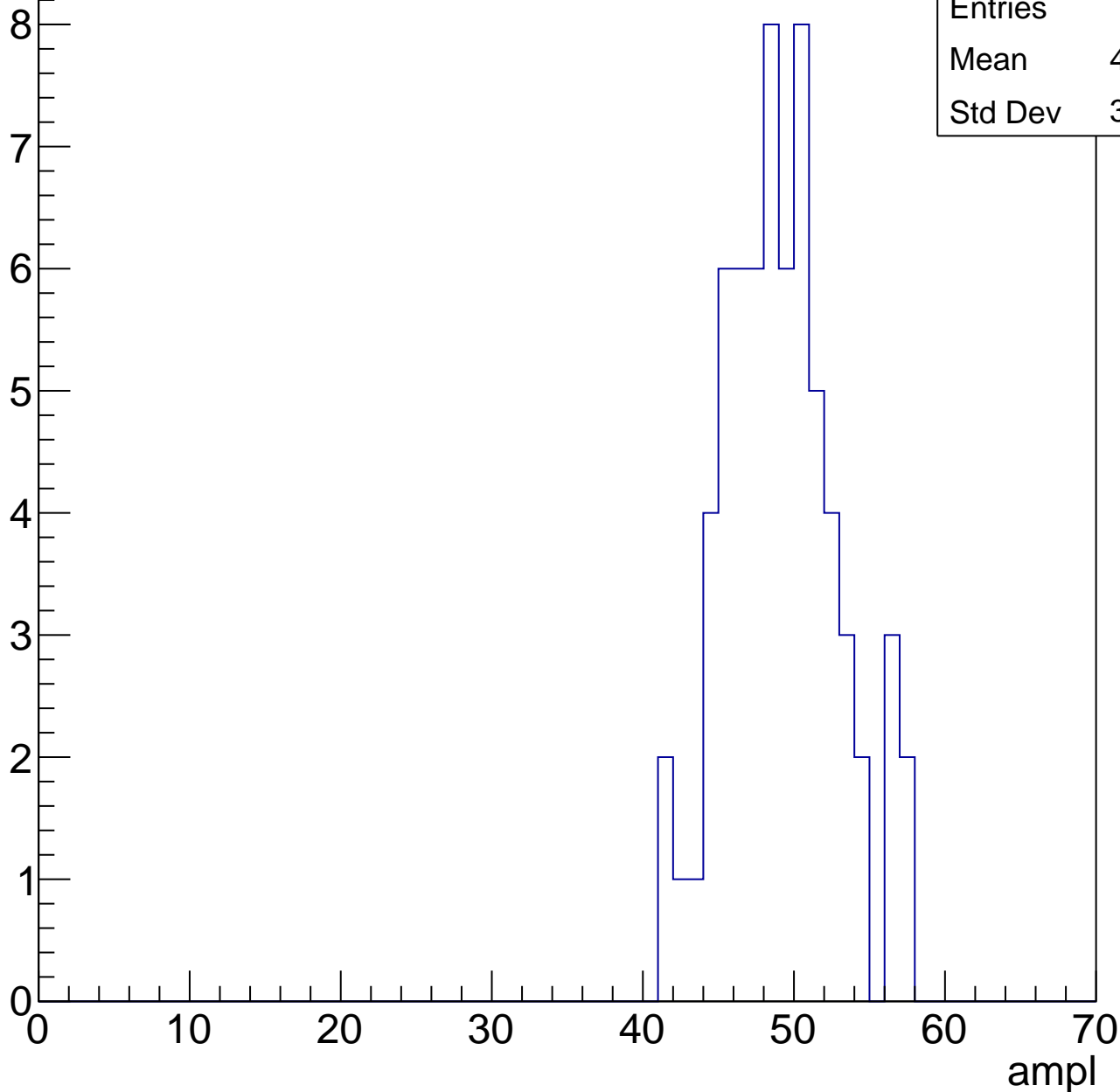


# B1L003S, U22-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	48.67
Std Dev	3.715

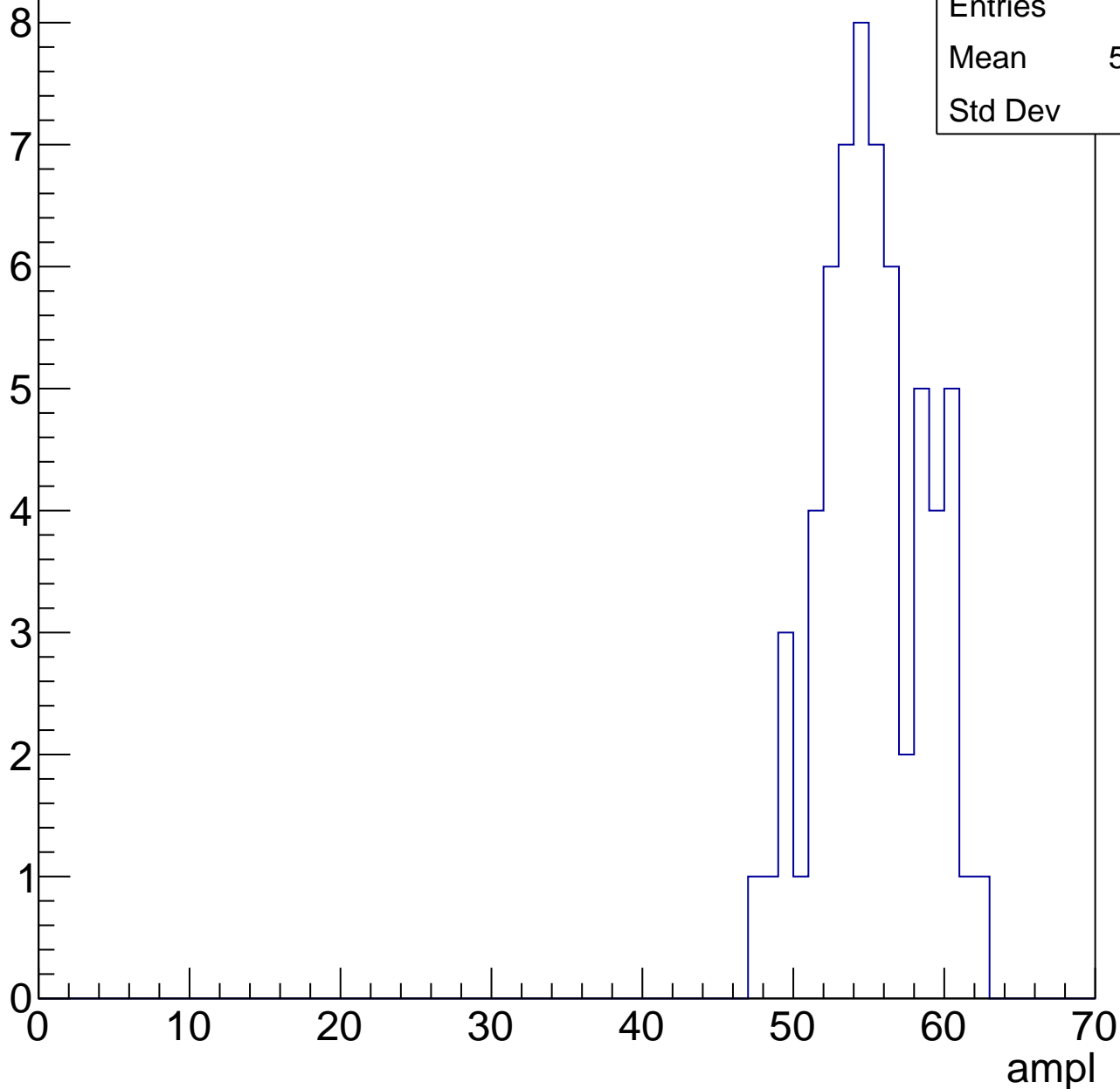


# B1L003S, U22-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	54.76
Std Dev	3.43

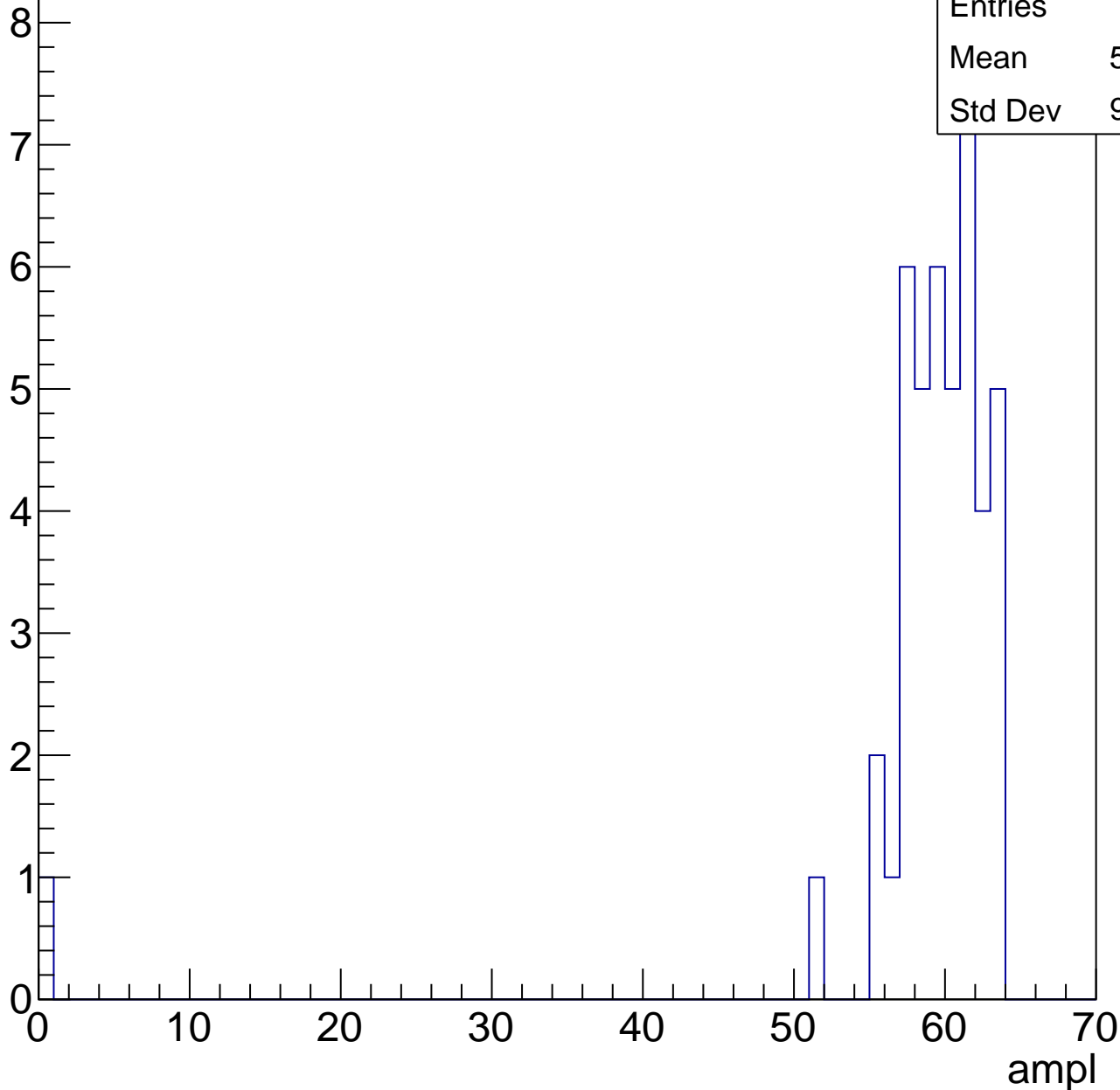


# B1L003S, U22-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	58.05
Std Dev	9.205

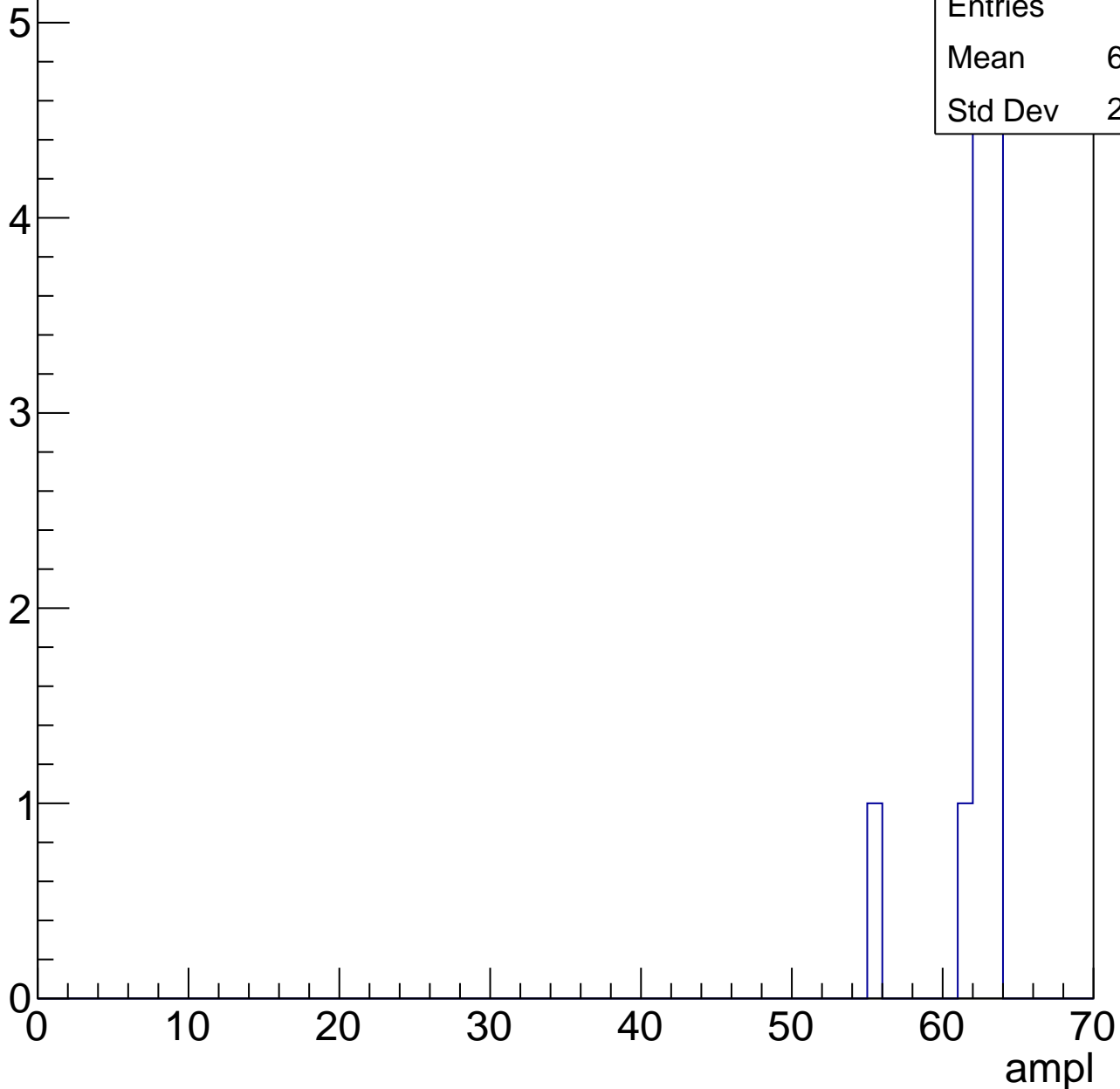


# B1L003S, U22-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	61.75
Std Dev	2.126





# B1L003S, U22-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch83, adc0

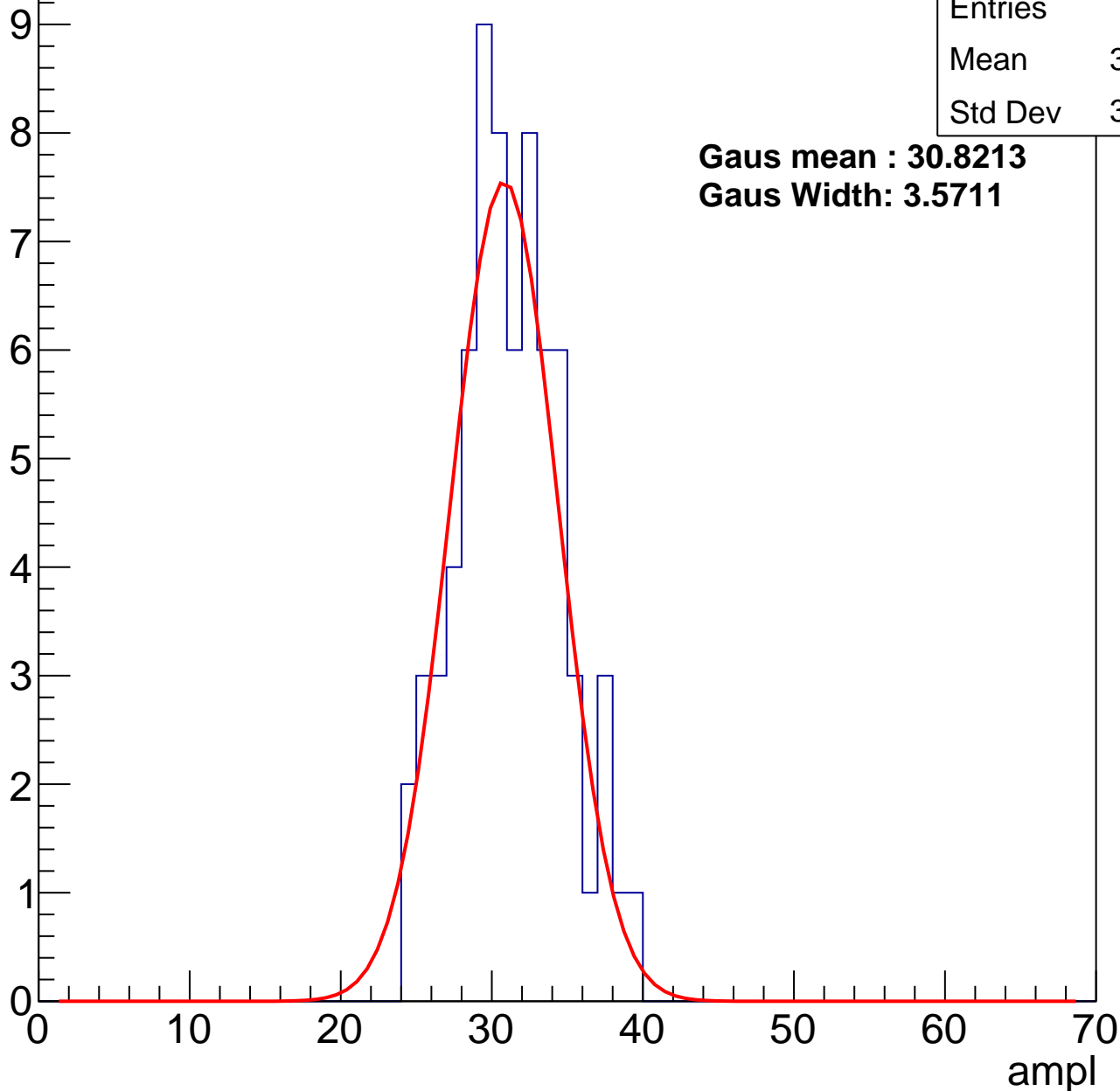
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.73
Std Dev	3.418

**Gaus mean : 30.8213**

**Gaus Width: 3.5711**



# B1L003S, U22-ch83, adc1

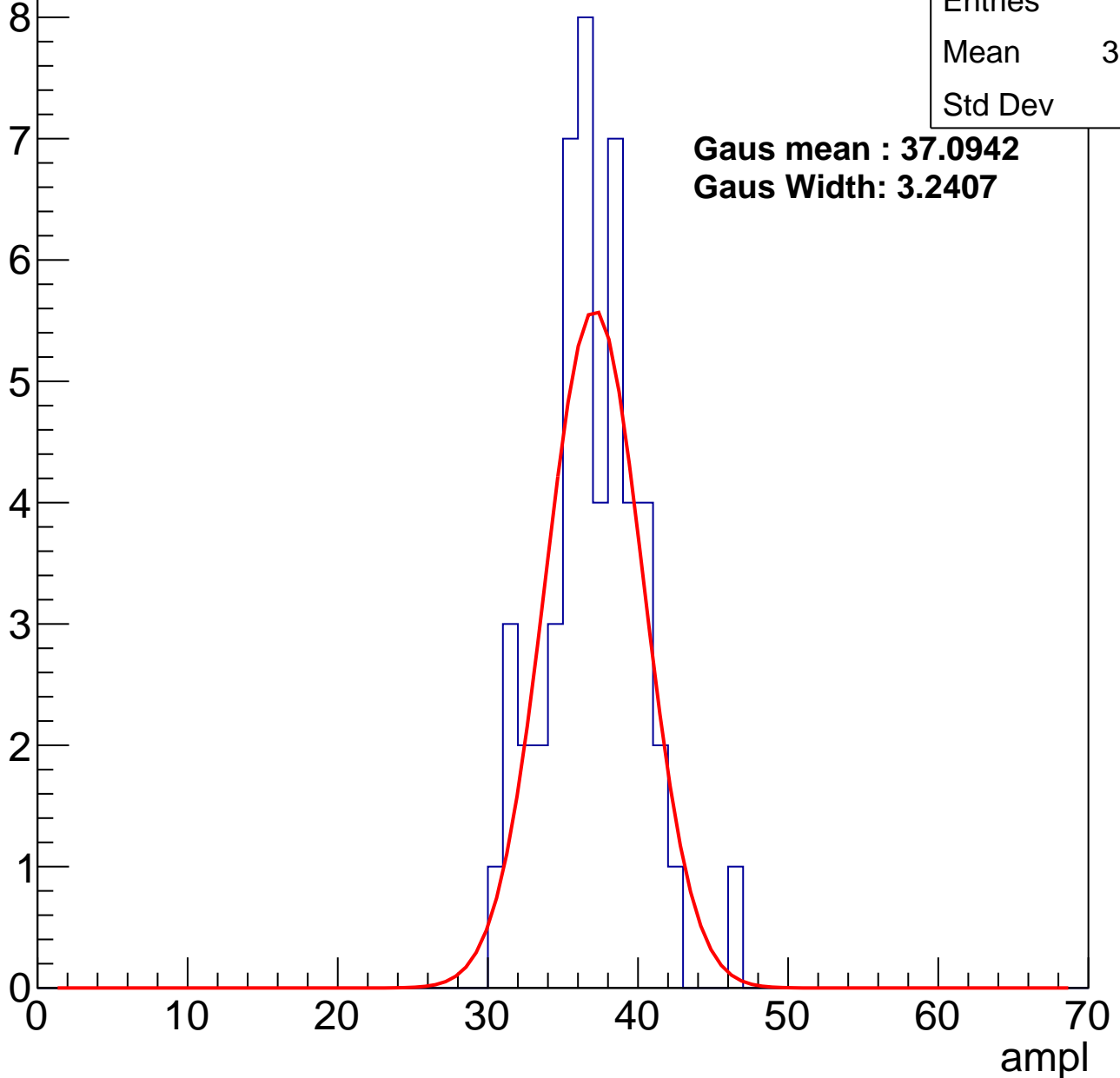
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	36.49
Std Dev	3.15

**Gaus mean : 37.0942**

**Gaus Width: 3.2407**



# B1L003S, U22-ch83, adc2

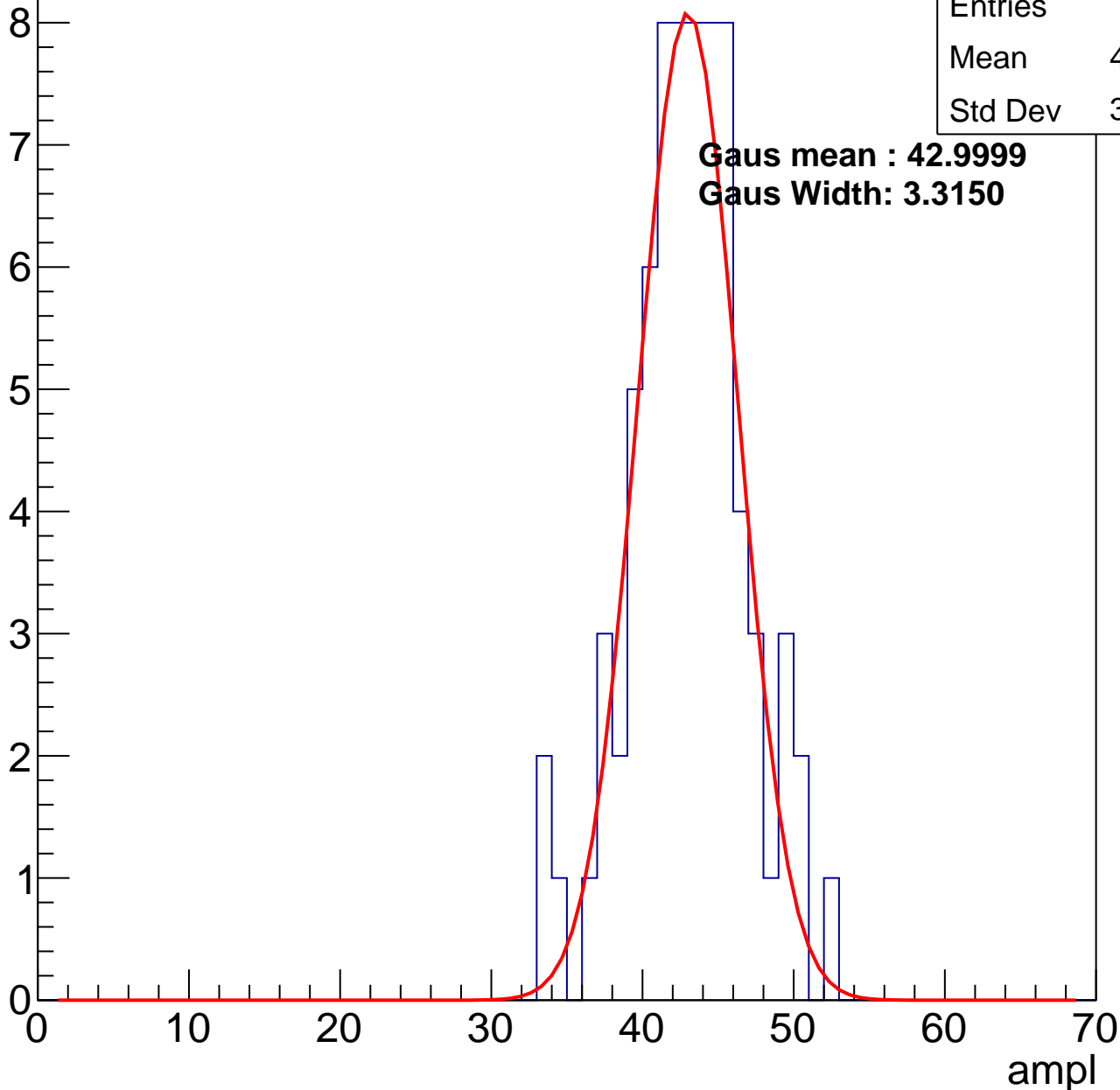
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	42.57
Std Dev	3.828

**Gaus mean : 42.9999**

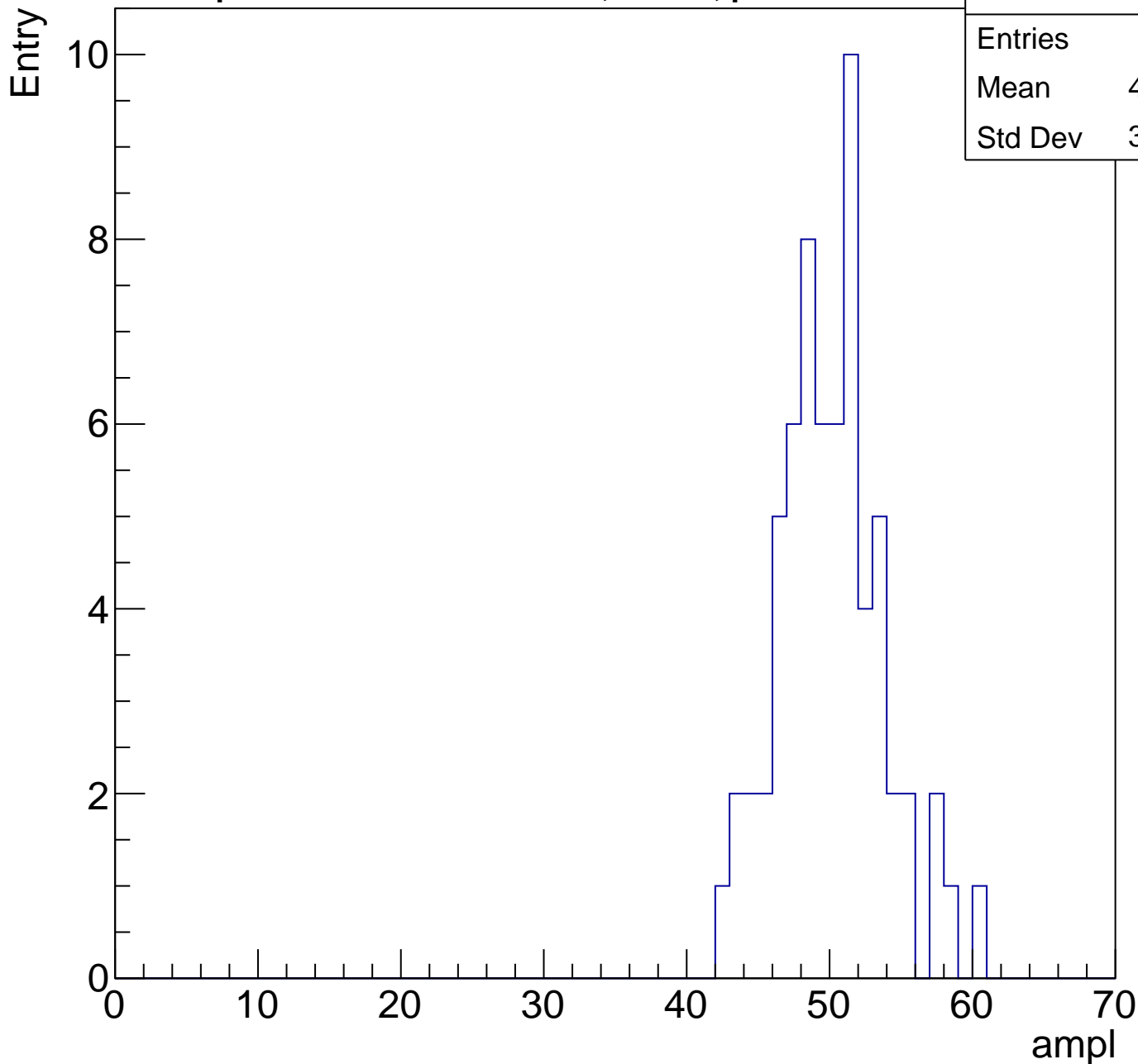
**Gaus Width: 3.3150**



# B1L003S, U22-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	49.68
Std Dev	3.646

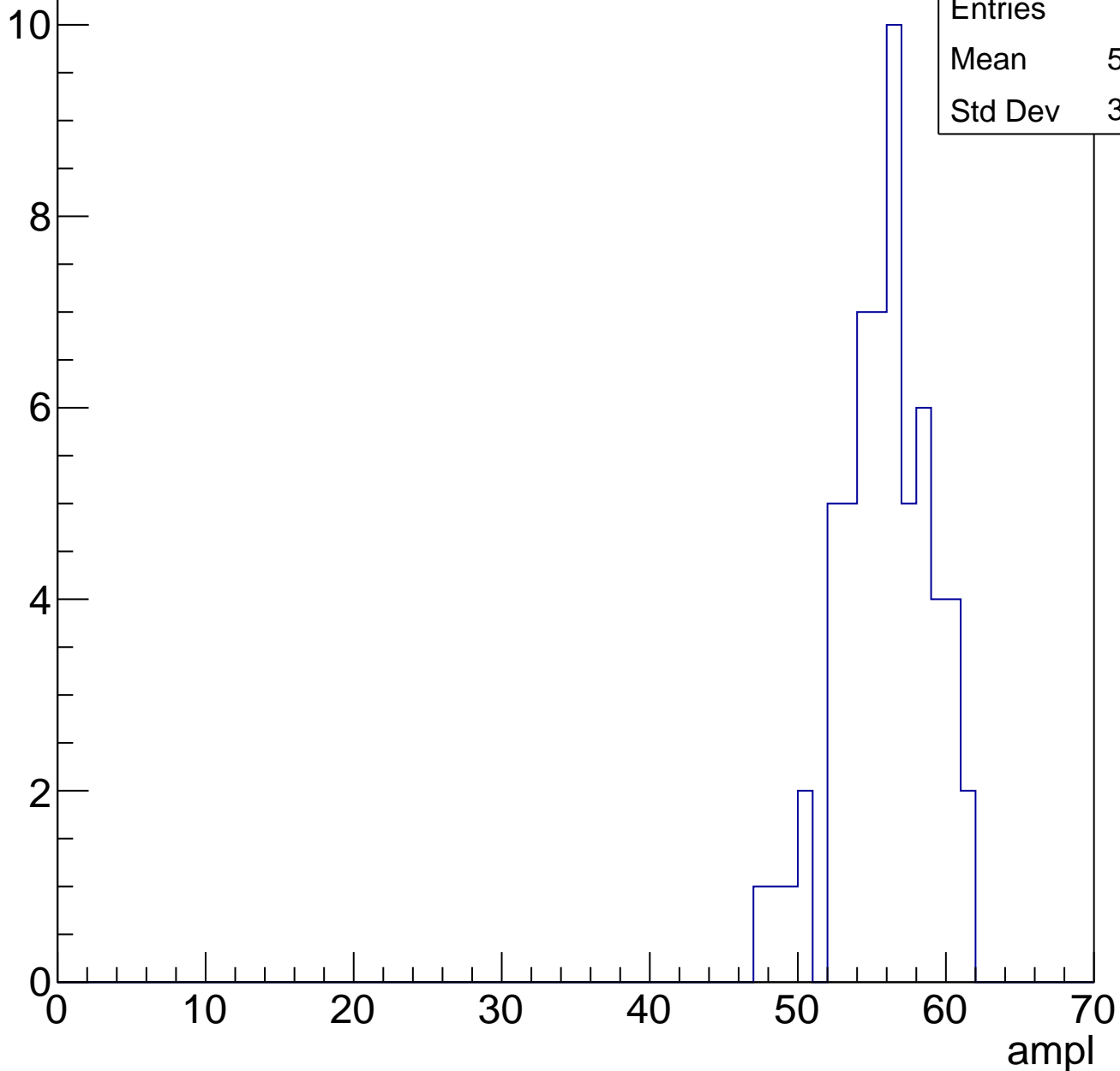


# B1L003S, U22-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	55.38
Std Dev	3.115

Entry

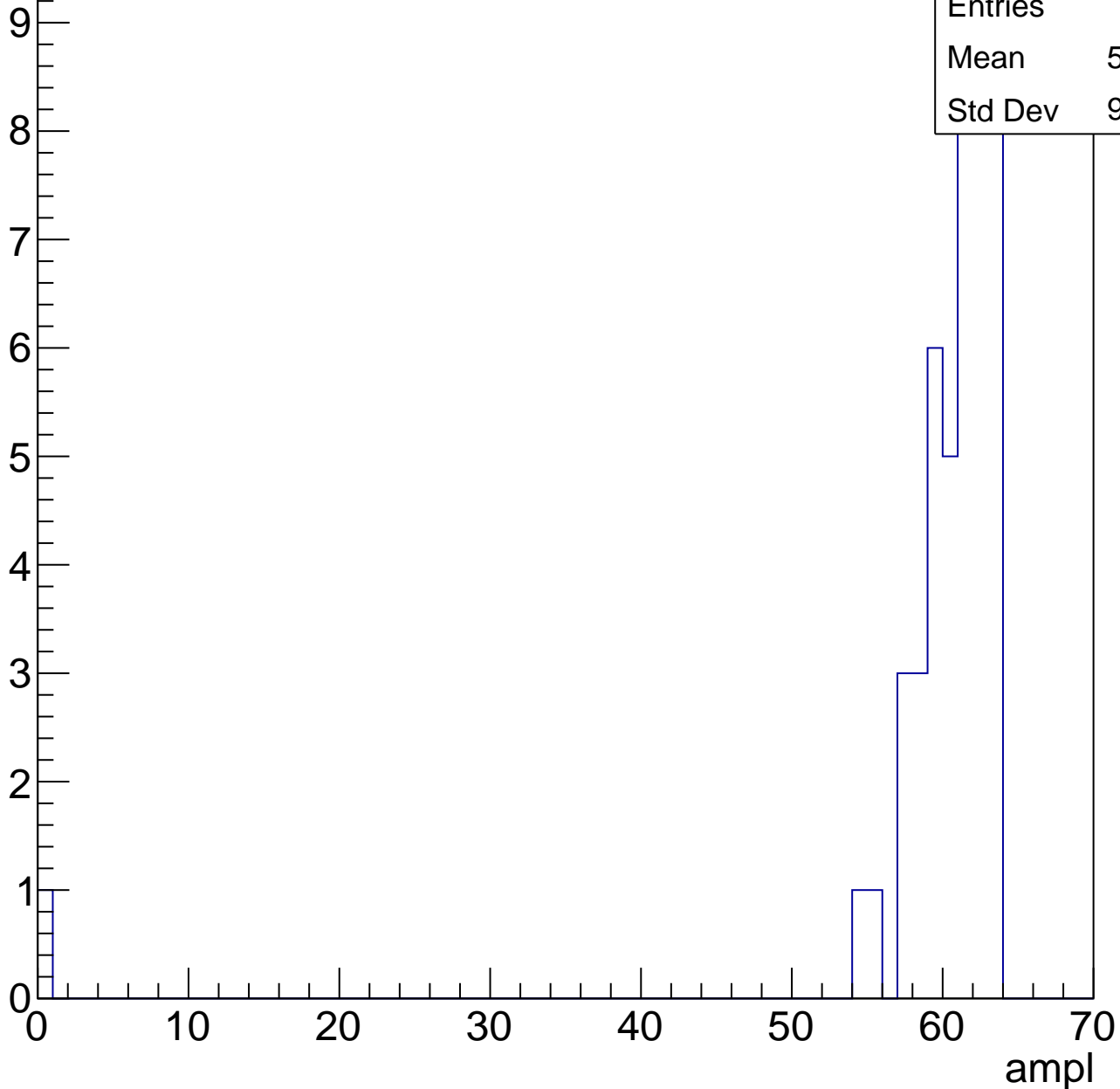


# B1L003S, U22-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	59.15
Std Dev	9.086



# B1L003S, U22-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch84, adc0

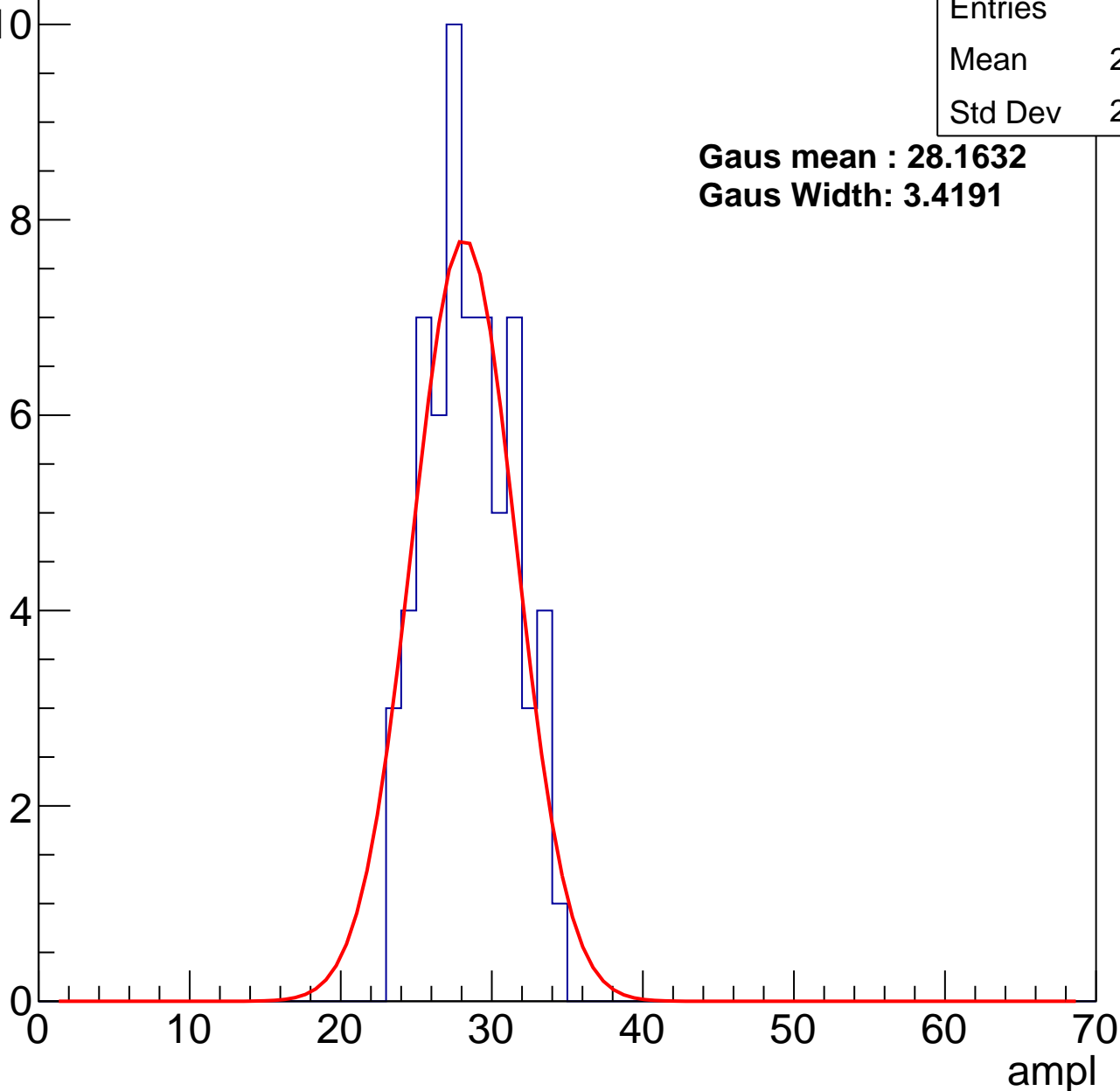
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.03
Std Dev	2.823

**Gaus mean : 28.1632**

**Gaus Width: 3.4191**



# B1L003S, U22-ch84, adc1

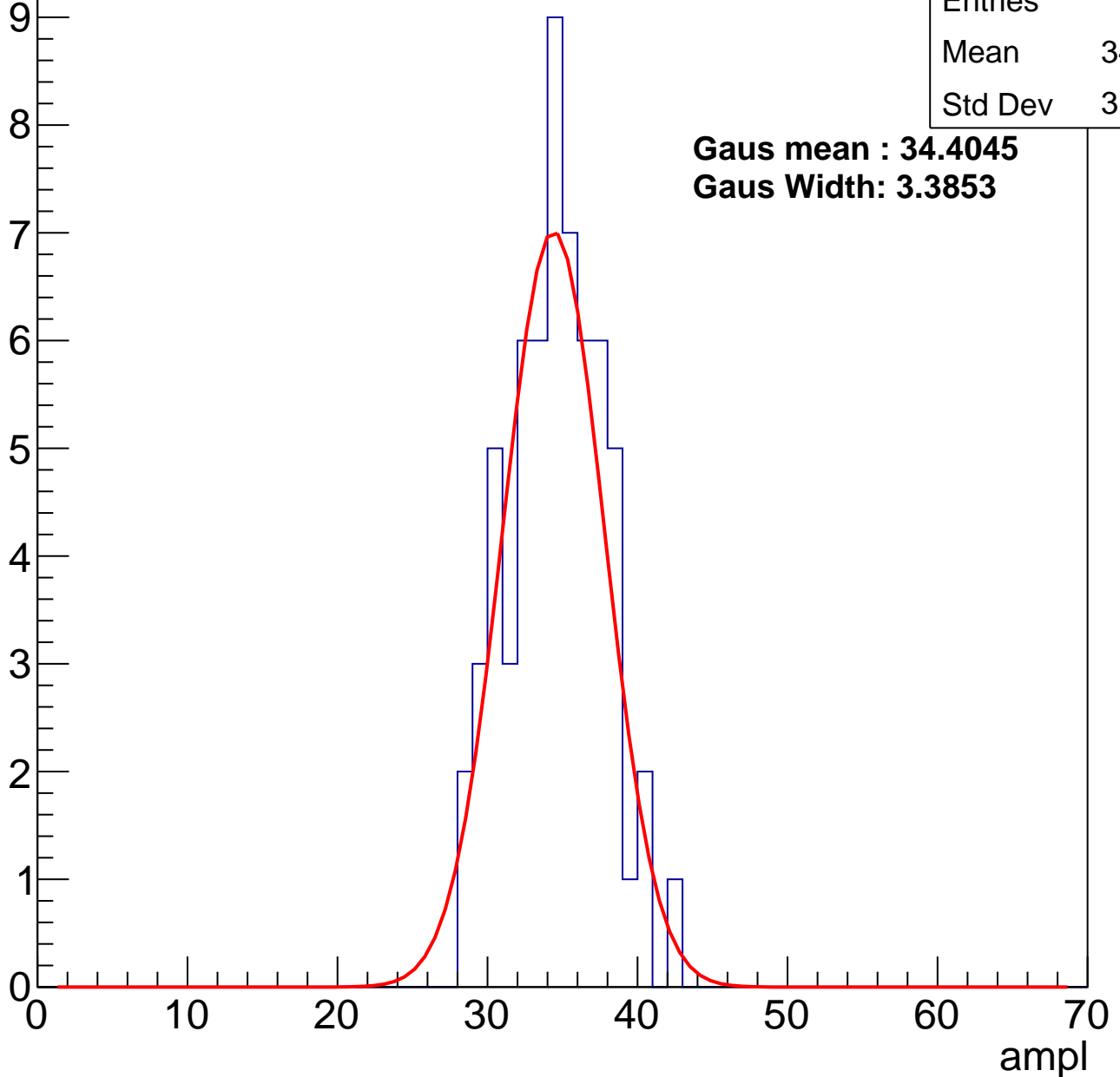
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	34.13
Std Dev	3.134

**Gaus mean : 34.4045**

**Gaus Width: 3.3853**



# B1L003S, U22-ch84, adc2

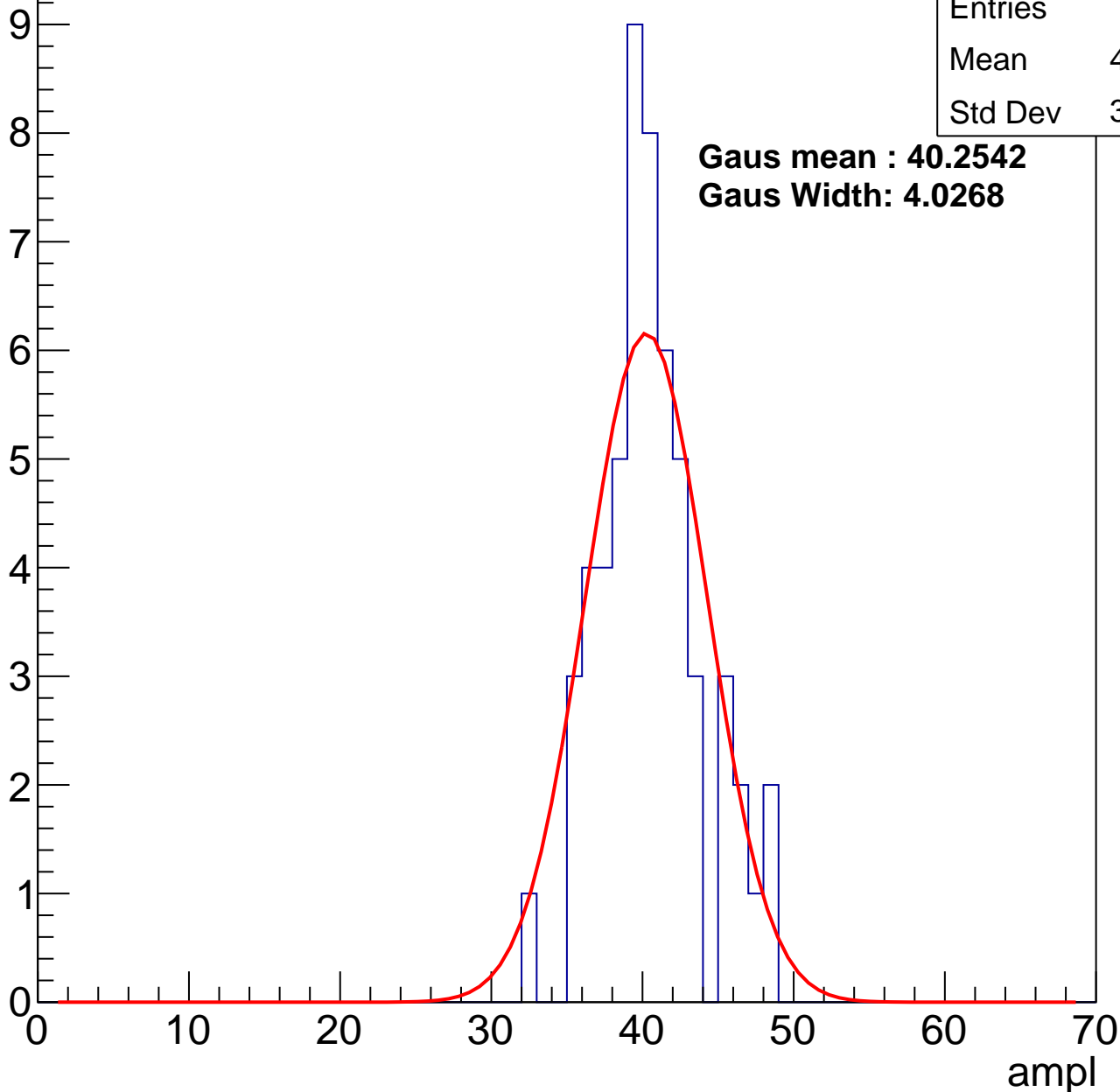
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	40.09
Std Dev	3.393

**Gaus mean : 40.2542**

**Gaus Width: 4.0268**



# B1L003S, U22-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	75
Mean	46.56
Std Dev	3.176

Entry

10

8

6

4

2

0

0

10

20

30

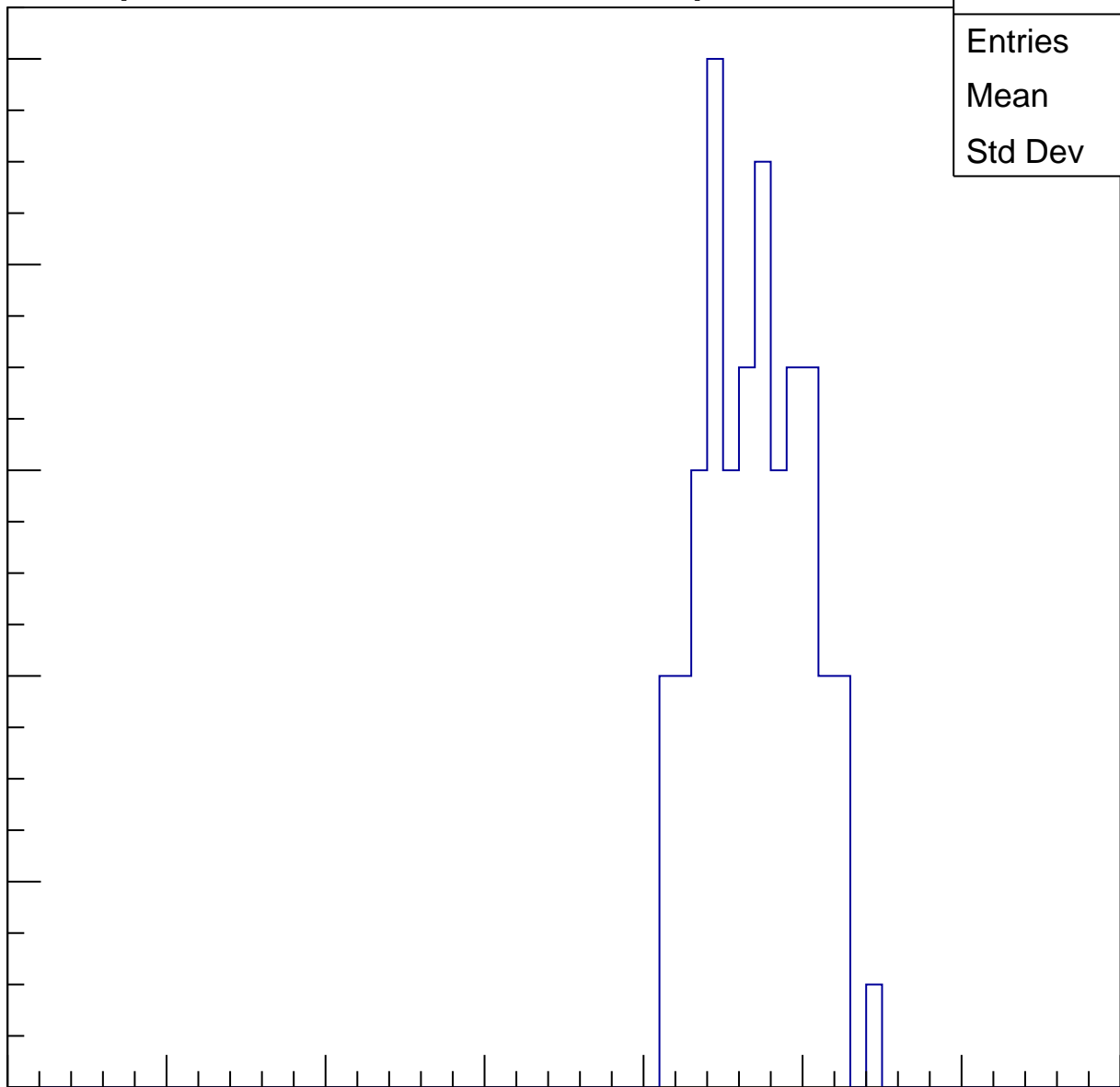
40

50

60

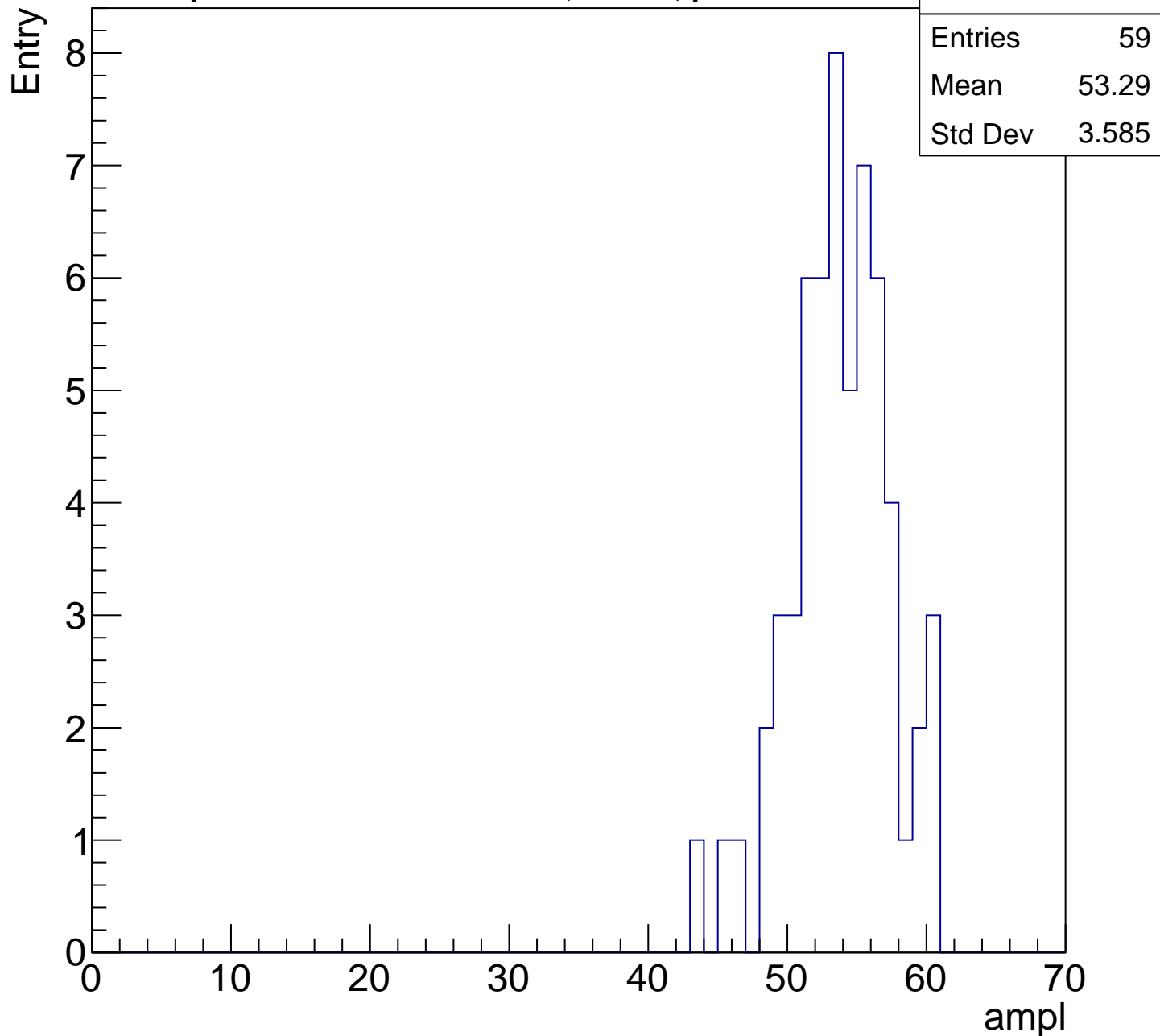
70

ampl



# B1L003S, U22-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

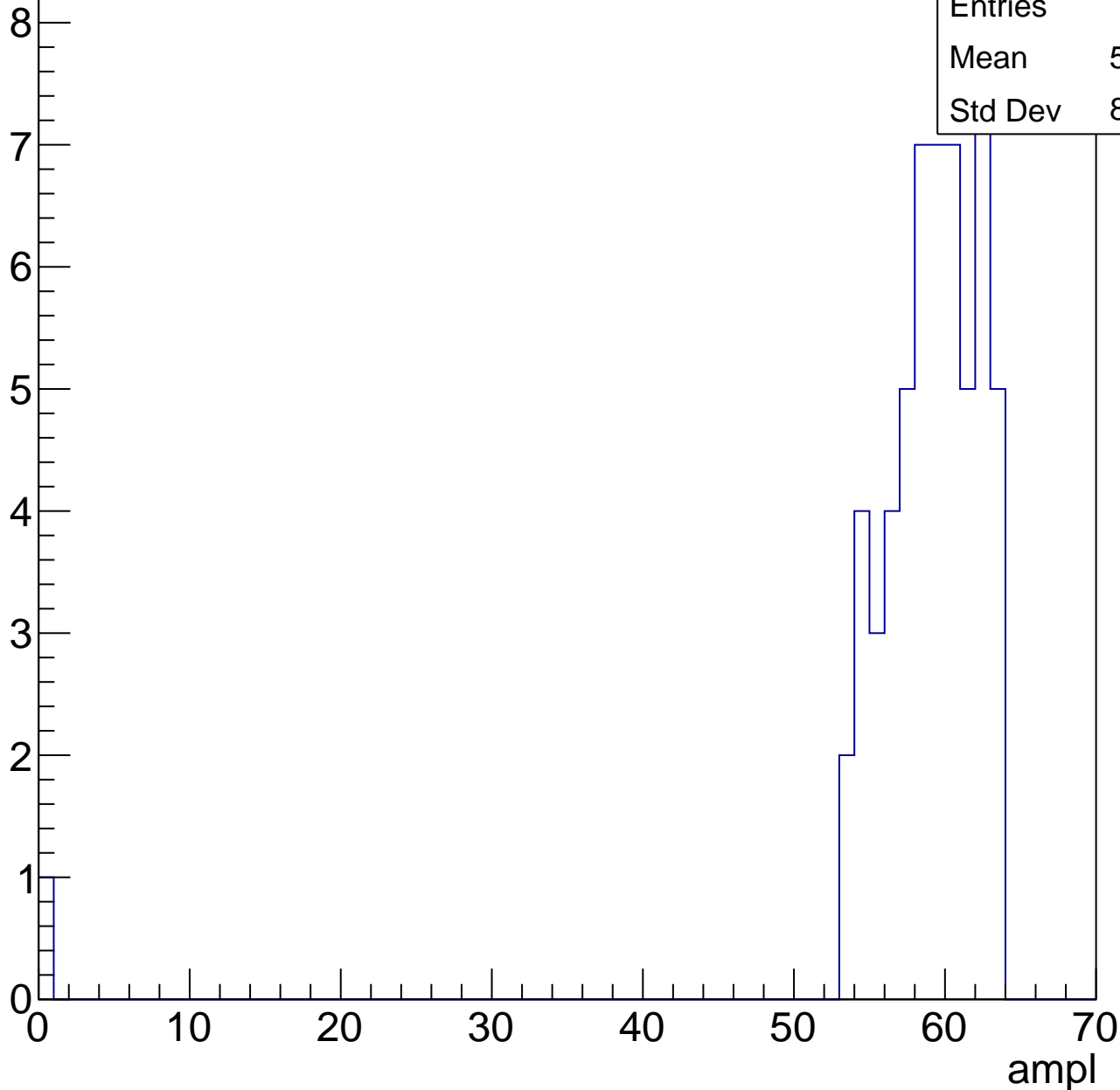


# B1L003S, U22-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	57.78
Std Dev	8.154

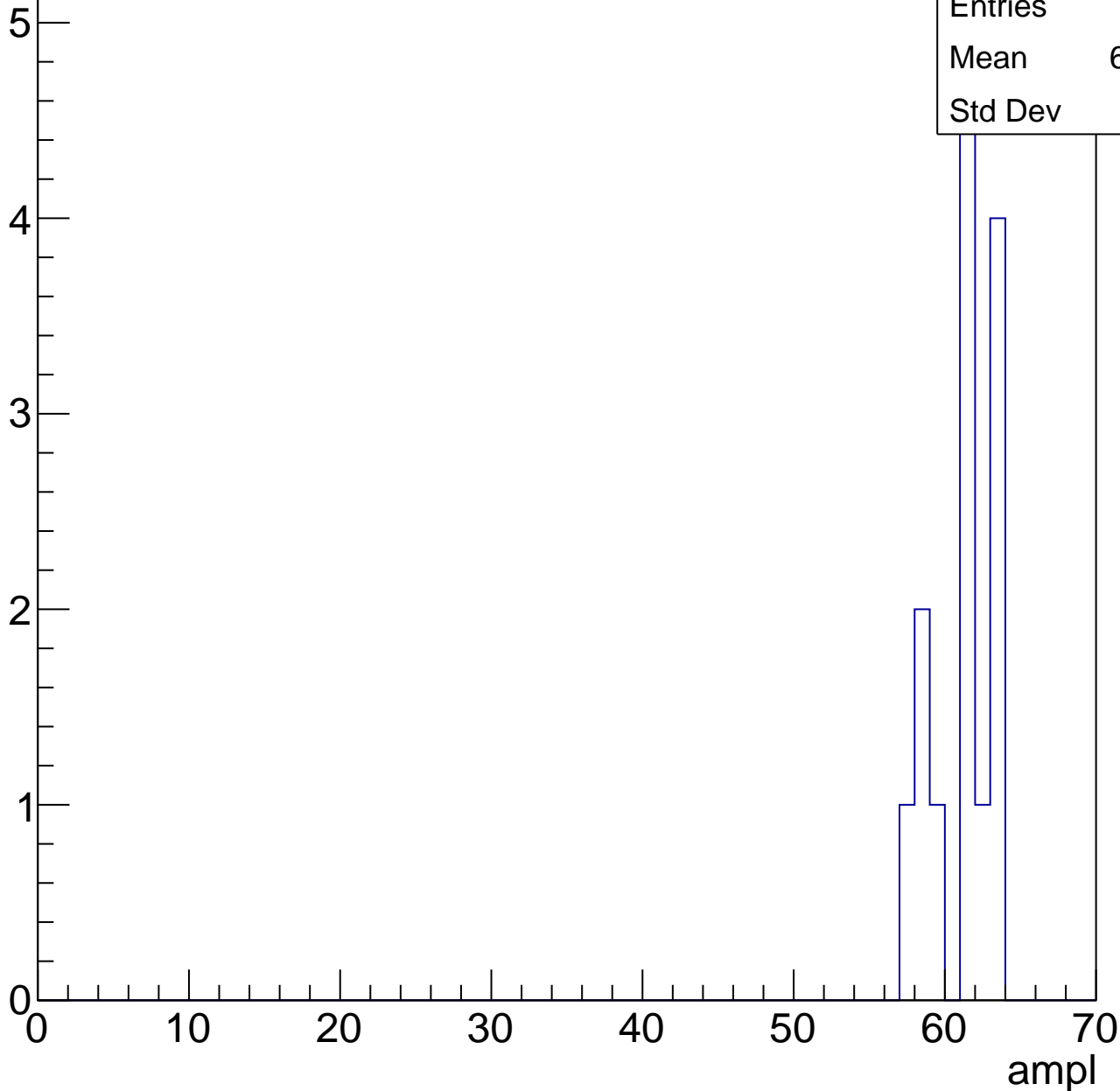


# B1L003S, U22-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	14
Mean	60.79
Std Dev	1.97





# B1L003S, U22-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch85, adc0

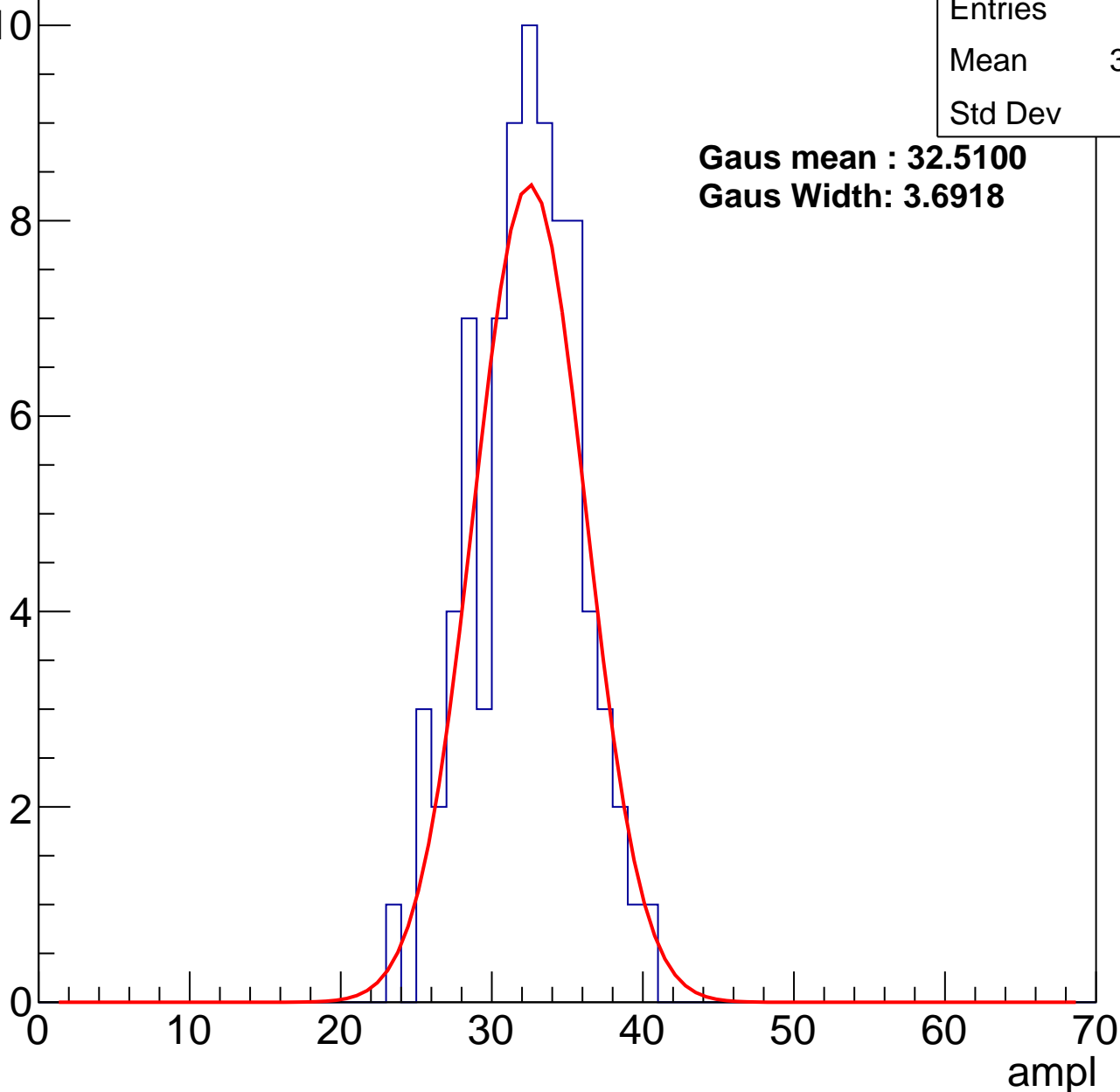
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	82
Mean	31.82
Std Dev	3.51

**Gaus mean : 32.5100**

**Gaus Width: 3.6918**



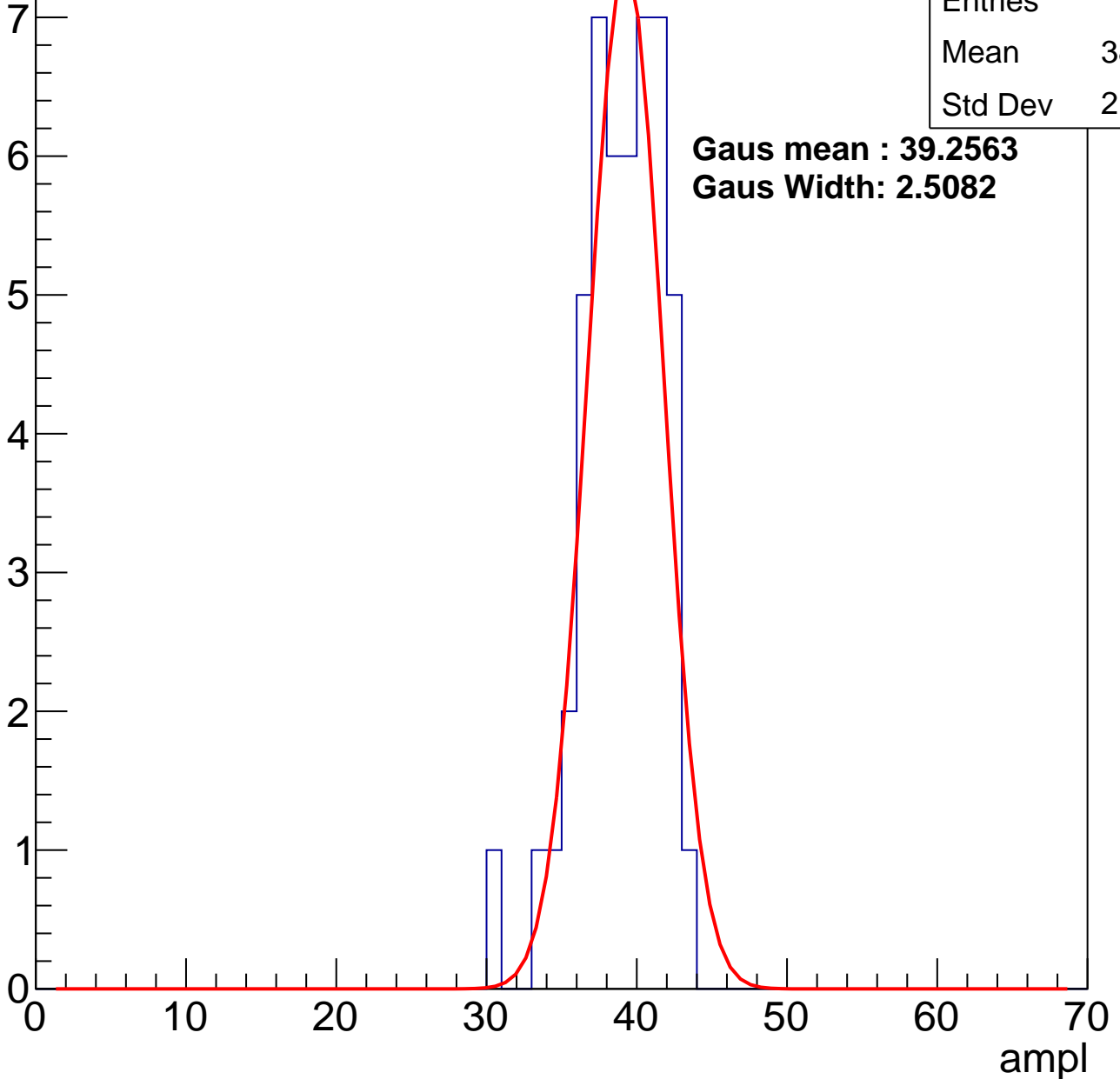
# B1L003S, U22-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	38.53
Std Dev	2.627

**Gaus mean : 39.2563**  
**Gaus Width: 2.5082**



# B1L003S, U22-ch85, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	44.38
Std Dev	3.161

**Gaus mean : 45.2594**

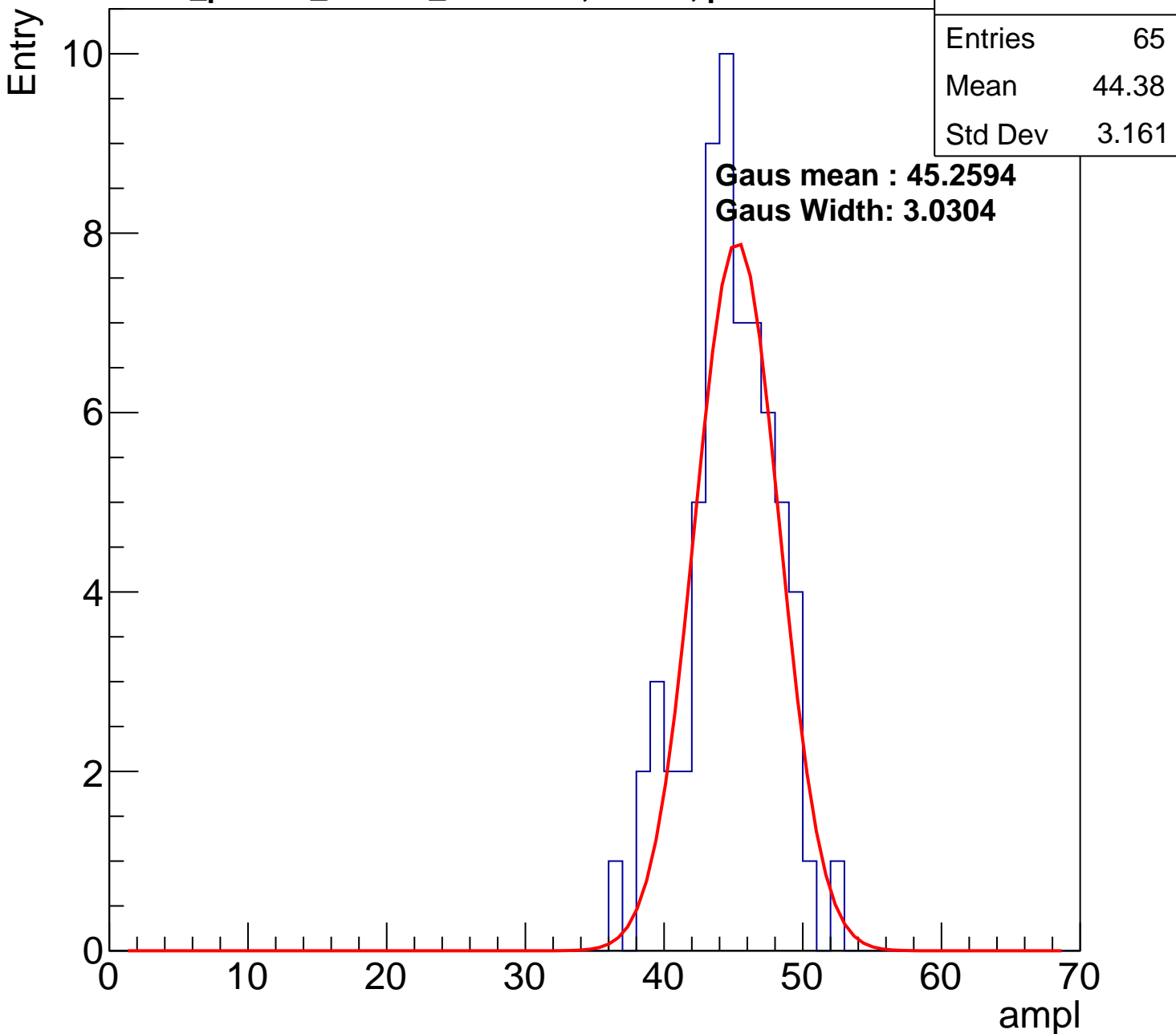
**Gaus Width: 3.0304**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

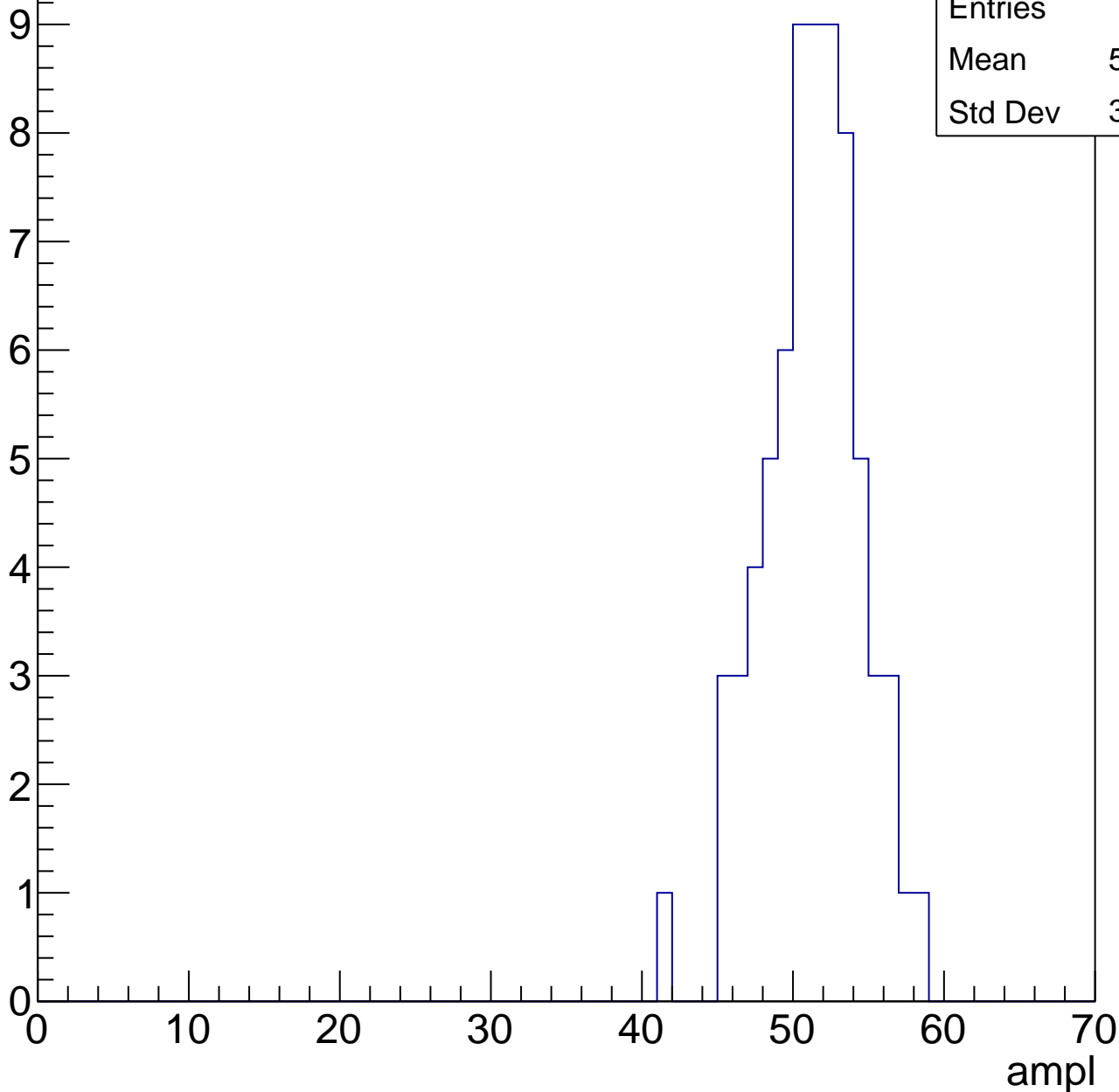


# B1L003S, U22-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	50.79
Std Dev	3.198

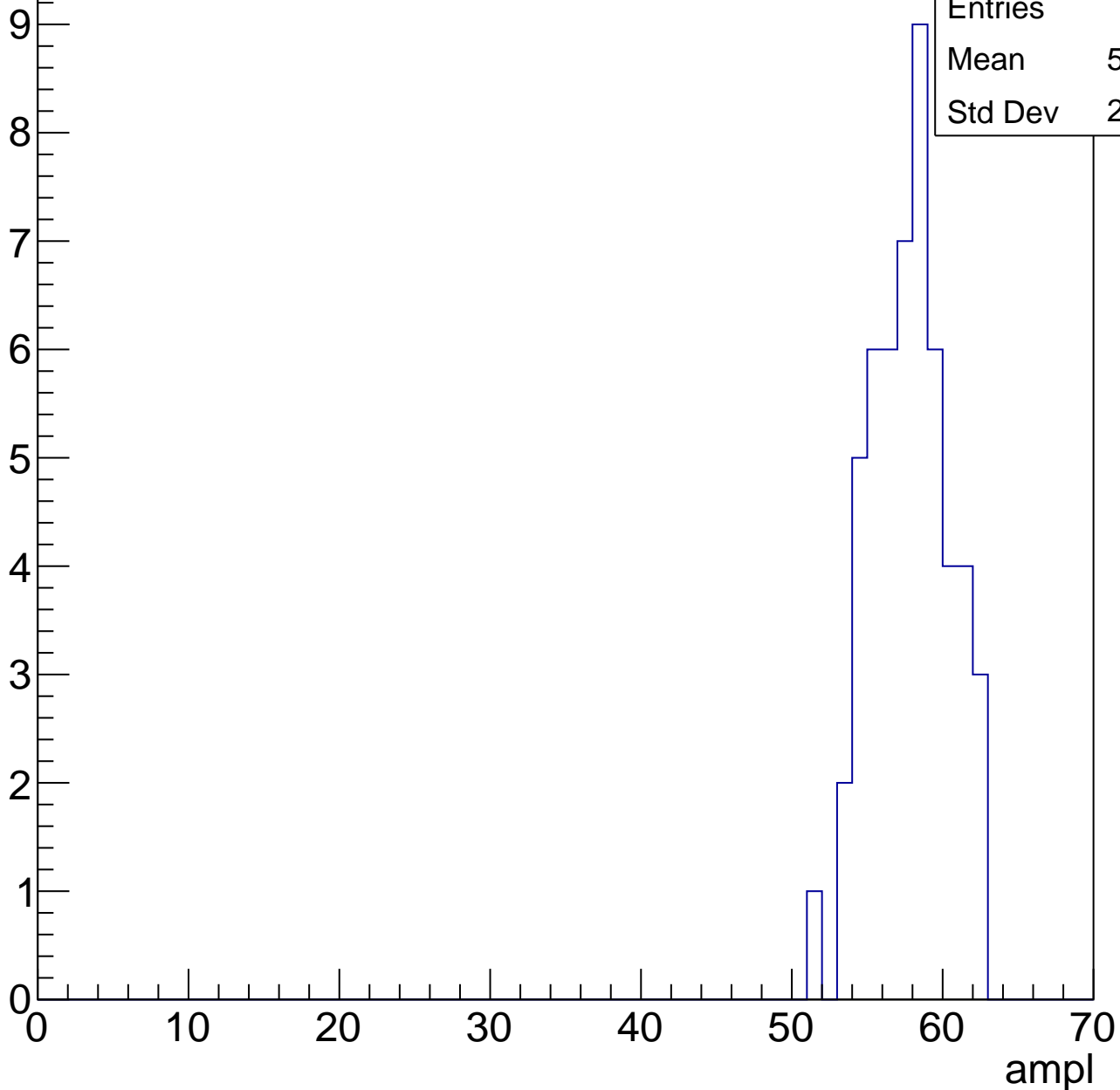


# B1L003S, U22-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	57.32
Std Dev	2.553

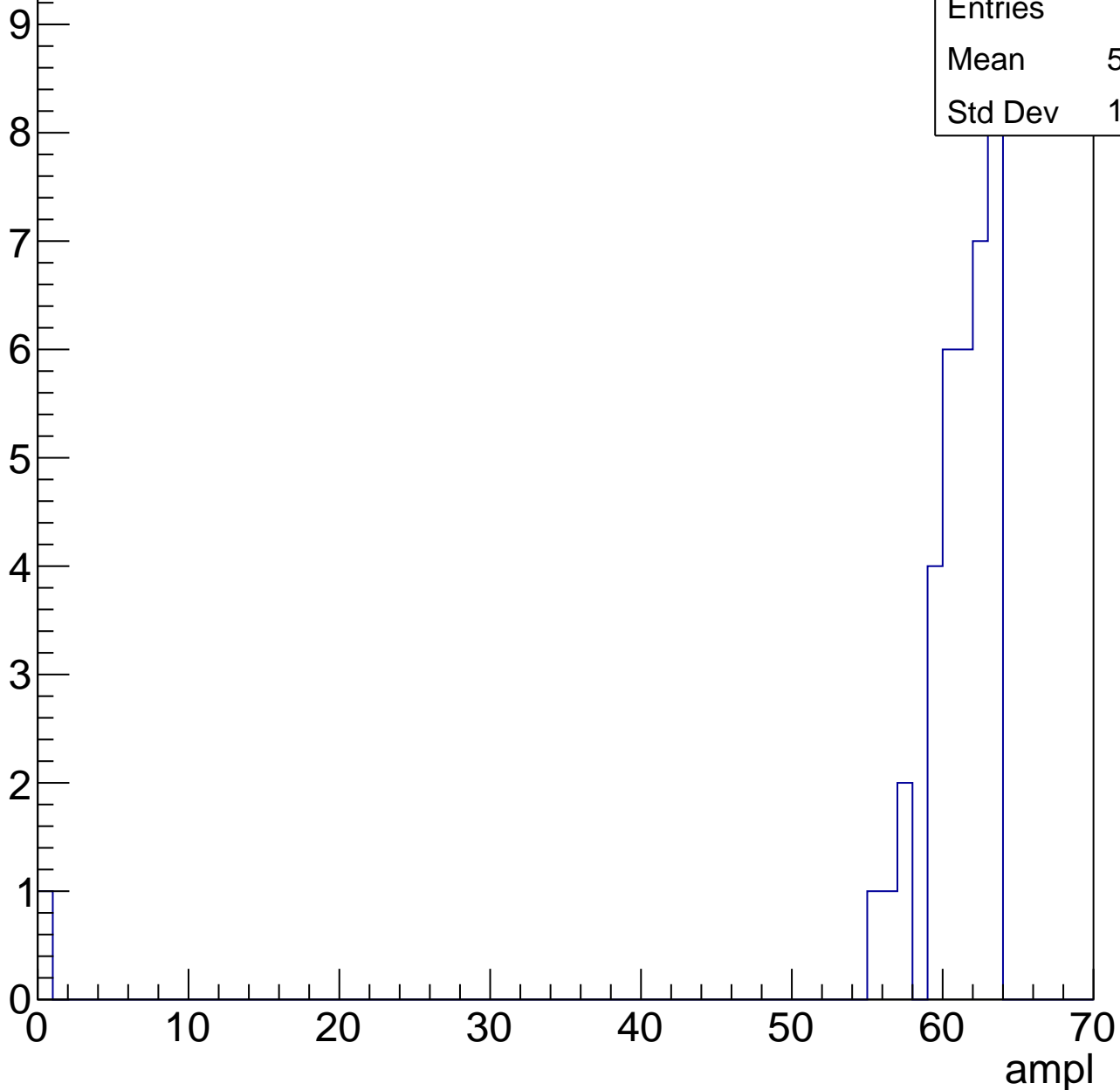


# B1L003S, U22-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	37
Mean	59.14
Std Dev	10.07



# B1L003S, U22-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	79
Mean	28.22
Std Dev	4.919

**Gaus mean : 29.0632**

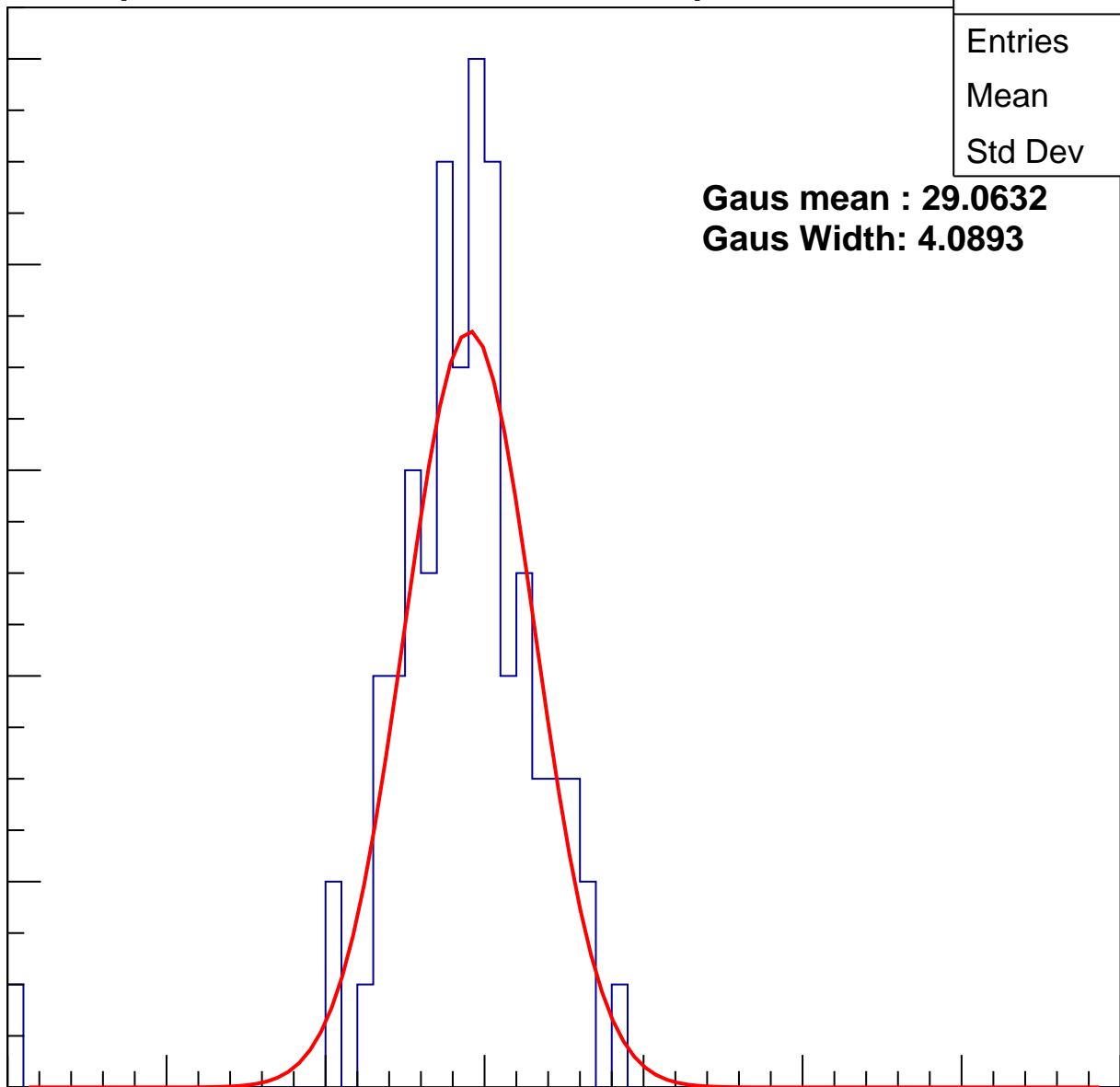
**Gaus Width: 4.0893**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



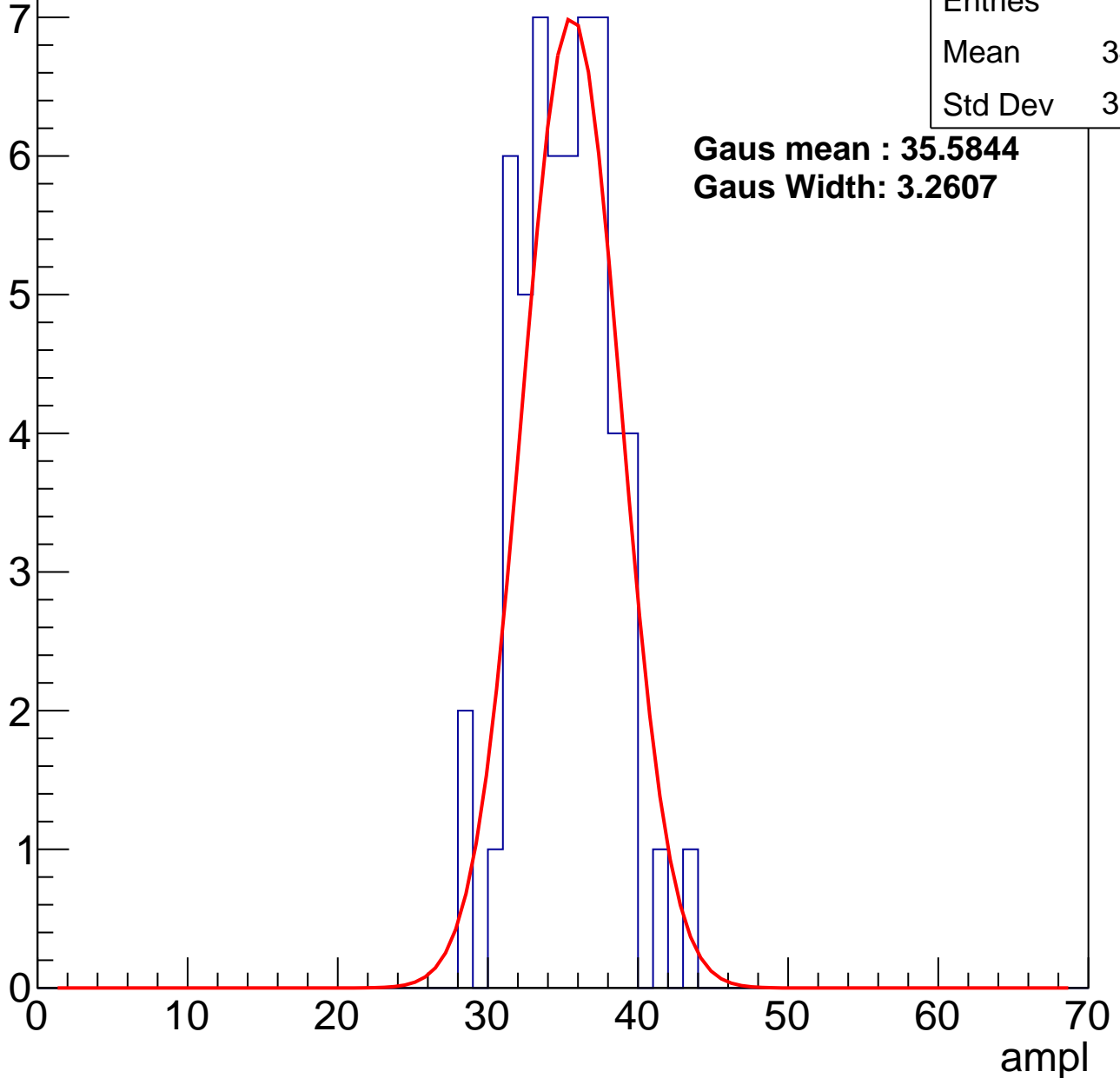
# B1L003S, U22-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	34.74
Std Dev	3.047

**Gaus mean : 35.5844**  
**Gaus Width: 3.2607**



# B1L003S, U22-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	58
Mean	41.07
Std Dev	2.924

**Gaus mean : 41.3040**

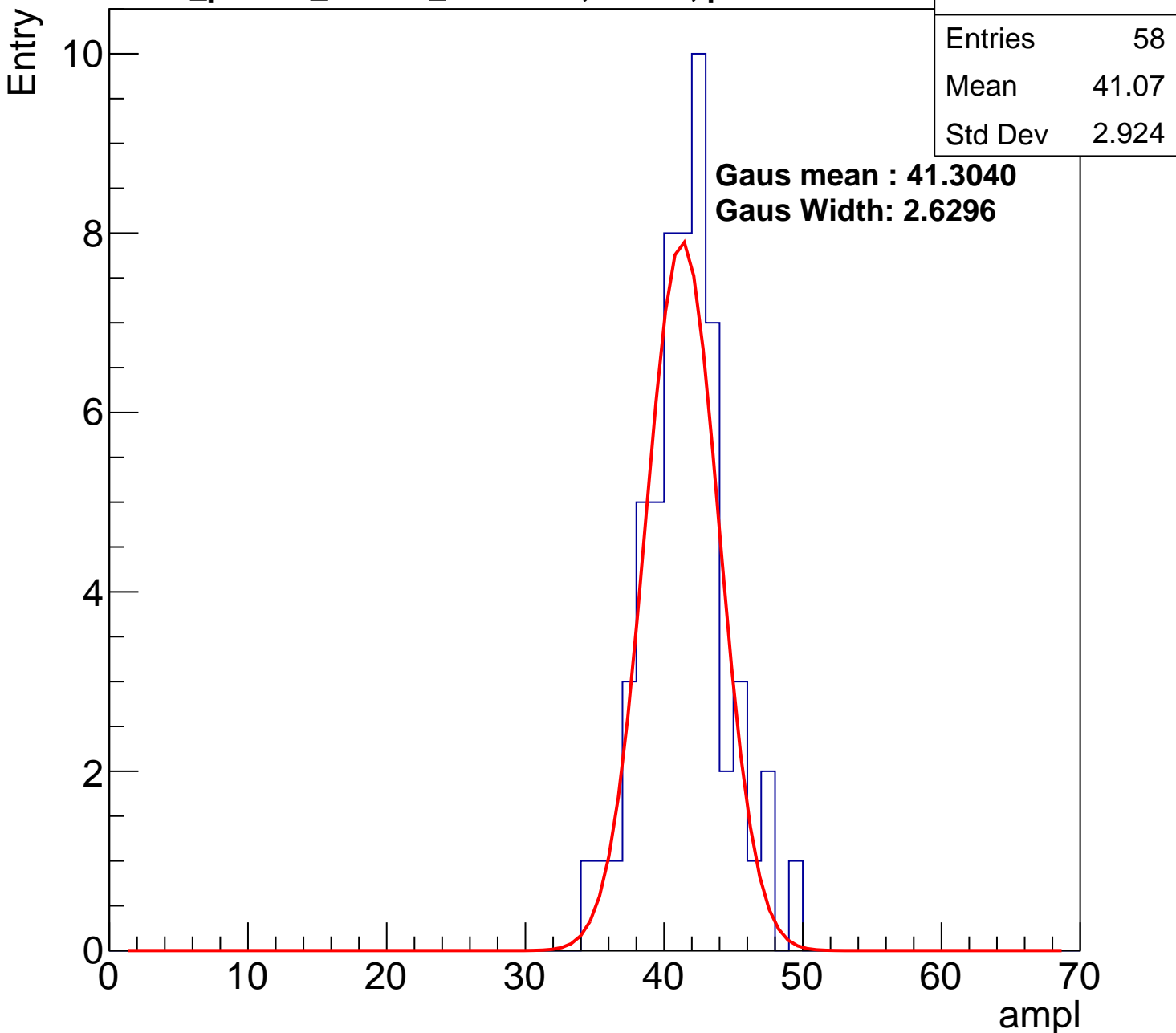
**Gaus Width: 2.6296**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

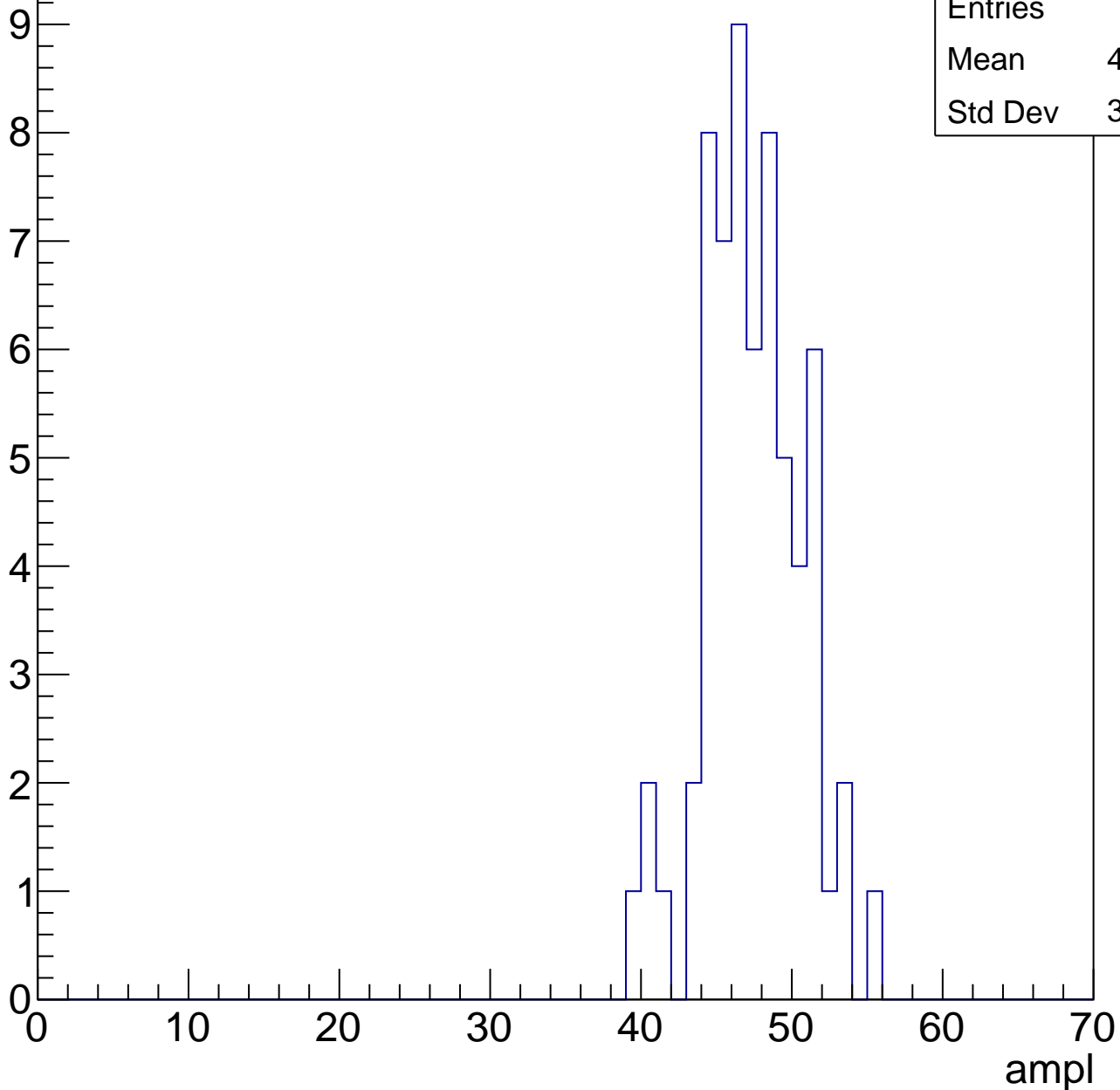


# B1L003S, U22-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	46.94
Std Dev	3.236

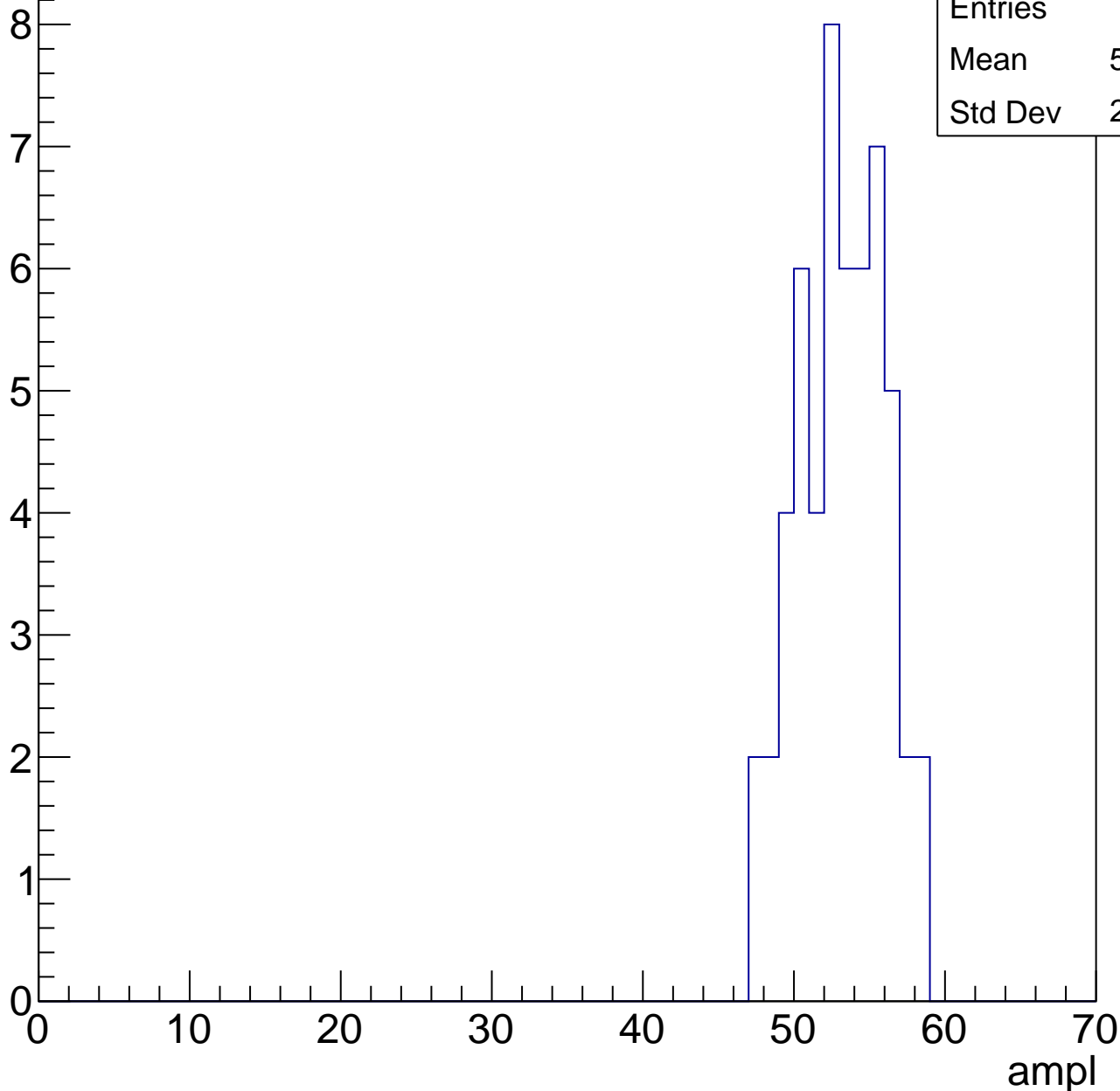


# B1L003S, U22-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	52.65
Std Dev	2.783

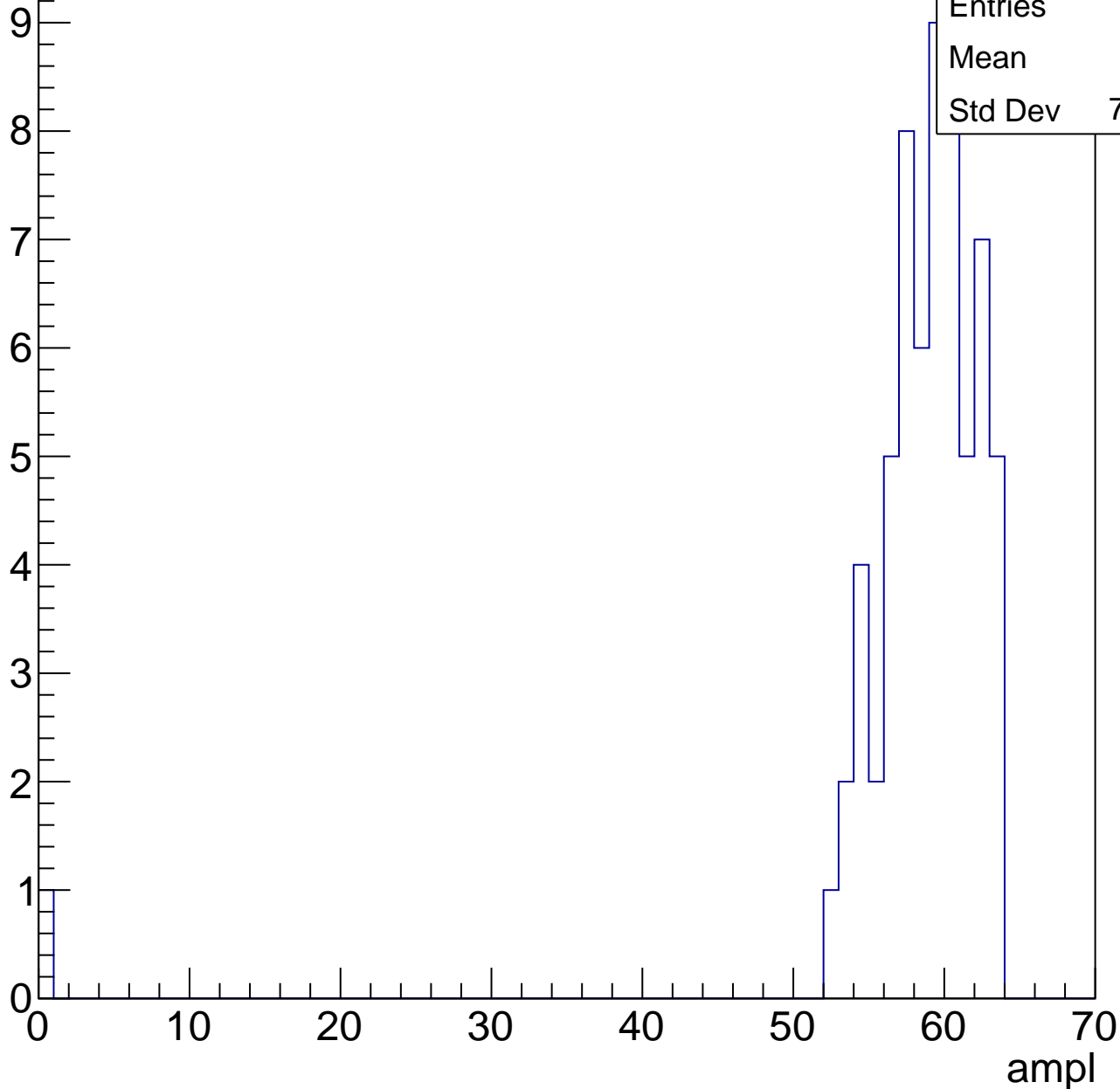


# B1L003S, U22-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

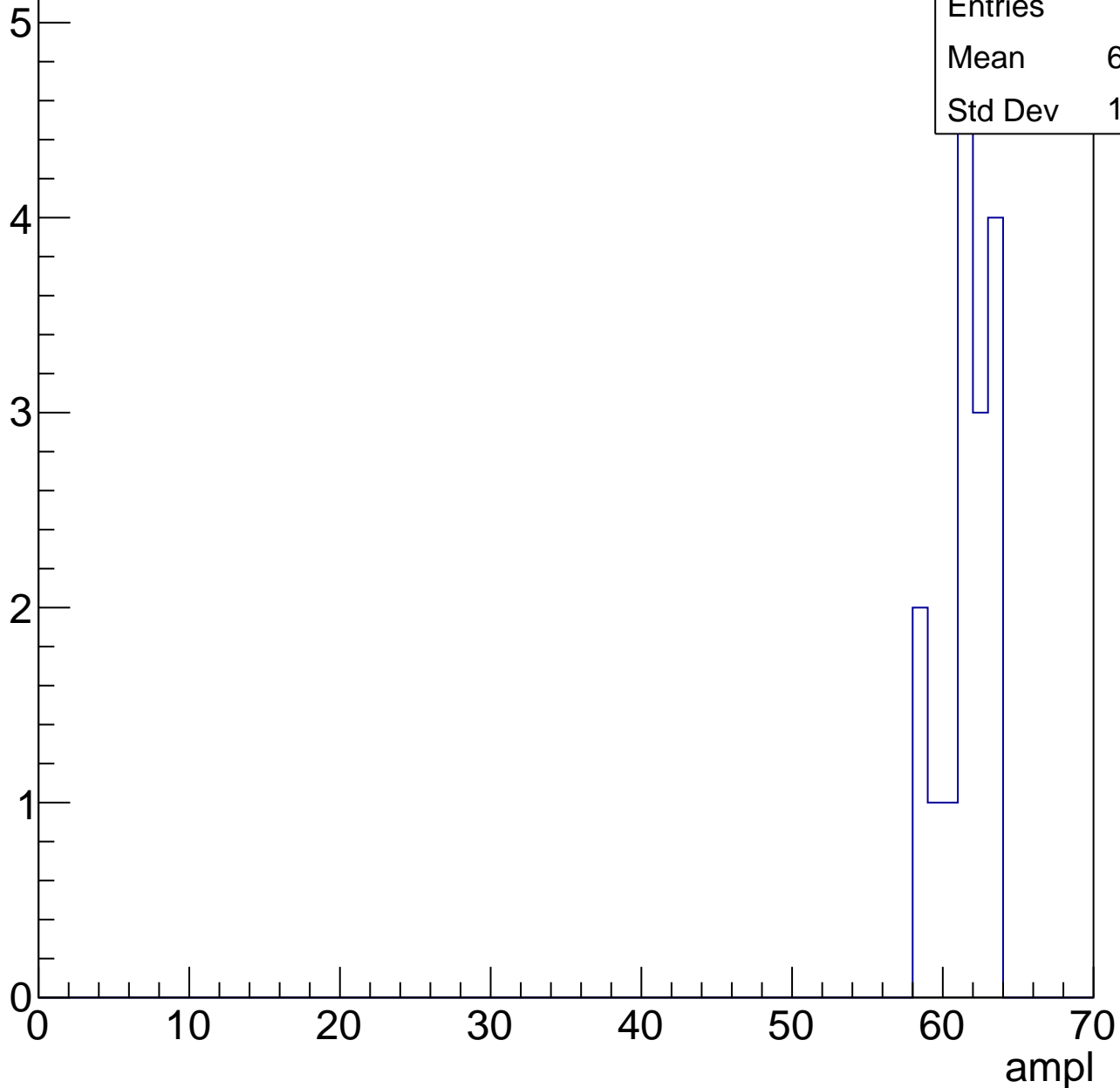
Entries	64
Mean	57.7
Std Dev	7.788



# B1L003S, U22-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch87, adc0

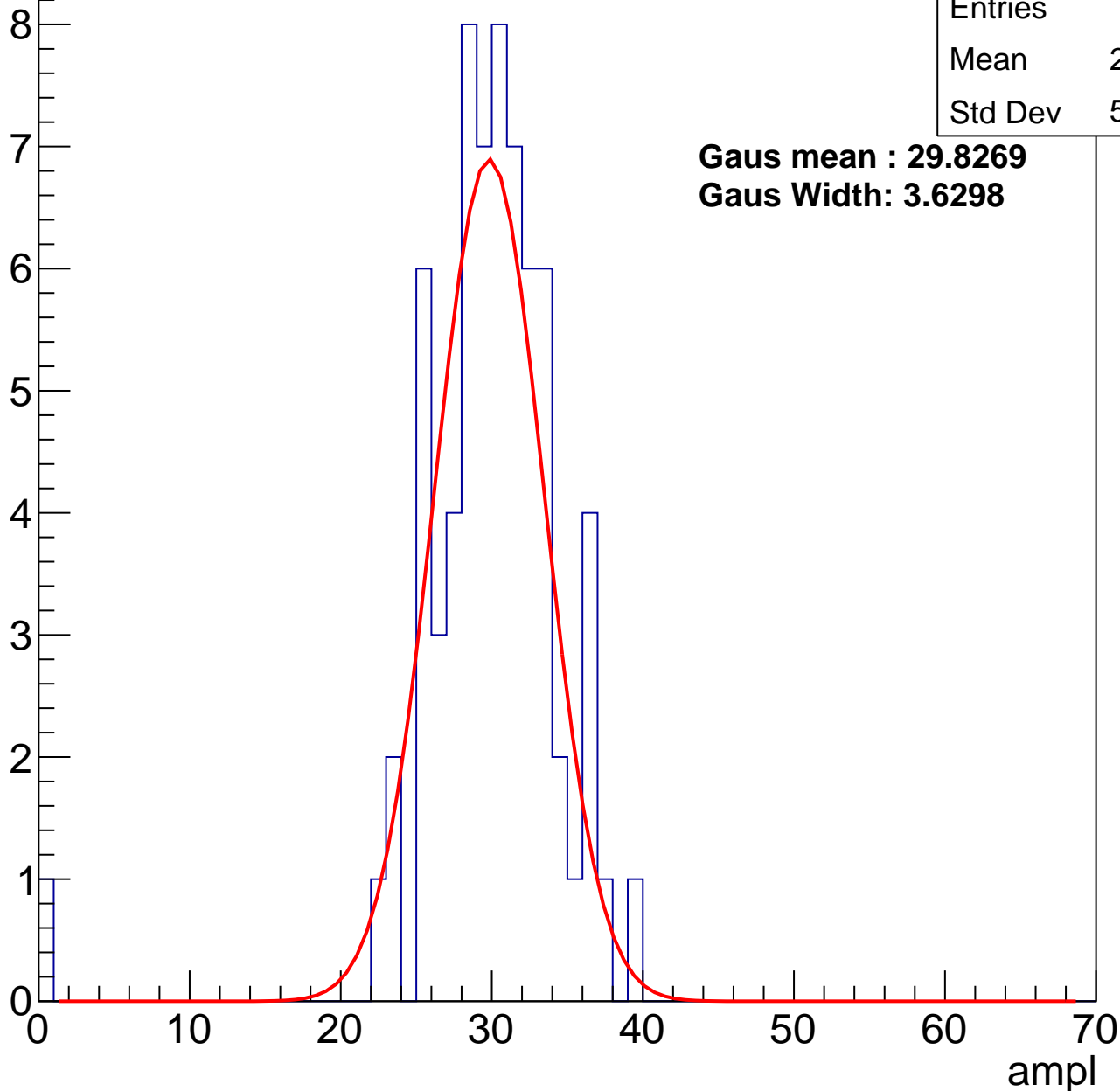
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.43
Std Dev	5.042

**Gaus mean : 29.8269**

**Gaus Width: 3.6298**



# B1L003S, U22-ch87, adc1

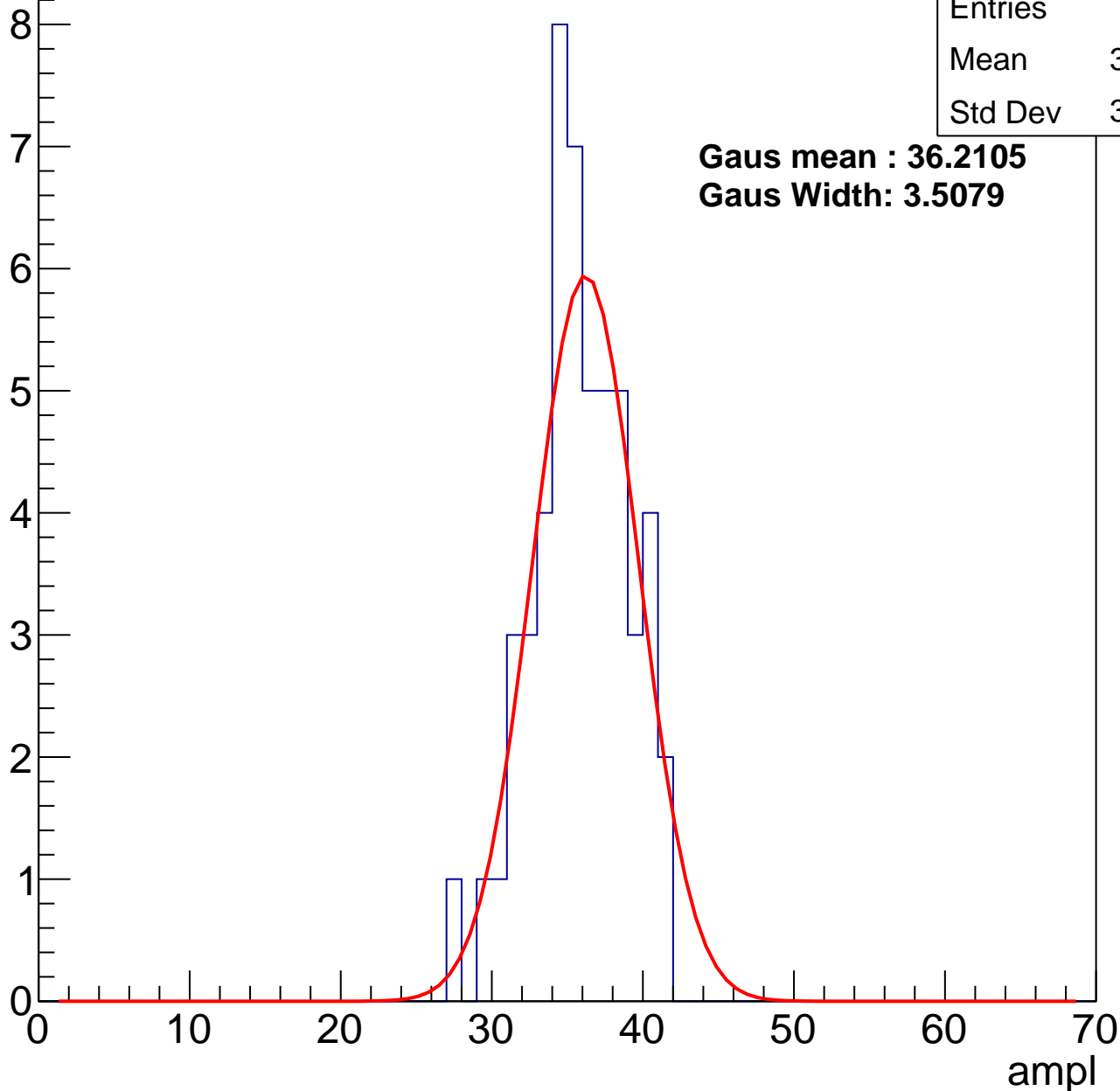
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	35.35
Std Dev	3.125

**Gaus mean : 36.2105**

**Gaus Width: 3.5079**



# B1L003S, U22-ch87, adc2

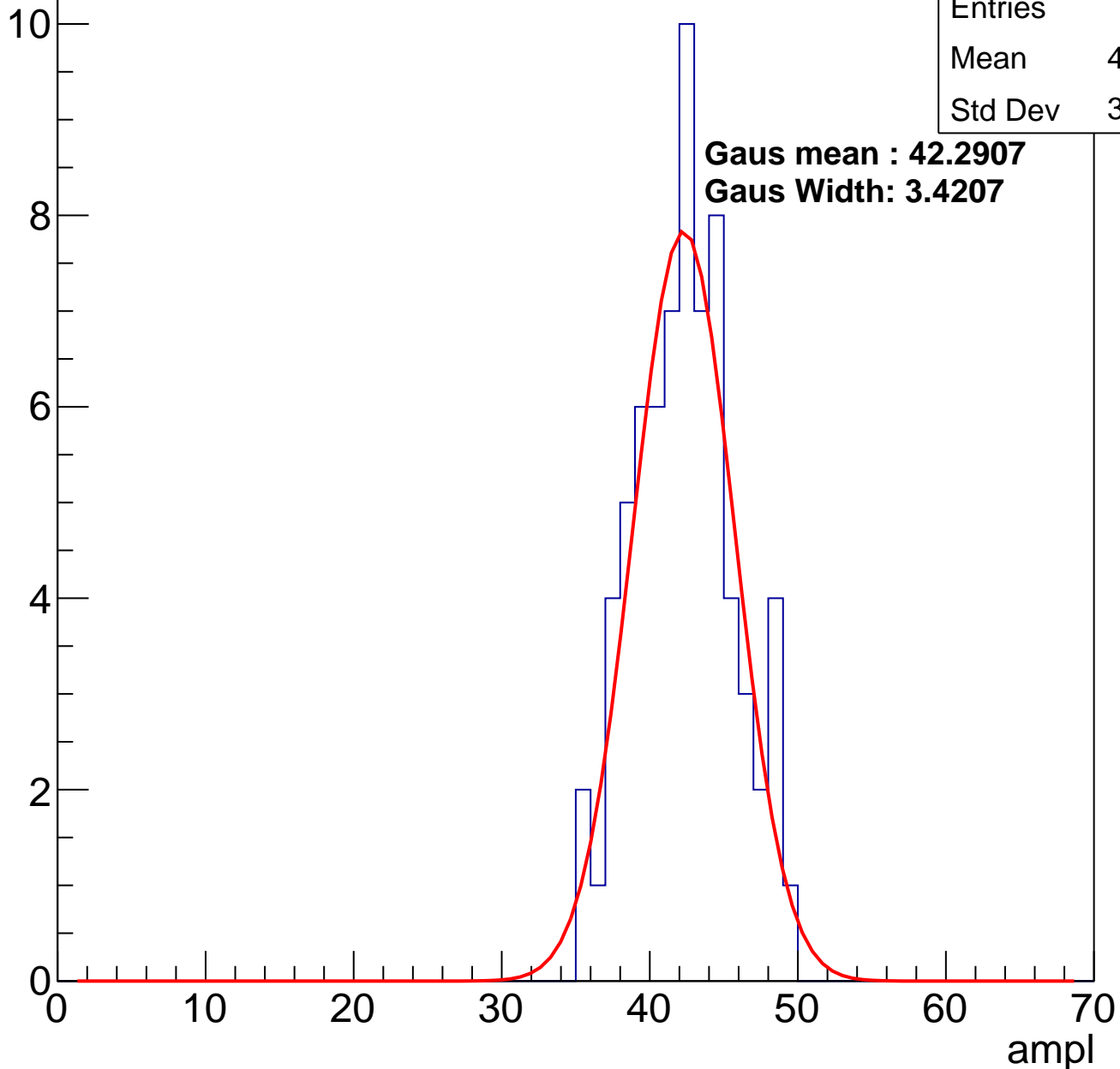
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	41.87
Std Dev	3.303

**Gaus mean : 42.2907**

**Gaus Width: 3.4207**

Entry

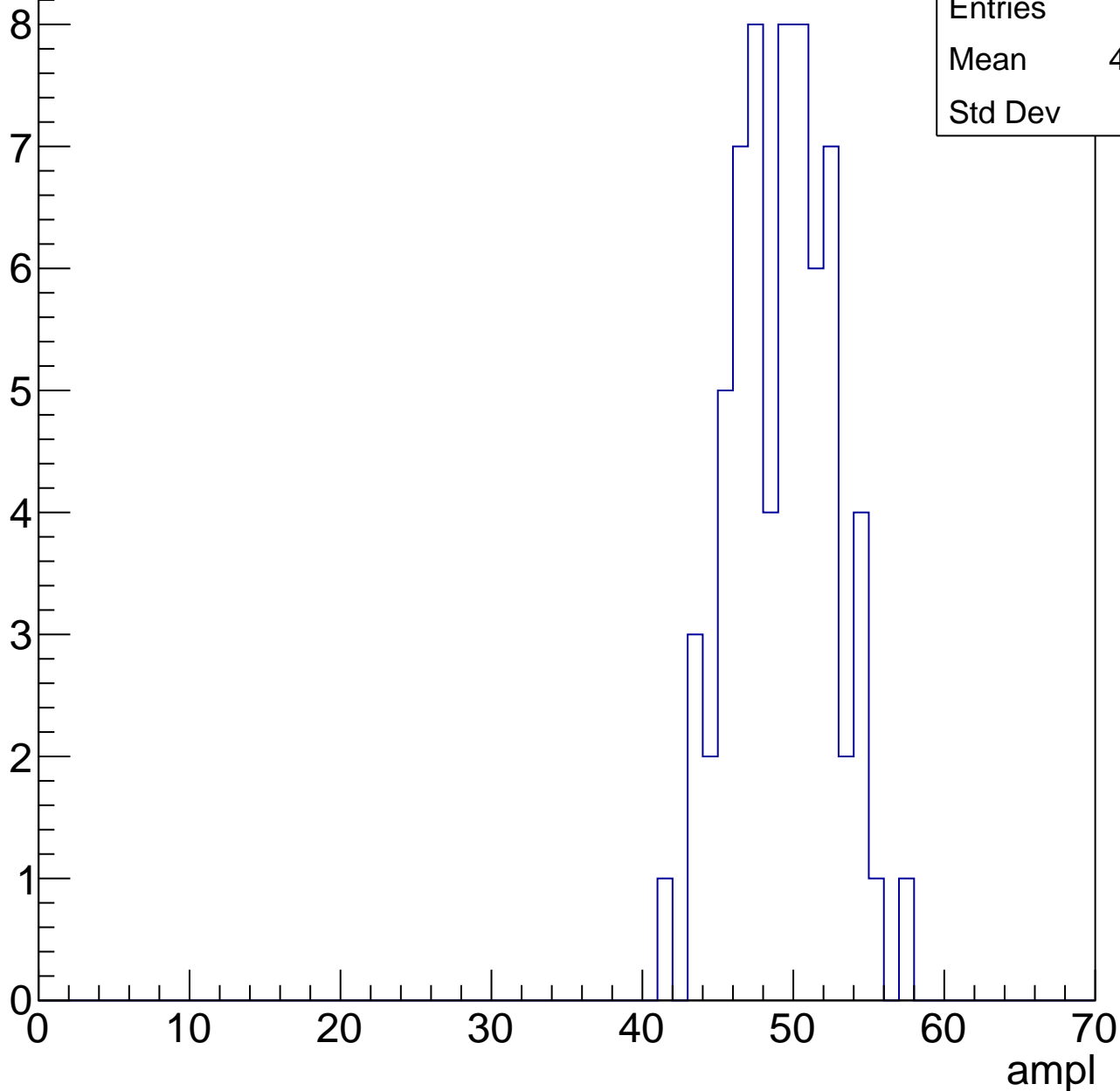


# B1L003S, U22-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

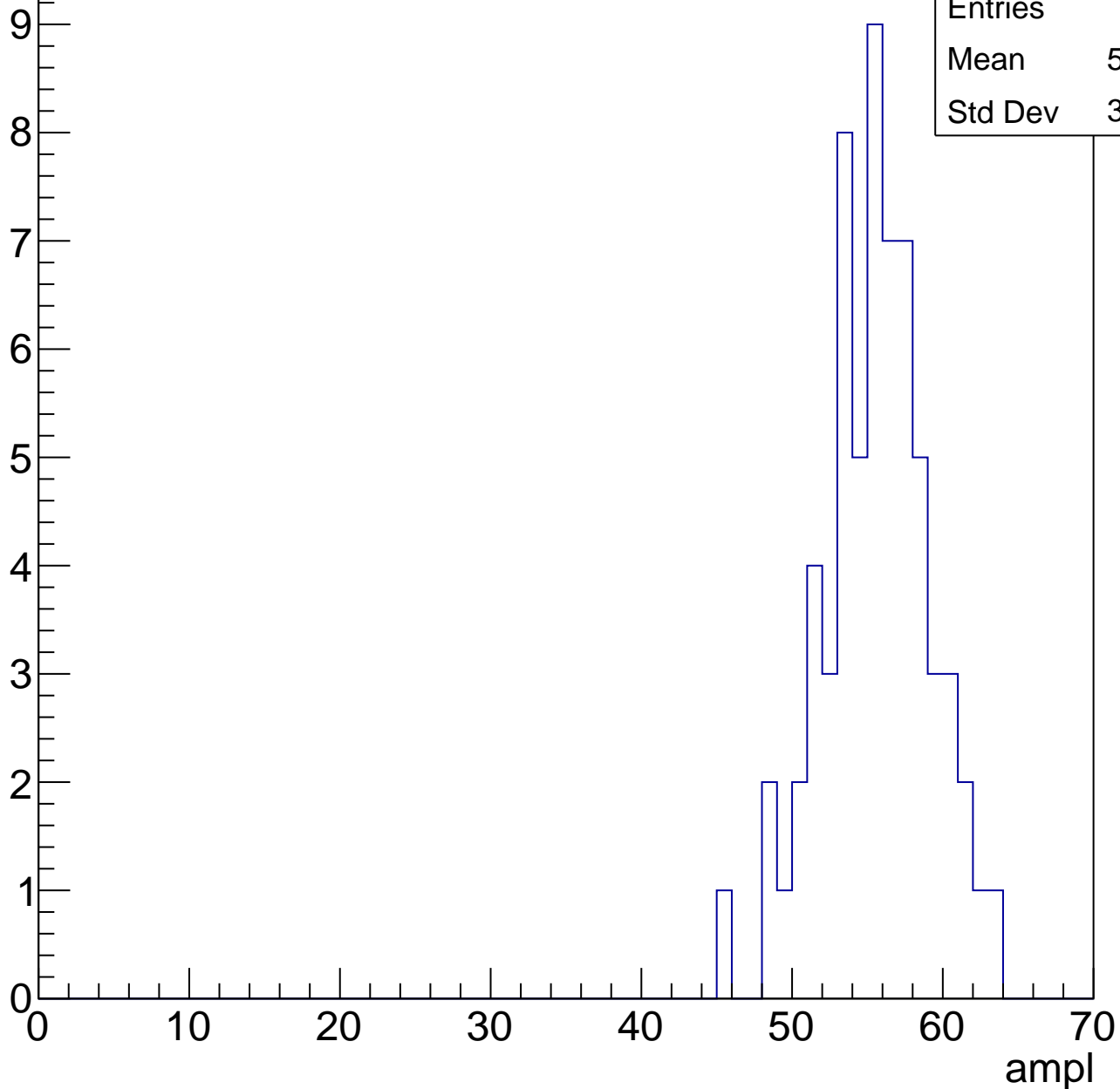
Entries	67
Mean	48.79
Std Dev	3.29



# B1L003S, U22-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

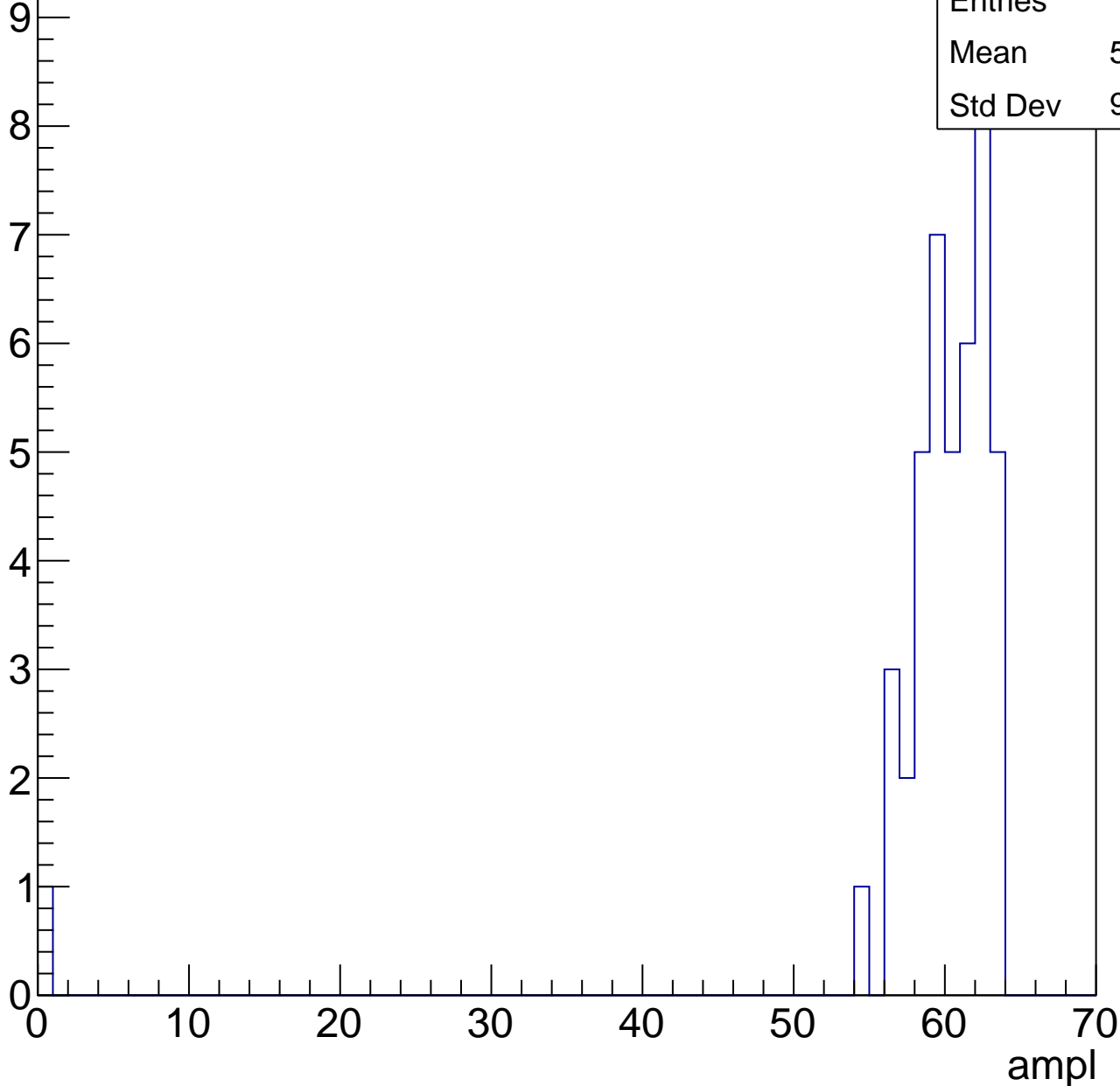


# B1L003S, U22-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

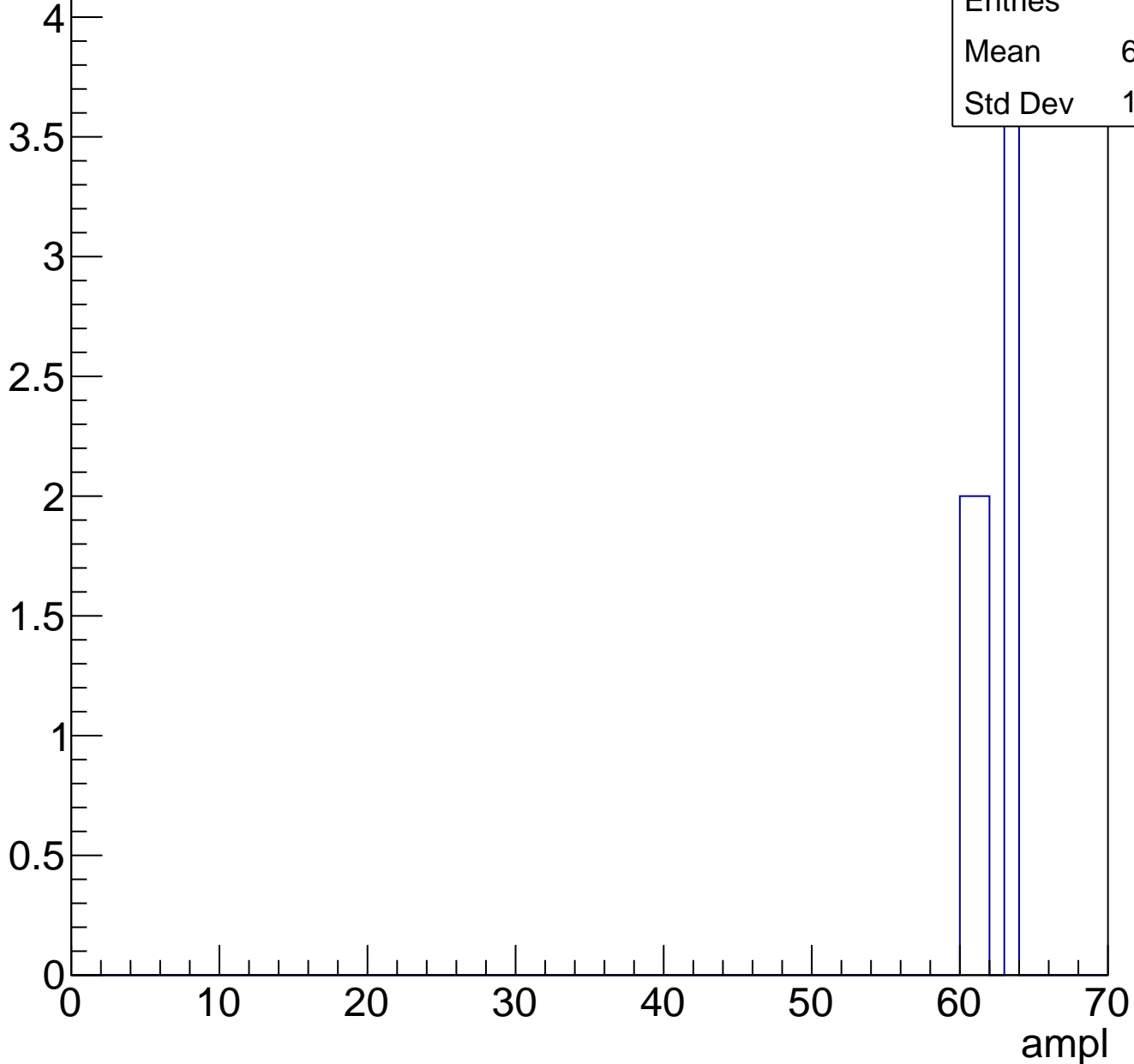
Entries	44
Mean	58.59
Std Dev	9.206



# B1L003S, U22-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

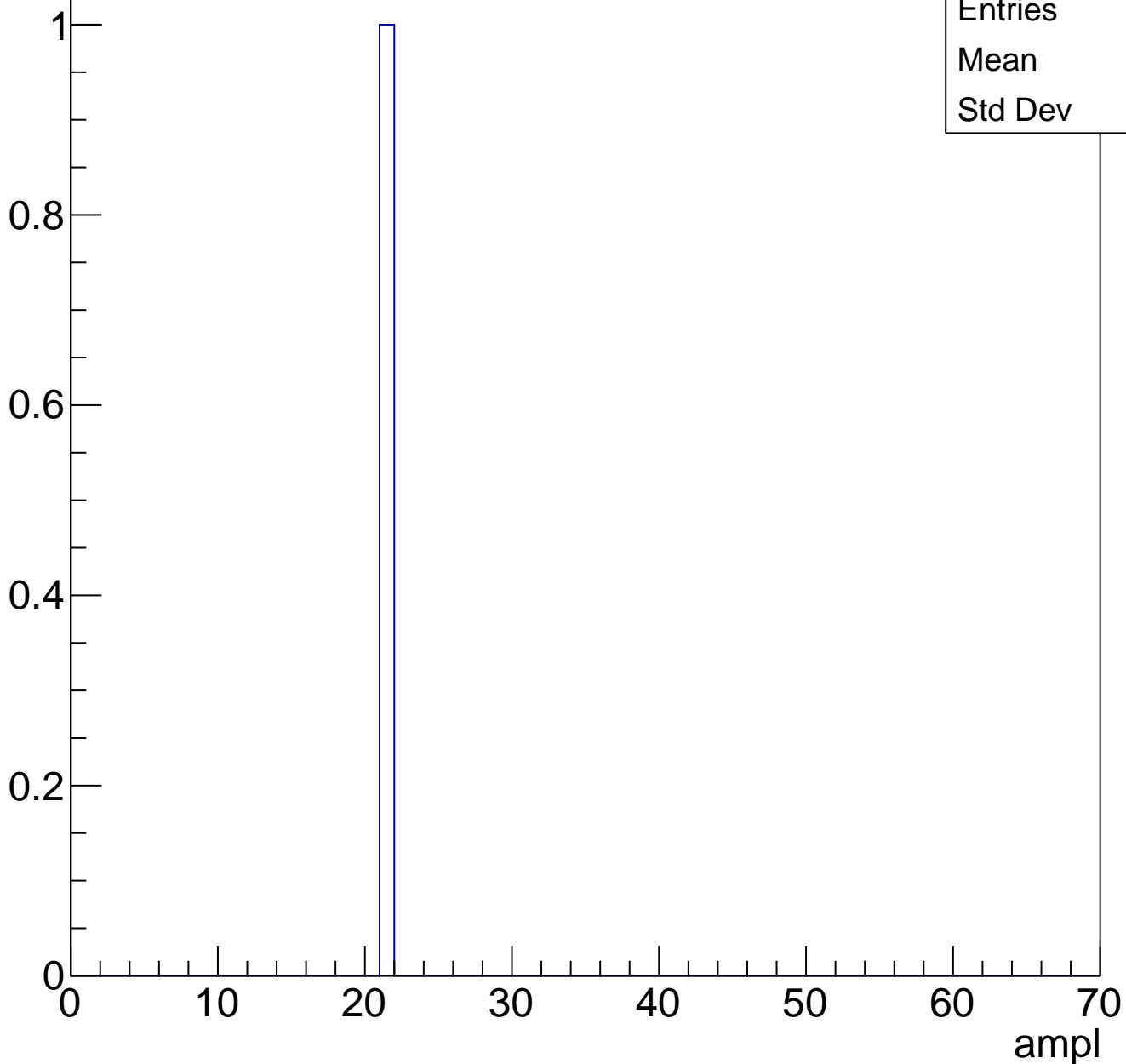




# B1L003S, U22-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch88, adc0

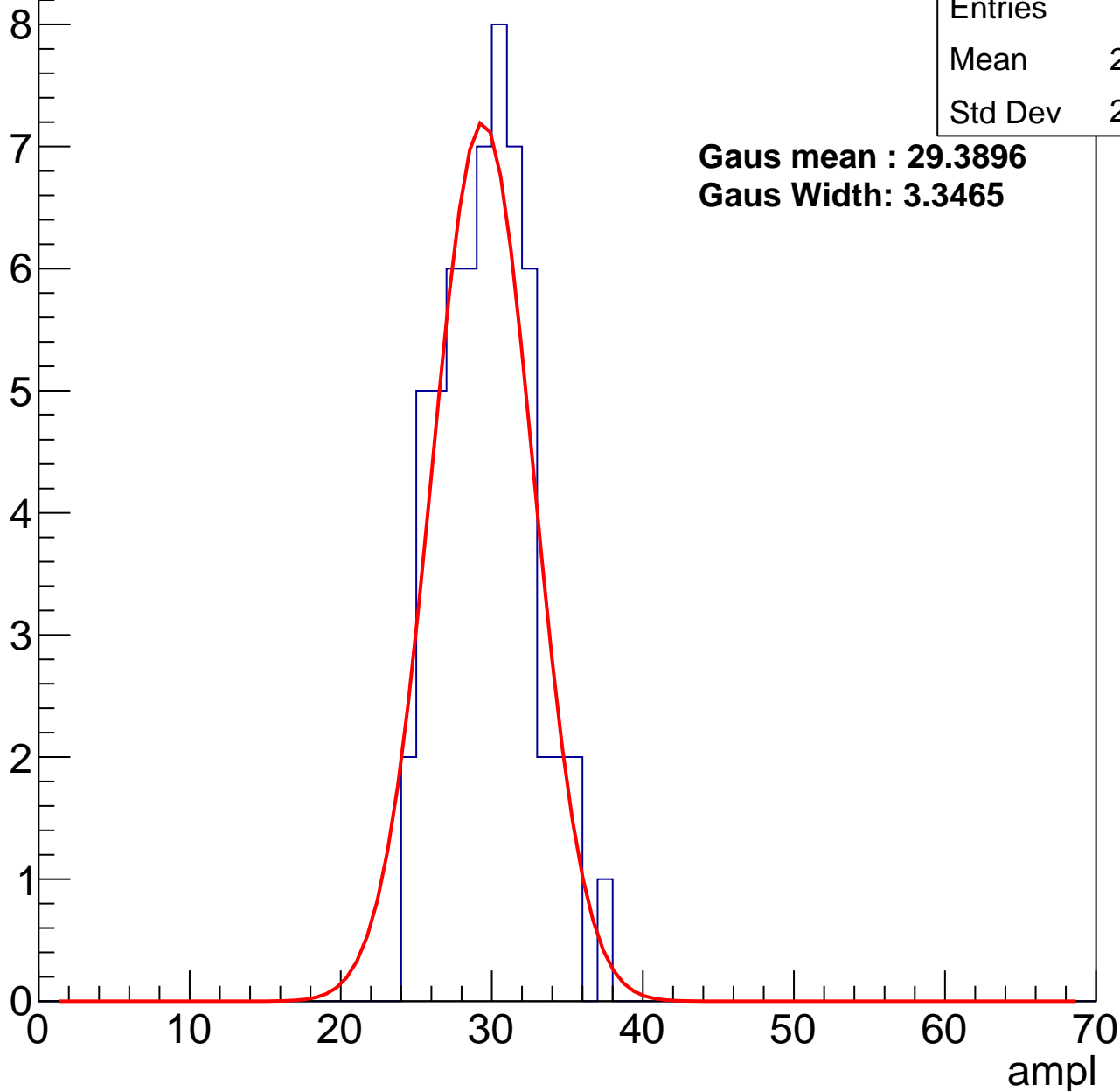
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.25
Std Dev	2.938

**Gaus mean : 29.3896**

**Gaus Width: 3.3465**



# B1L003S, U22-ch88, adc1

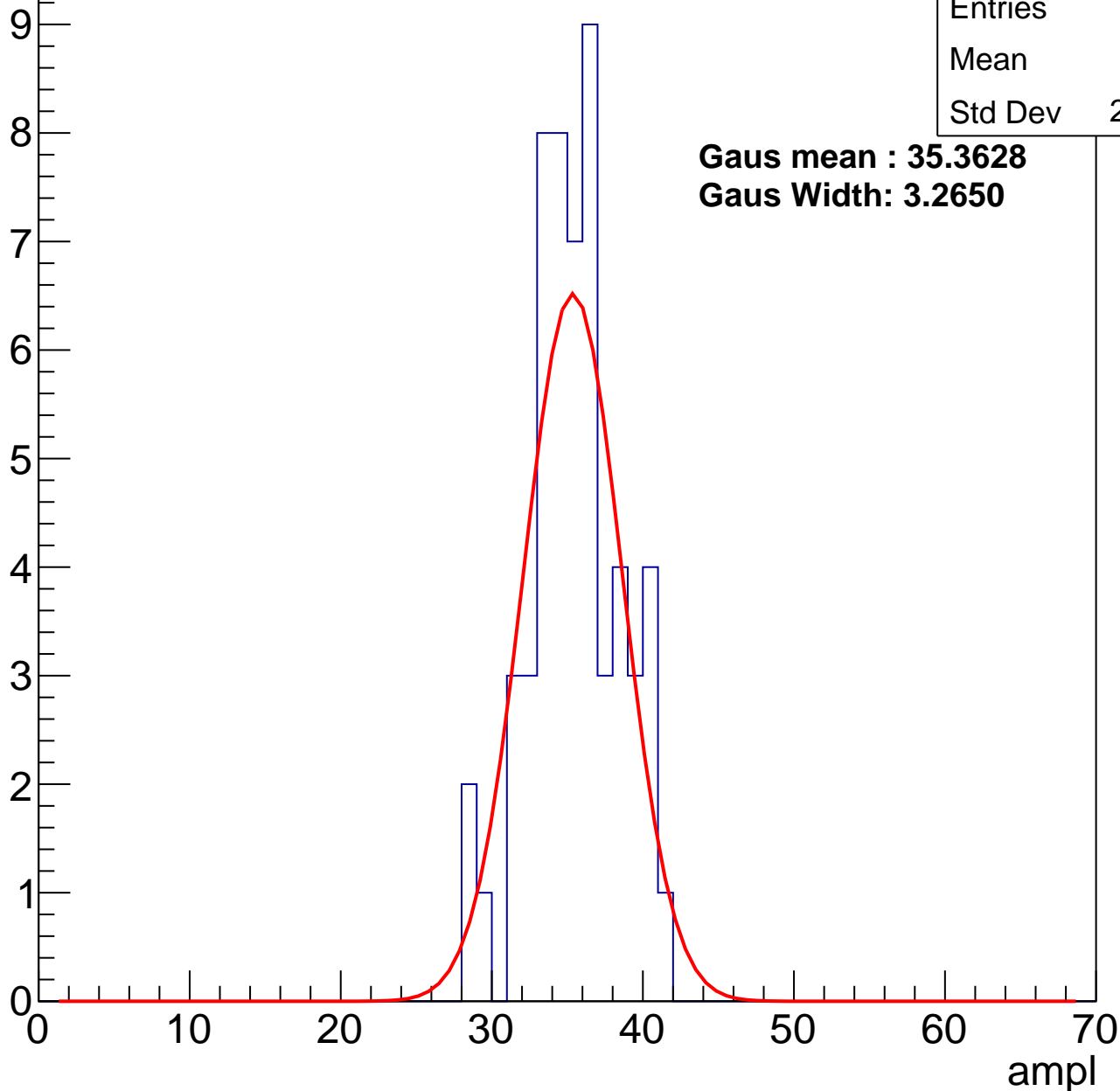
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35
Std Dev	2.958

**Gaus mean : 35.3628**

**Gaus Width: 3.2650**



# B1L003S, U22-ch88, adc2

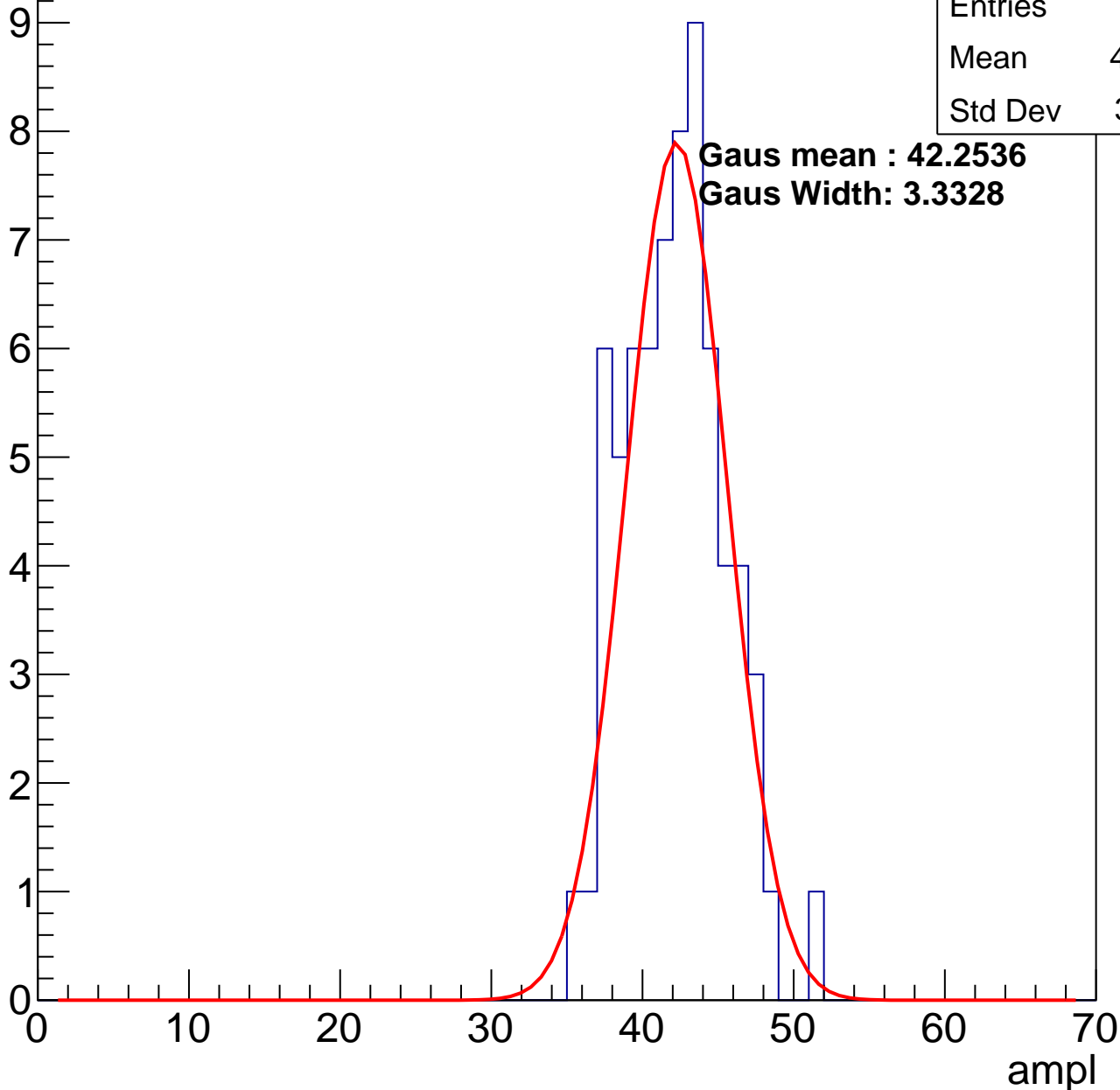
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	41.69
Std Dev	3.251

**Gaus mean : 42.2536**

**Gaus Width: 3.3328**

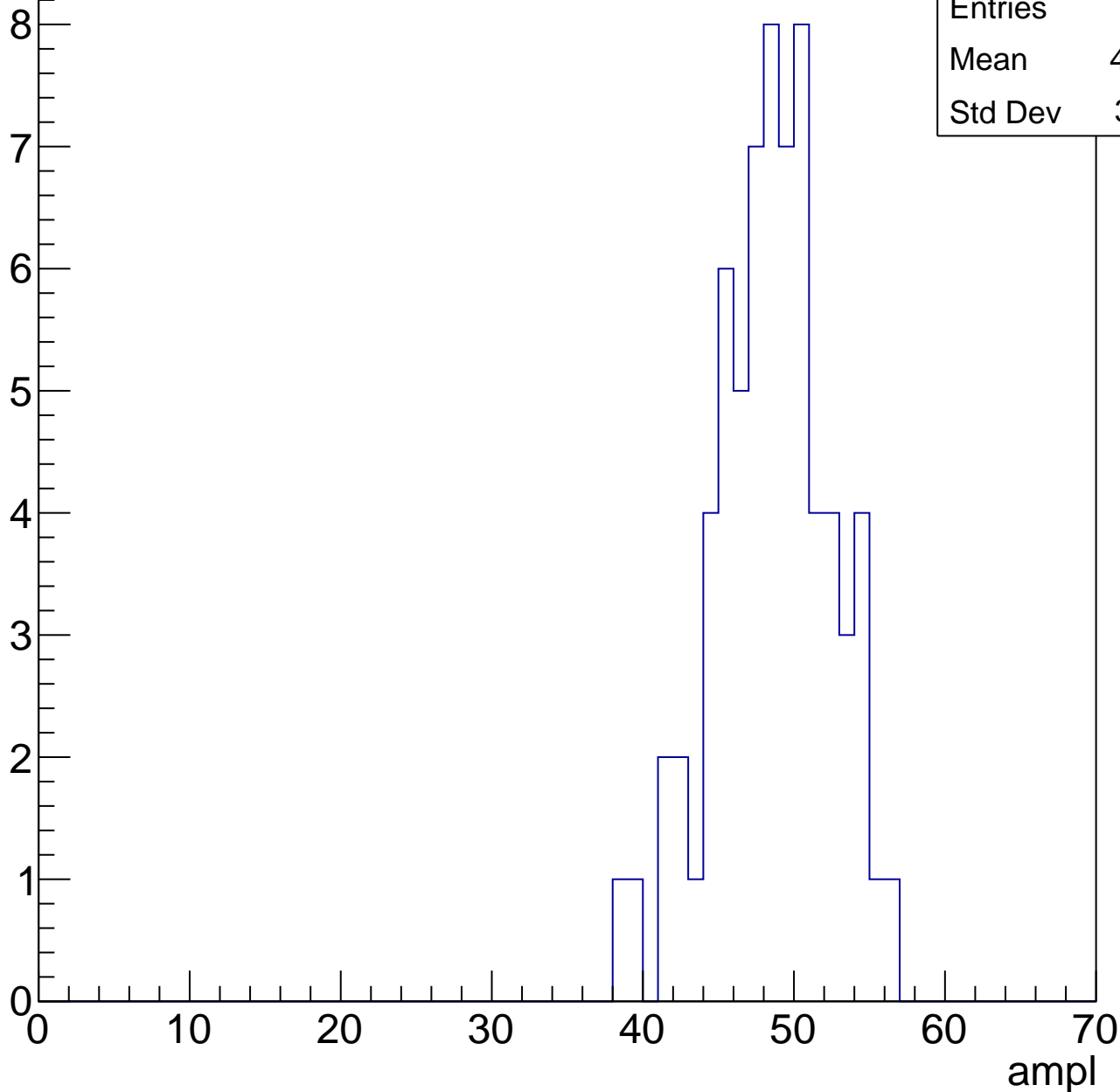


# B1L003S, U22-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	48.06
Std Dev	3.791

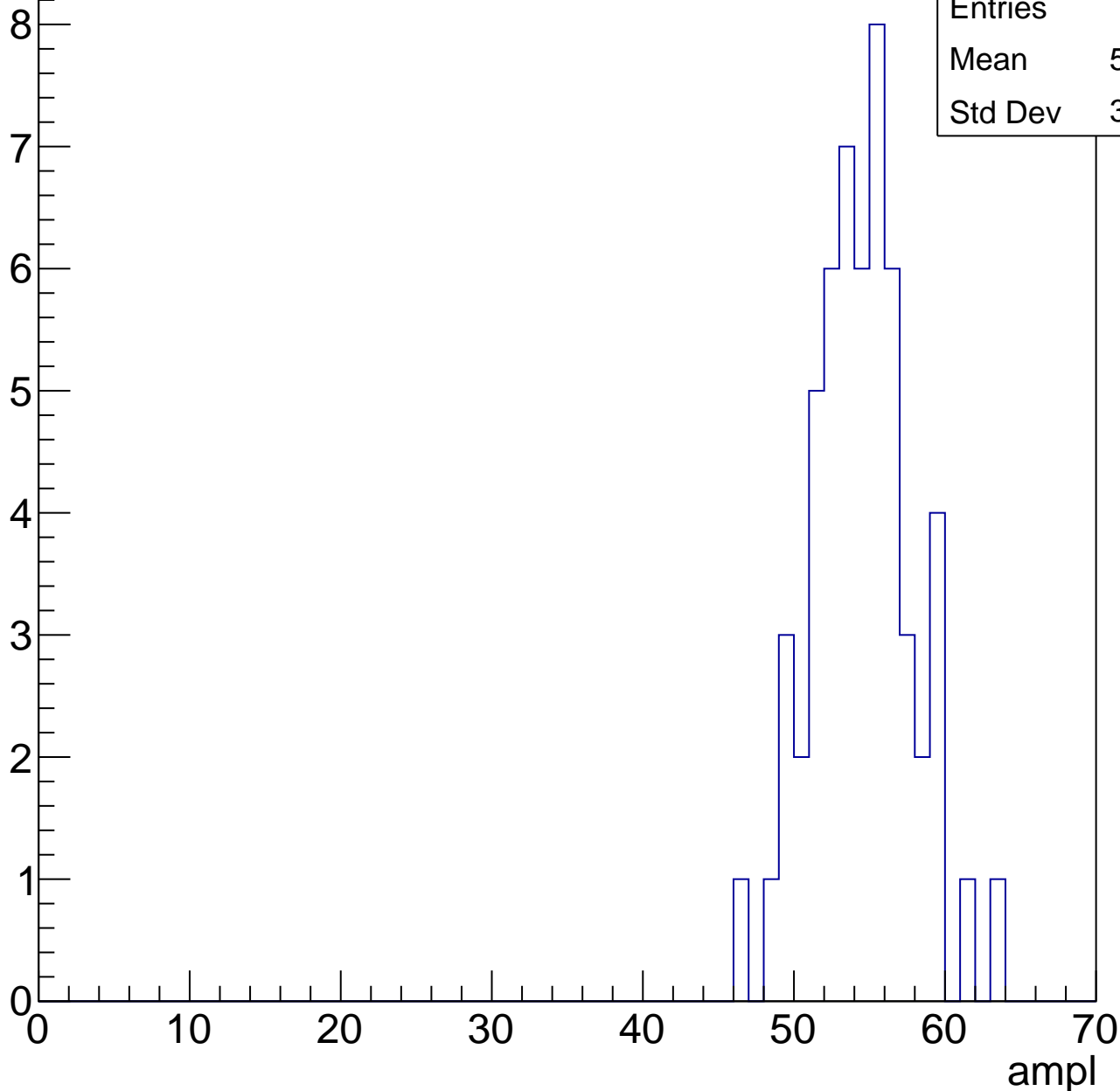


# B1L003S, U22-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.04
Std Dev	3.284

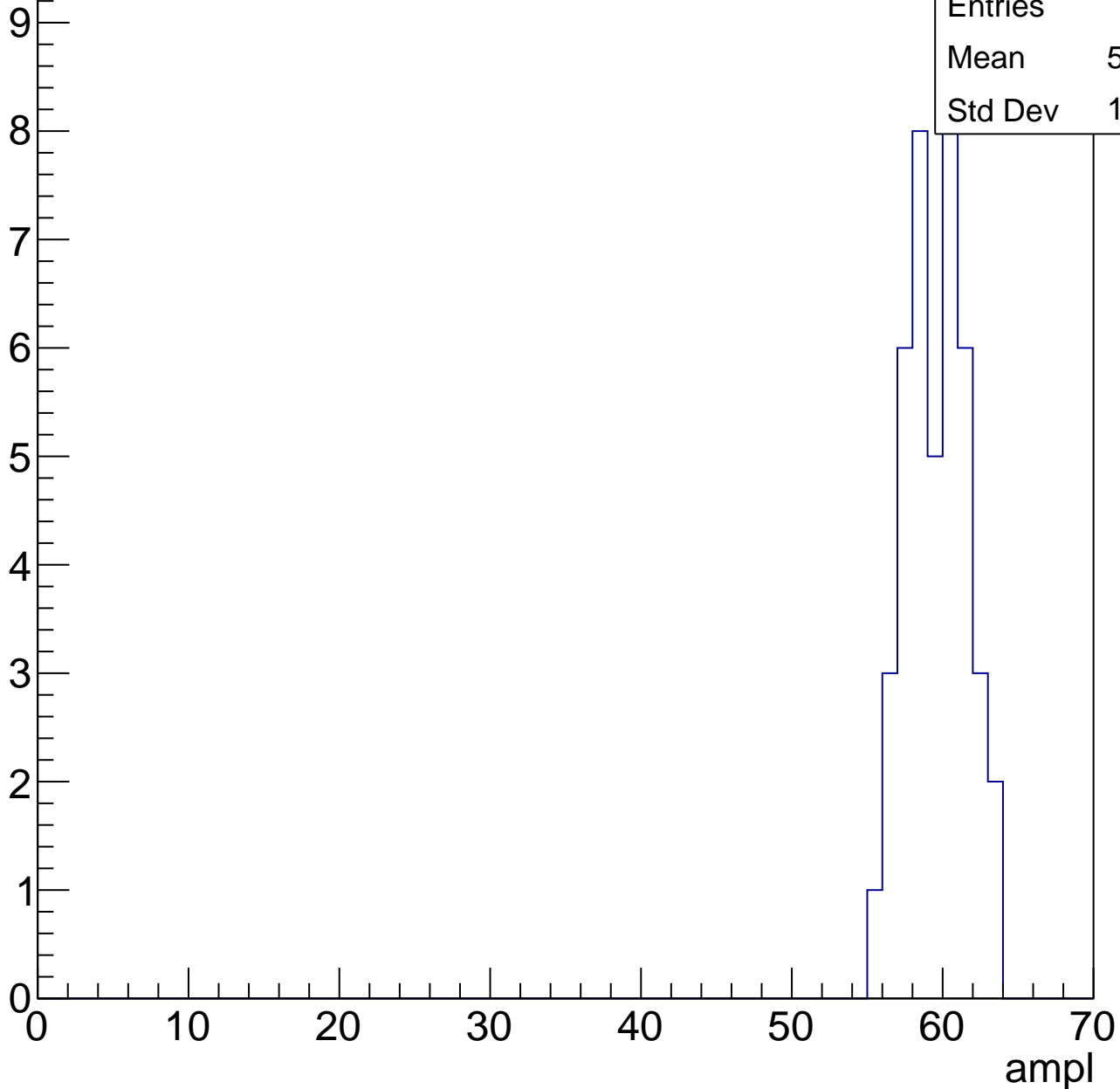


# B1L003S, U22-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	59.12
Std Dev	1.967

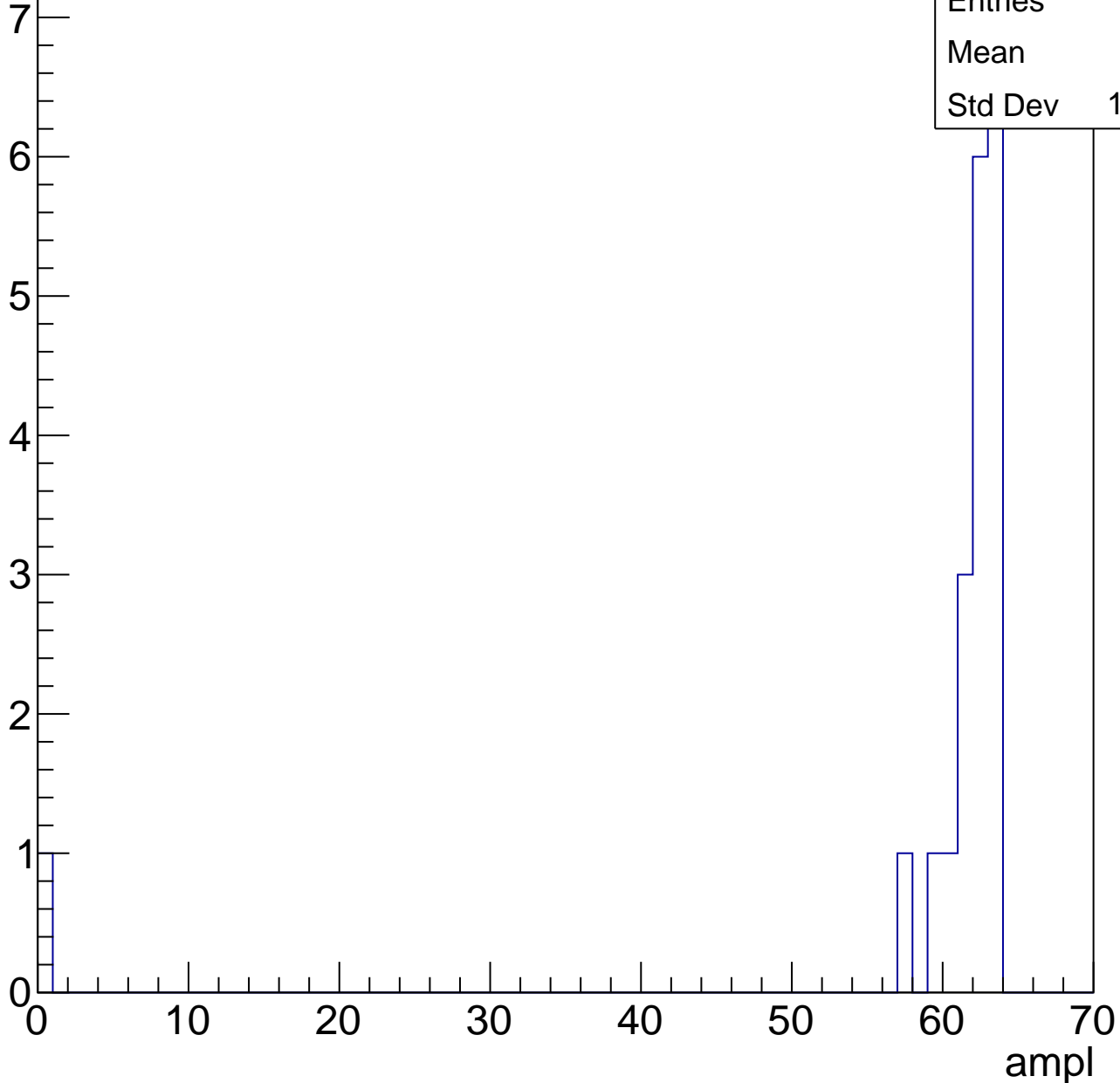


# B1L003S, U22-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	20
Mean	58.6
Std Dev	13.53





# B1L003S, U22-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch89, adc0

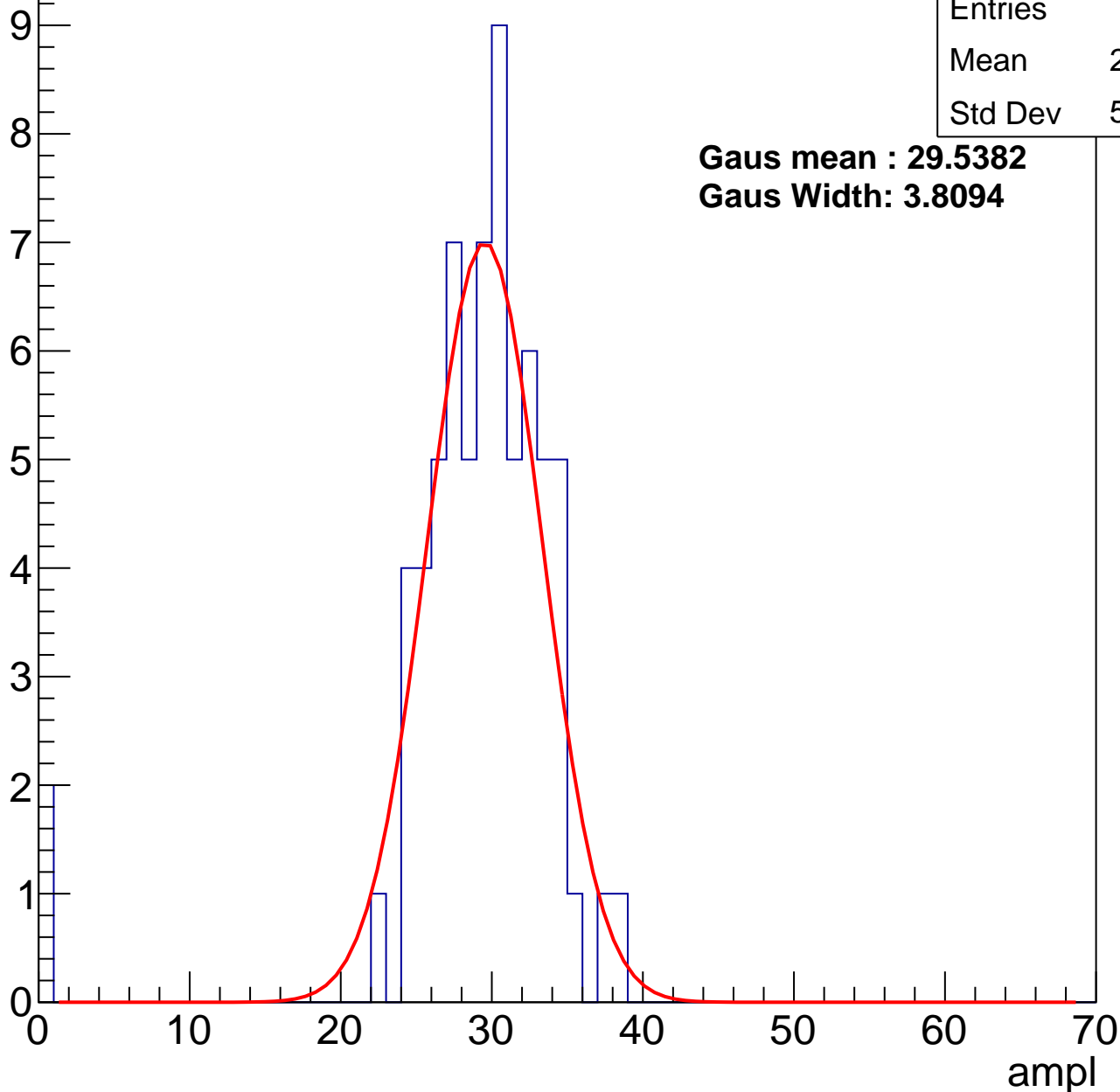
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.56
Std Dev	5.976

**Gaus mean : 29.5382**

**Gaus Width: 3.8094**



# B1L003S, U22-ch89, adc1

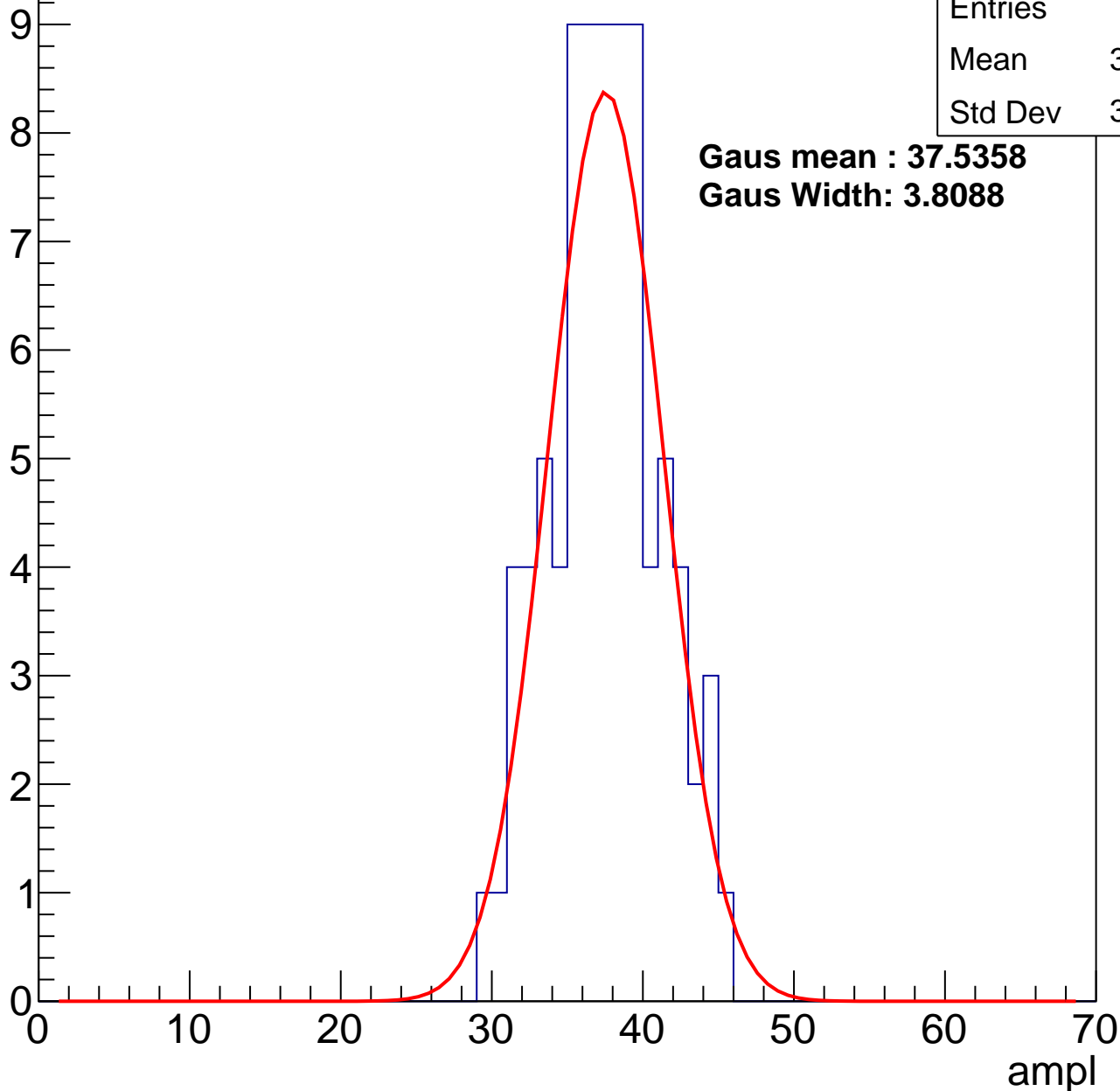
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	37.02
Std Dev	3.577

**Gaus mean : 37.5358**

**Gaus Width: 3.8088**



# B1L003S, U22-ch89, adc2

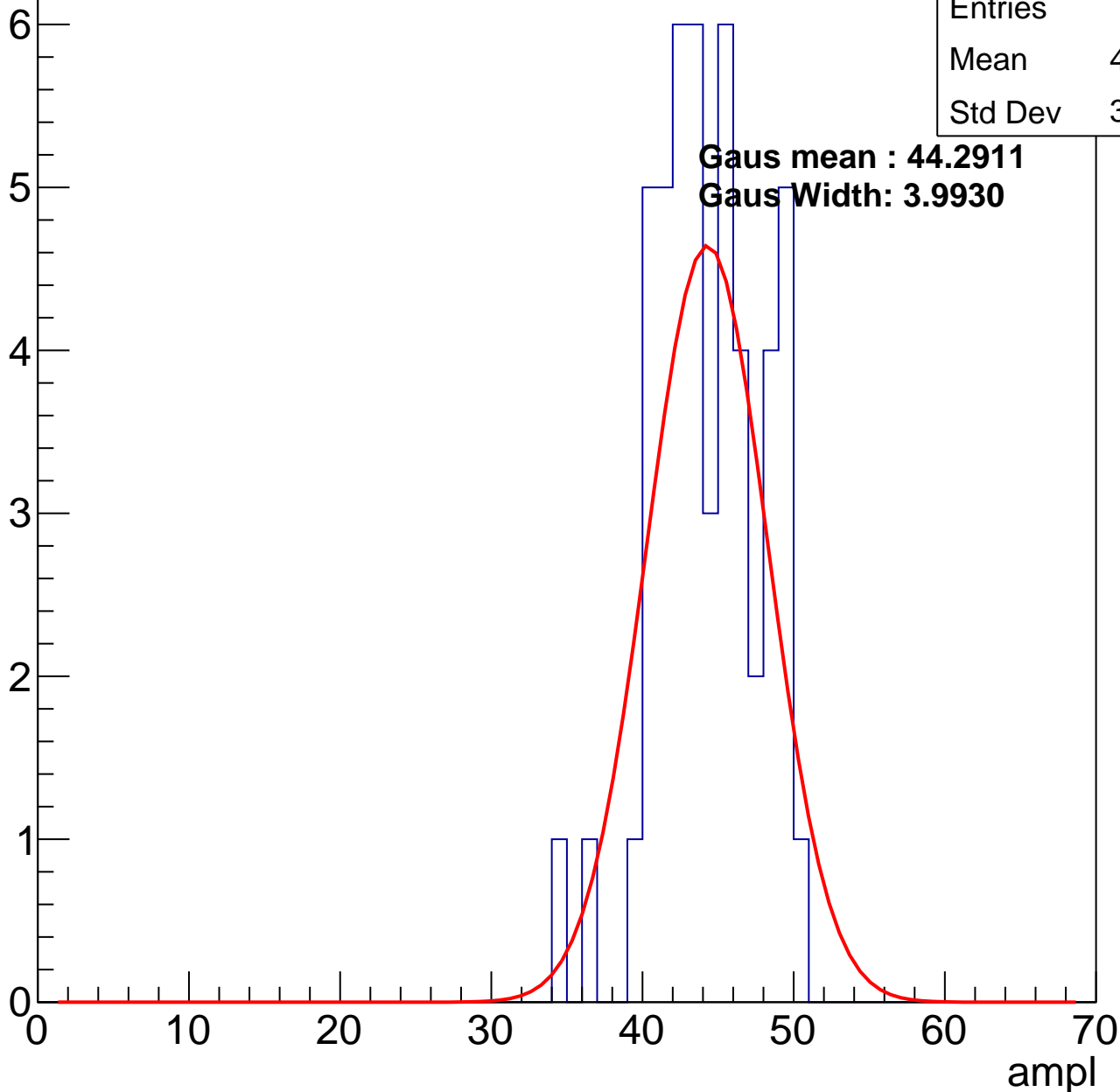
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.82
Std Dev	3.485

**Gaus mean : 44.2911**

**Gaus Width: 3.9930**

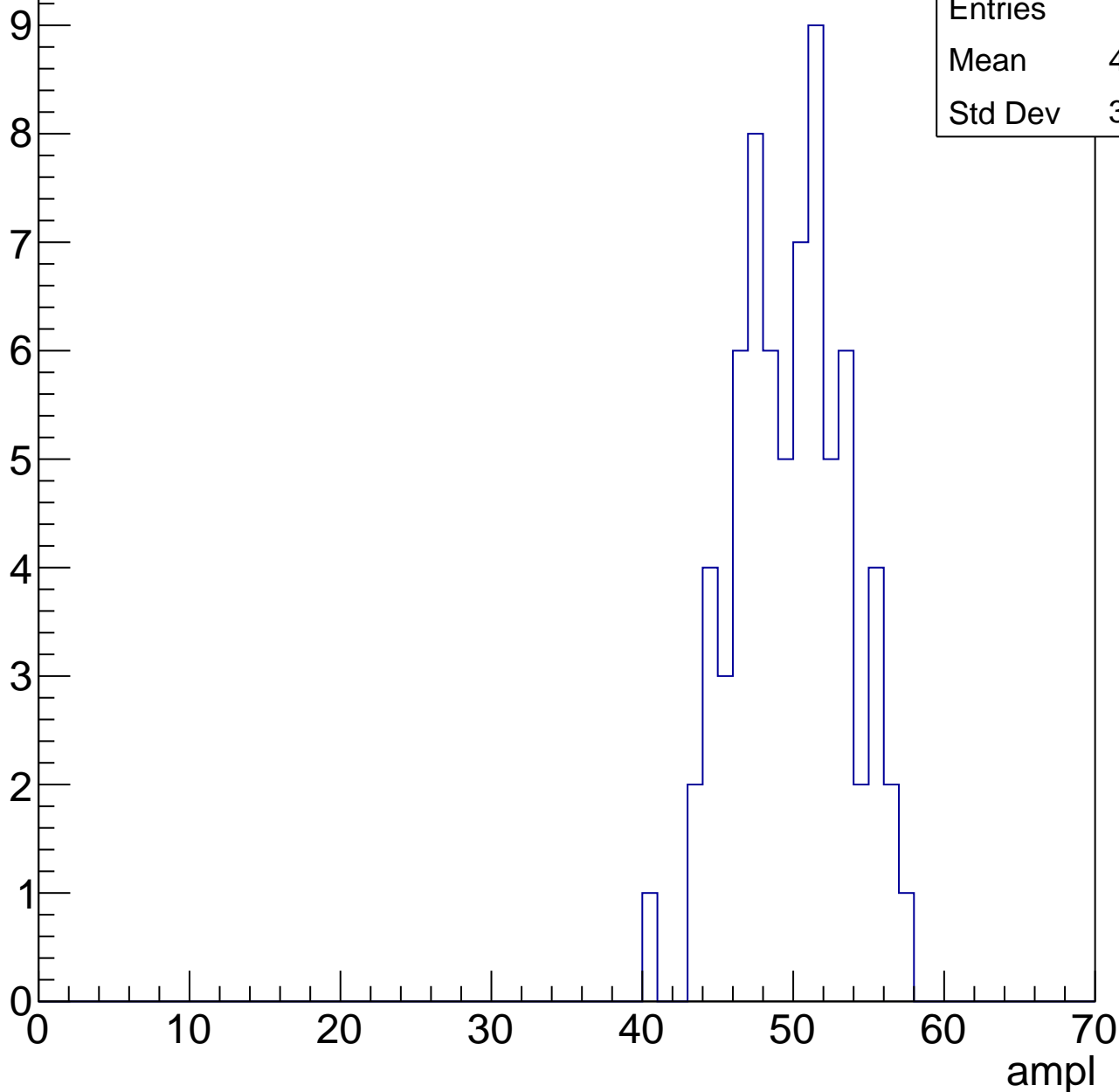


# B1L003S, U22-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	49.38
Std Dev	3.605

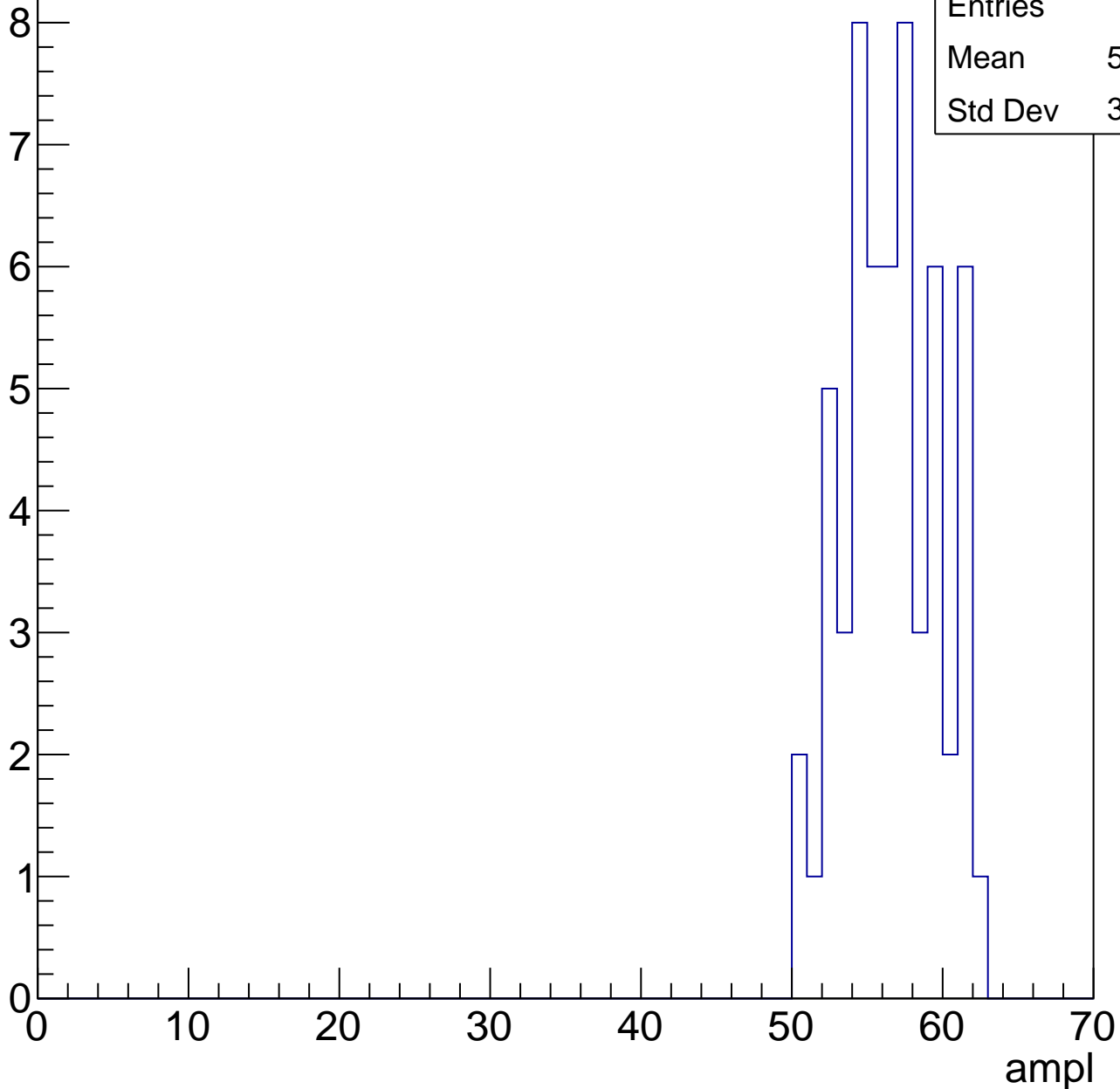


# B1L003S, U22-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.14
Std Dev	3.058



# B1L003S, U22-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

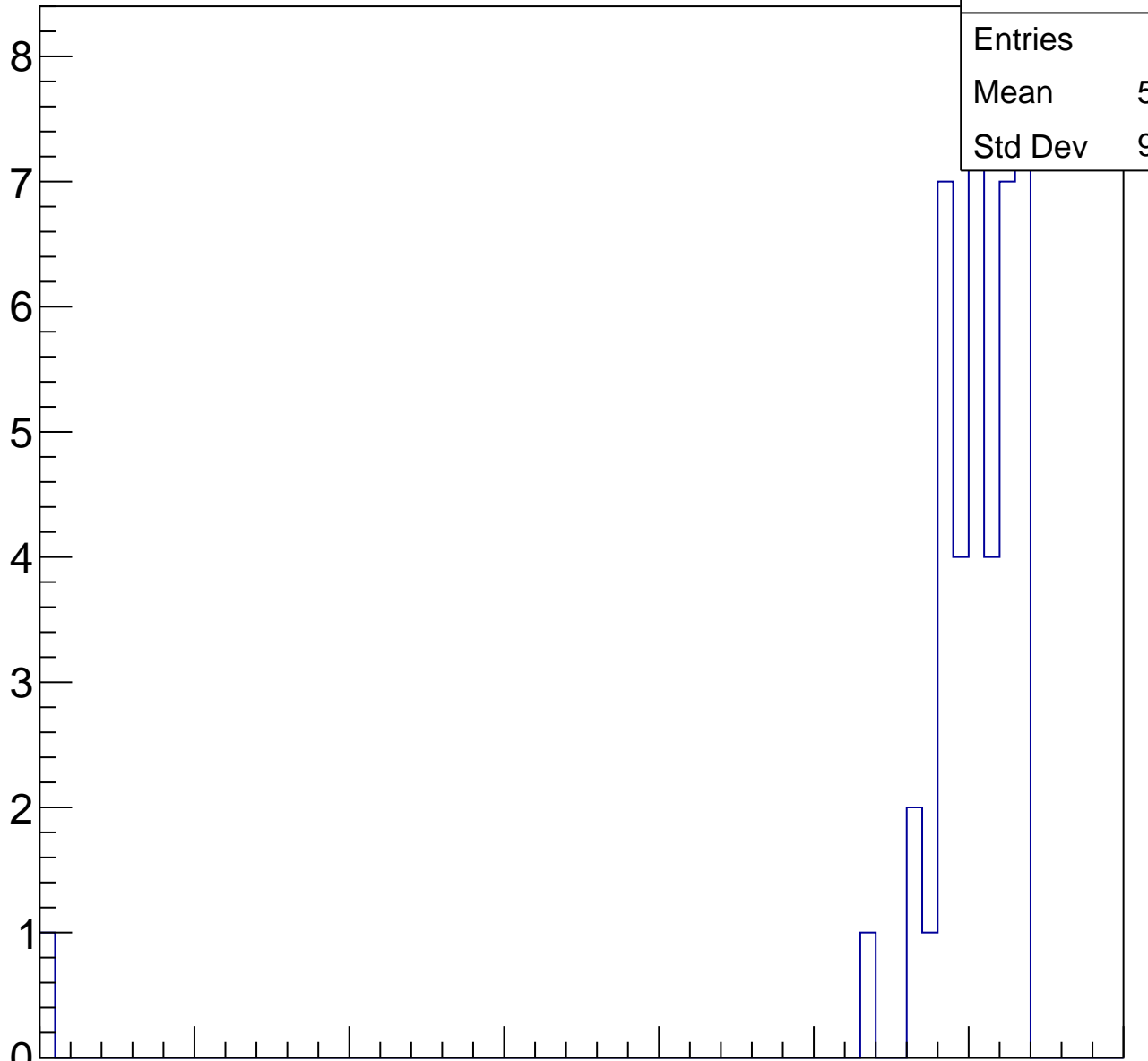
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	58.74
Std Dev	9.349

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L003S, U22-ch90, adc0

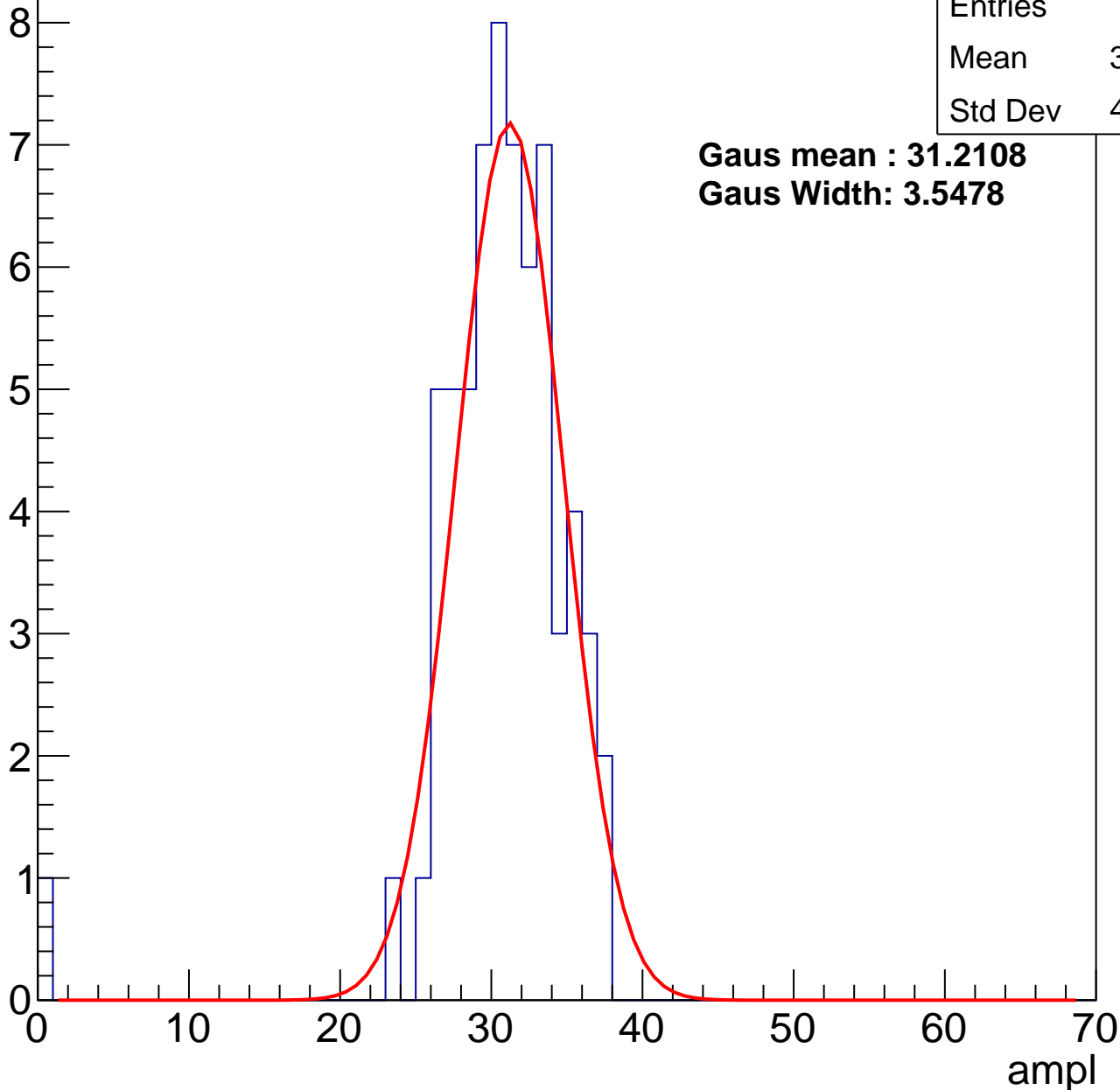
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	30.15
Std Dev	4.919

**Gaus mean : 31.2108**

**Gaus Width: 3.5478**



# B1L003S, U22-ch90, adc1

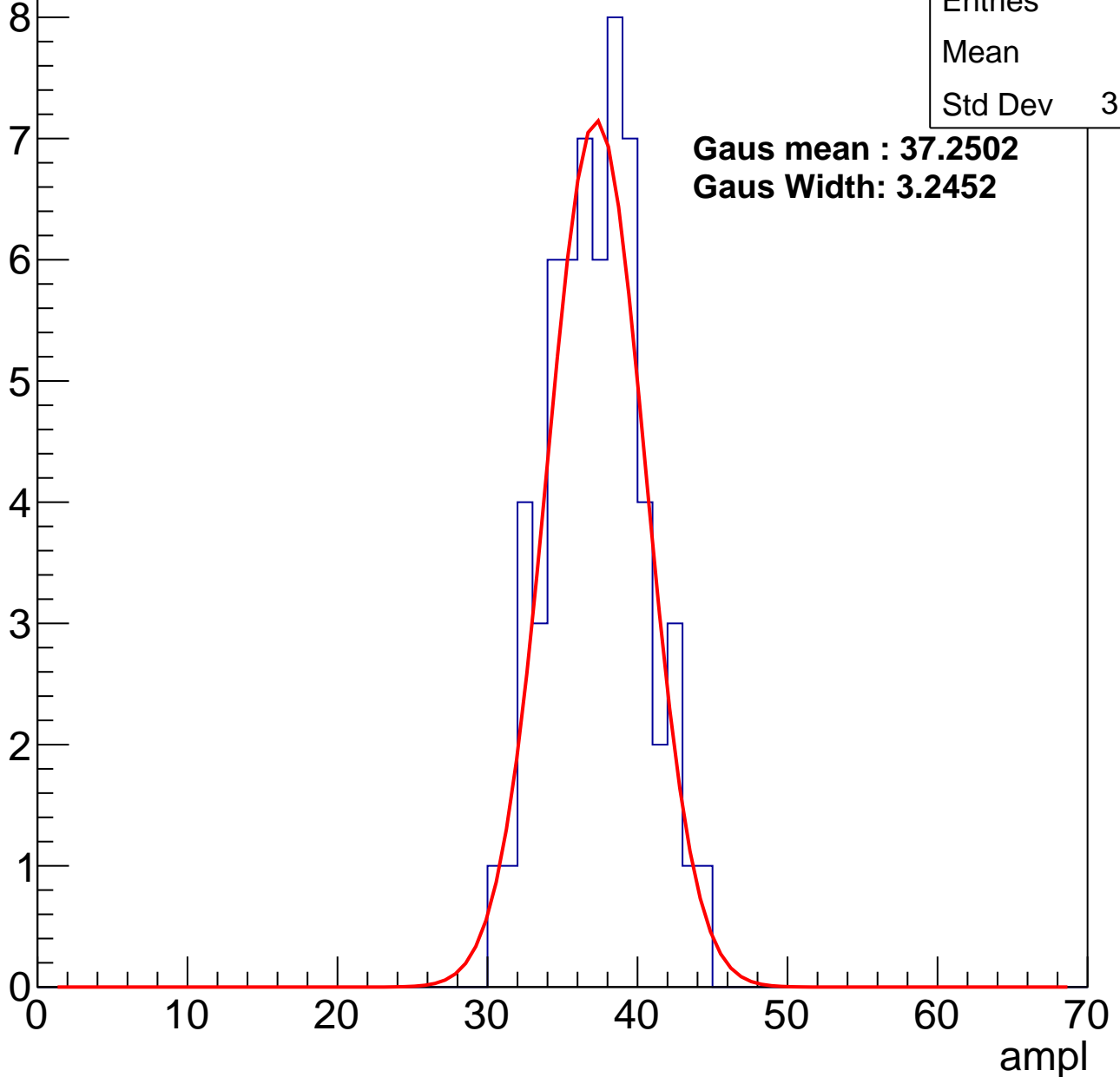
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.8
Std Dev	3.108

**Gaus mean : 37.2502**

**Gaus Width: 3.2452**



# B1L003S, U22-ch90, adc2

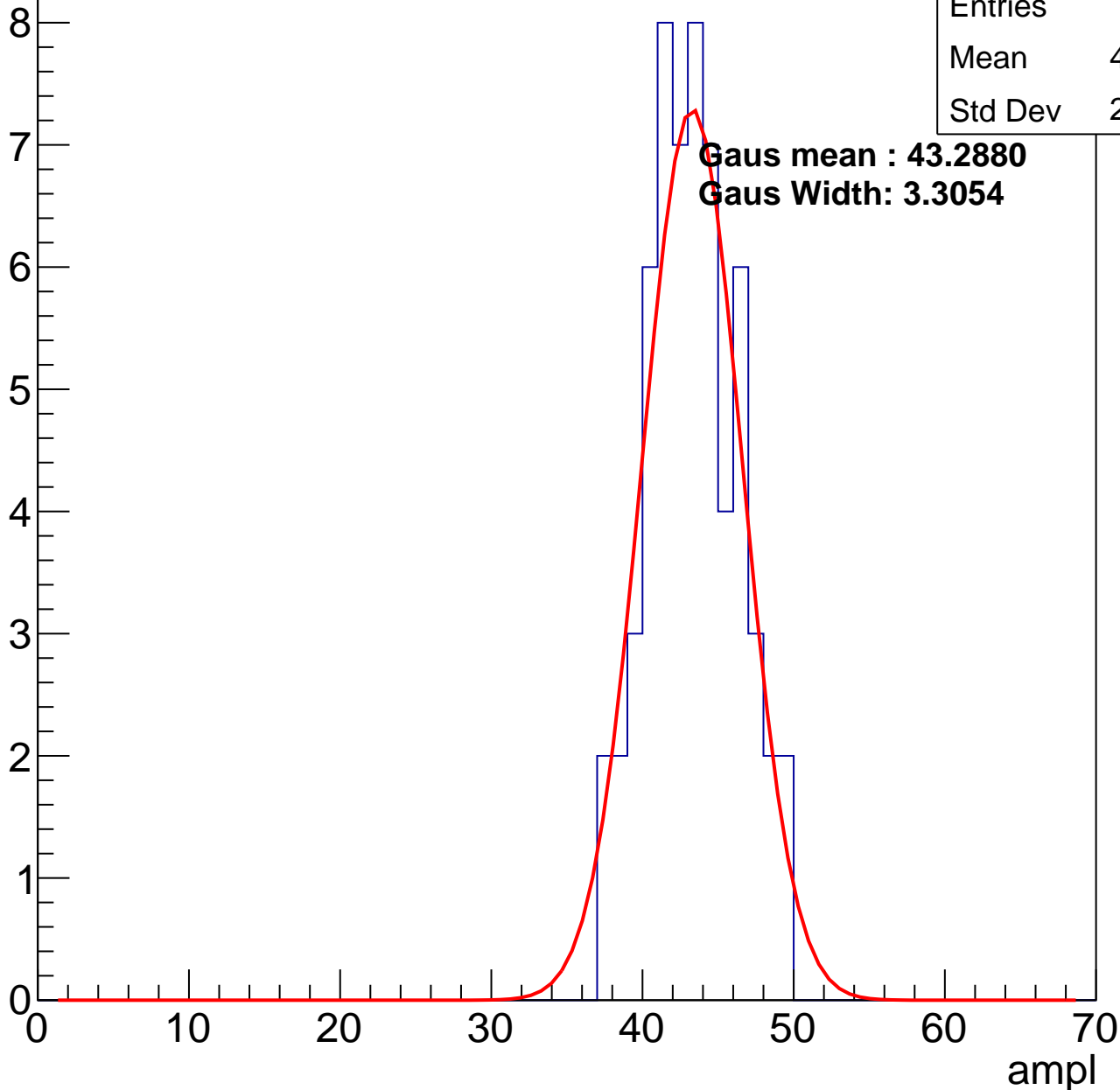
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.87
Std Dev	2.912

**Gaus mean : 43.2880**

**Gaus Width: 3.3054**

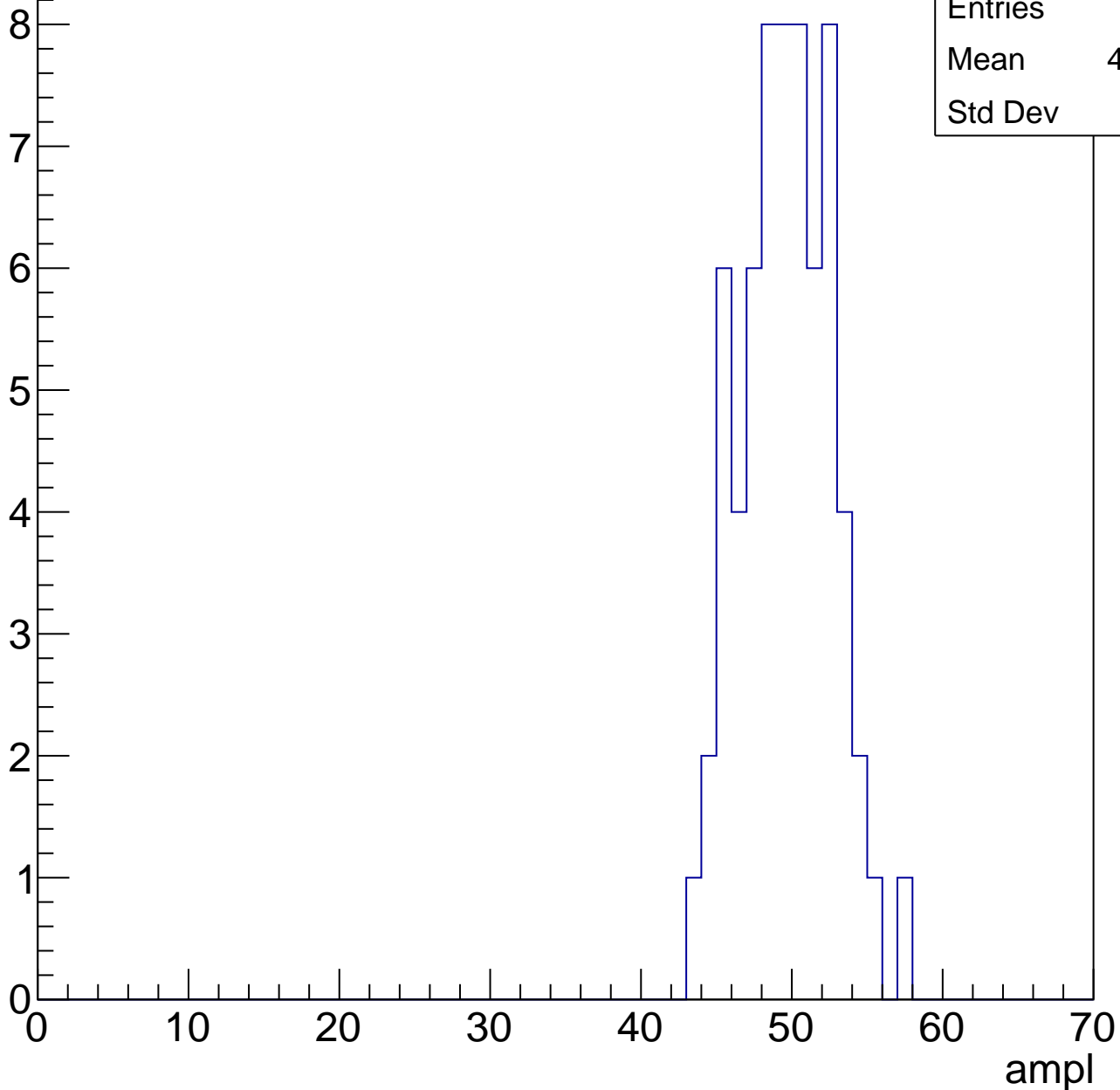


# B1L003S, U22-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.18
Std Dev	2.95

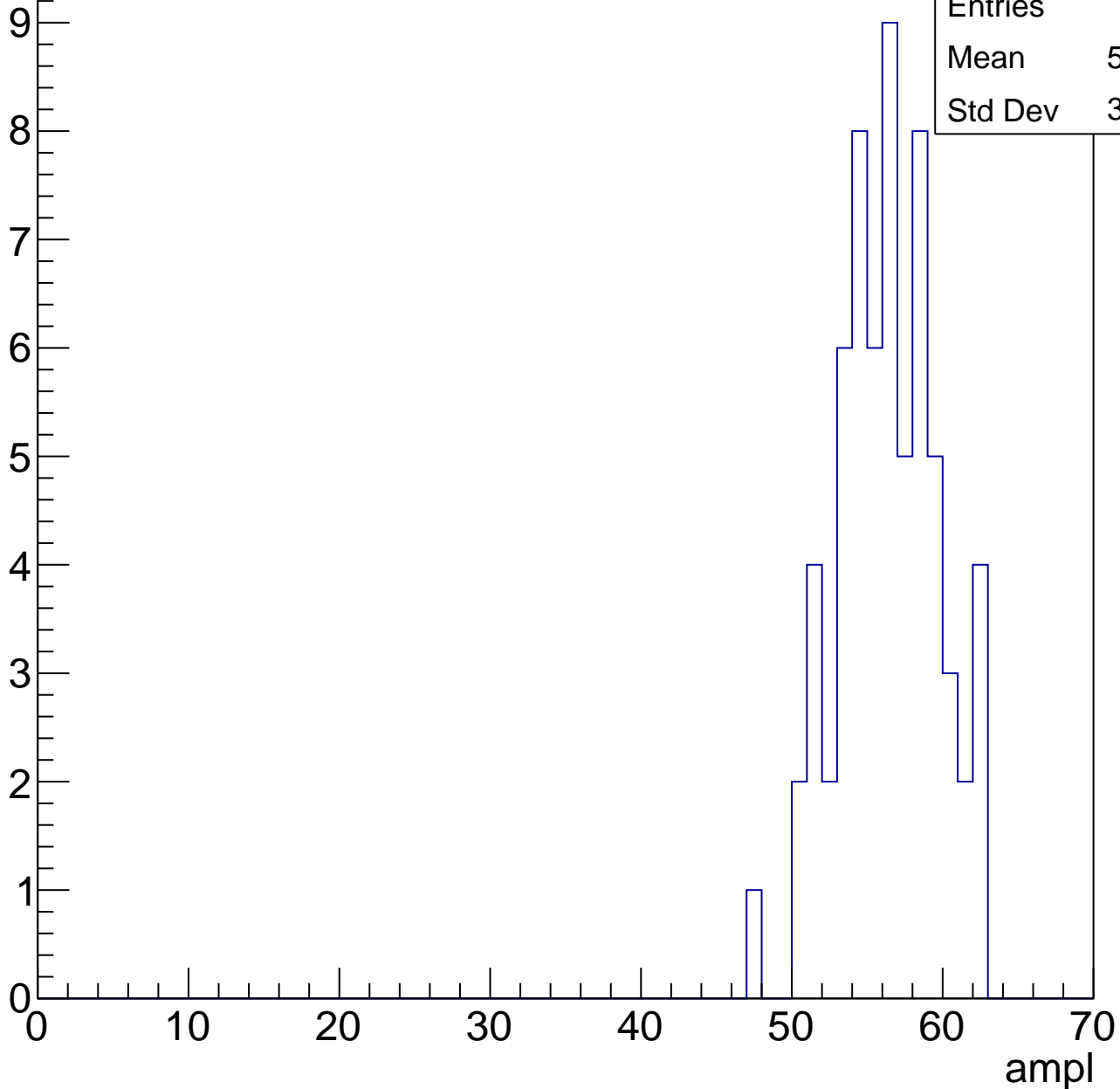


# B1L003S, U22-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.89
Std Dev	3.282

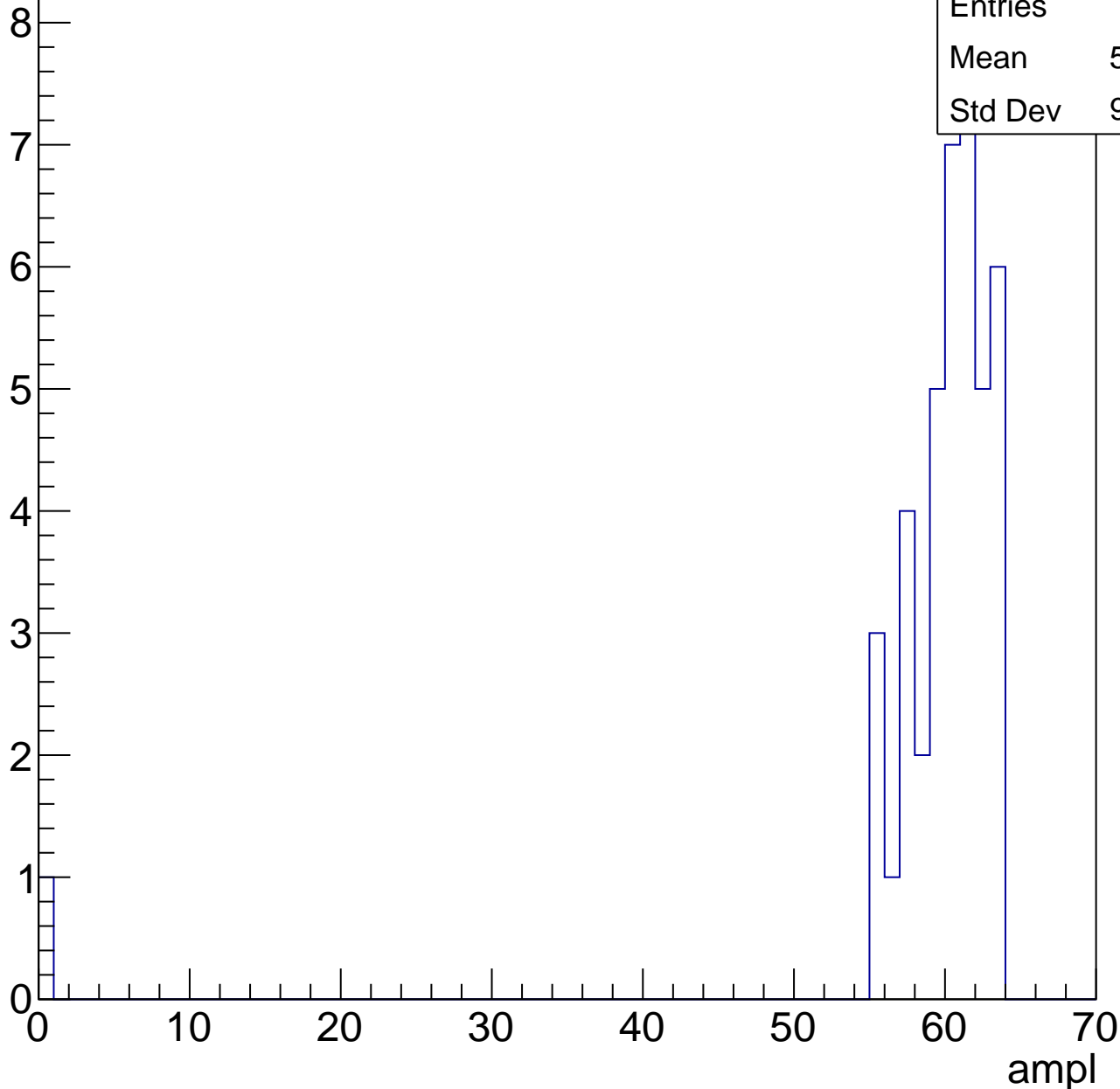


# B1L003S, U22-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

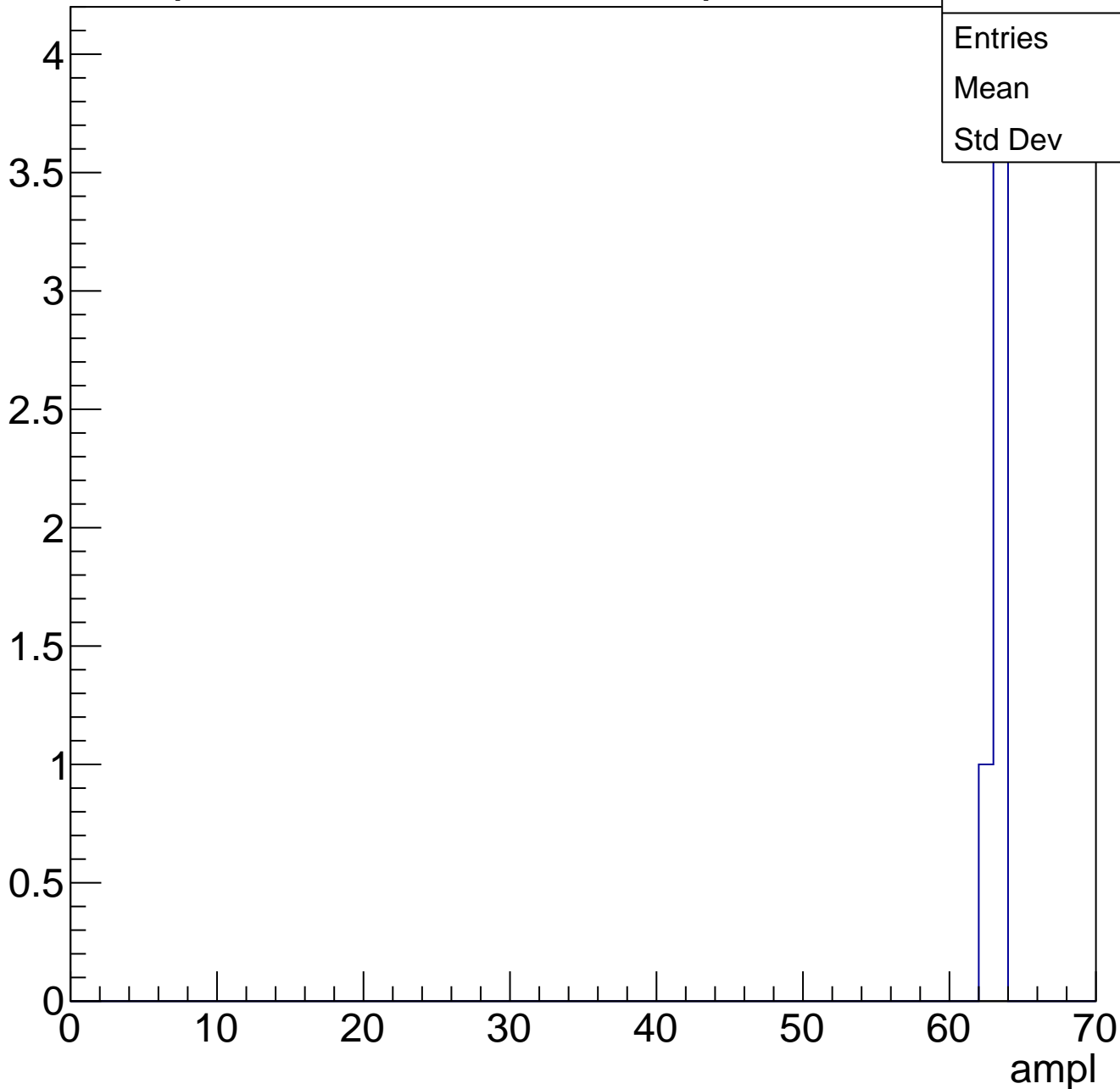
Entries	42
Mean	58.48
Std Dev	9.417



# B1L003S, U22-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

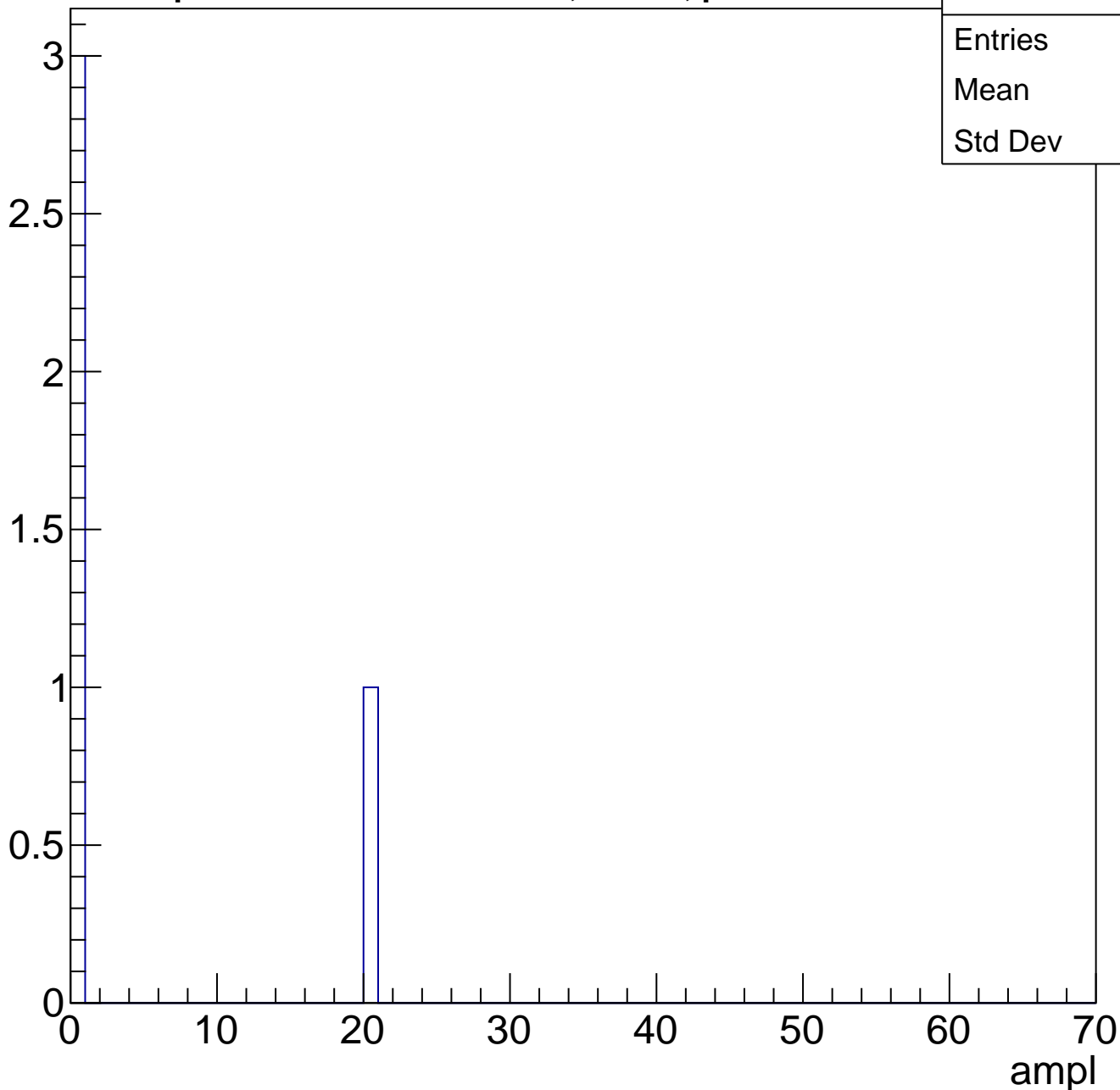




# B1L003S, U22-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	4
Mean	5
Std Dev	8.66

# B1L003S, U22-ch91, adc0

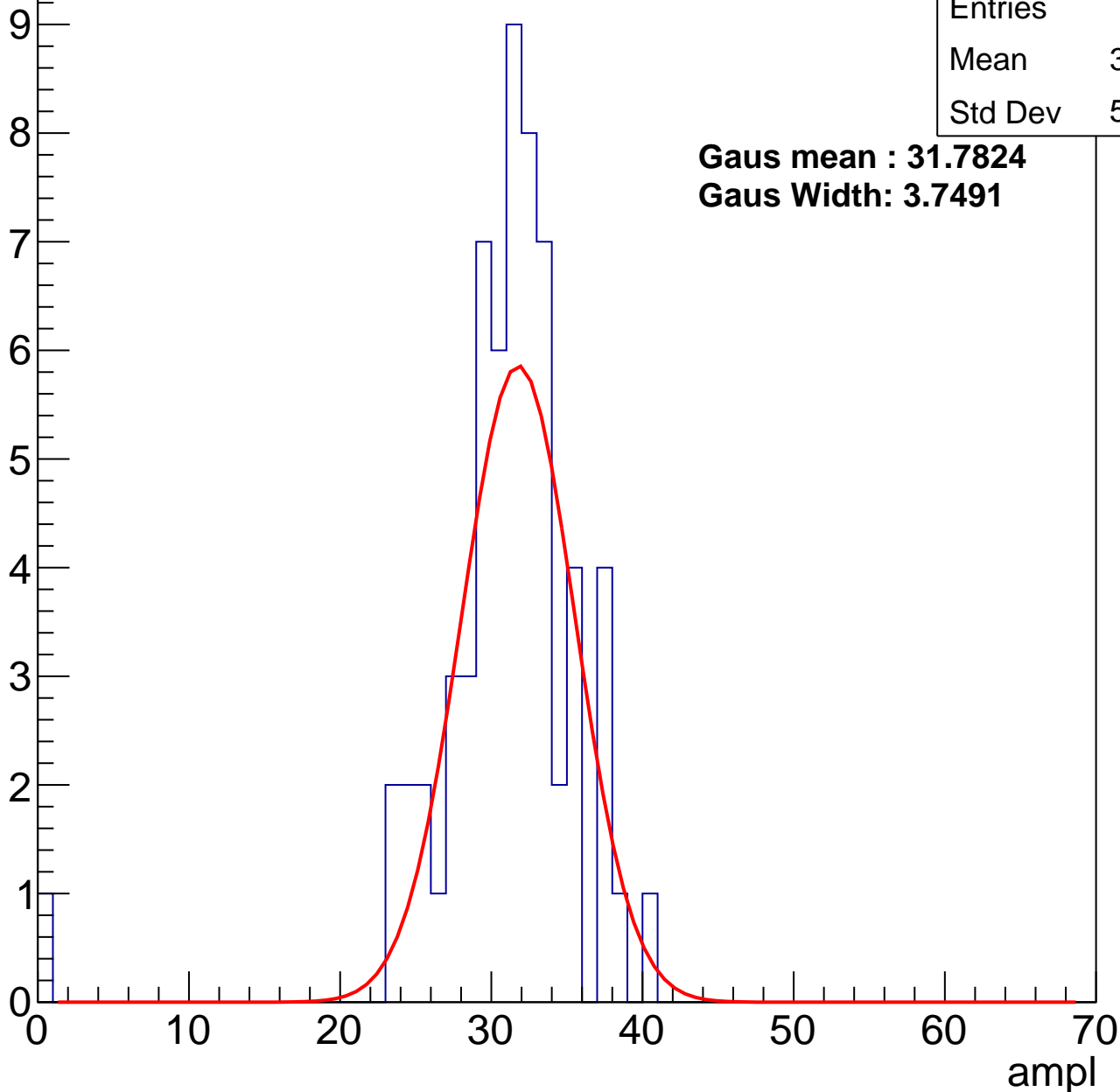
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.44
Std Dev	5.294

**Gaus mean : 31.7824**

**Gaus Width: 3.7491**



# B1L003S, U22-ch91, adc1

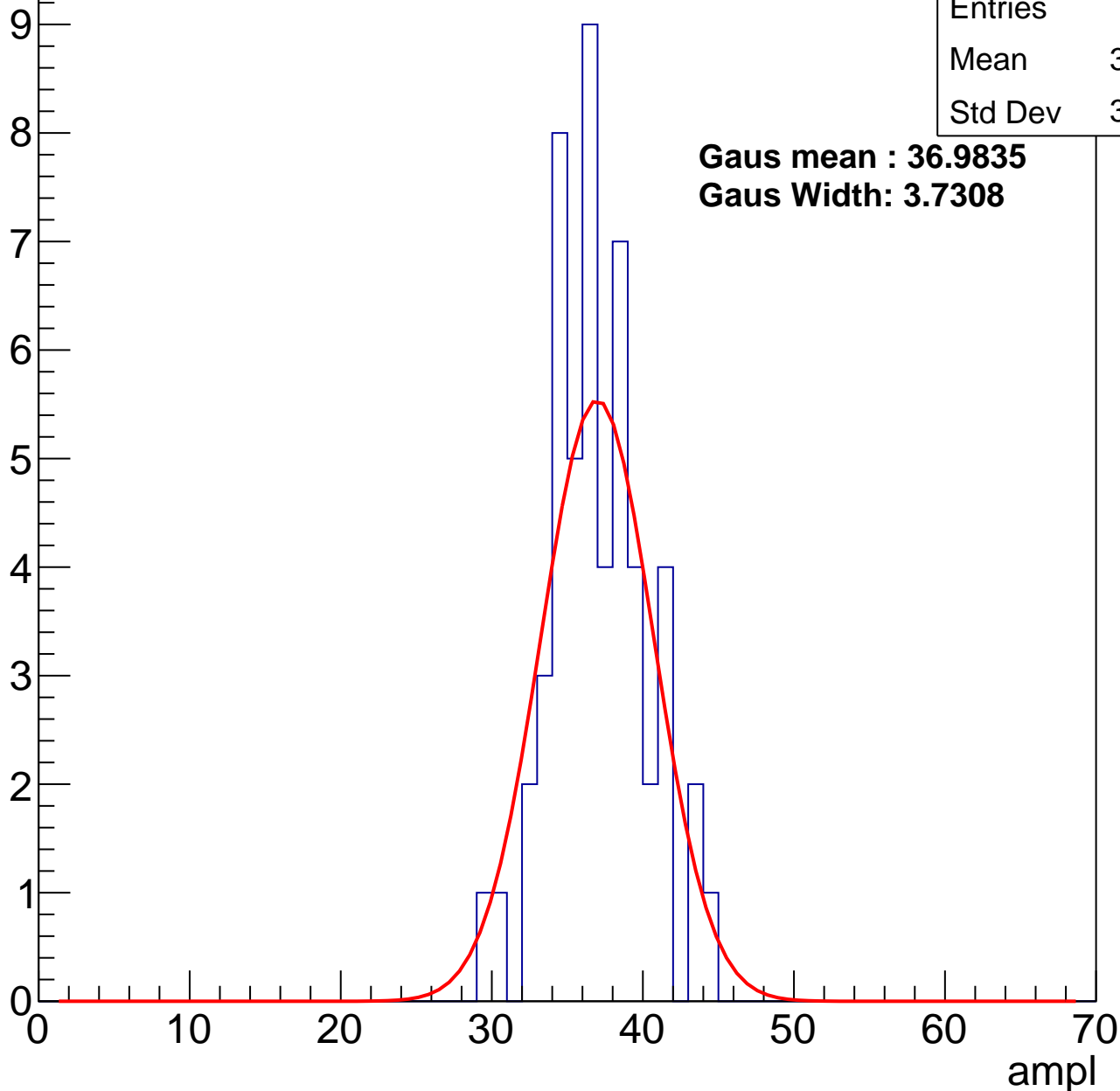
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.55
Std Dev	3.154

**Gaus mean : 36.9835**

**Gaus Width: 3.7308**



# B1L003S, U22-ch91, adc2

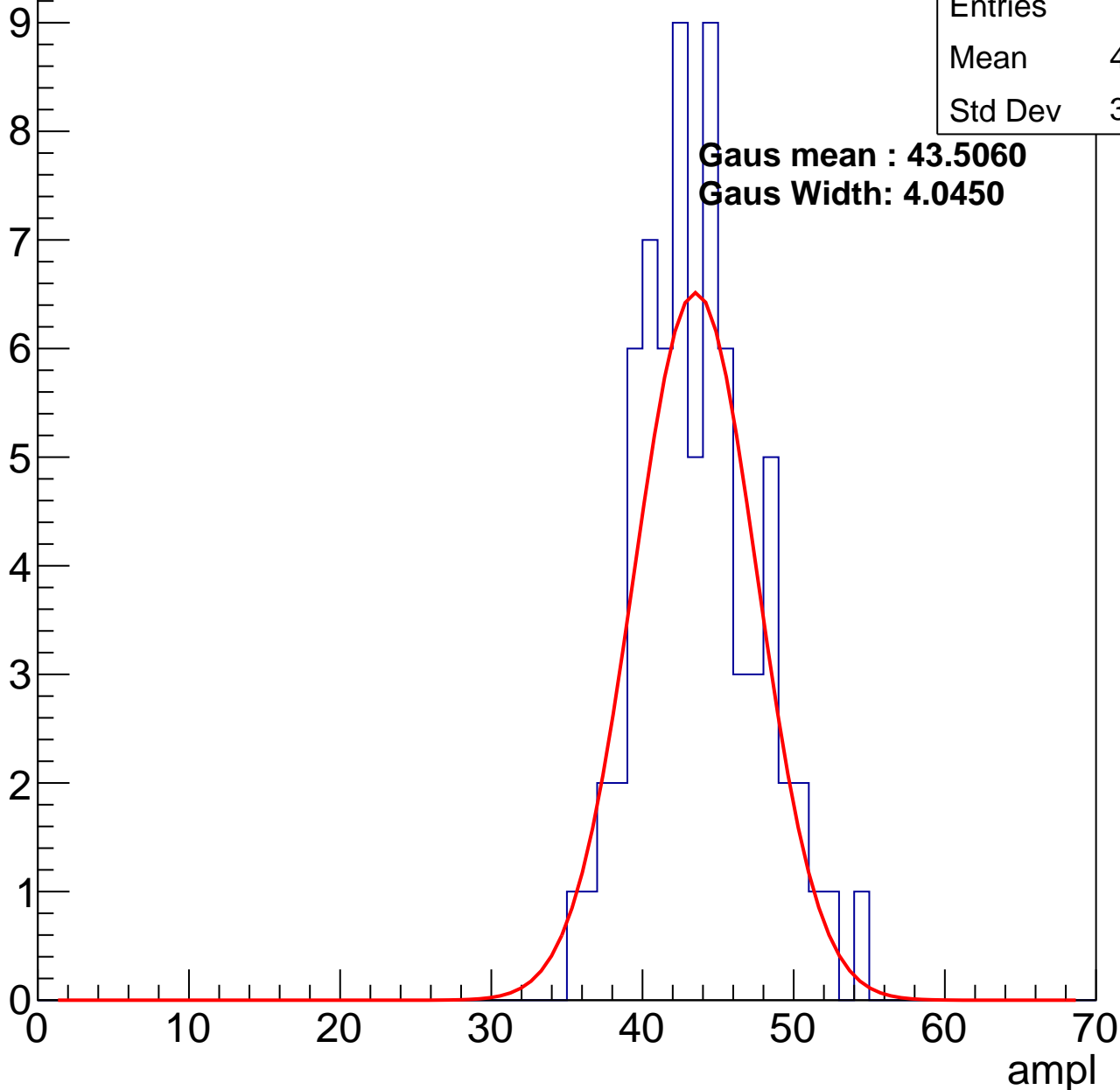
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	43.25
Std Dev	3.894

**Gaus mean : 43.5060**

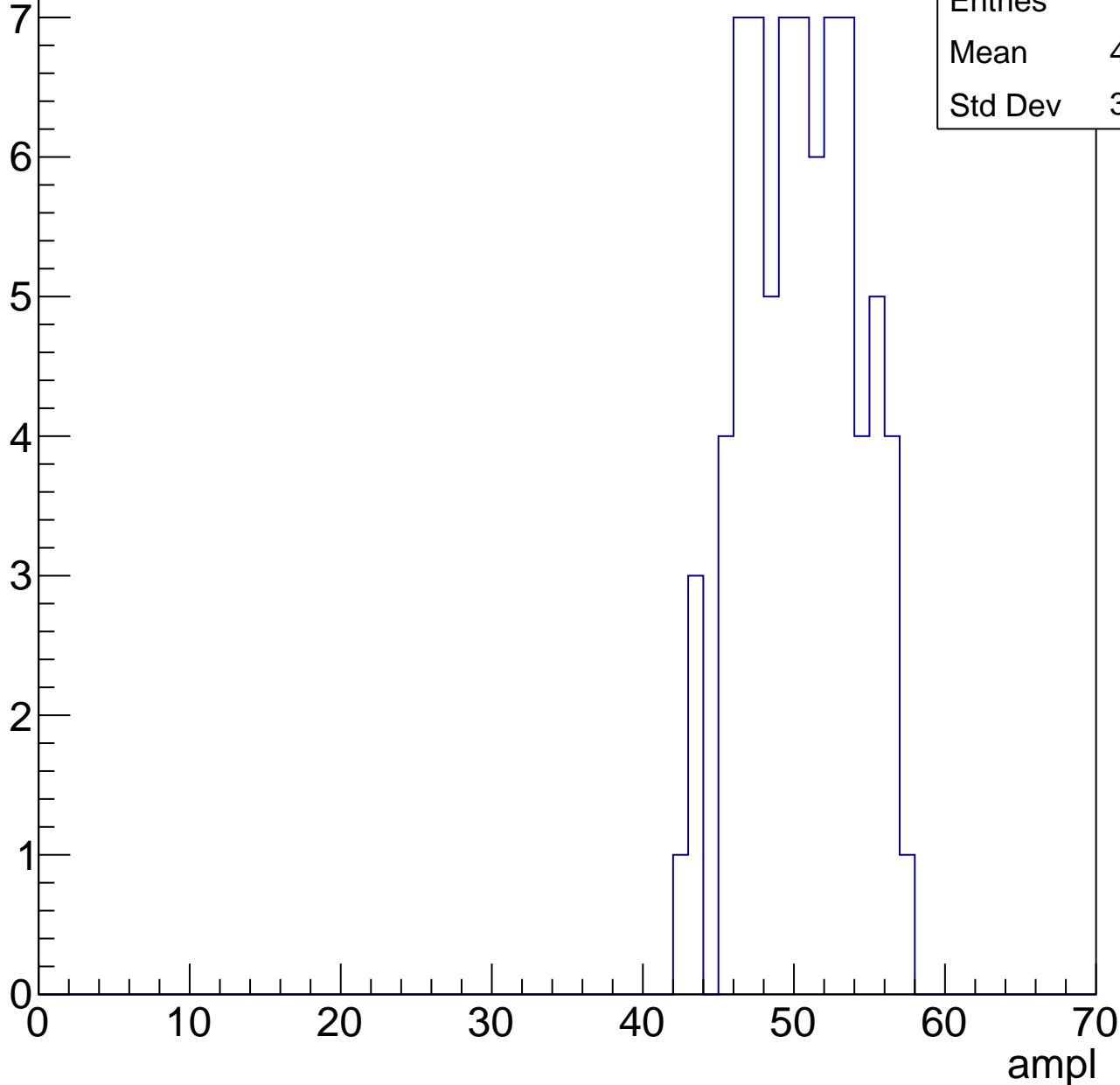
**Gaus Width: 4.0450**



# B1L003S, U22-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



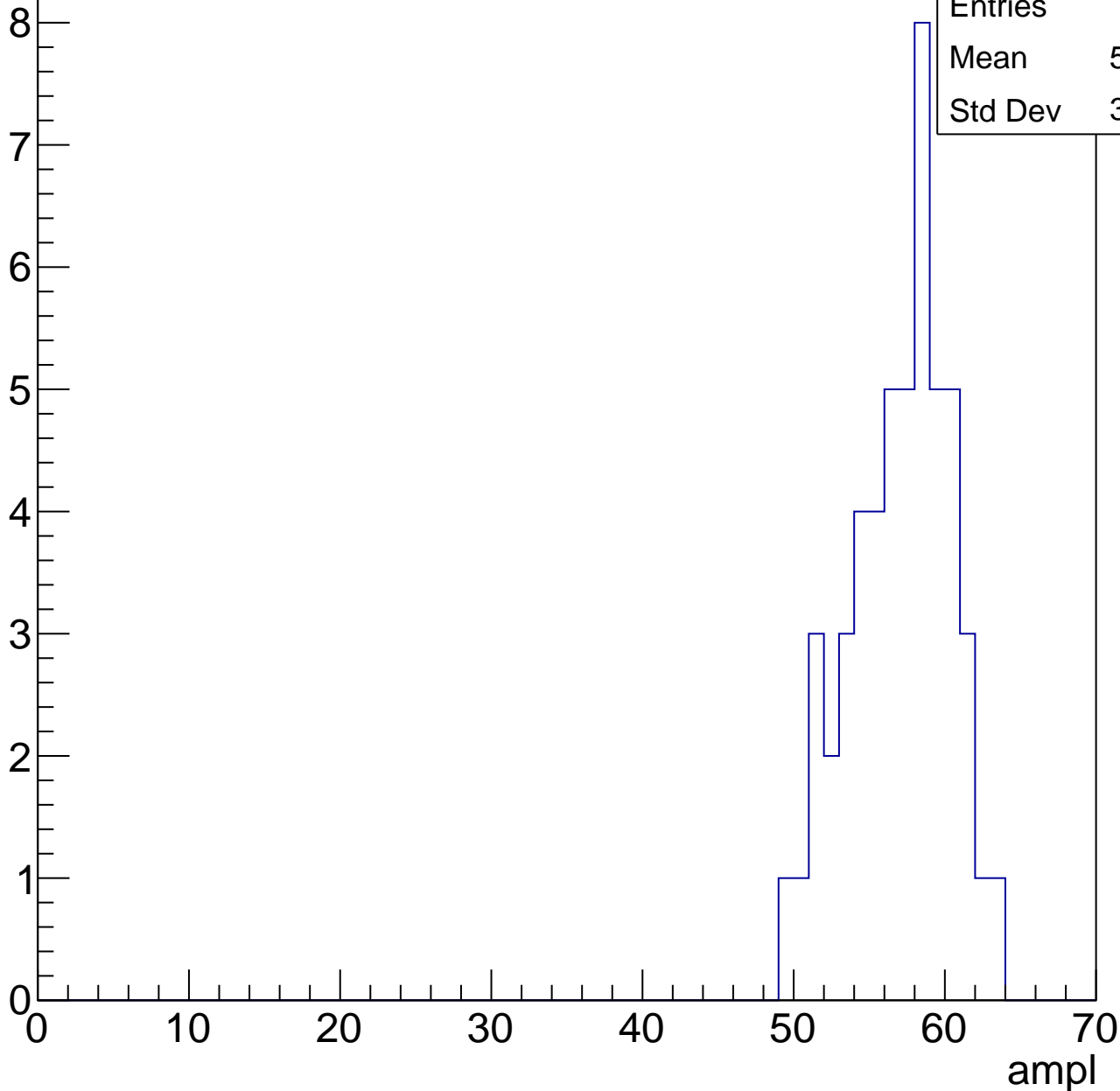
Entries	75
Mean	49.97
Std Dev	3.637

# B1L003S, U22-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

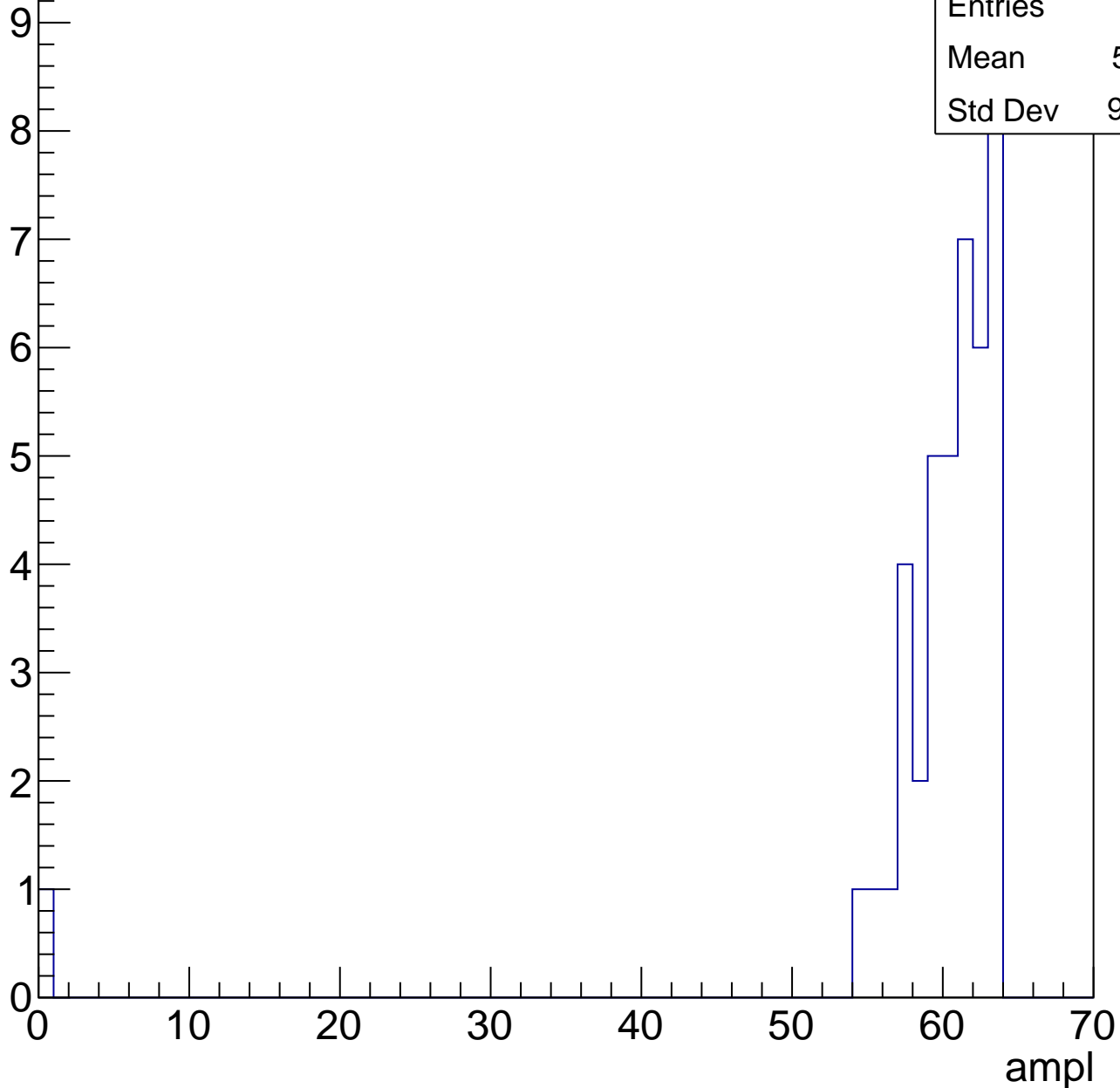
Entries	51
Mean	56.53
Std Dev	3.274



# B1L003S, U22-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch92, adc0

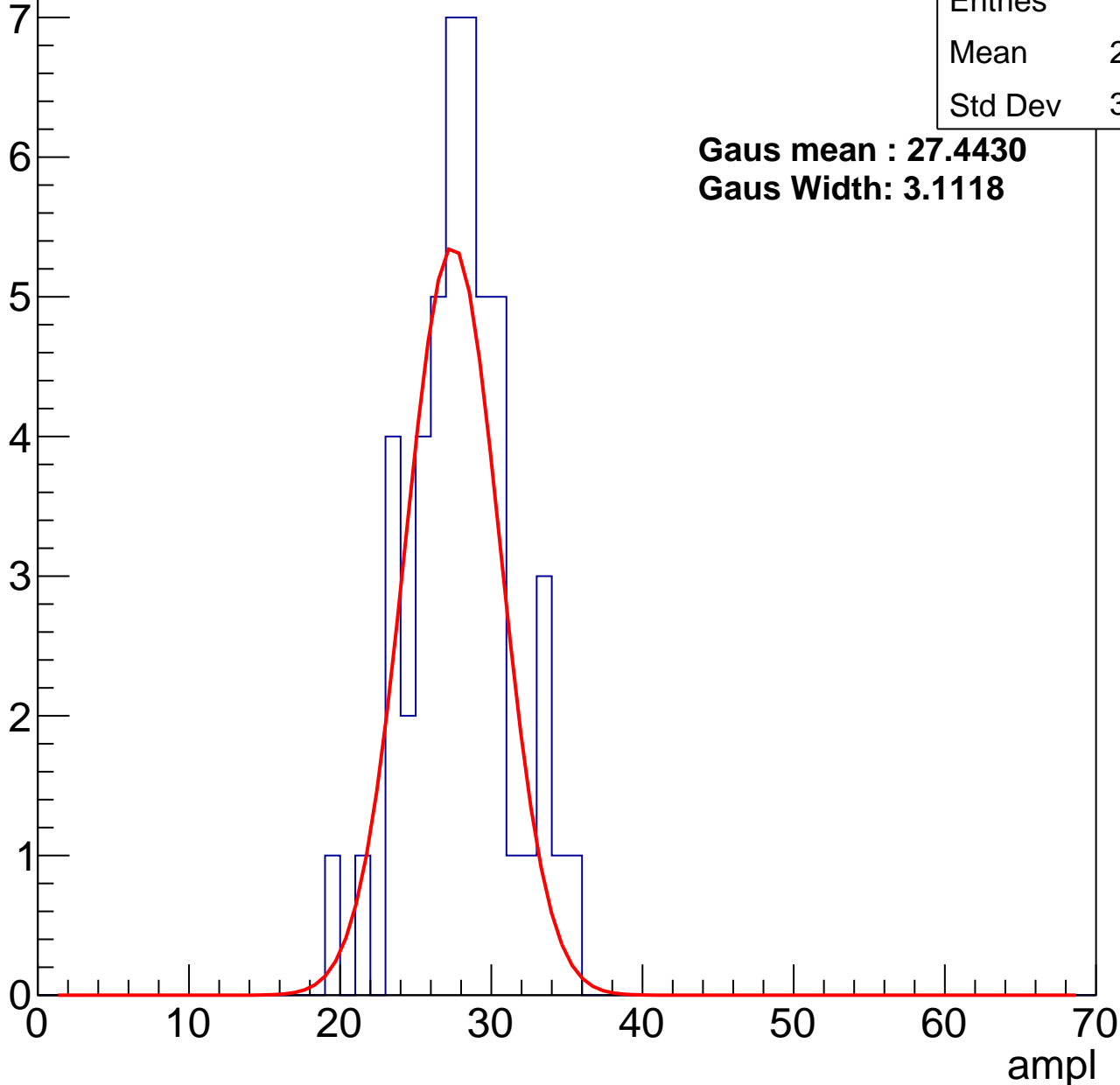
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	27.52
Std Dev	3.304

**Gaus mean : 27.4430**

**Gaus Width: 3.1118**



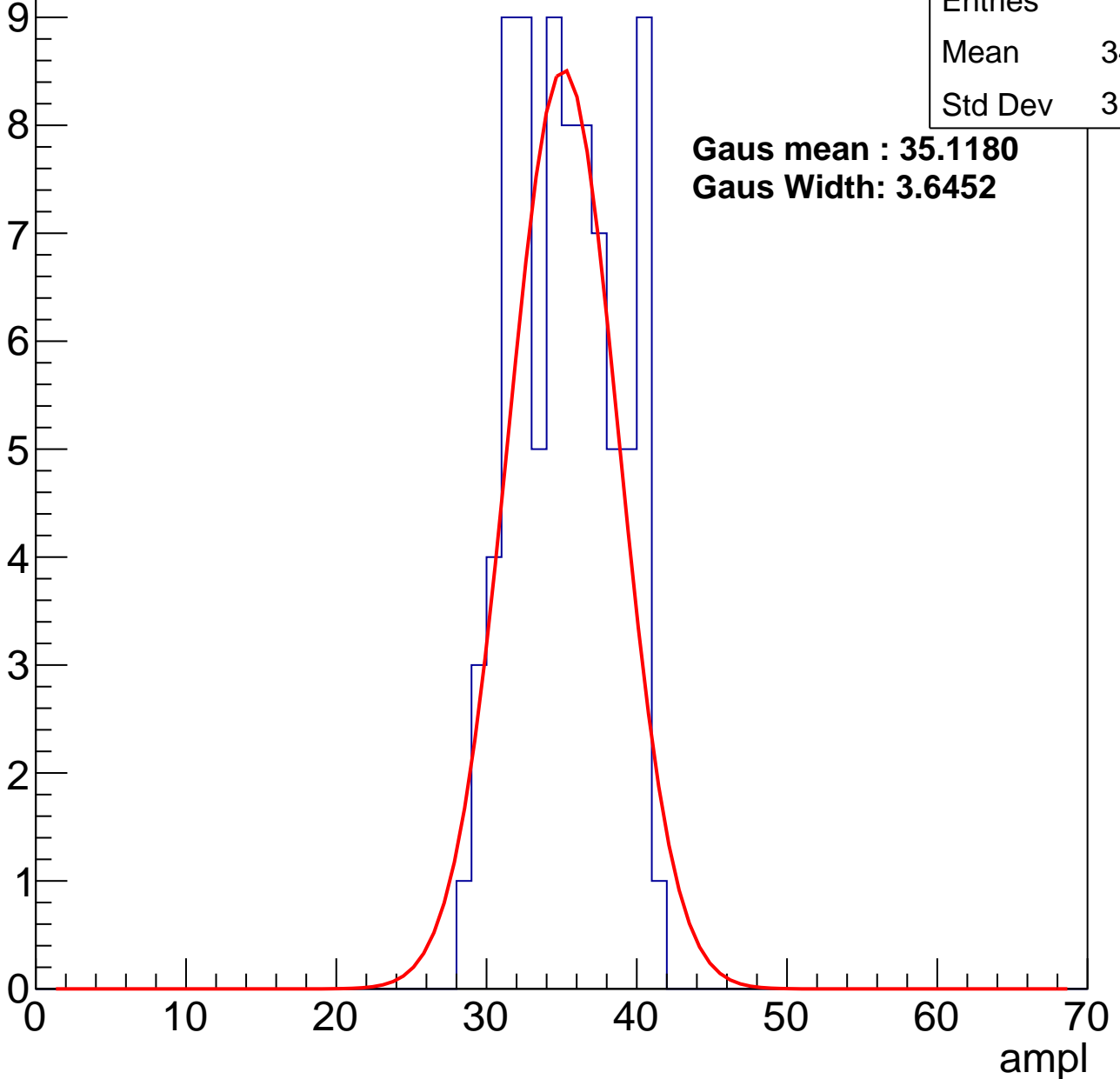
# B1L003S, U22-ch92, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	34.77
Std Dev	3.345

**Gaus mean : 35.1180**  
**Gaus Width: 3.6452**



# B1L003S, U22-ch92, adc2

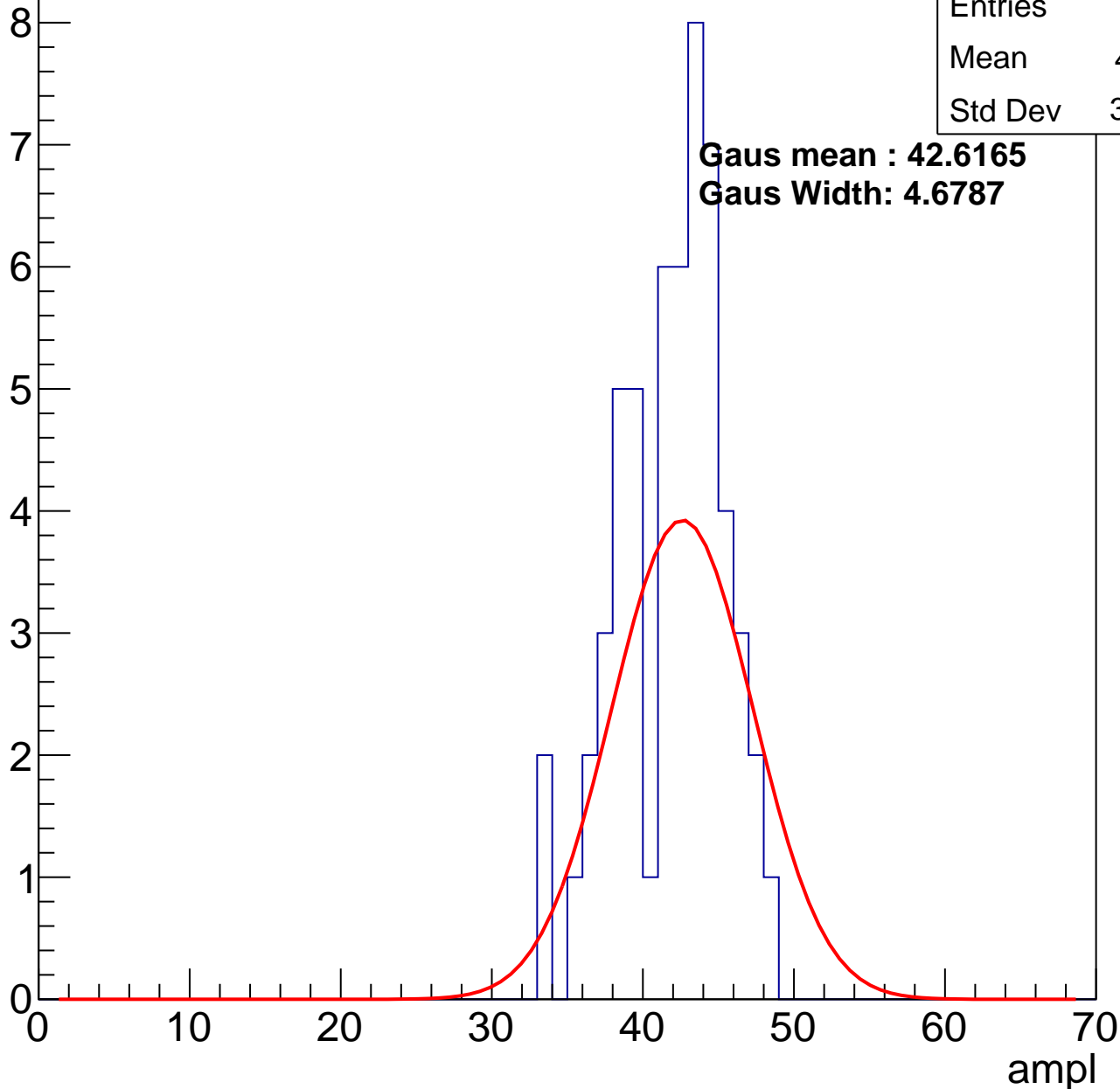
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.41
Std Dev	3.484

**Gaus mean : 42.6165**

**Gaus Width: 4.6787**

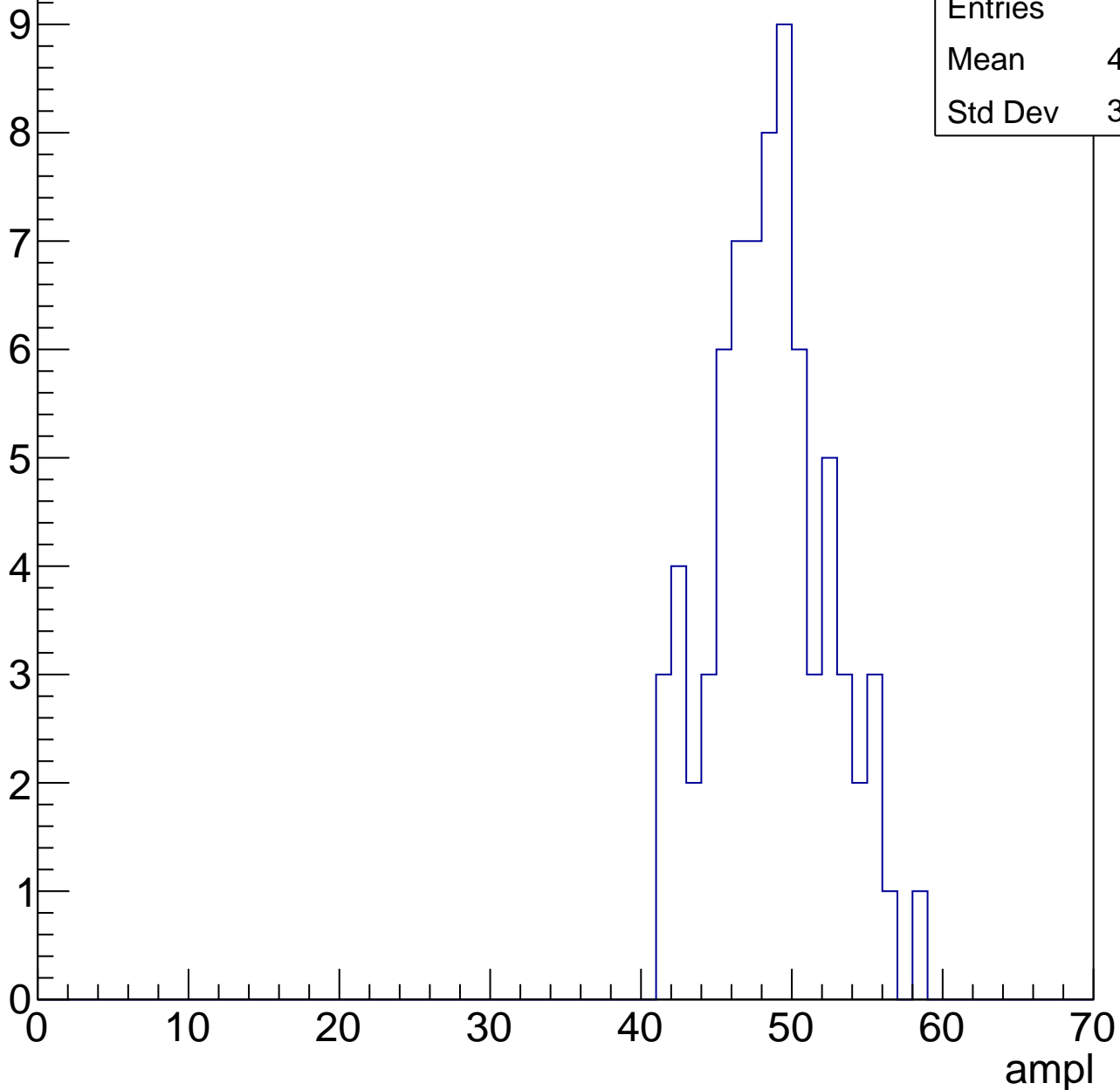


# B1L003S, U22-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	48.14
Std Dev	3.837

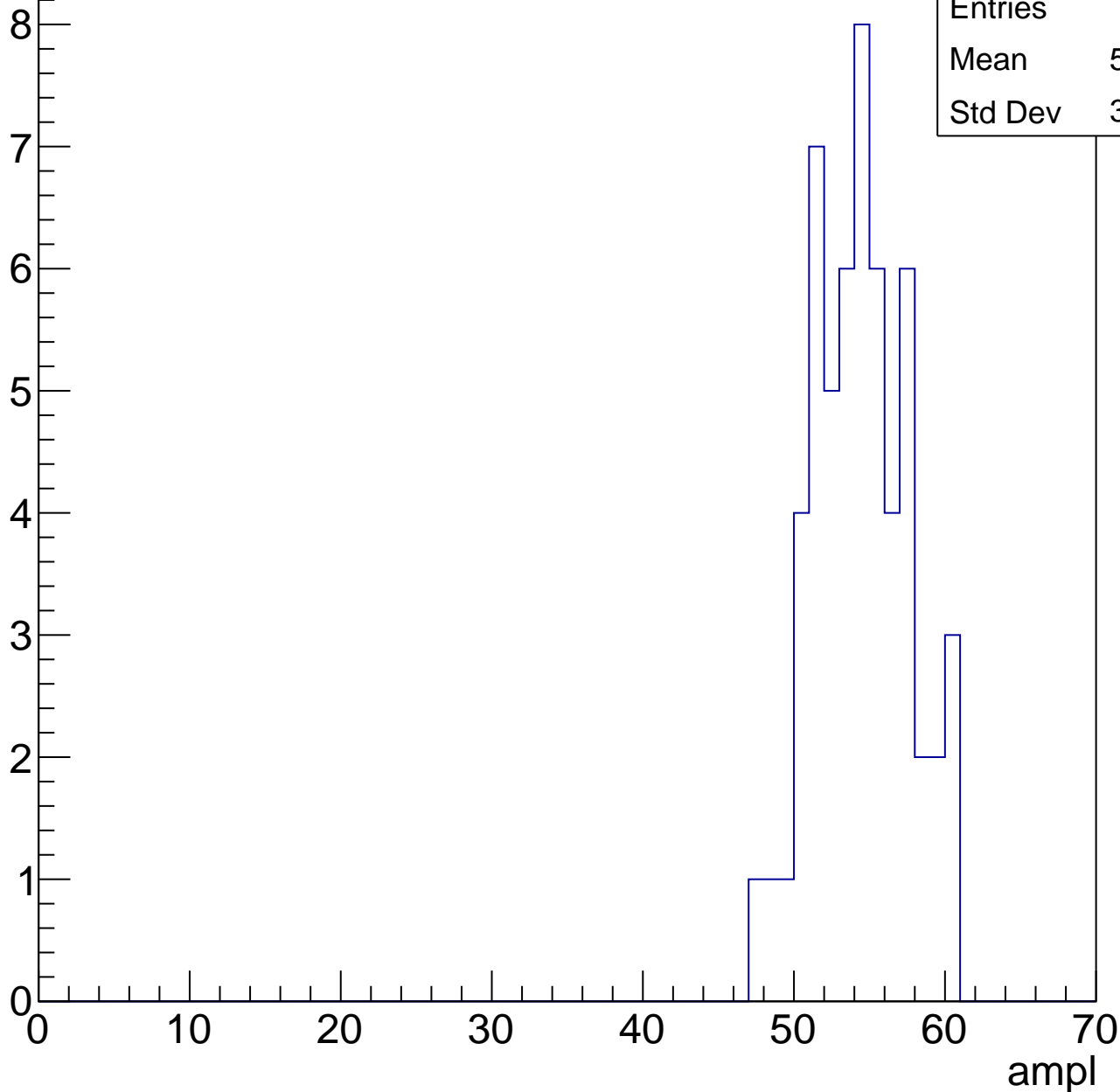


# B1L003S, U22-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

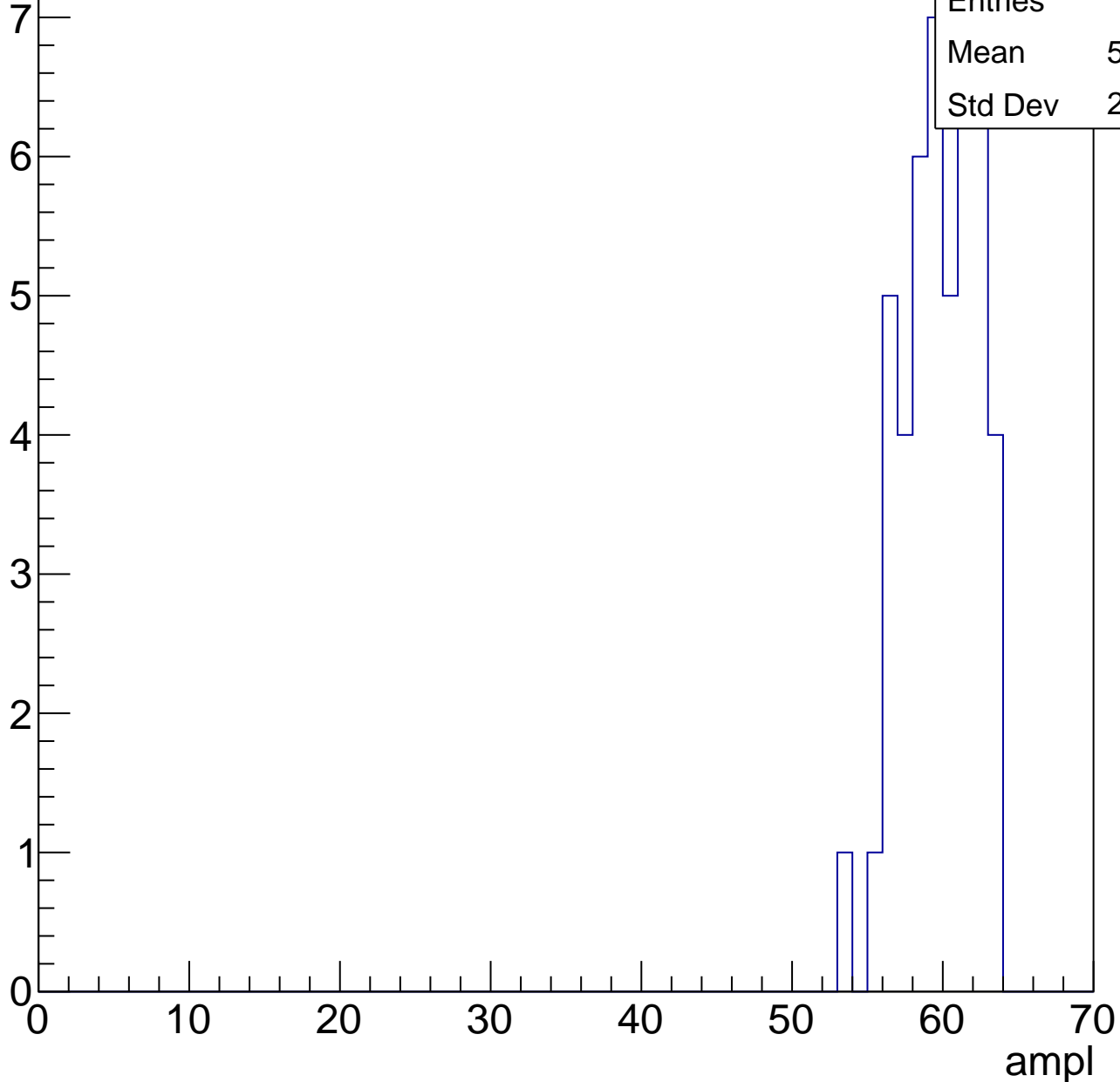
Entries	56
Mean	53.95
Std Dev	3.073



# B1L003S, U22-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

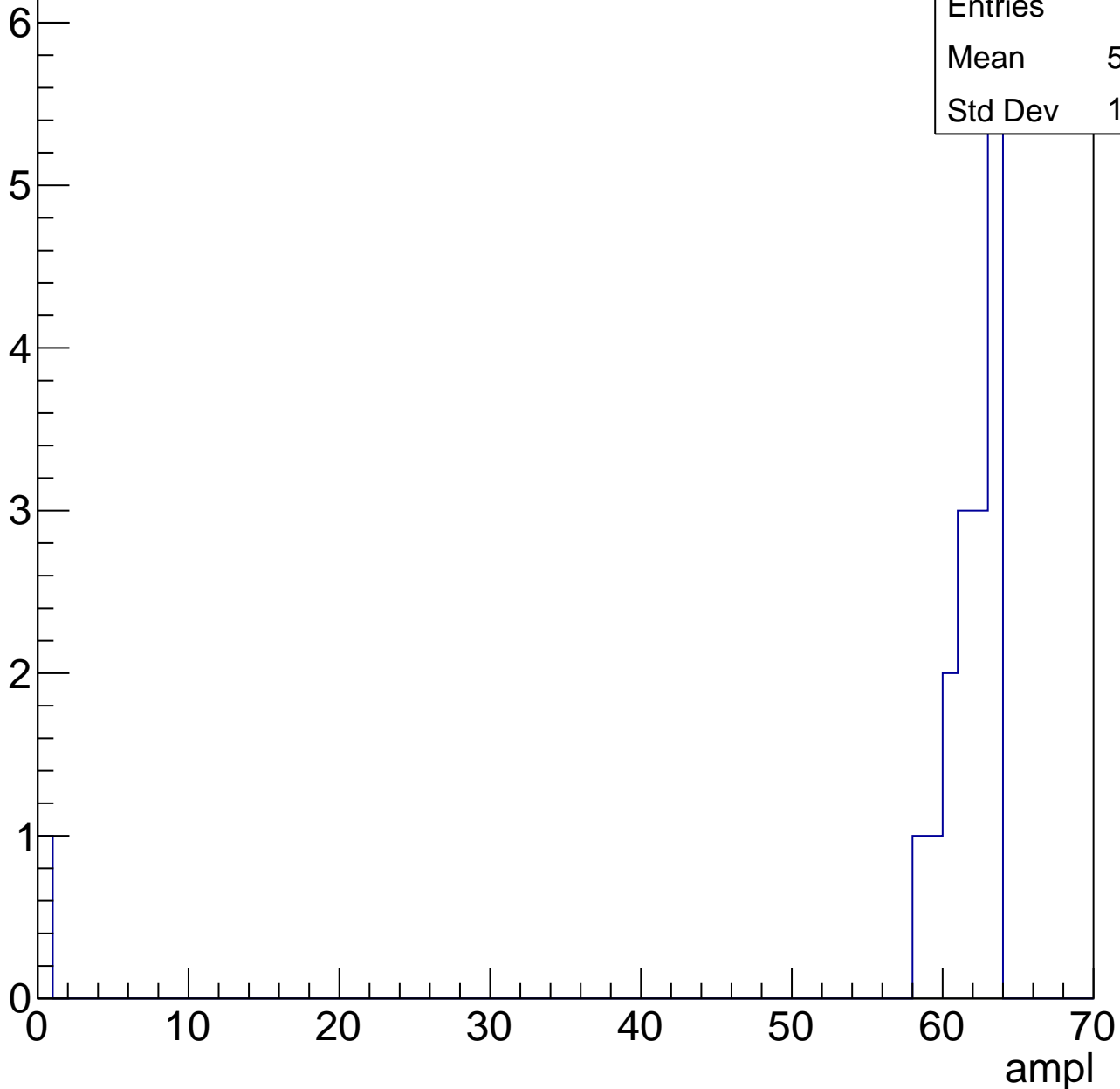


# B1L003S, U22-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	17
Mean	57.88
Std Dev	14.55





# B1L003S, U22-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch93, adc0

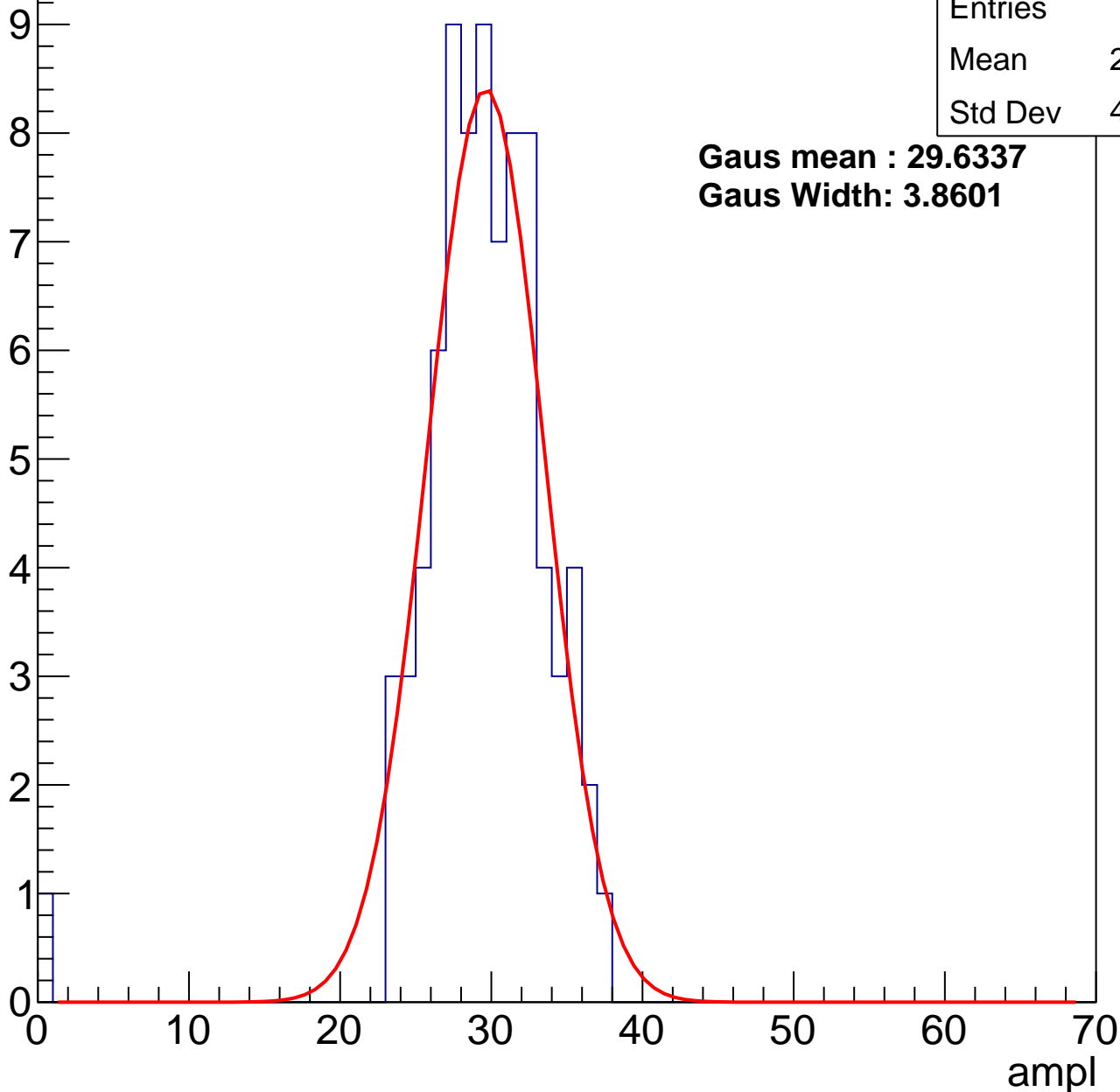
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	29.02
Std Dev	4.666

**Gaus mean : 29.6337**

**Gaus Width: 3.8601**



# B1L003S, U22-ch93, adc1

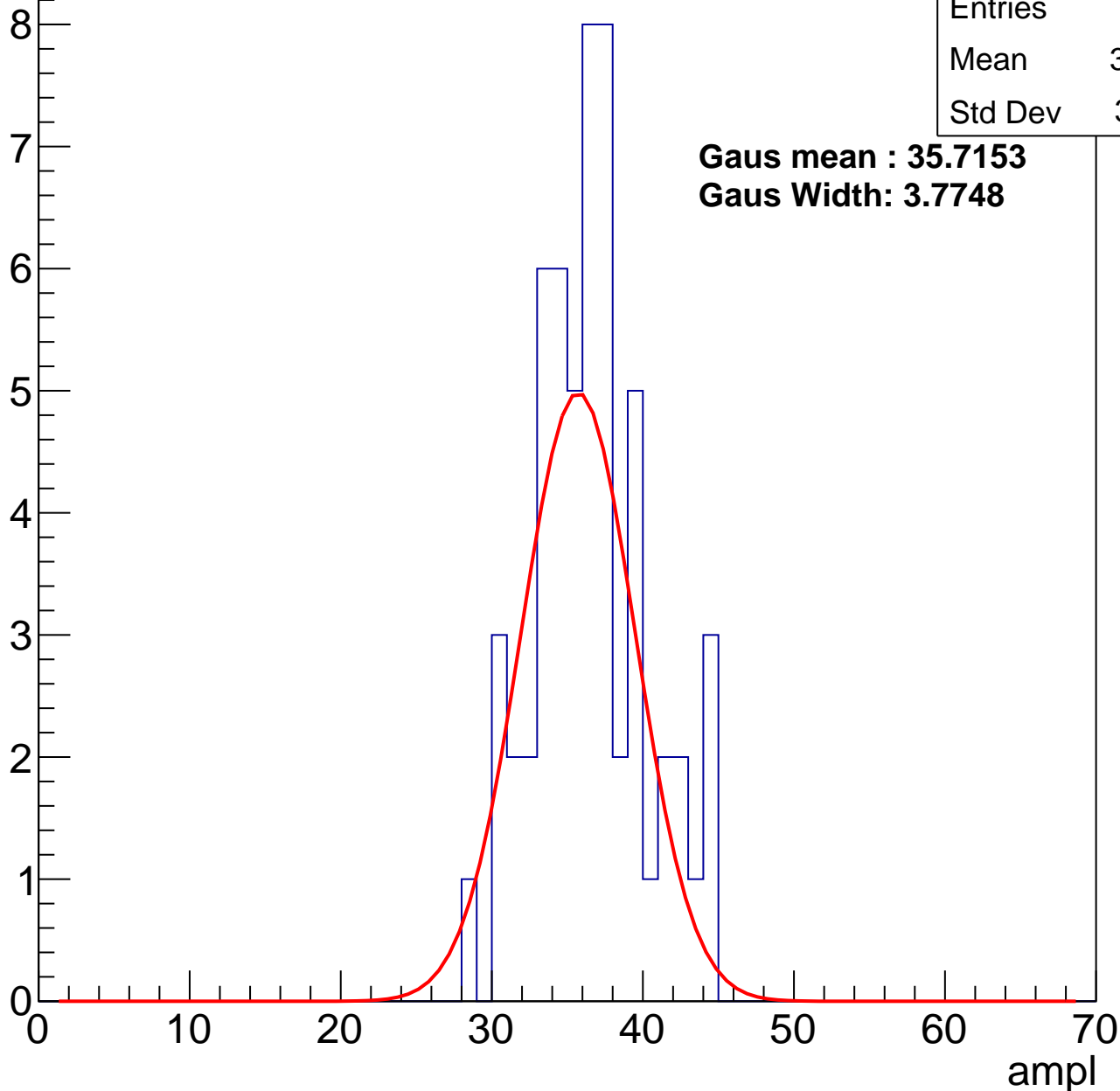
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.09
Std Dev	3.691

**Gaus mean : 35.7153**

**Gaus Width: 3.7748**



# B1L003S, U22-ch93, adc2

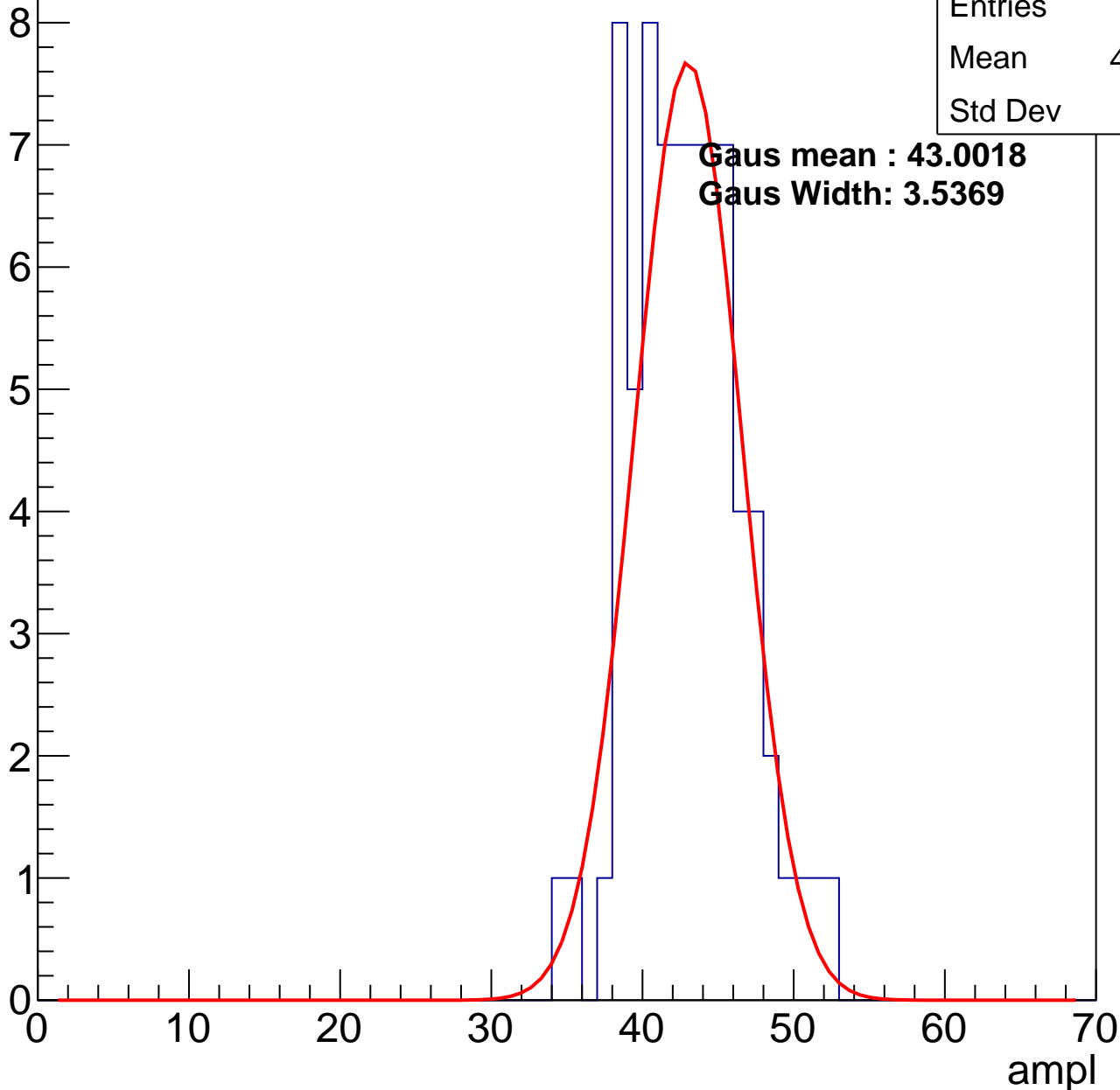
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.47
Std Dev	3.63

**Gaus mean : 43.0018**

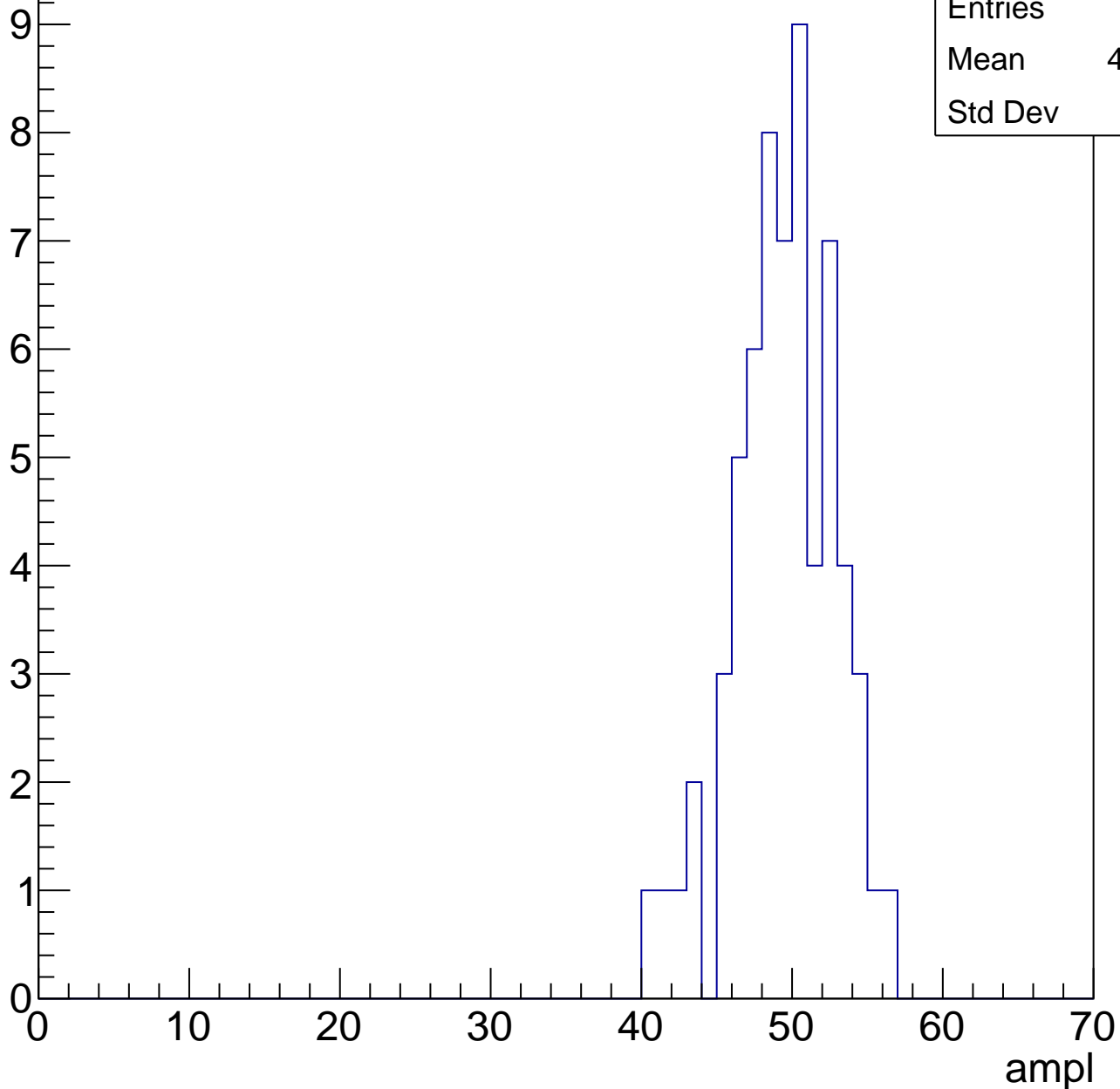
**Gaus Width: 3.5369**



# B1L003S, U22-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

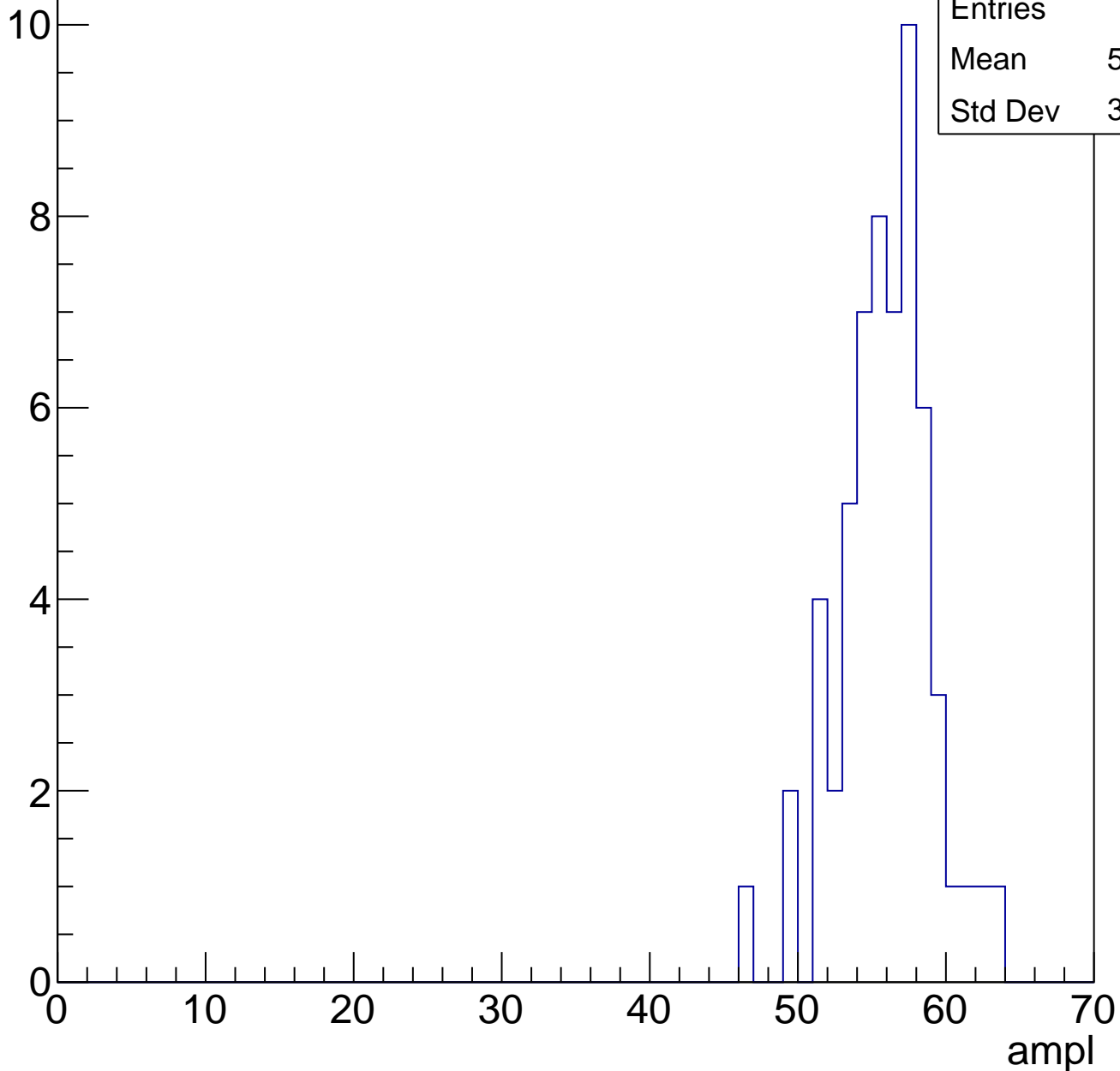


# B1L003S, U22-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.39
Std Dev	3.125



# B1L003S, U22-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.94
Std Dev	8.71

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

# B1L003S, U22-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch94, adc0

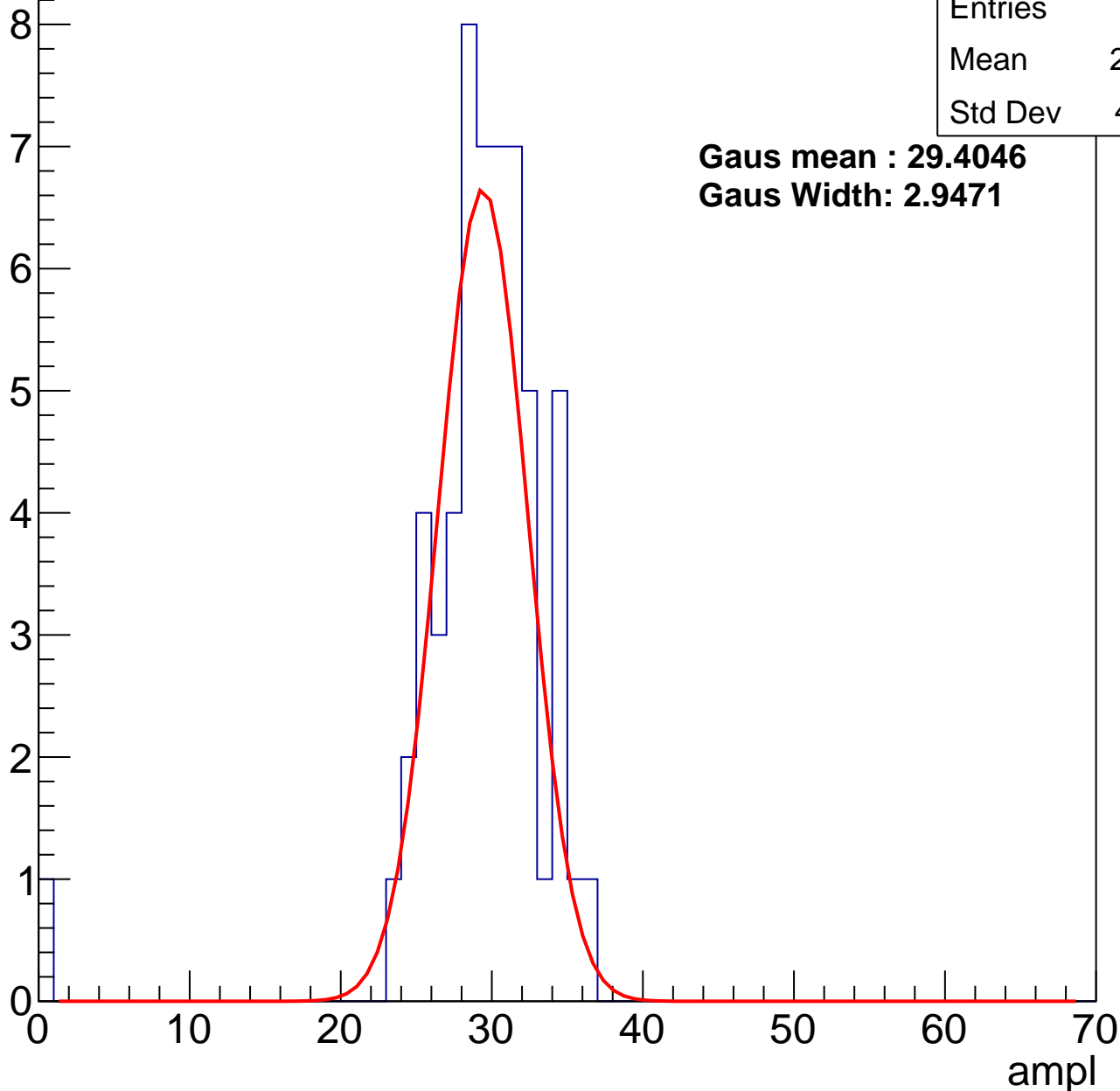
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	28.86
Std Dev	4.861

**Gaus mean : 29.4046**

**Gaus Width: 2.9471**



# B1L003S, U22-ch94, adc1

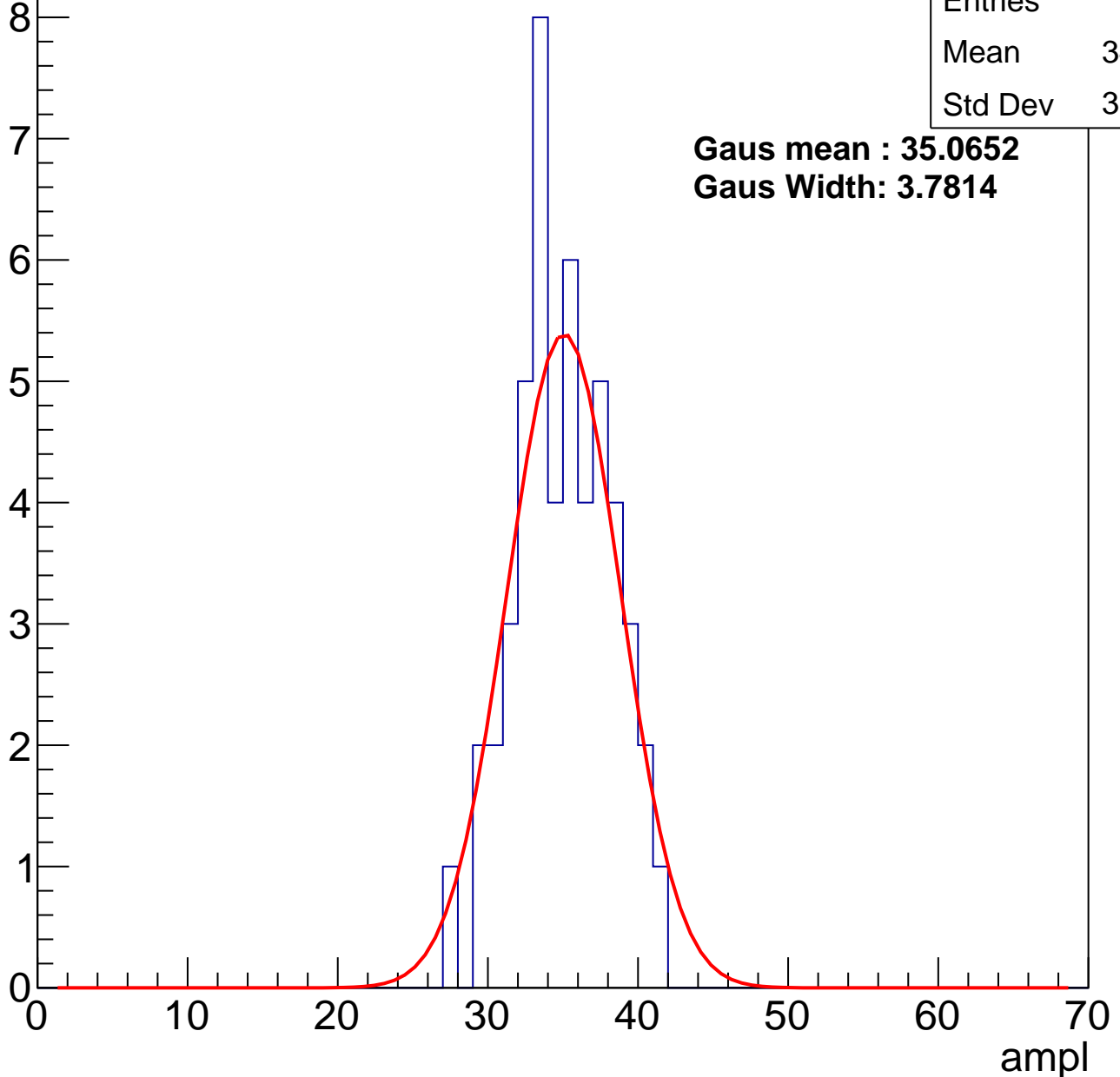
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	34.54
Std Dev	3.157

**Gaus mean : 35.0652**

**Gaus Width: 3.7814**



# B1L003S, U22-ch94, adc2

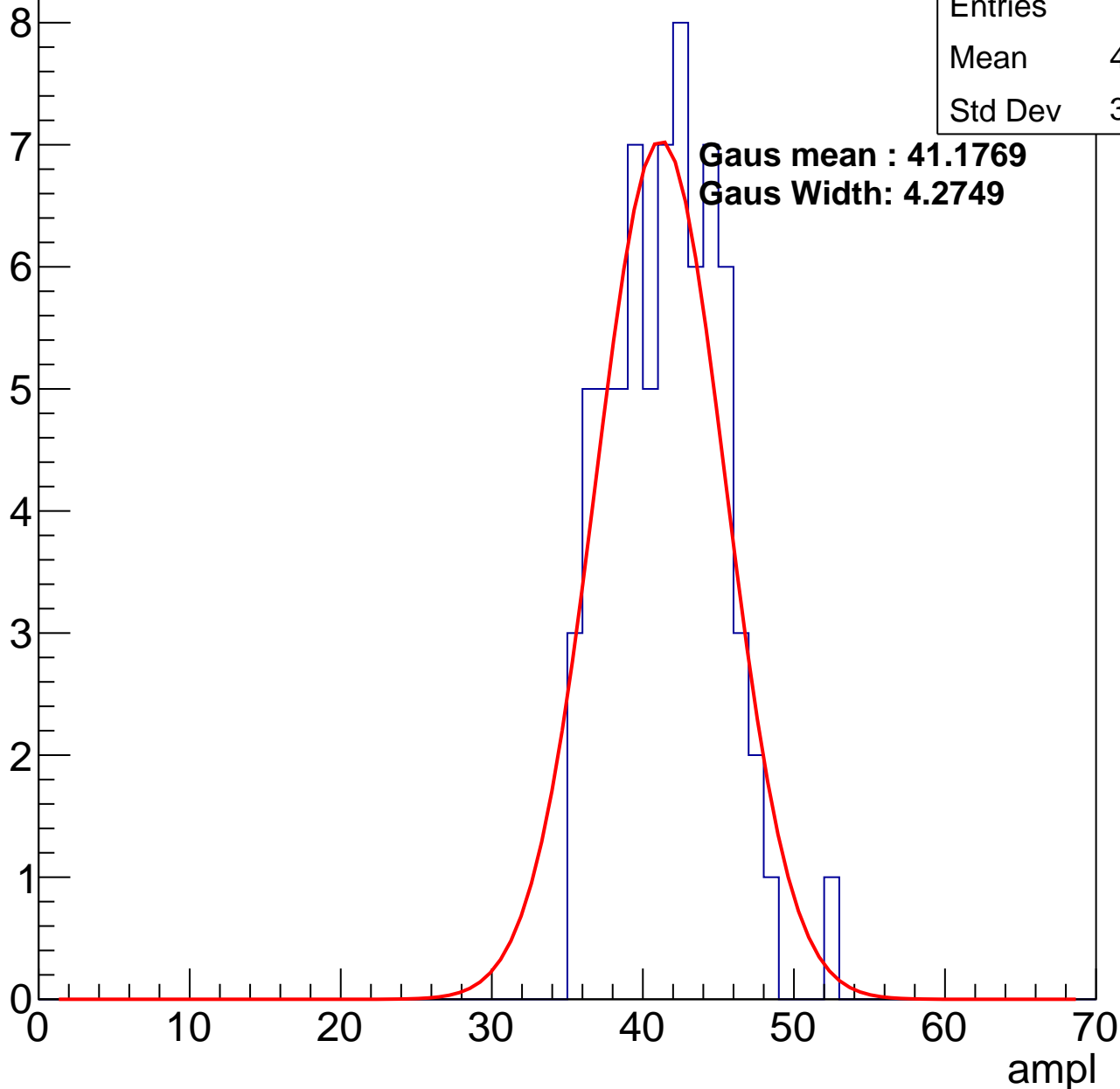
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.18
Std Dev	3.554

**Gaus mean : 41.1769**

**Gaus Width: 4.2749**

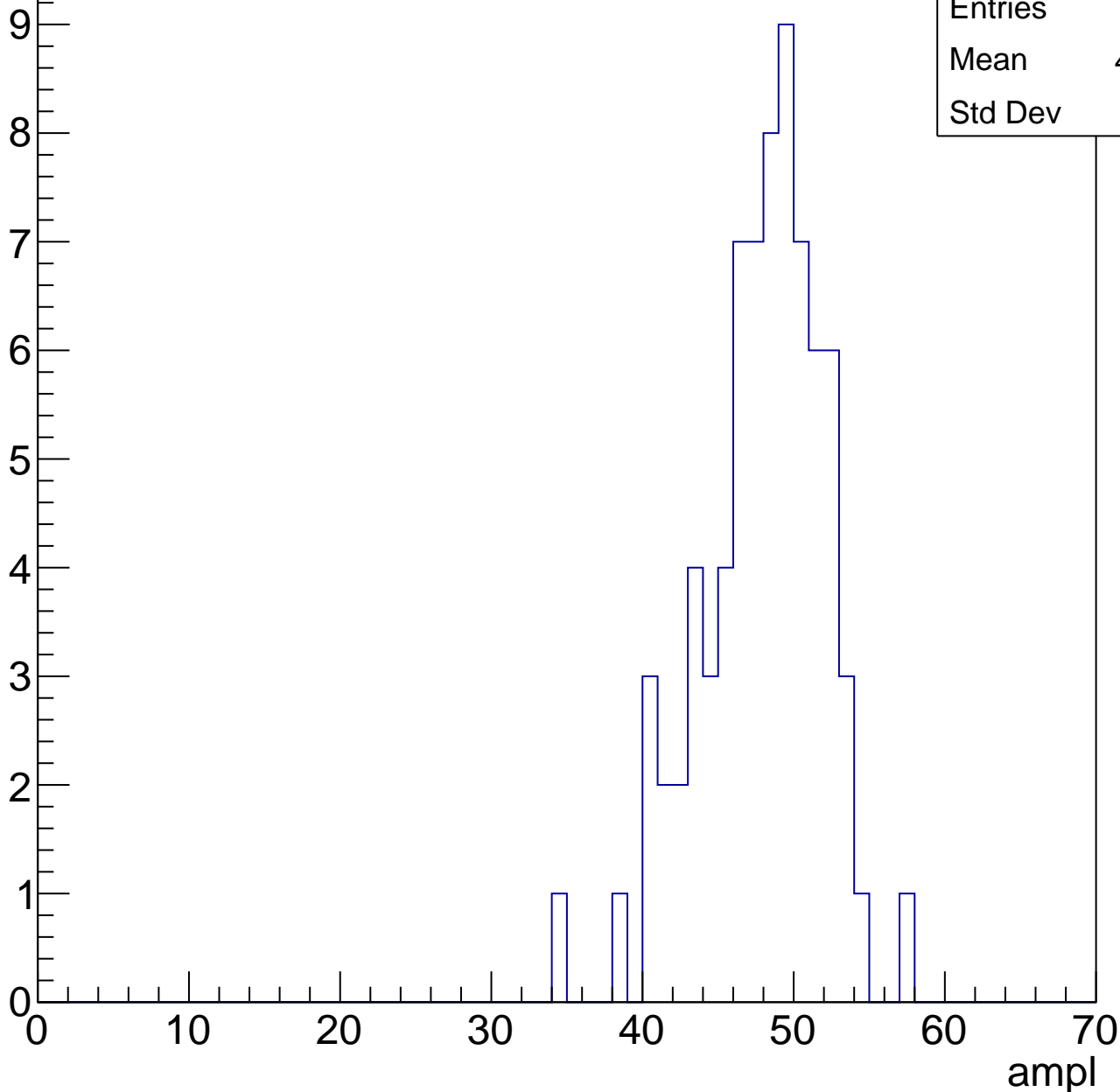


# B1L003S, U22-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	47.41
Std Dev	4.05

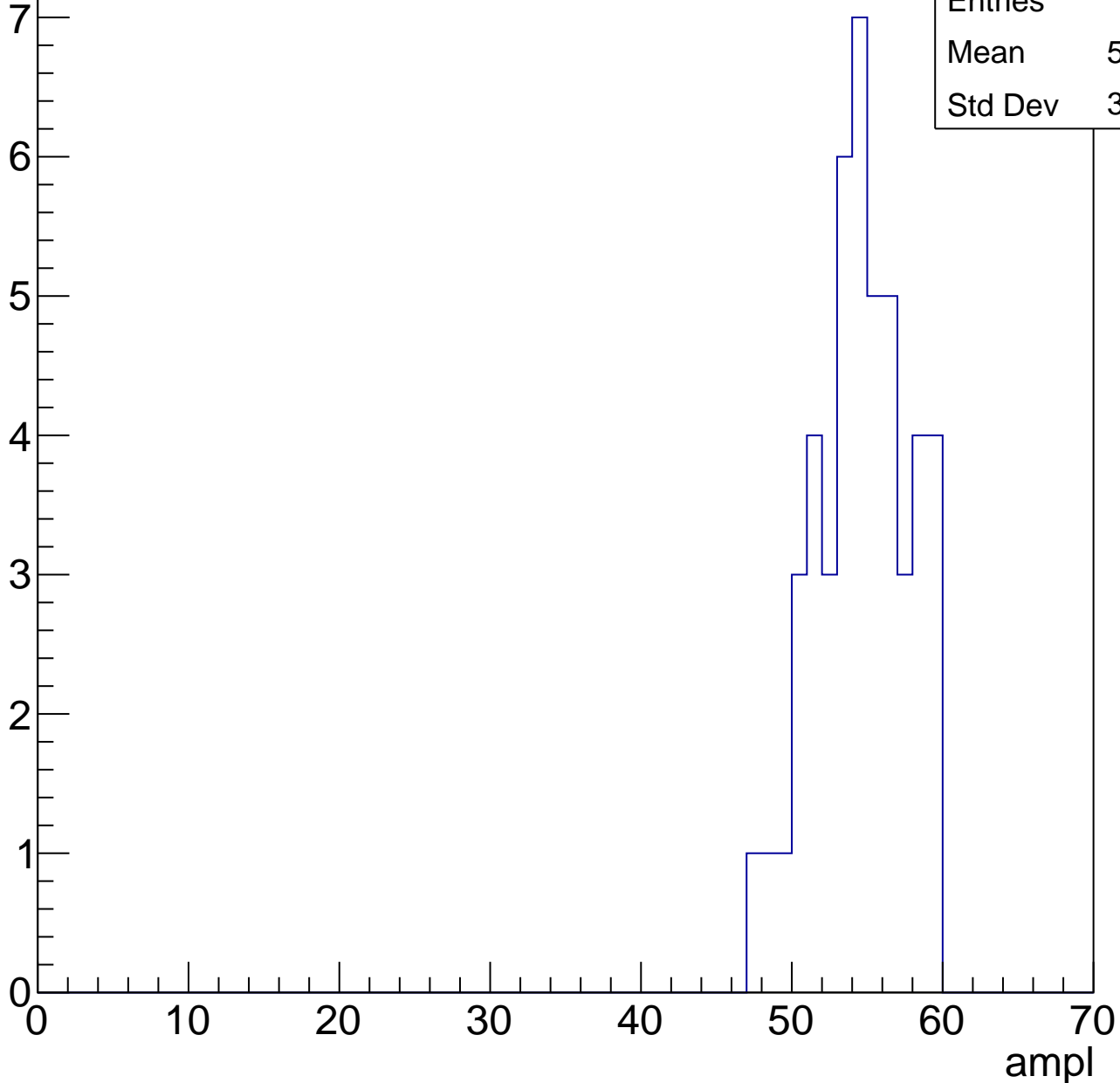


# B1L003S, U22-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	54.13
Std Dev	3.015



# B1L003S, U22-ch94, adc5

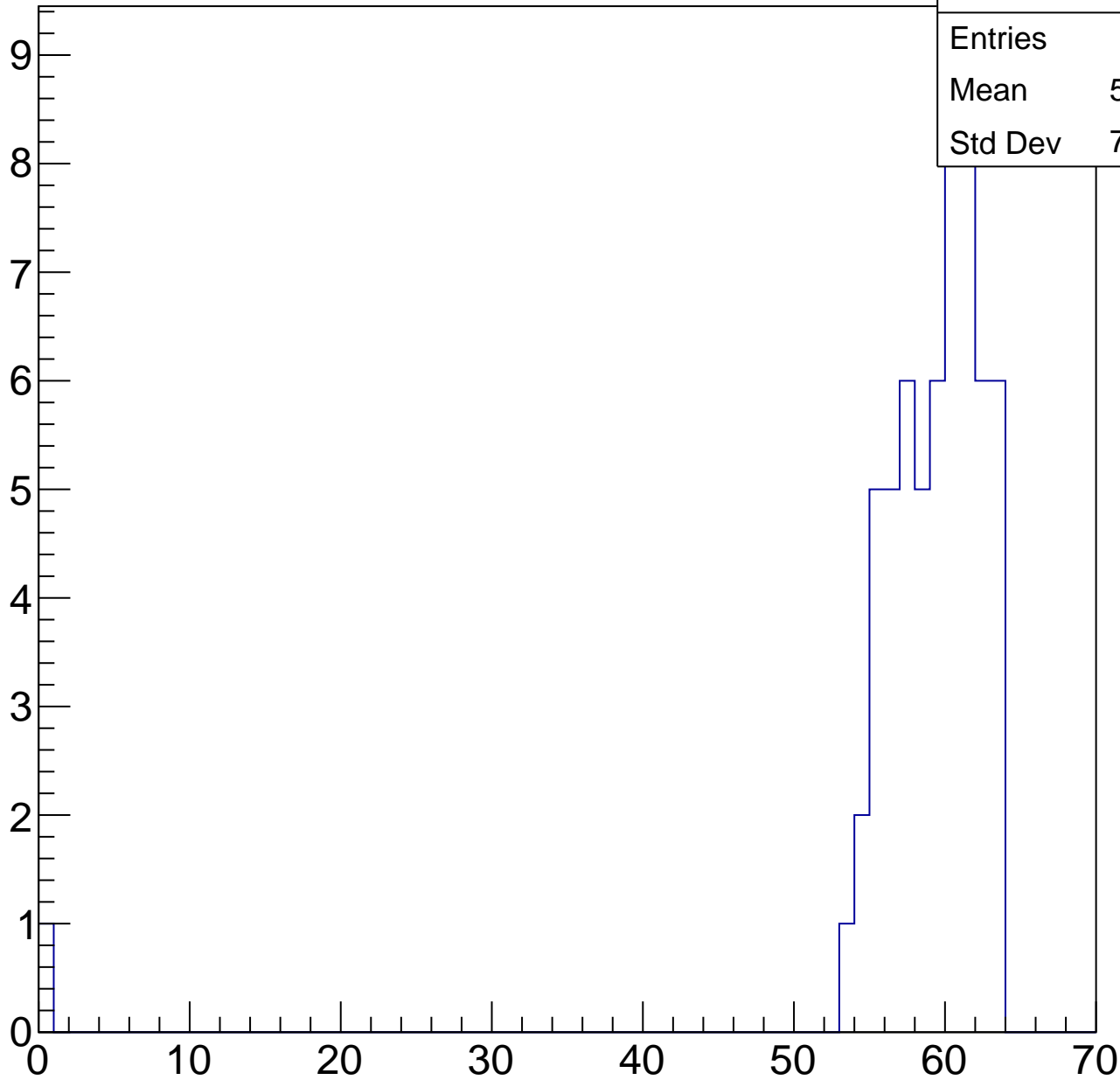
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	58.05
Std Dev	7.956

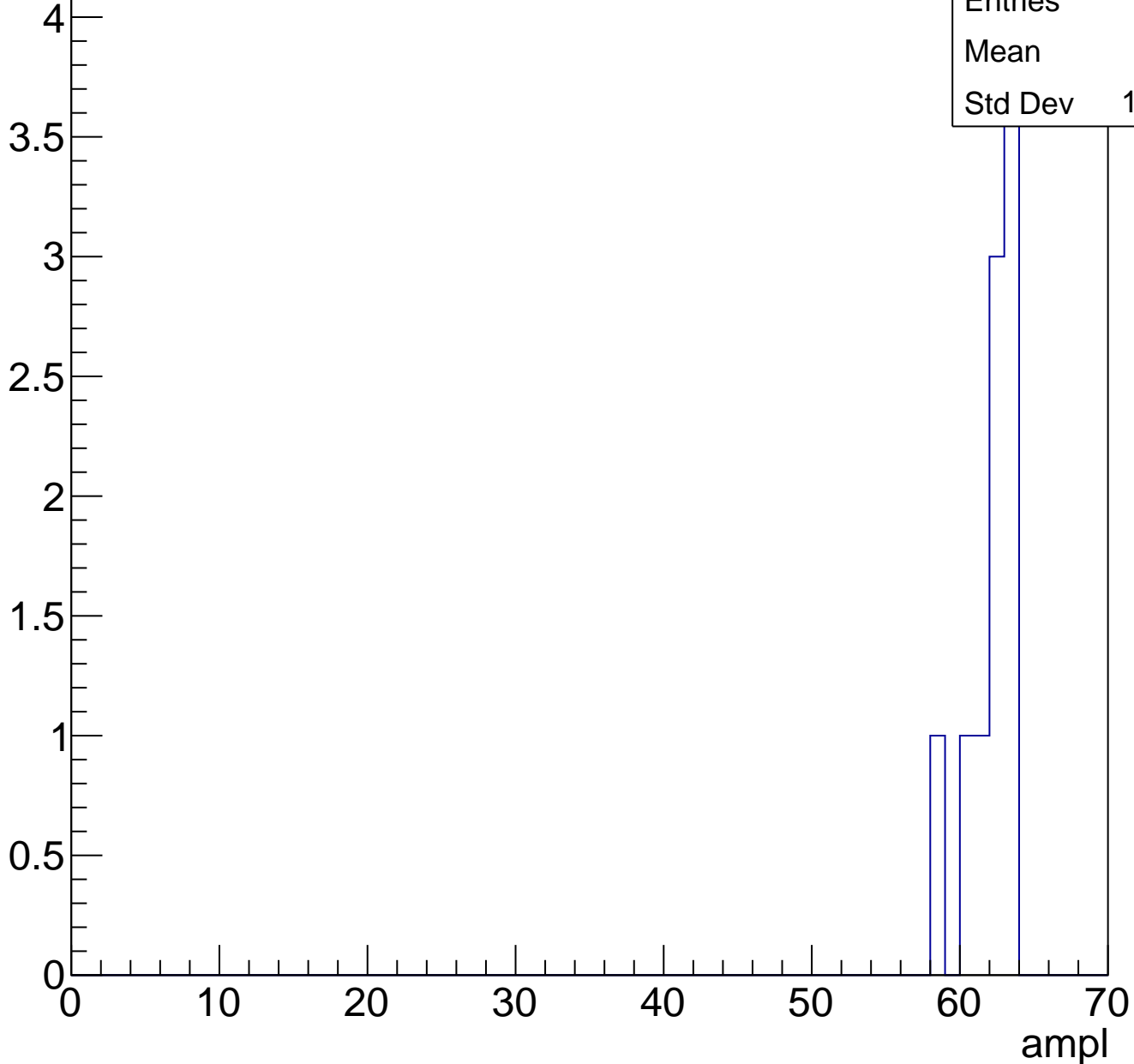
ampl



# B1L003S, U22-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	10
Mean	61.7
Std Dev	1.552



# B1L003S, U22-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	80
Mean	29.77
Std Dev	4.767

**Gaus mean : 30.9785**

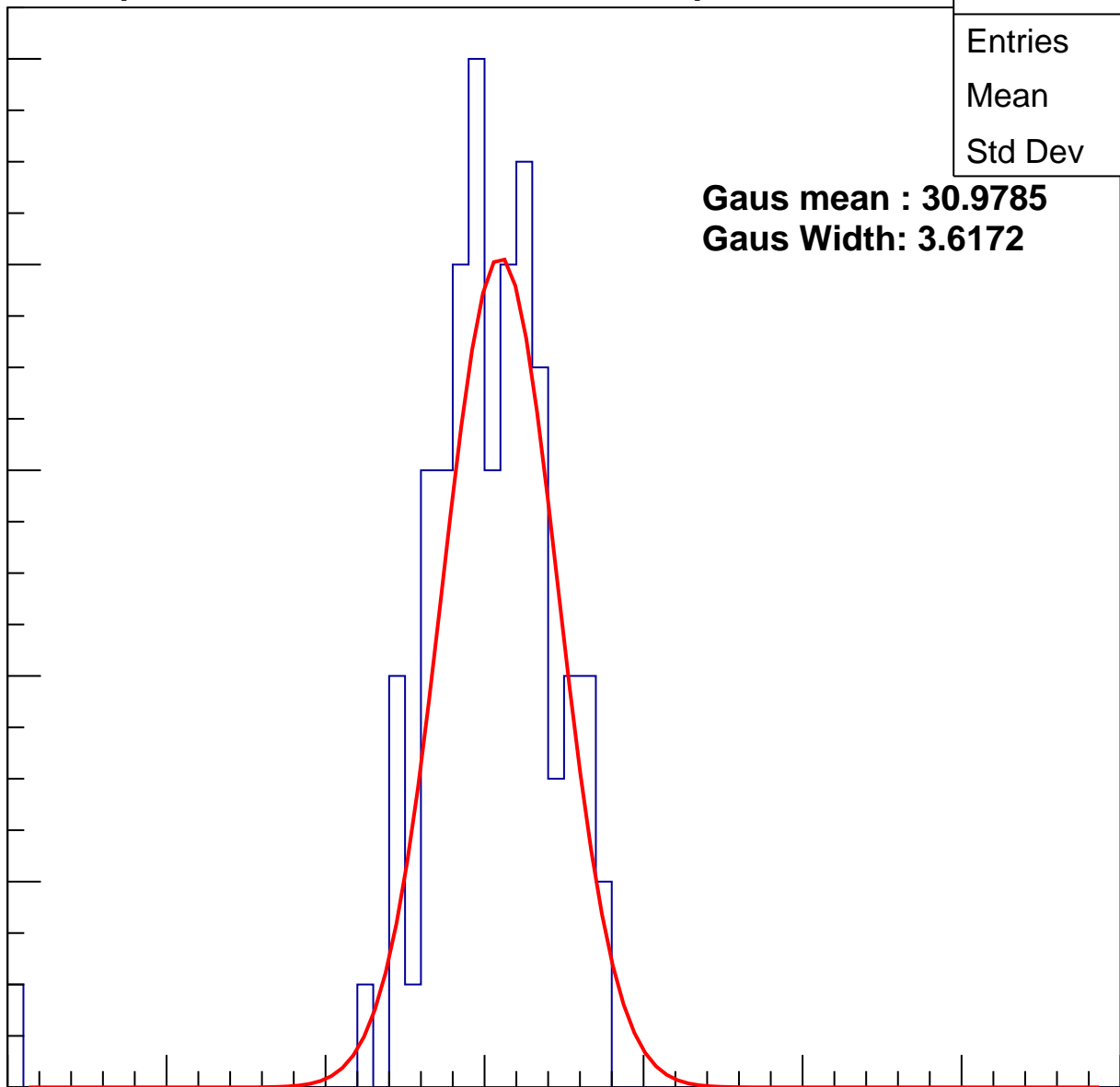
**Gaus Width: 3.6172**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



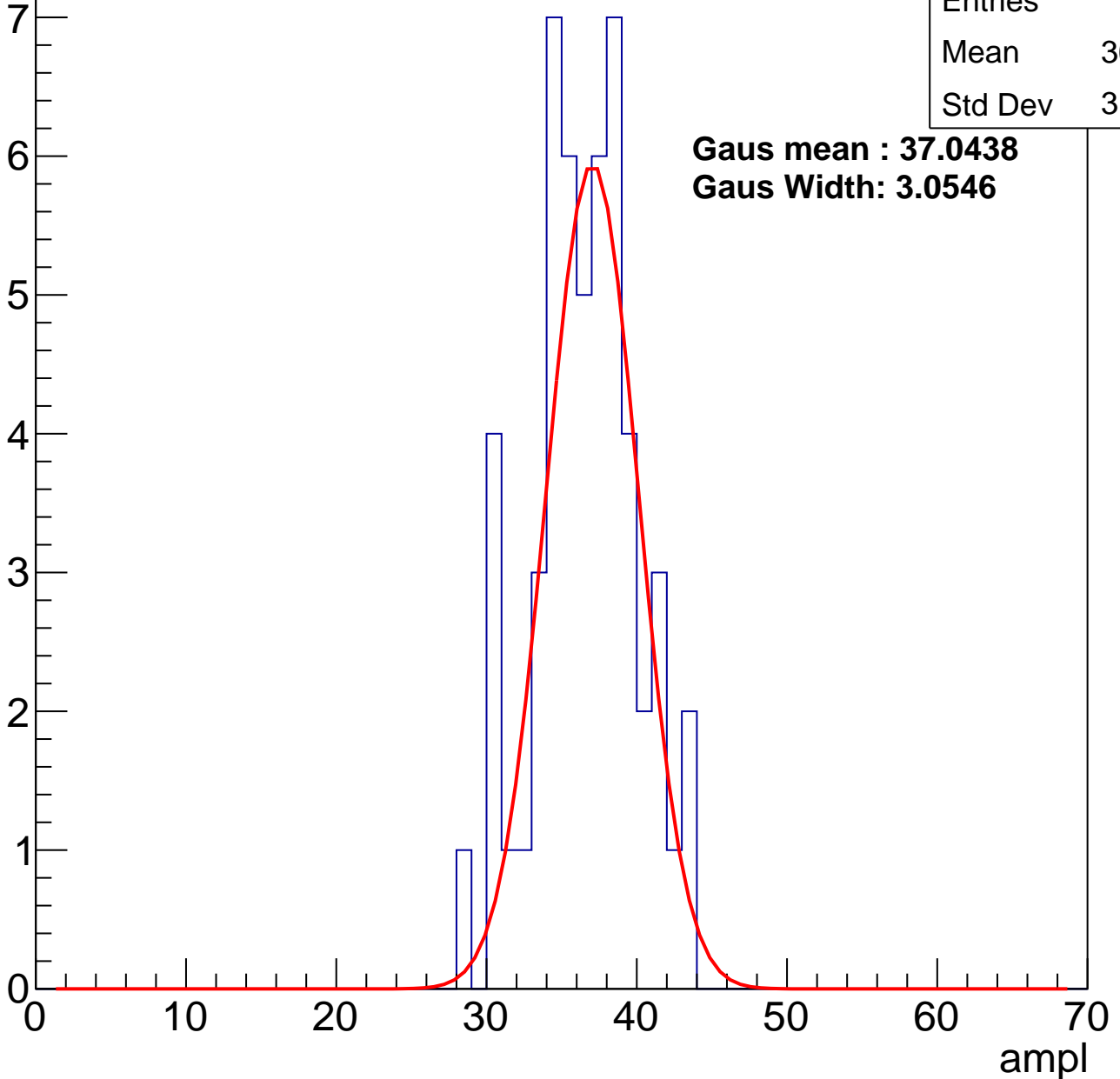
# B1L003S, U22-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.09
Std Dev	3.422

**Gaus mean : 37.0438**  
**Gaus Width: 3.0546**



# B1L003S, U22-ch95, adc2

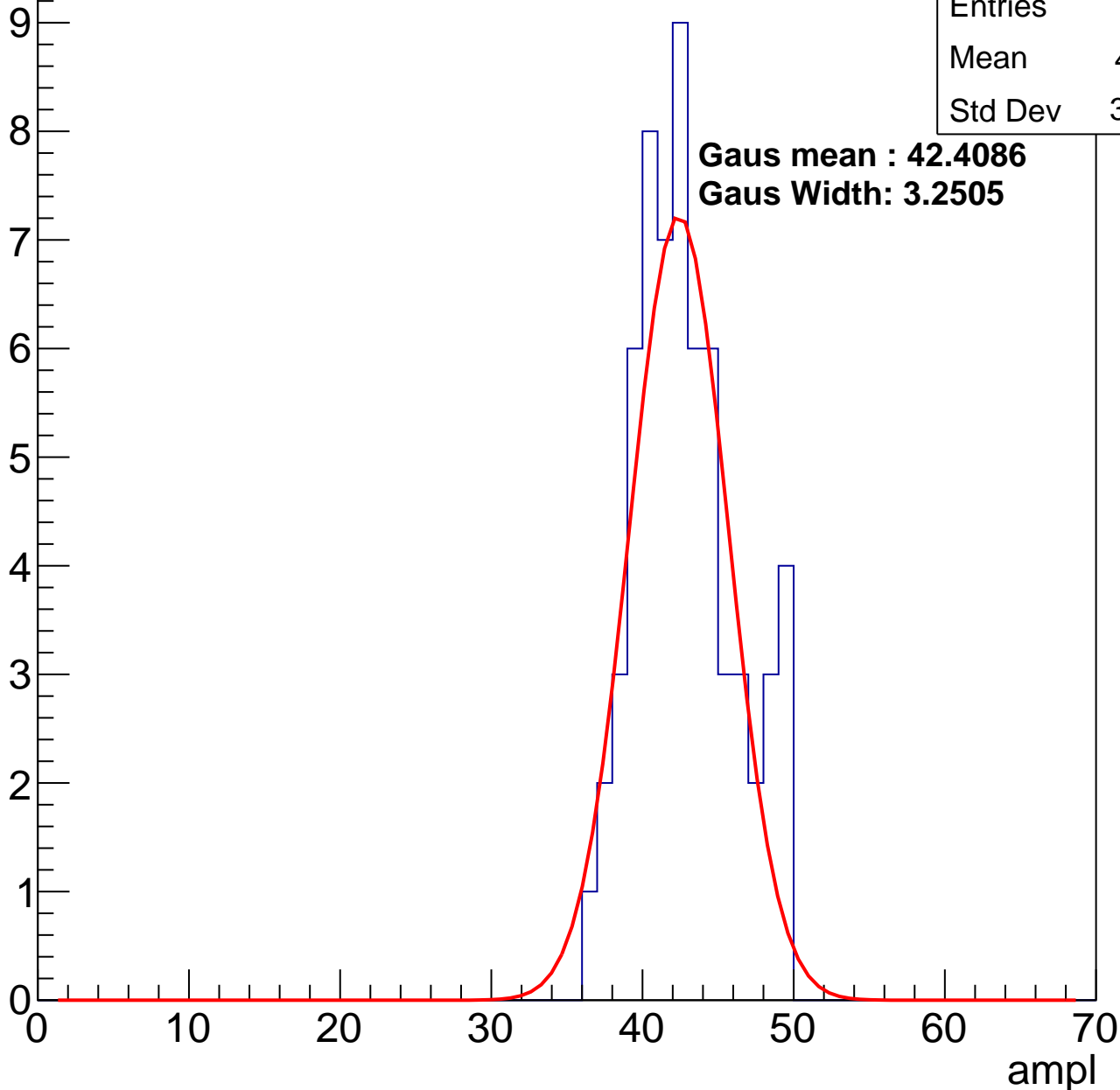
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.41
Std Dev	3.274

**Gaus mean : 42.4086**

**Gaus Width: 3.2505**

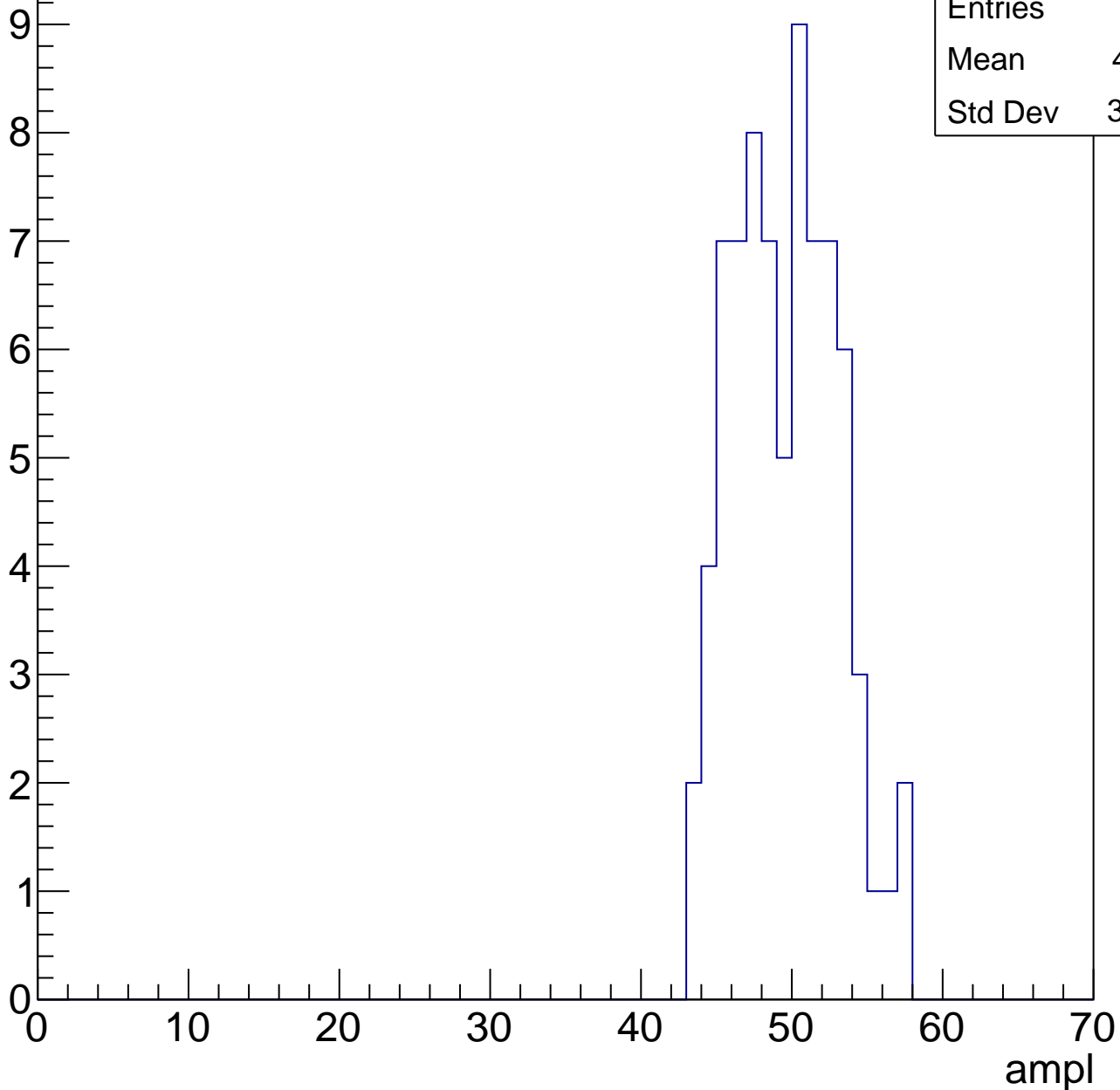


# B1L003S, U22-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	49.11
Std Dev	3.382

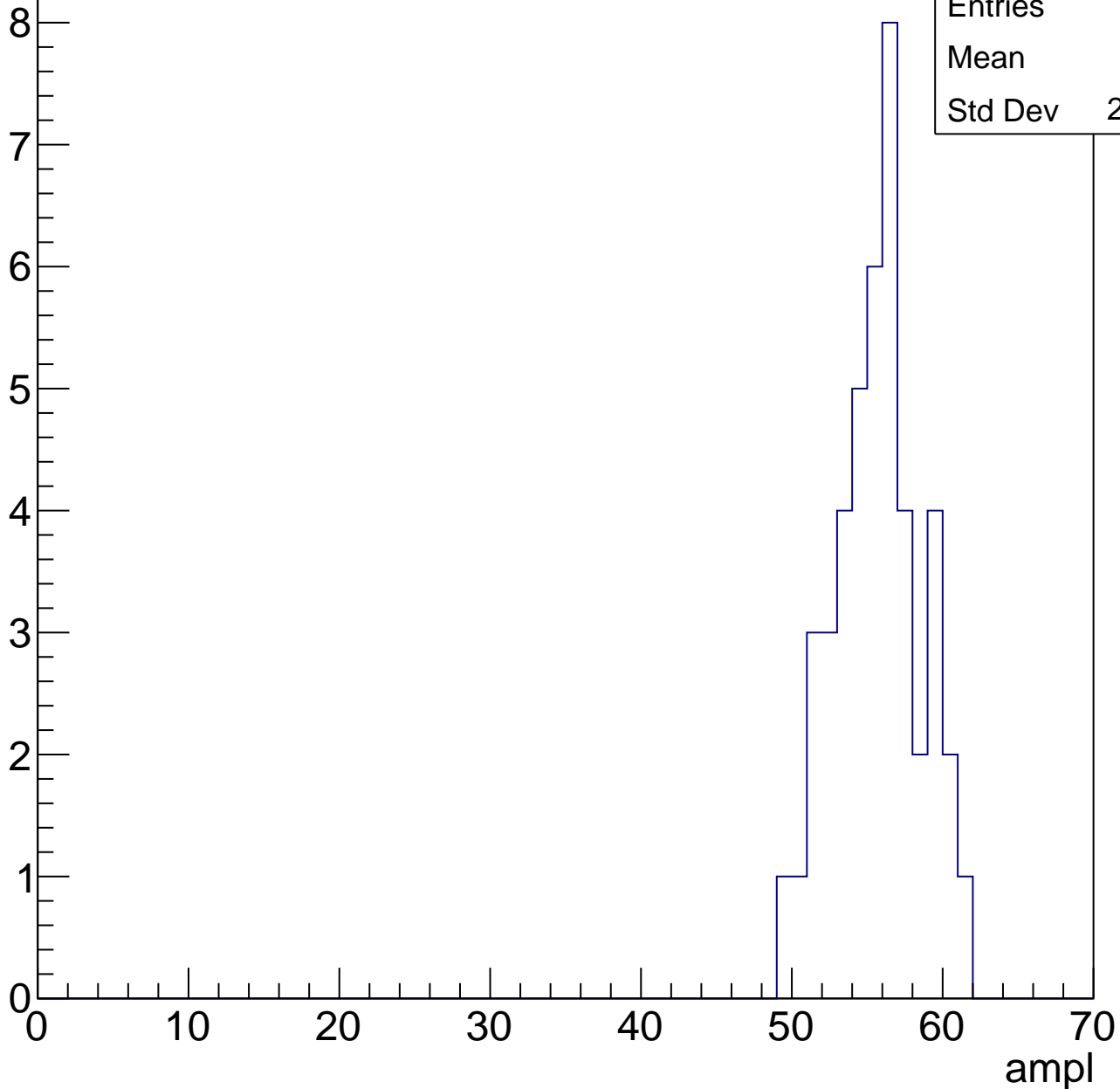


# B1L003S, U22-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

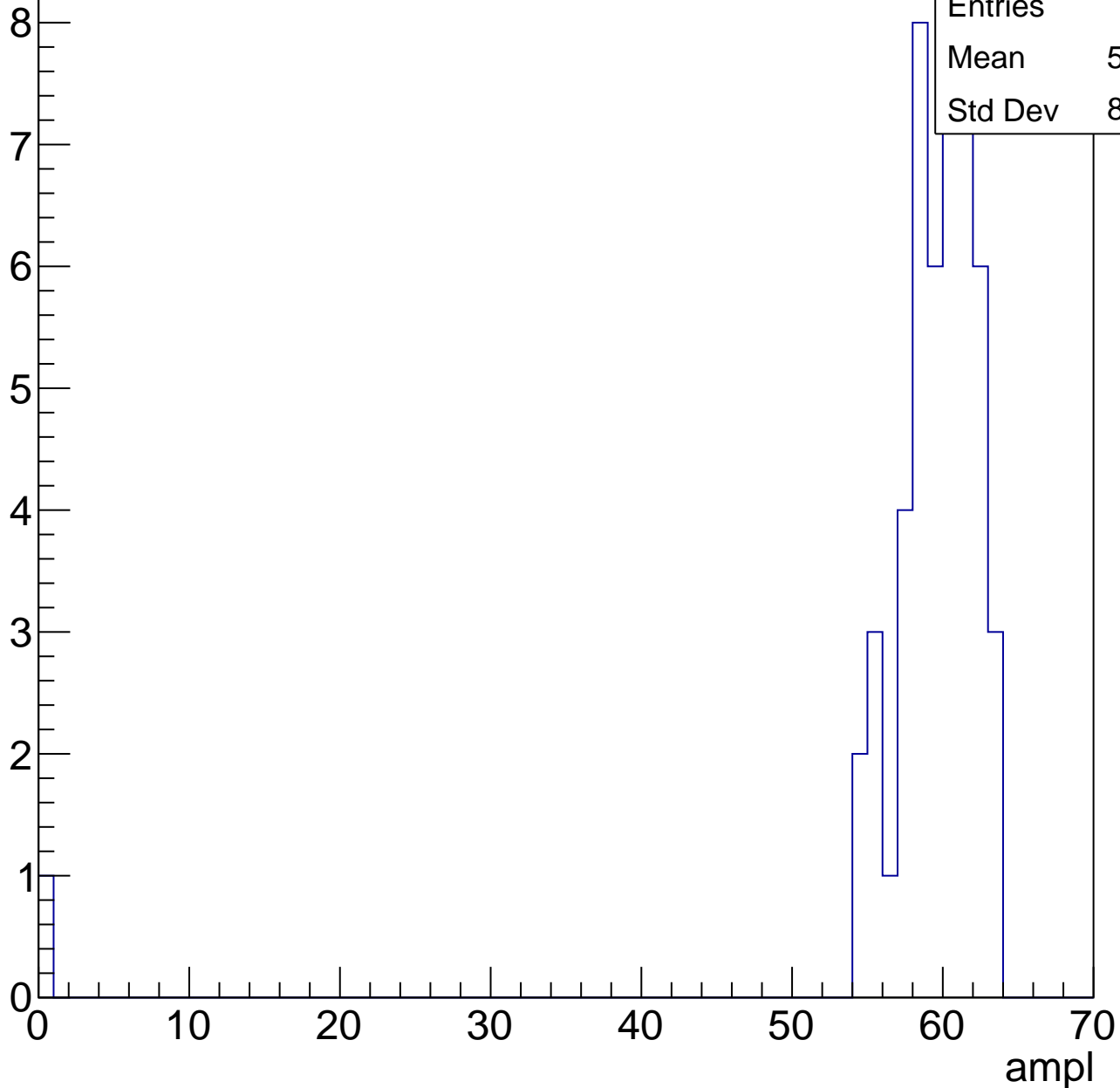
Entries	44
Mean	55.2
Std Dev	2.809



# B1L003S, U22-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

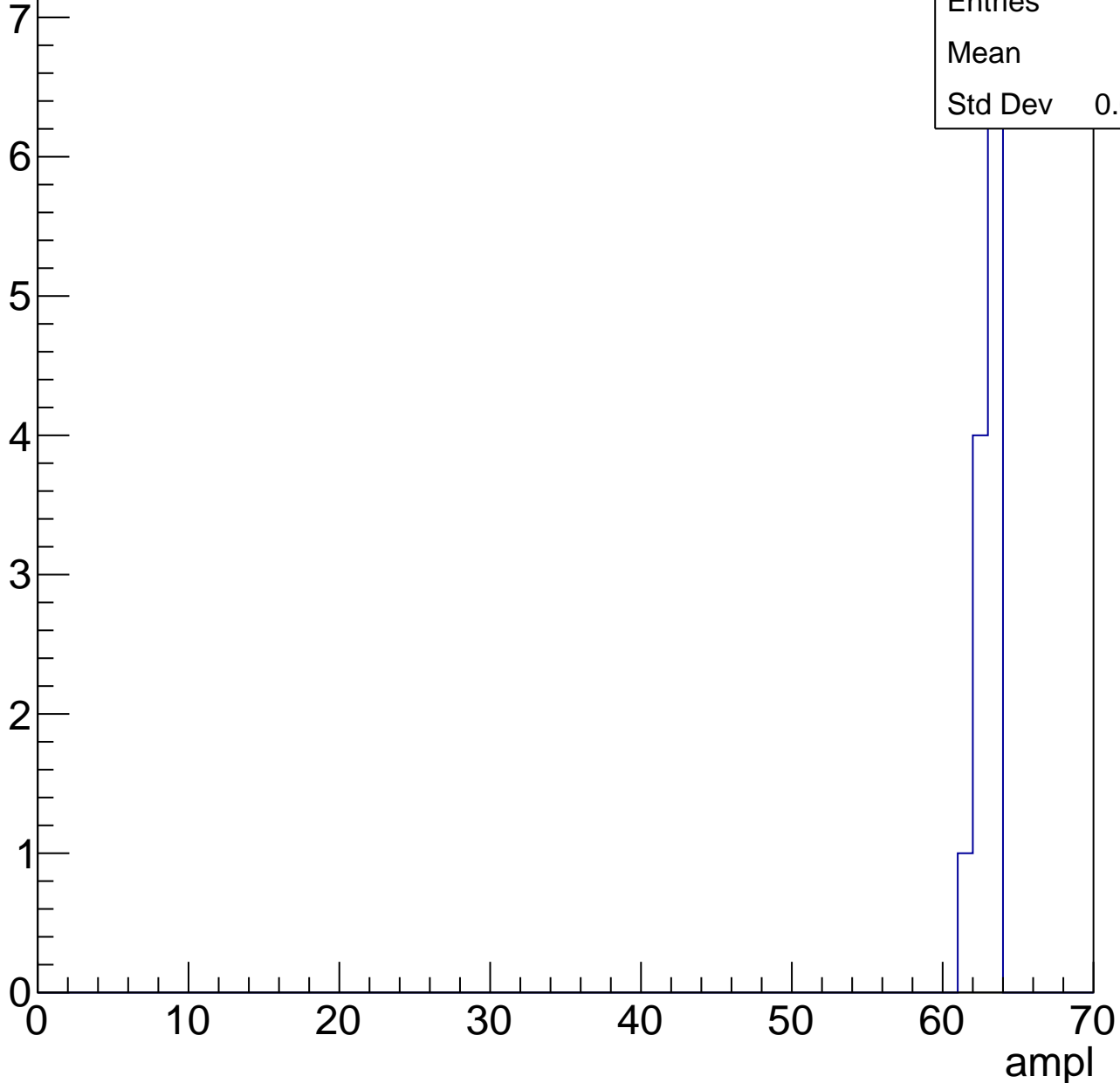


# B1L003S, U22-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	62.5
Std Dev	0.6455





# B1L003S, U22-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch96, adc0

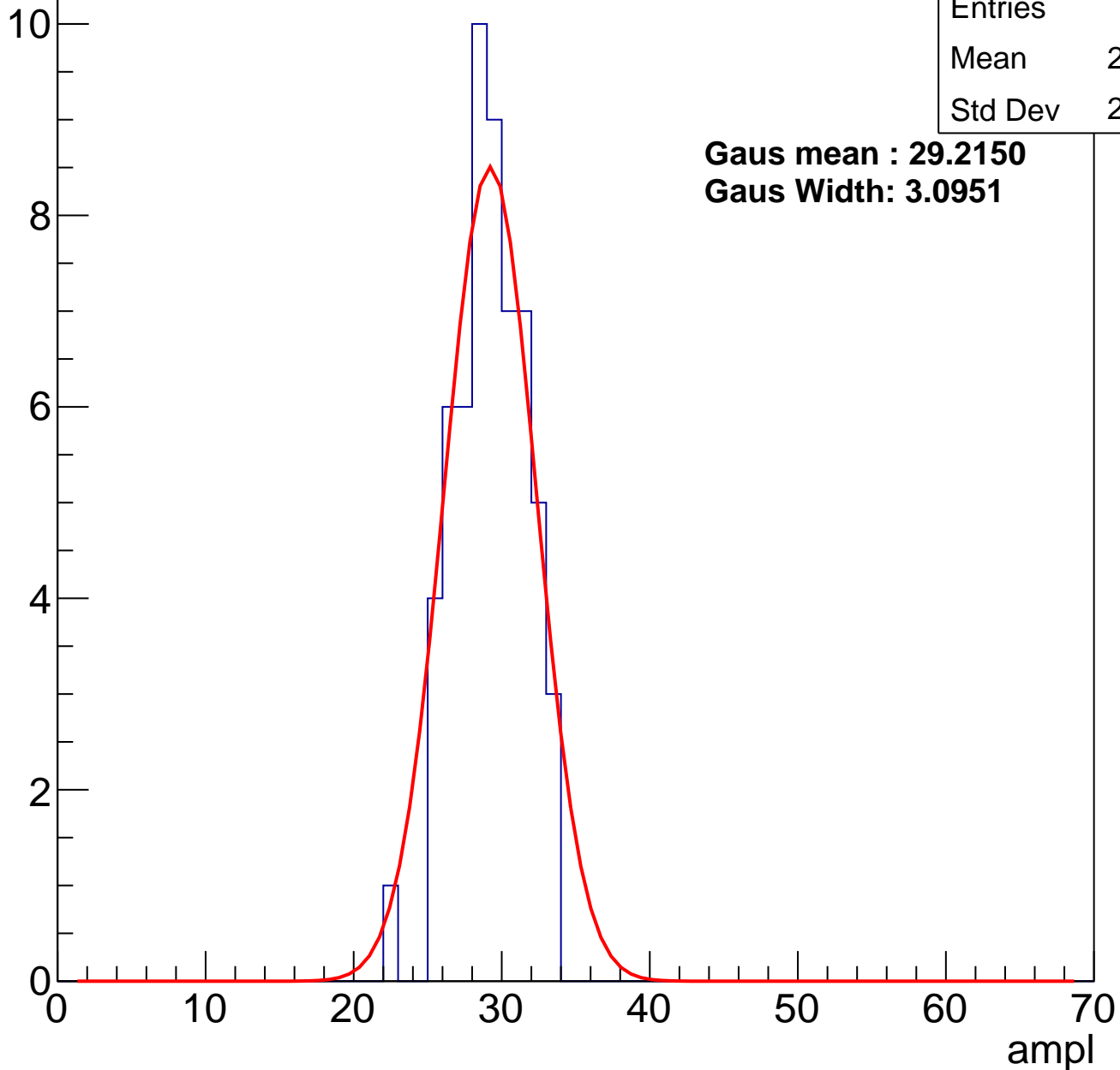
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	58
Mean	28.74
Std Dev	2.368

**Gaus mean : 29.2150**

**Gaus Width: 3.0951**

Entry



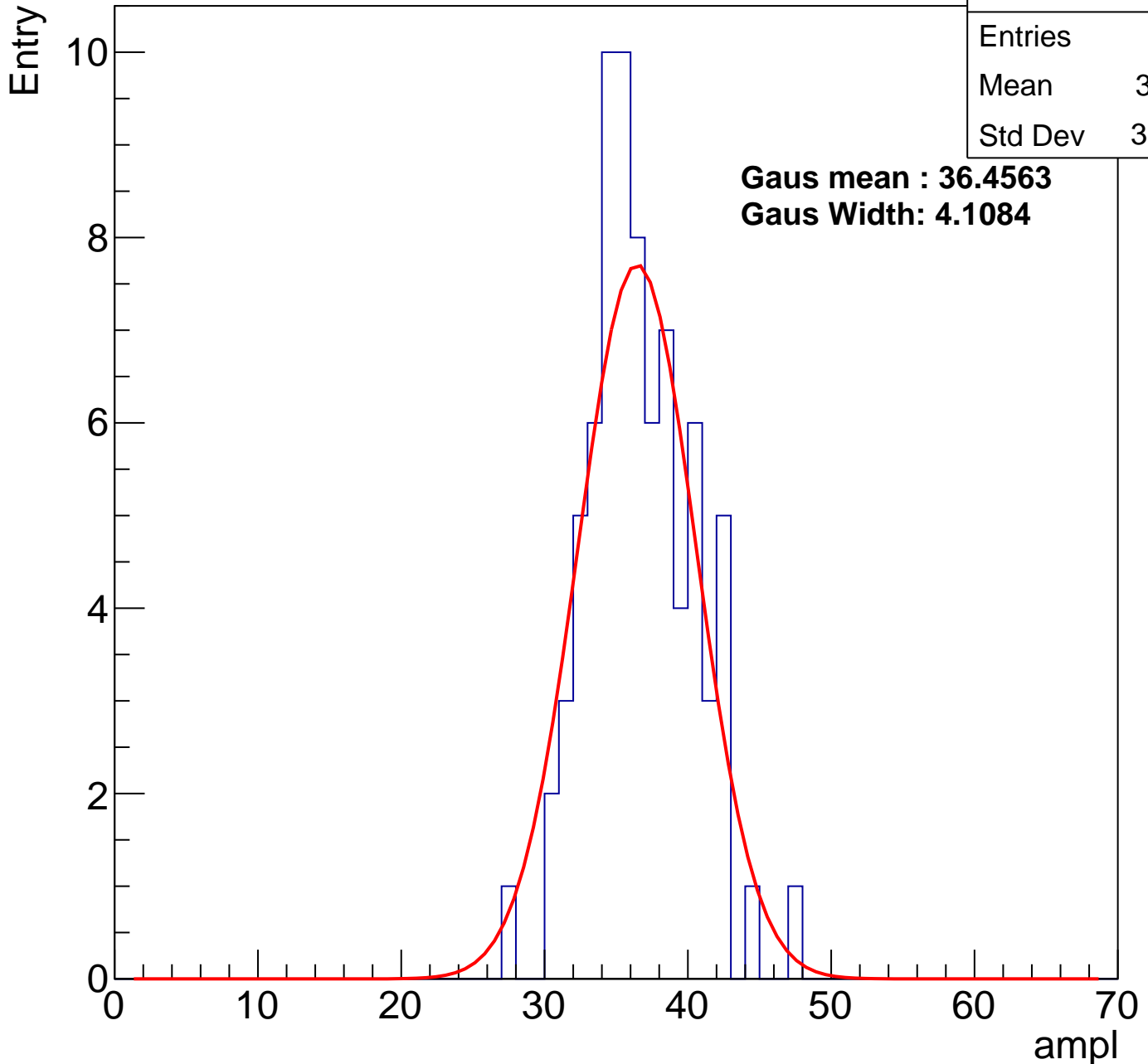
# B1L003S, U22-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	78
Mean	36.21
Std Dev	3.607

**Gaus mean : 36.4563**

**Gaus Width: 4.1084**



# B1L003S, U22-ch96, adc2

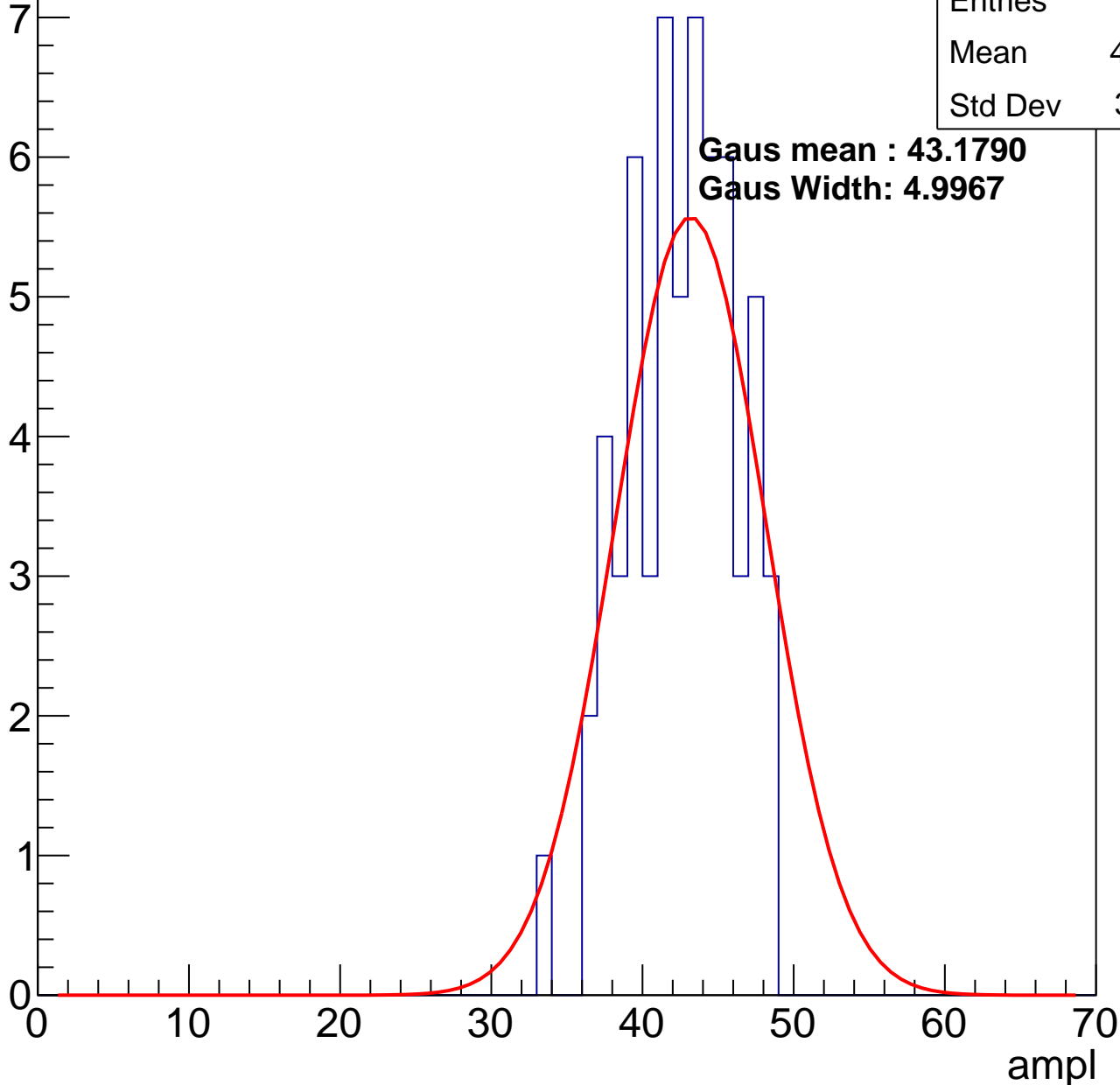
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.13
Std Dev	3.481

**Gaus mean : 43.1790**

**Gaus Width: 4.9967**

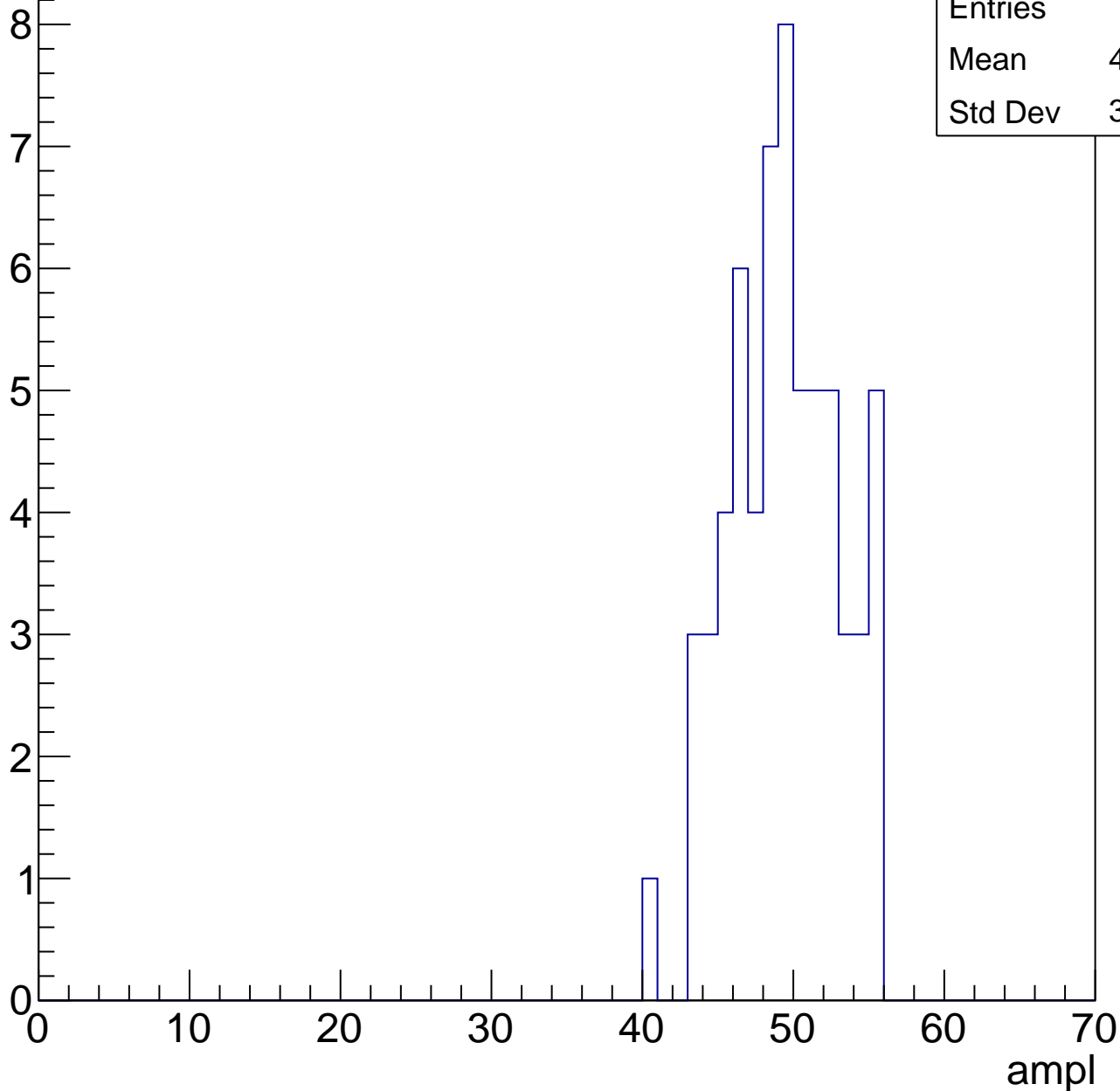


# B1L003S, U22-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

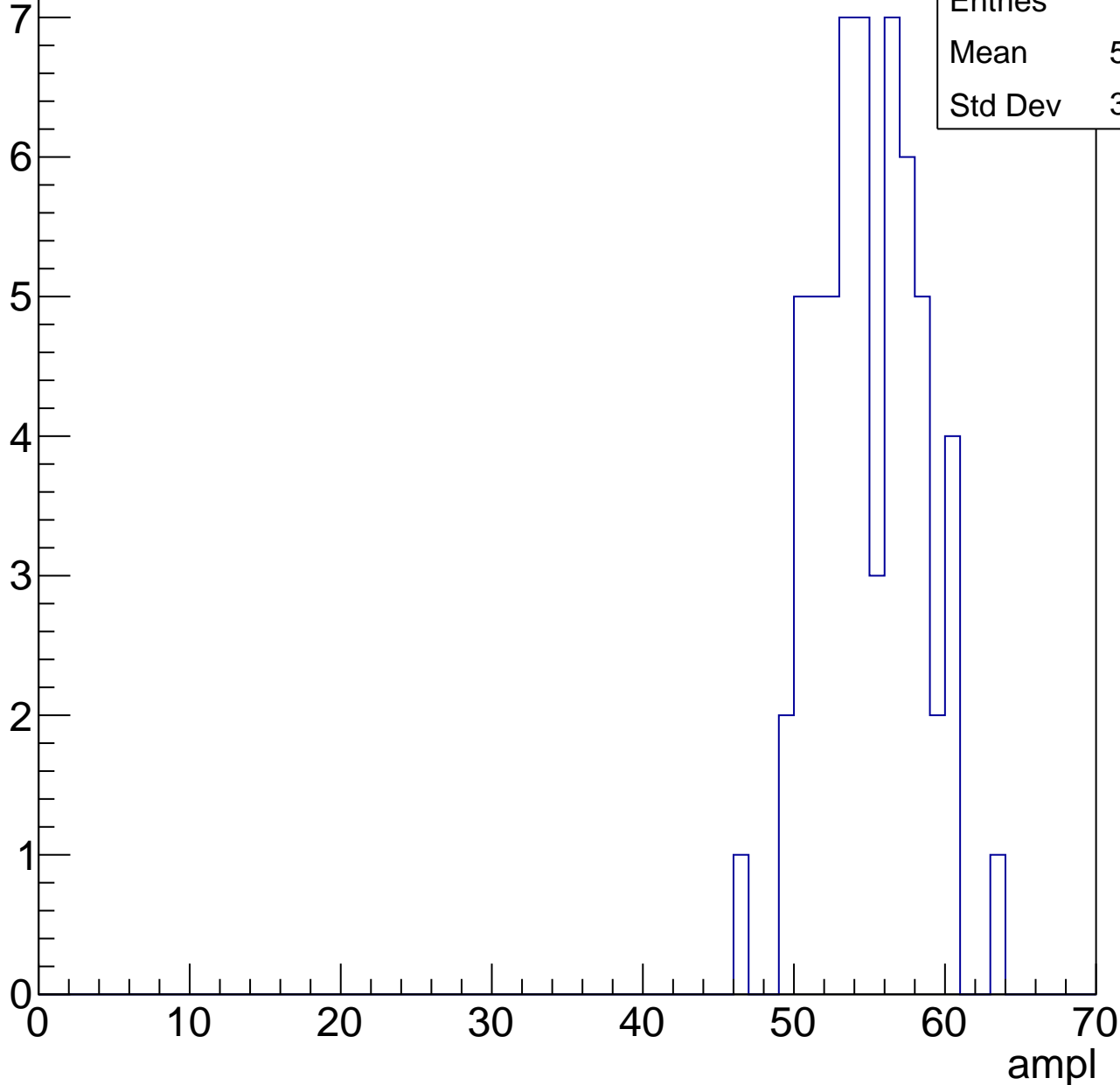
Entries	62
Mean	48.94
Std Dev	3.542



# B1L003S, U22-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

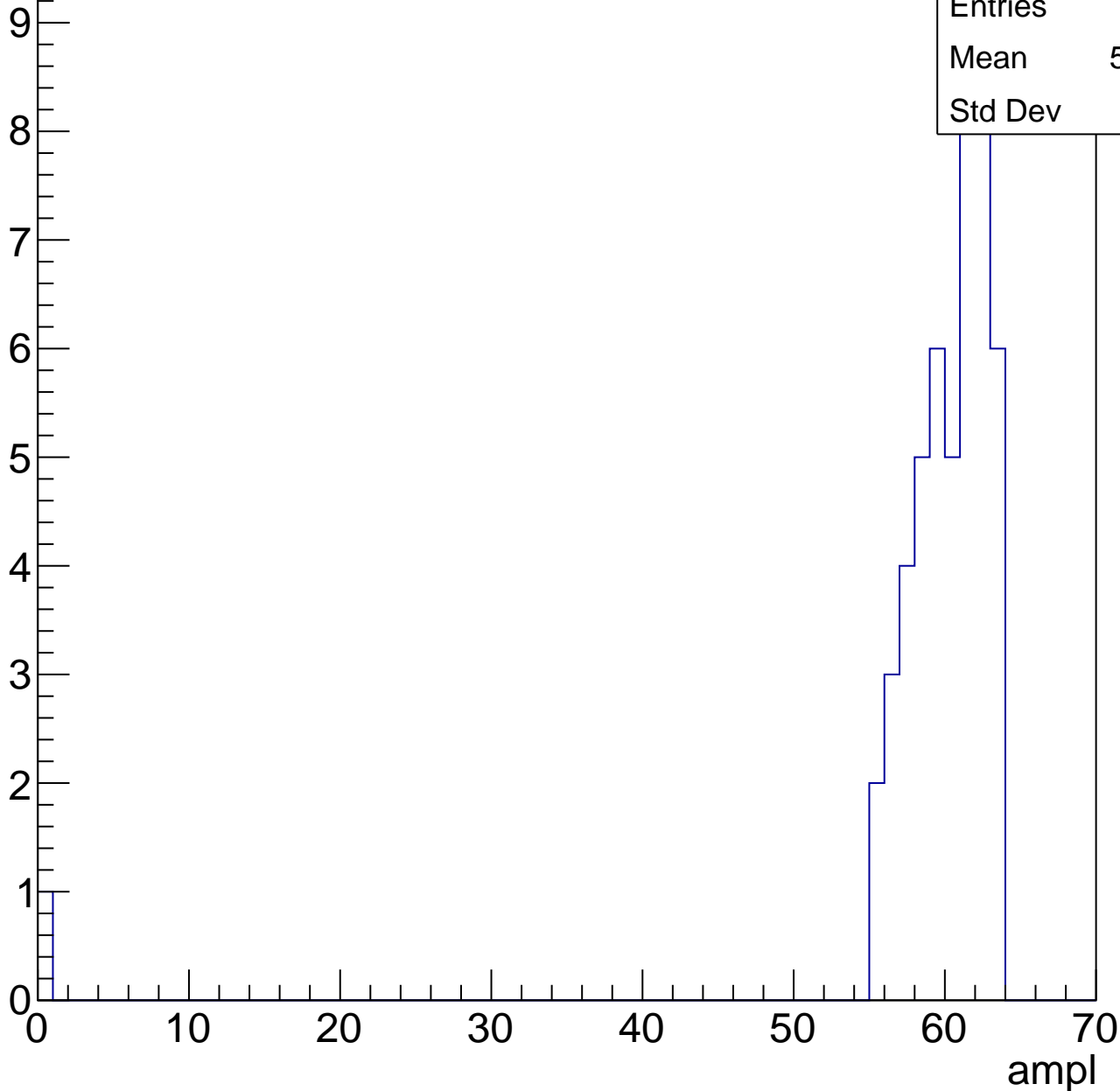


# B1L003S, U22-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.65
Std Dev	8.77



# B1L003S, U22-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

Mean

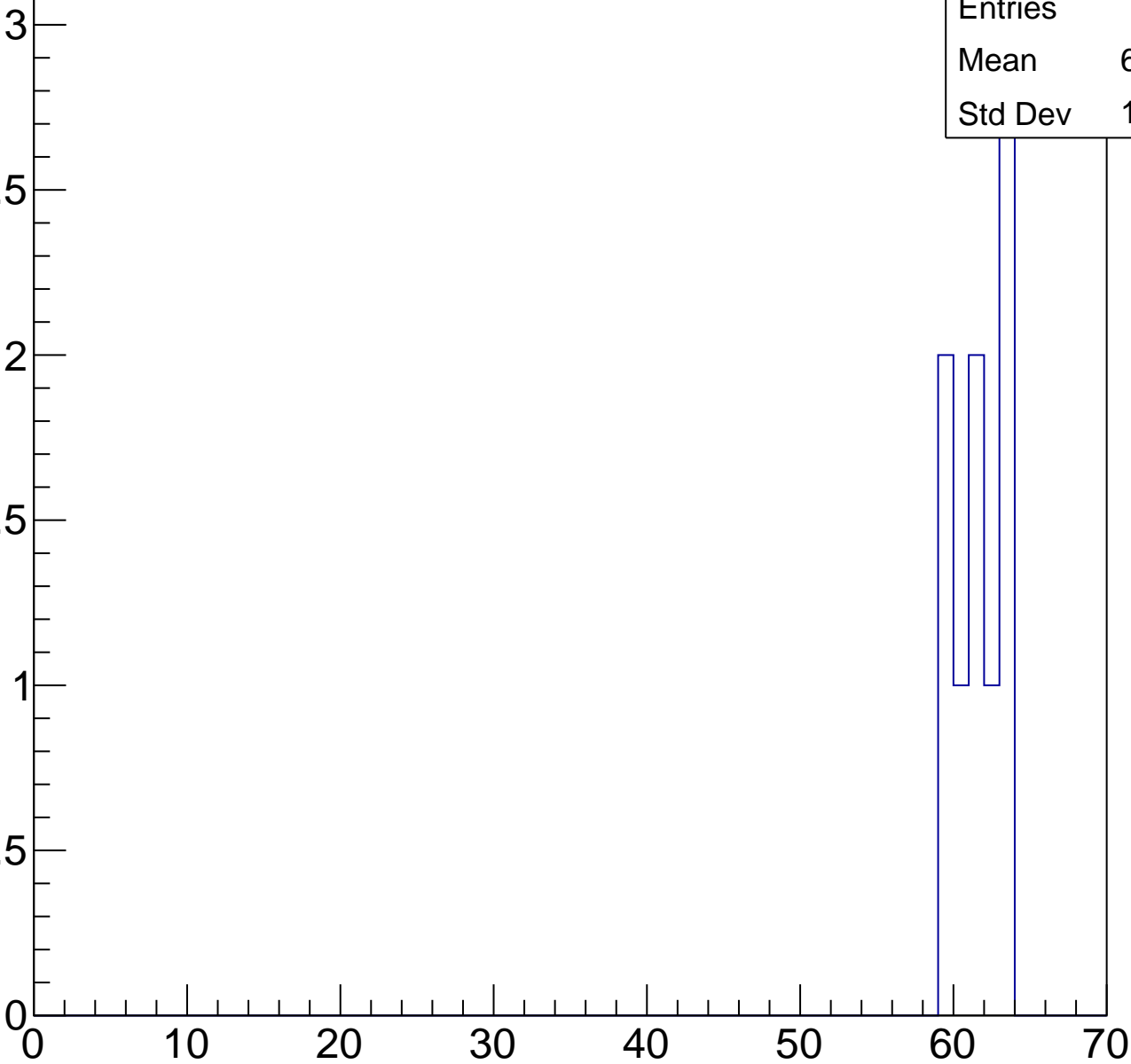
61.22

Std Dev

1.548

ampl

0 10 20 30 40 50 60 70





# B1L003S, U22-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch97, adc0

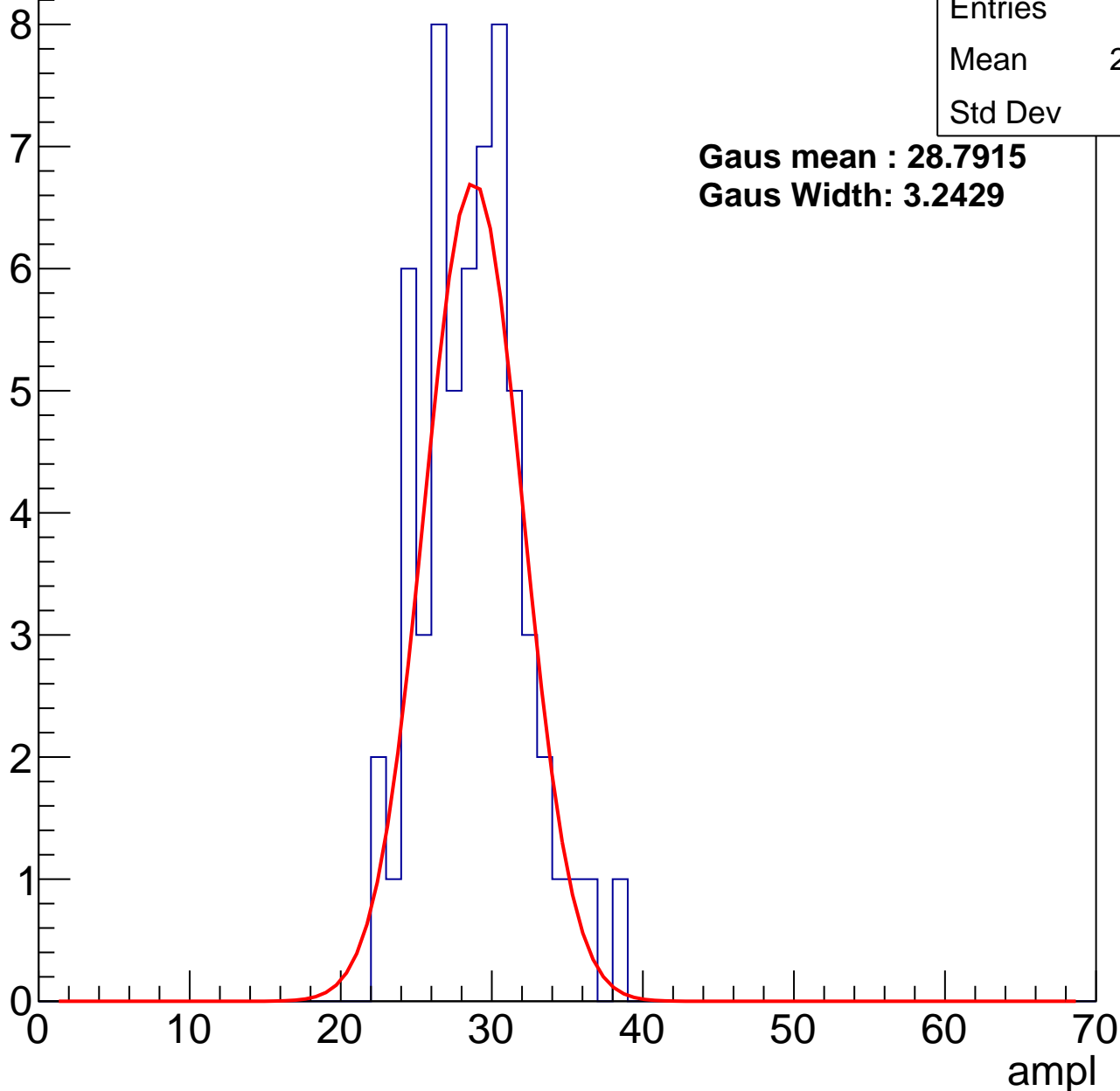
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.33
Std Dev	3.37

**Gaus mean : 28.7915**

**Gaus Width: 3.2429**



# B1L003S, U22-ch97, adc1

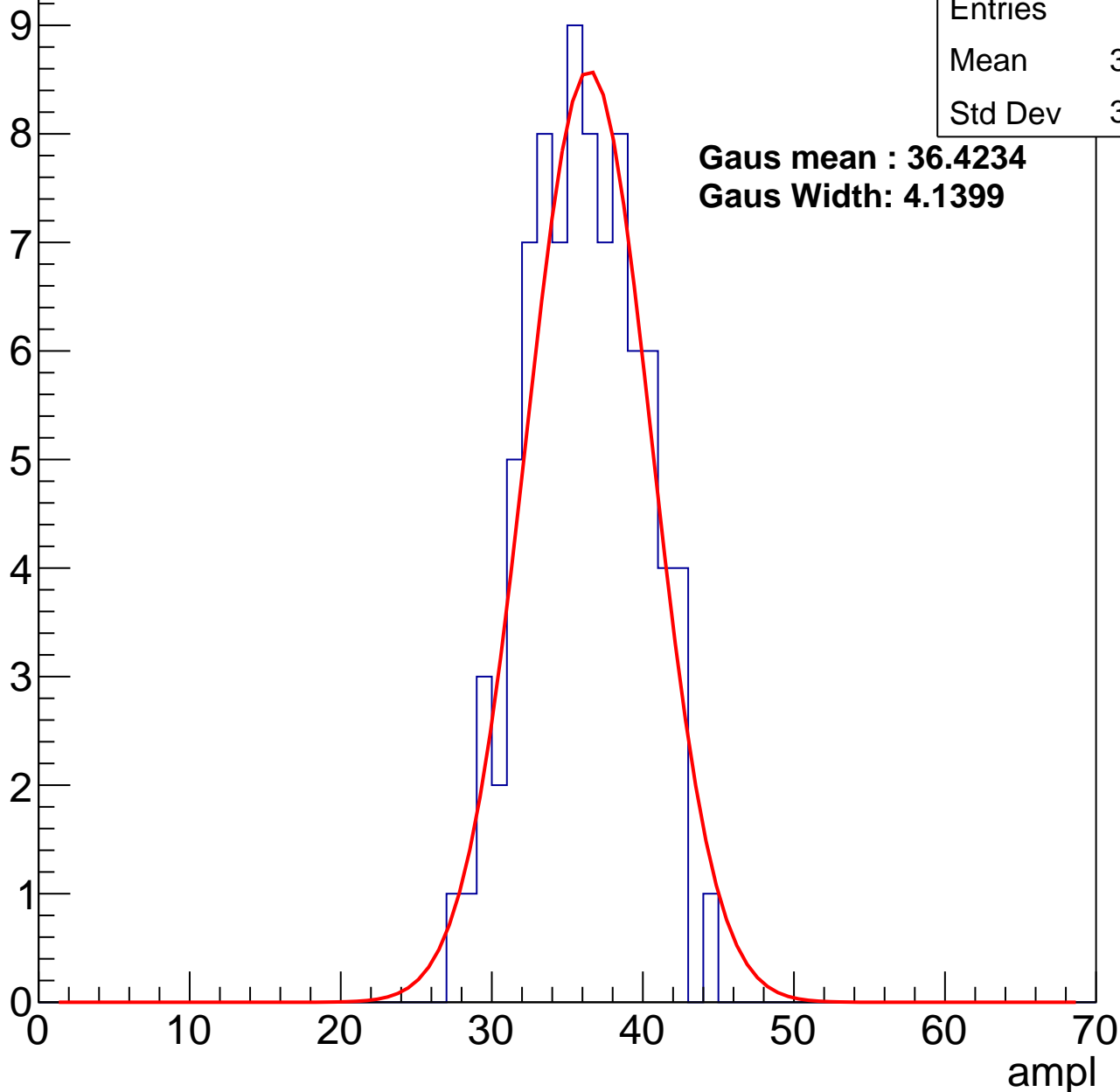
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	87
Mean	35.62
Std Dev	3.705

**Gaus mean : 36.4234**

**Gaus Width: 4.1399**



# B1L003S, U22-ch97, adc2

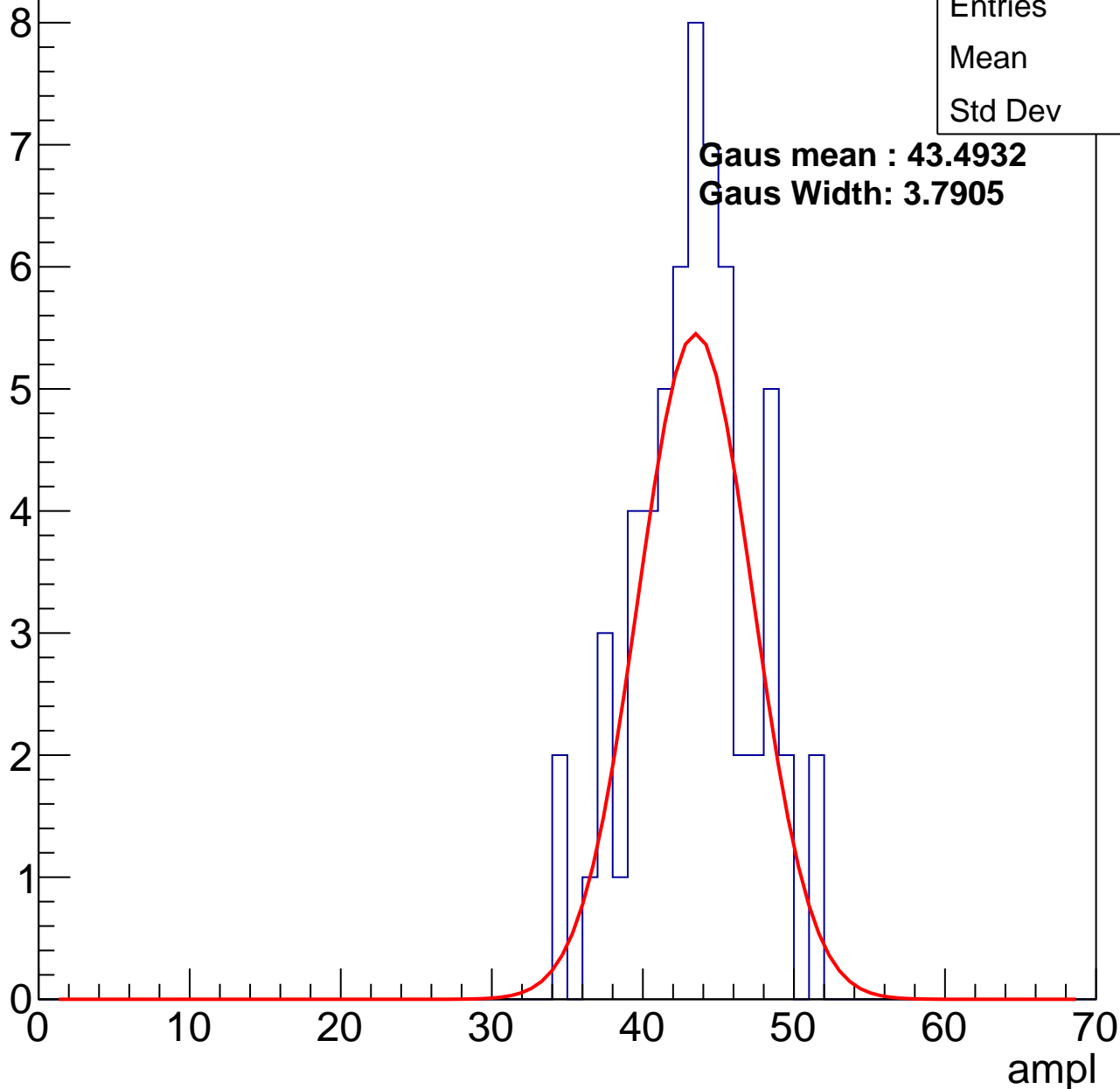
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.9
Std Dev	3.82

**Gaus mean : 43.4932**

**Gaus Width: 3.7905**

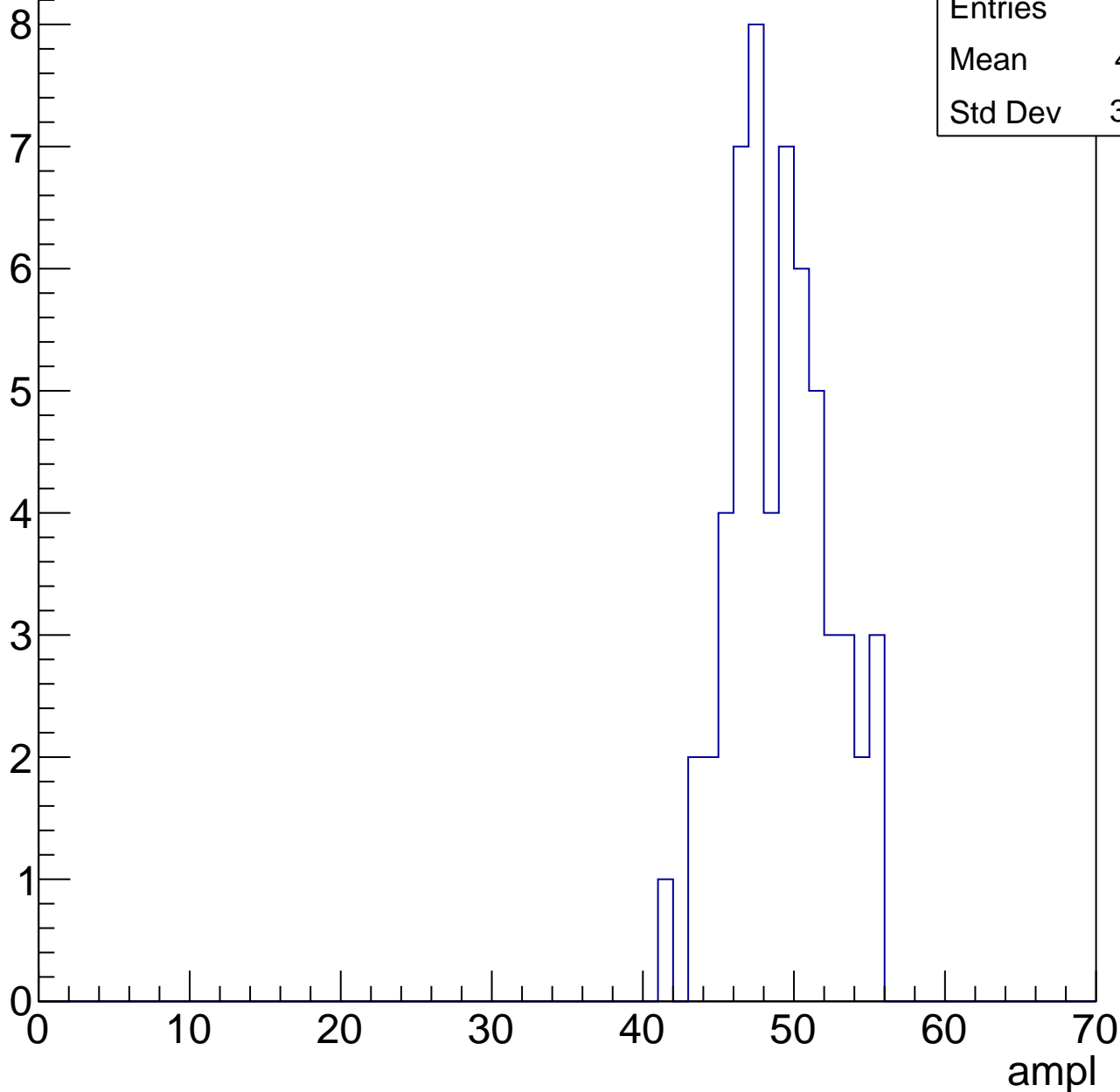


# B1L003S, U22-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	48.61
Std Dev	3.243

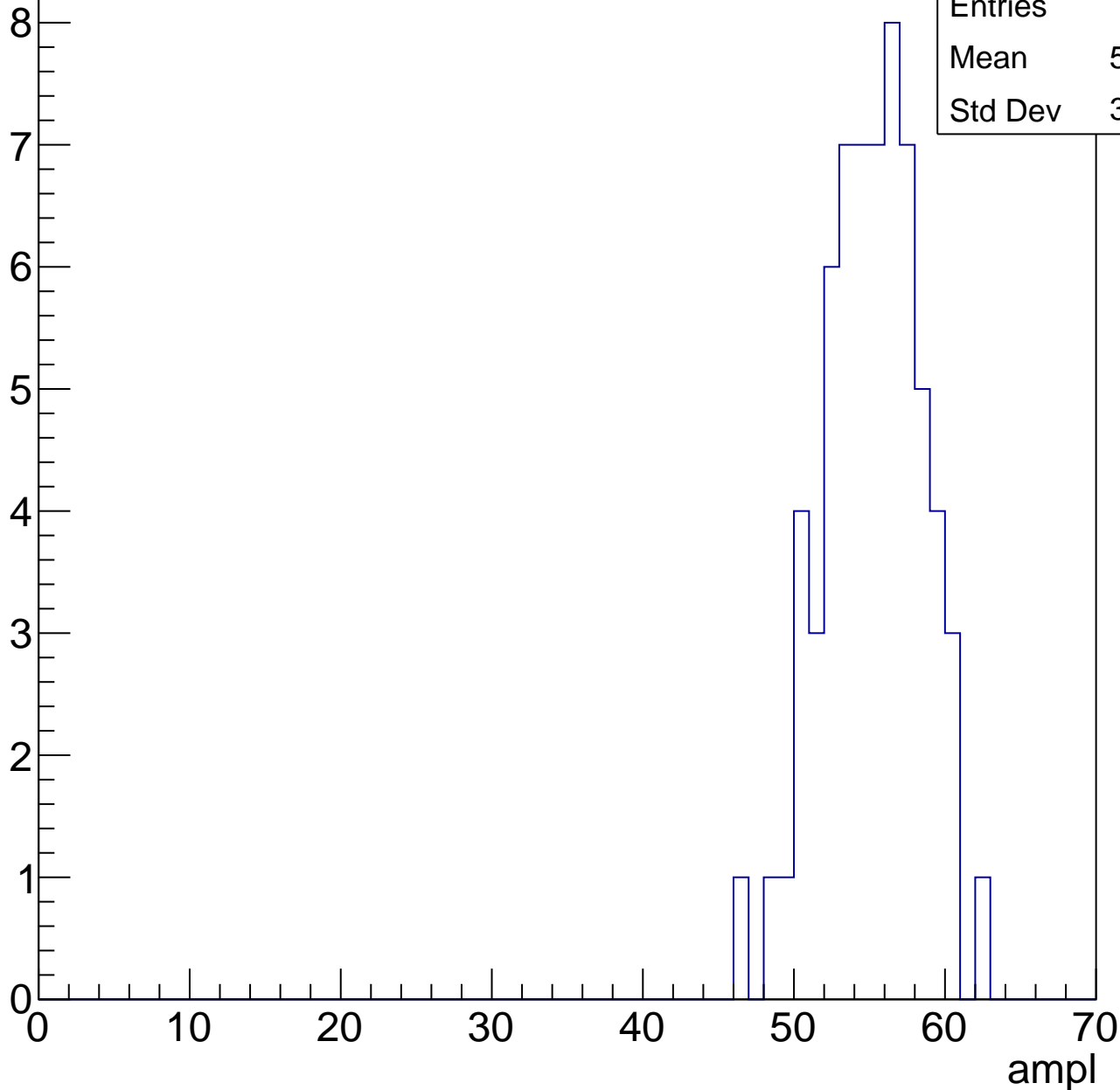


# B1L003S, U22-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.72
Std Dev	3.203

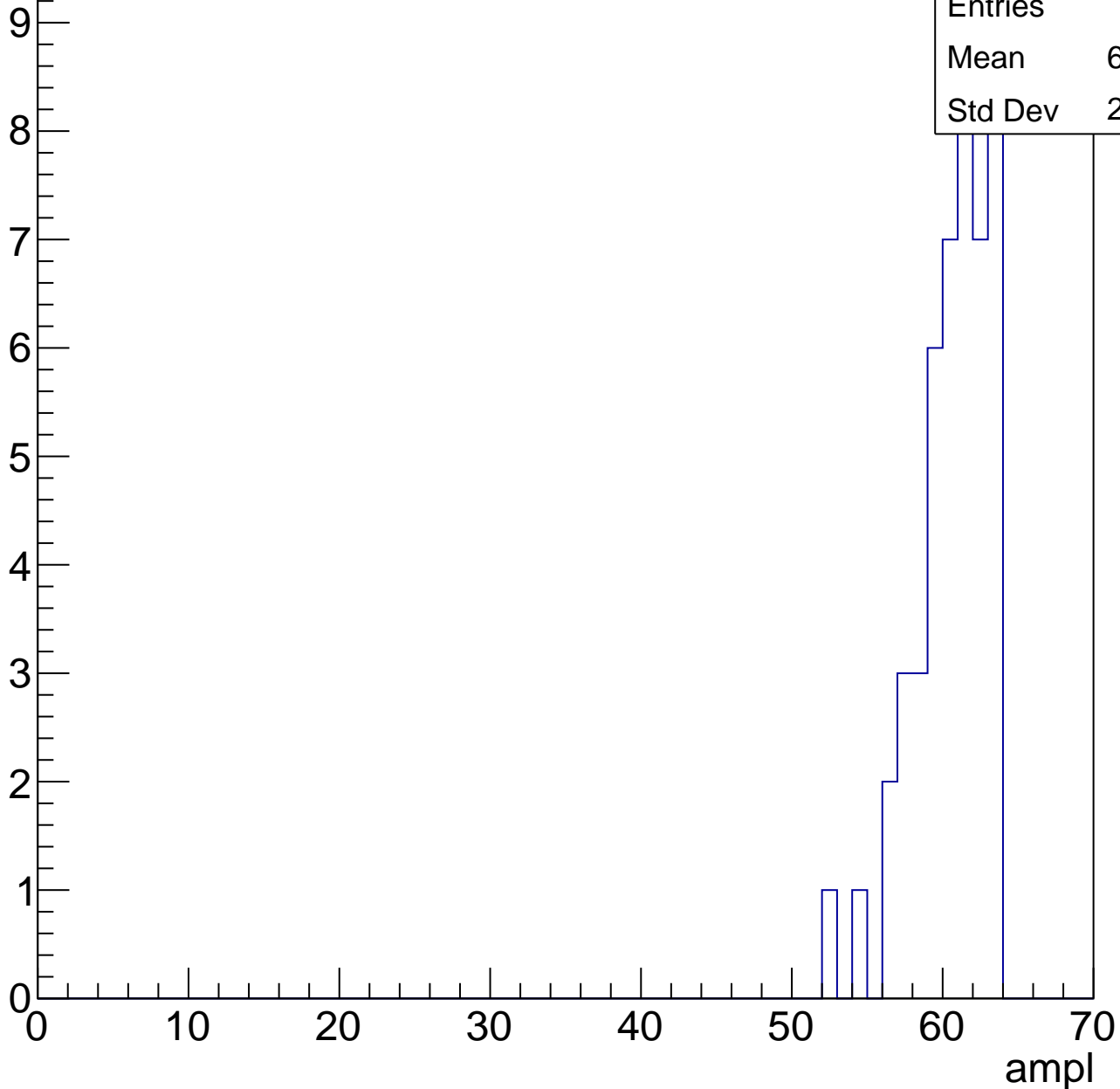


# B1L003S, U22-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	60.09
Std Dev	2.465



# B1L003S, U22-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

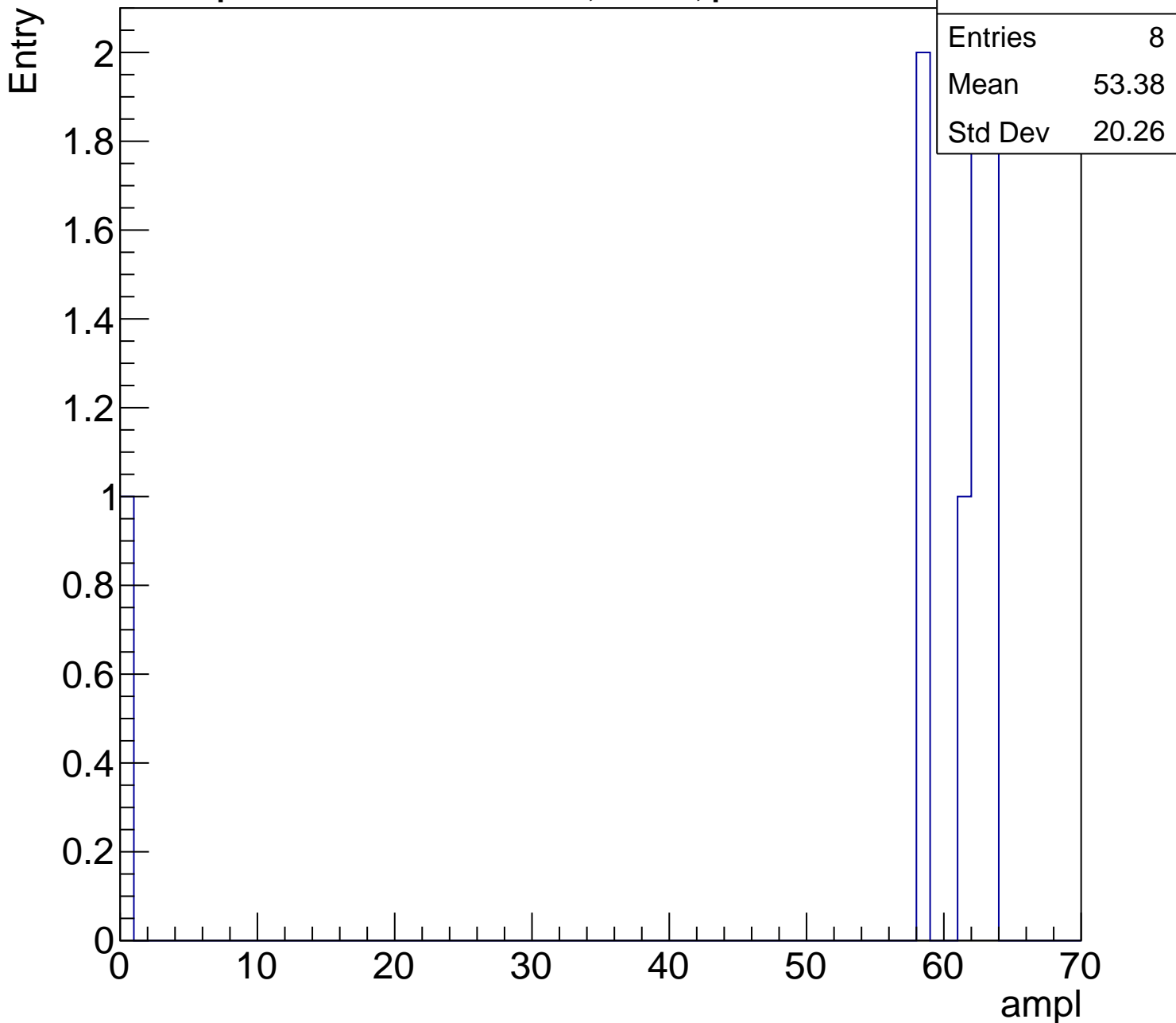
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.38
Std Dev	20.26

0 10 20 30 40 50 60 70

ampl





# B1L003S, U22-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L003S, U22-ch98, adc0

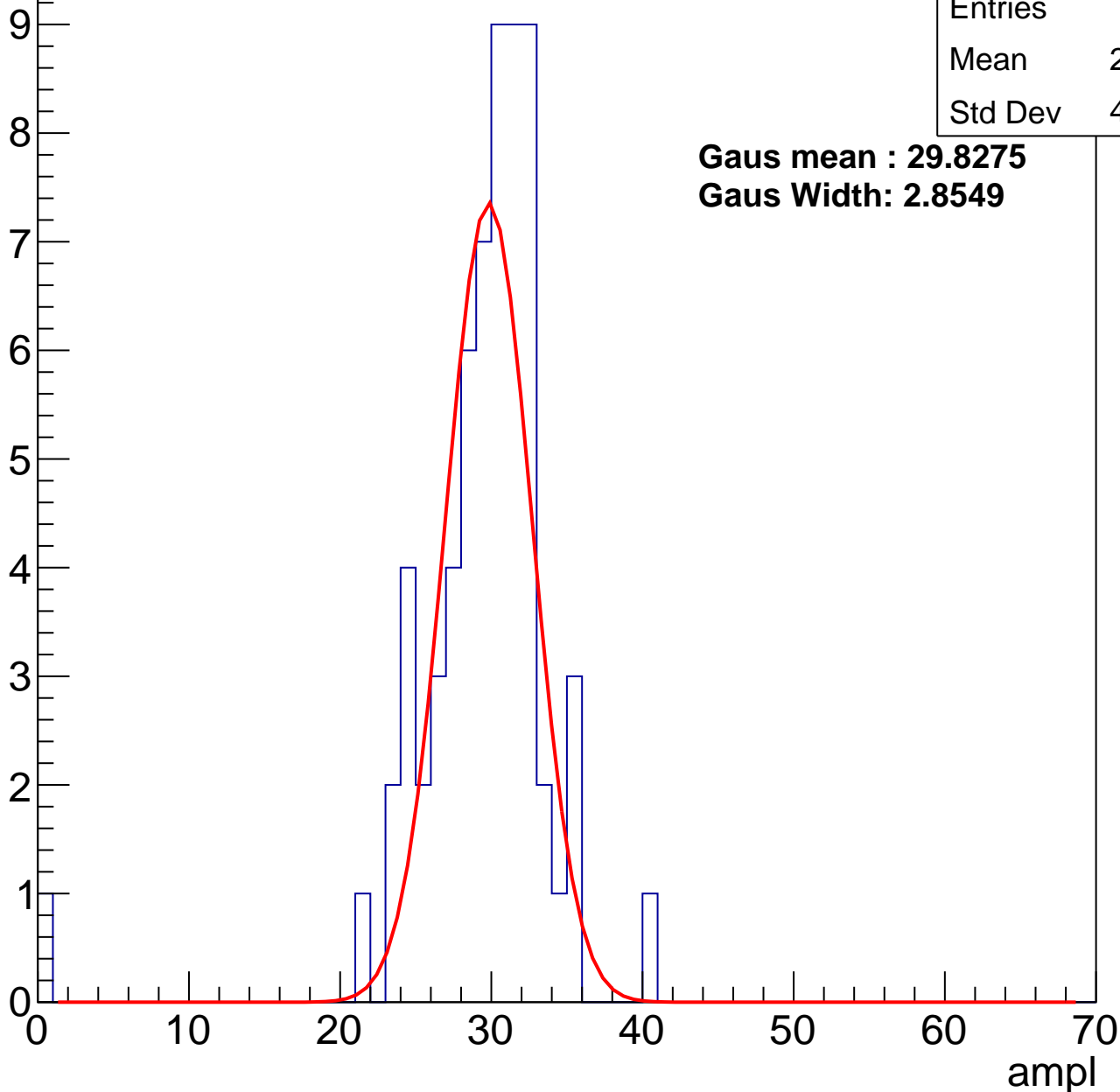
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.94
Std Dev	4.953

**Gaus mean : 29.8275**

**Gaus Width: 2.8549**



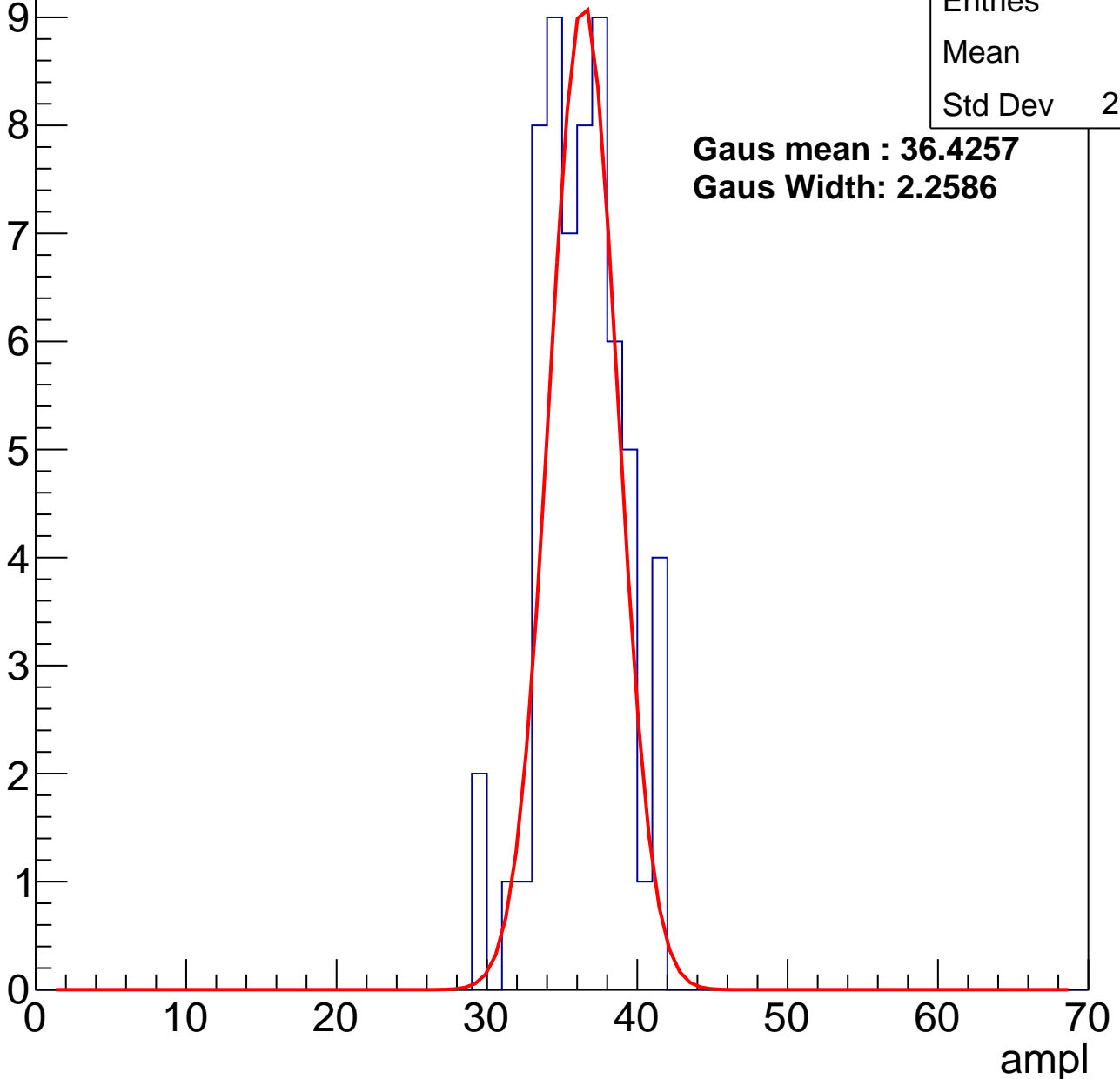
# B1L003S, U22-ch98, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	35.8
Std Dev	2.703

**Gaus mean : 36.4257**  
**Gaus Width: 2.2586**



# B1L003S, U22-ch98, adc2

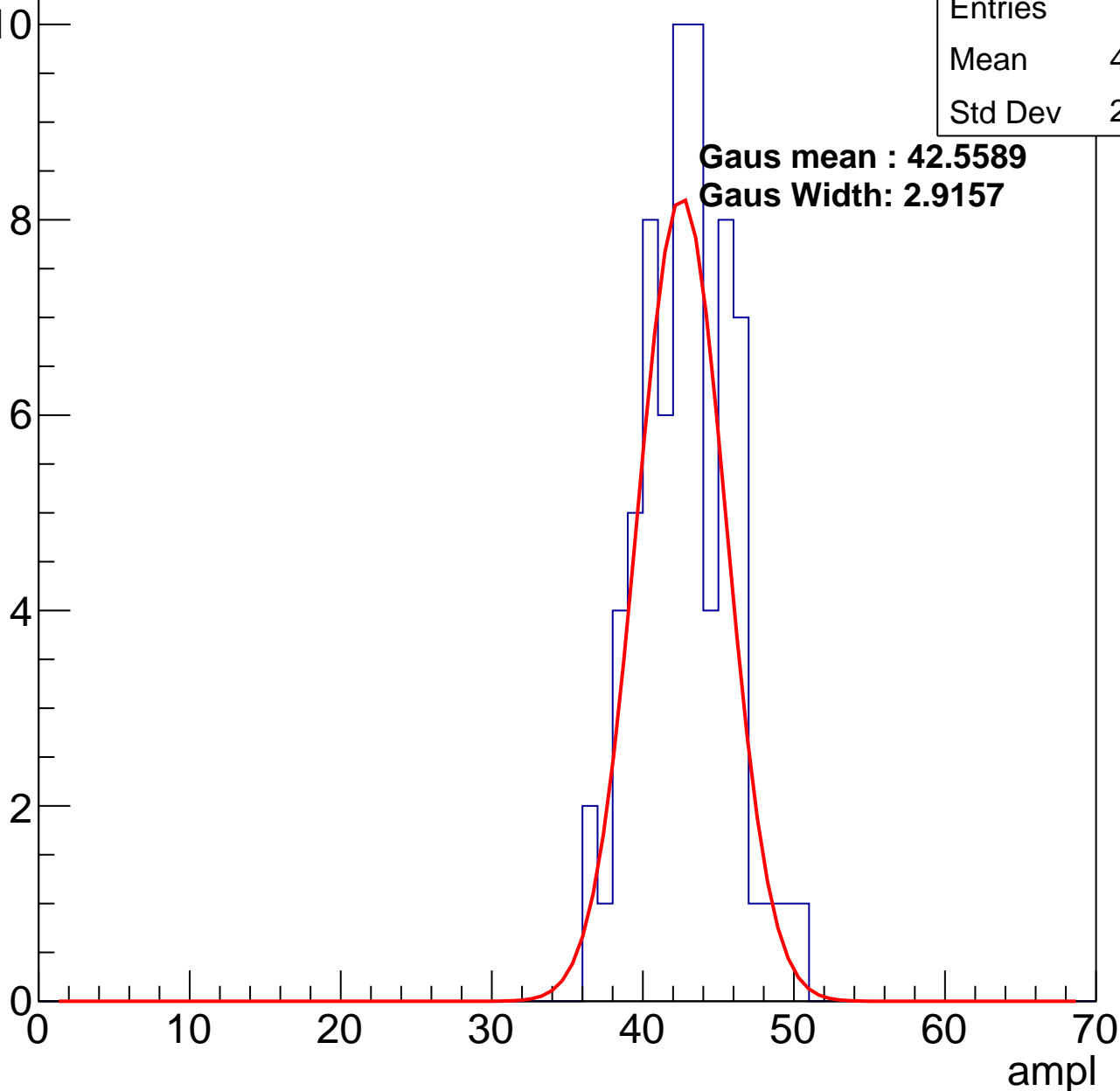
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.38
Std Dev	2.993

**Gaus mean : 42.5589**

**Gaus Width: 2.9157**

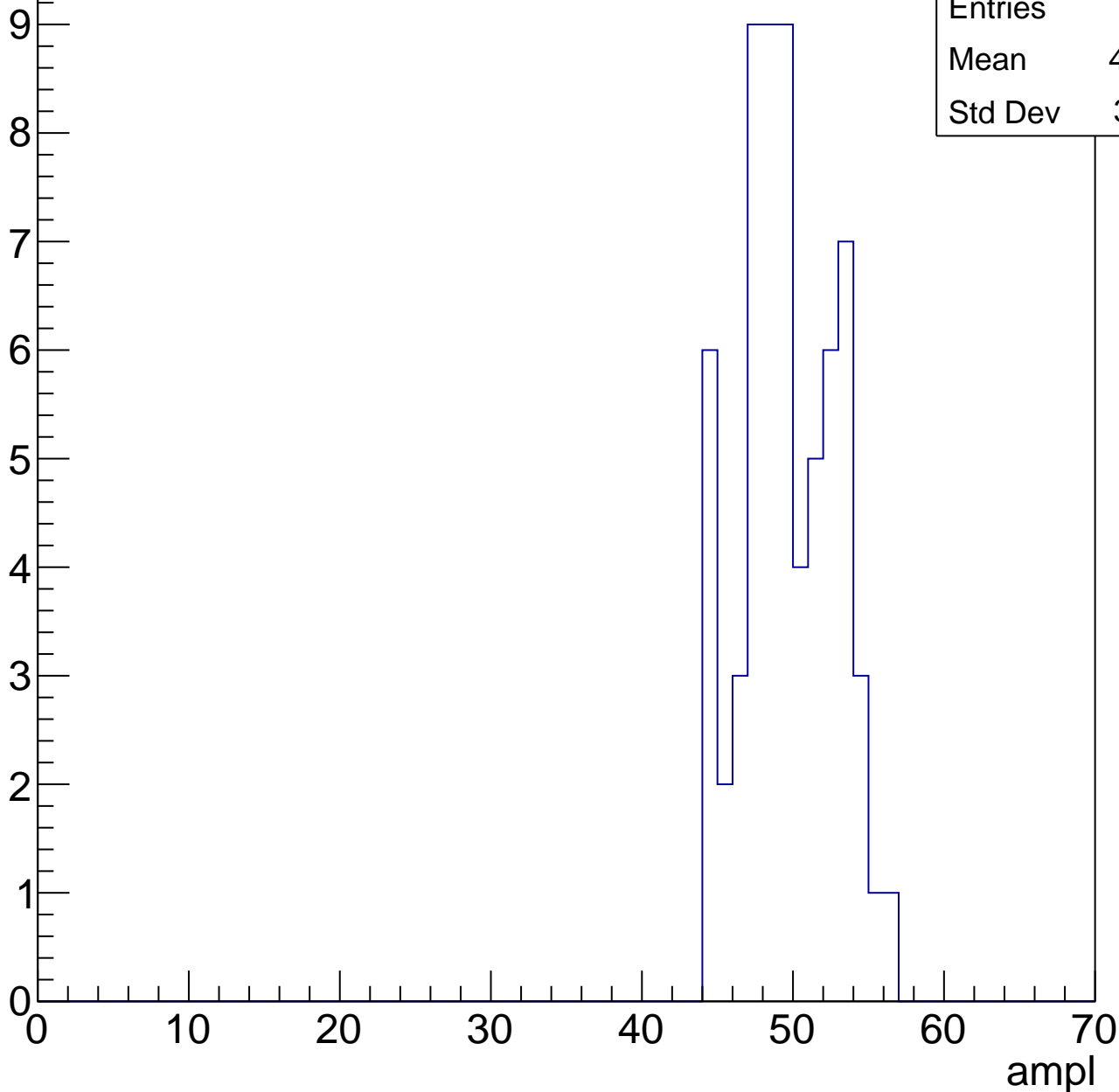


# B1L003S, U22-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	49.22
Std Dev	3.041

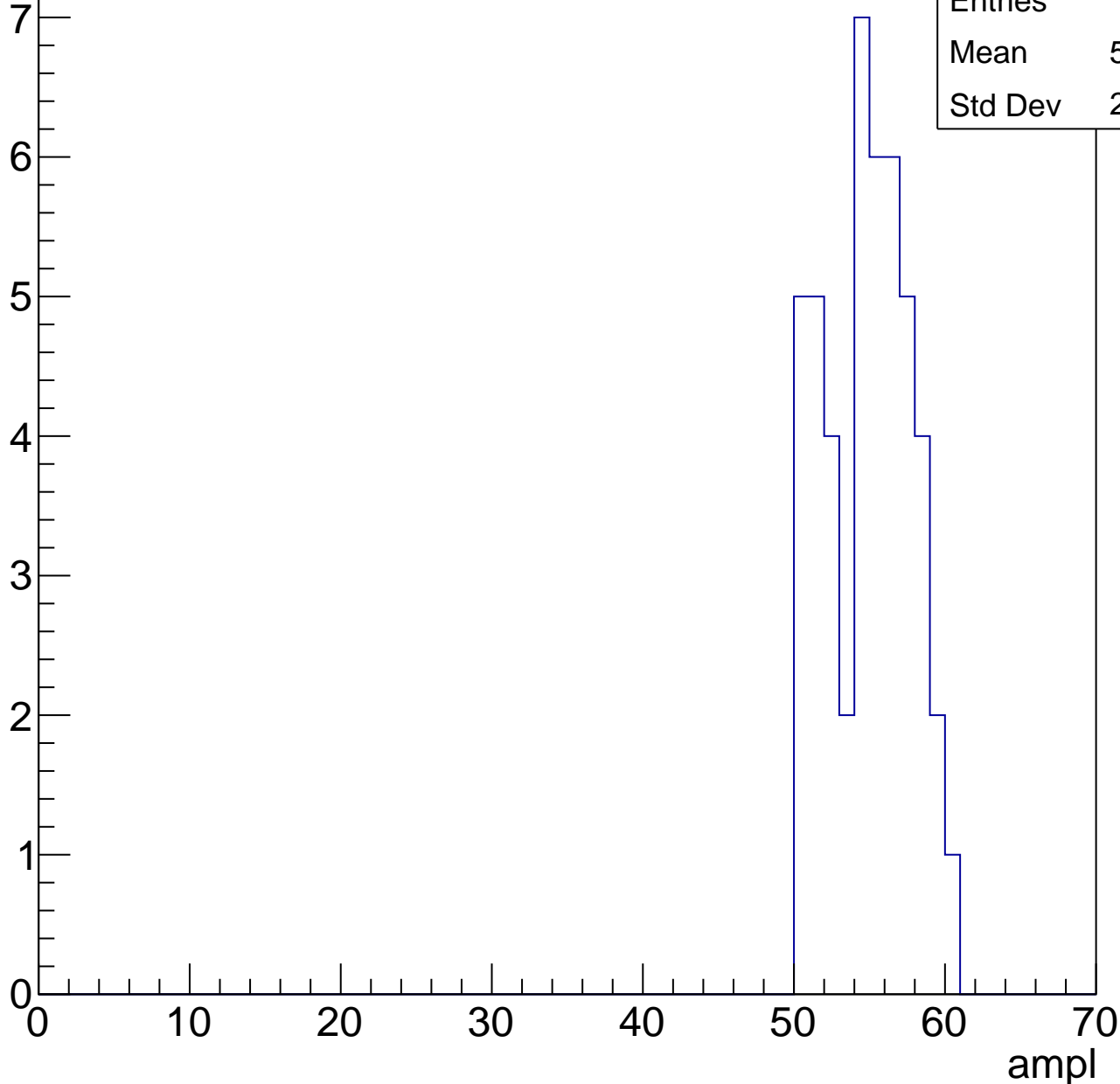


# B1L003S, U22-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	54.43
Std Dev	2.766

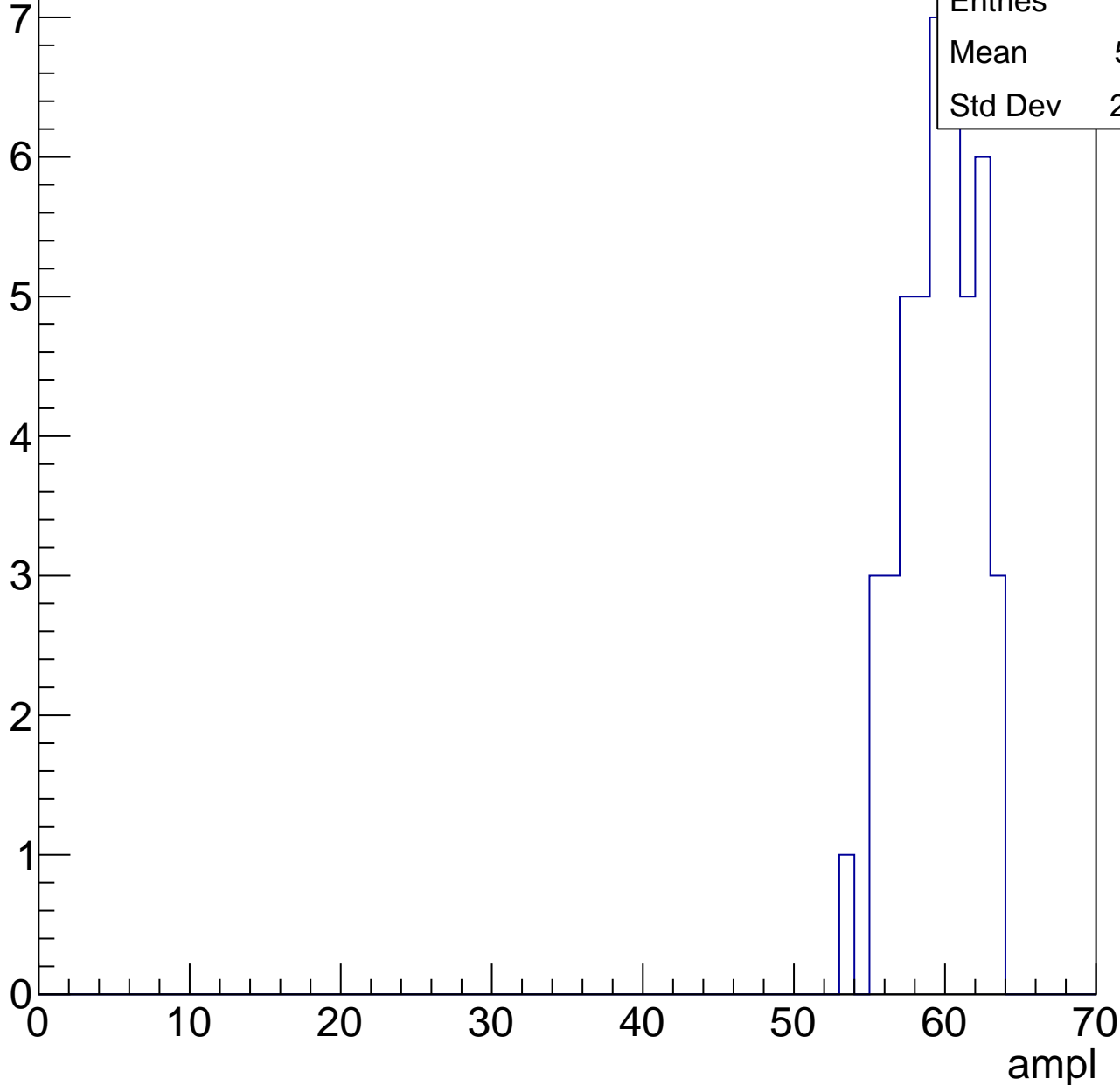


# B1L003S, U22-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.11
Std Dev	2.424

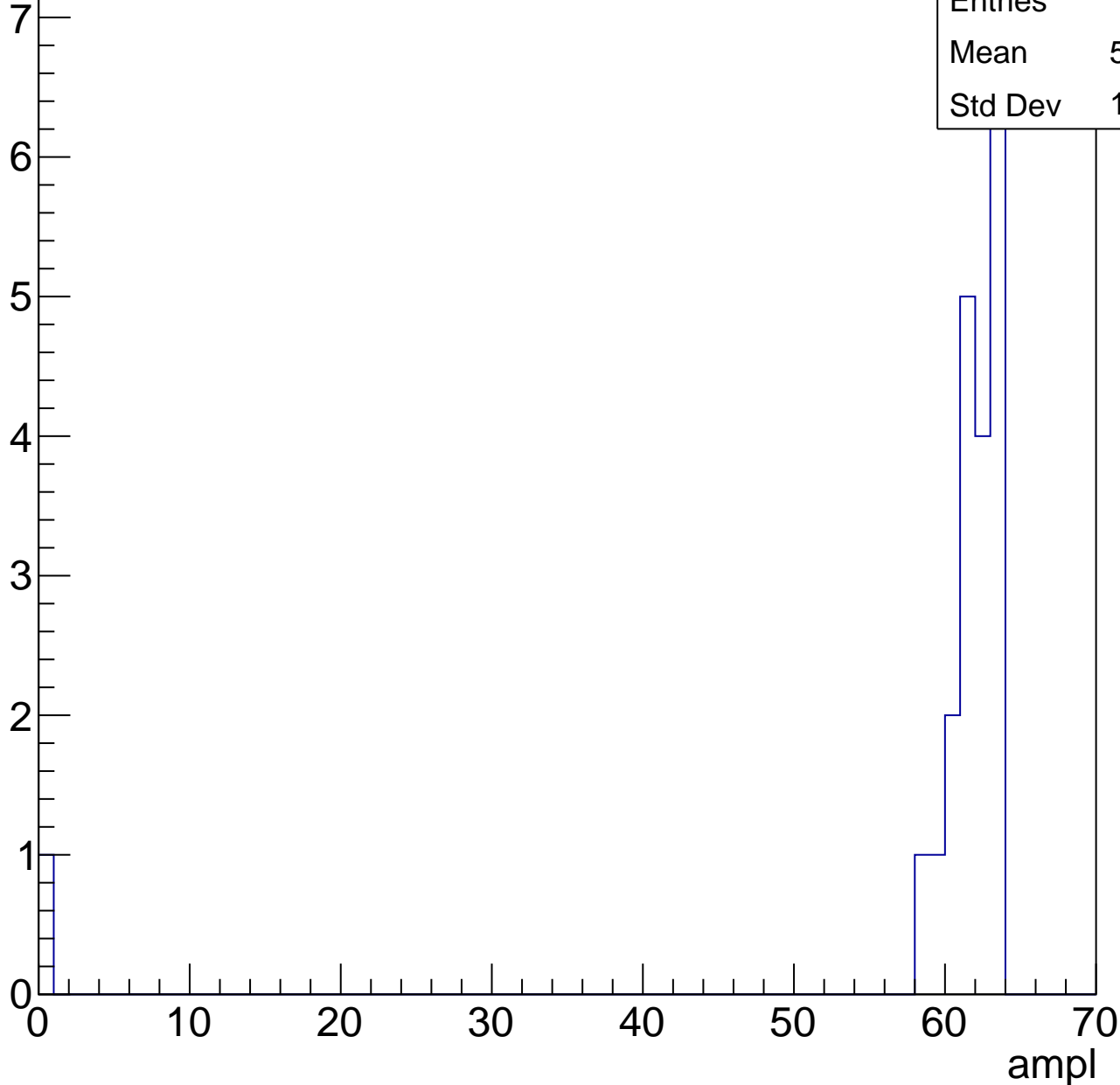


# B1L003S, U22-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	21
Mean	58.62
Std Dev	13.18





# B1L003S, U22-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch99, adc0

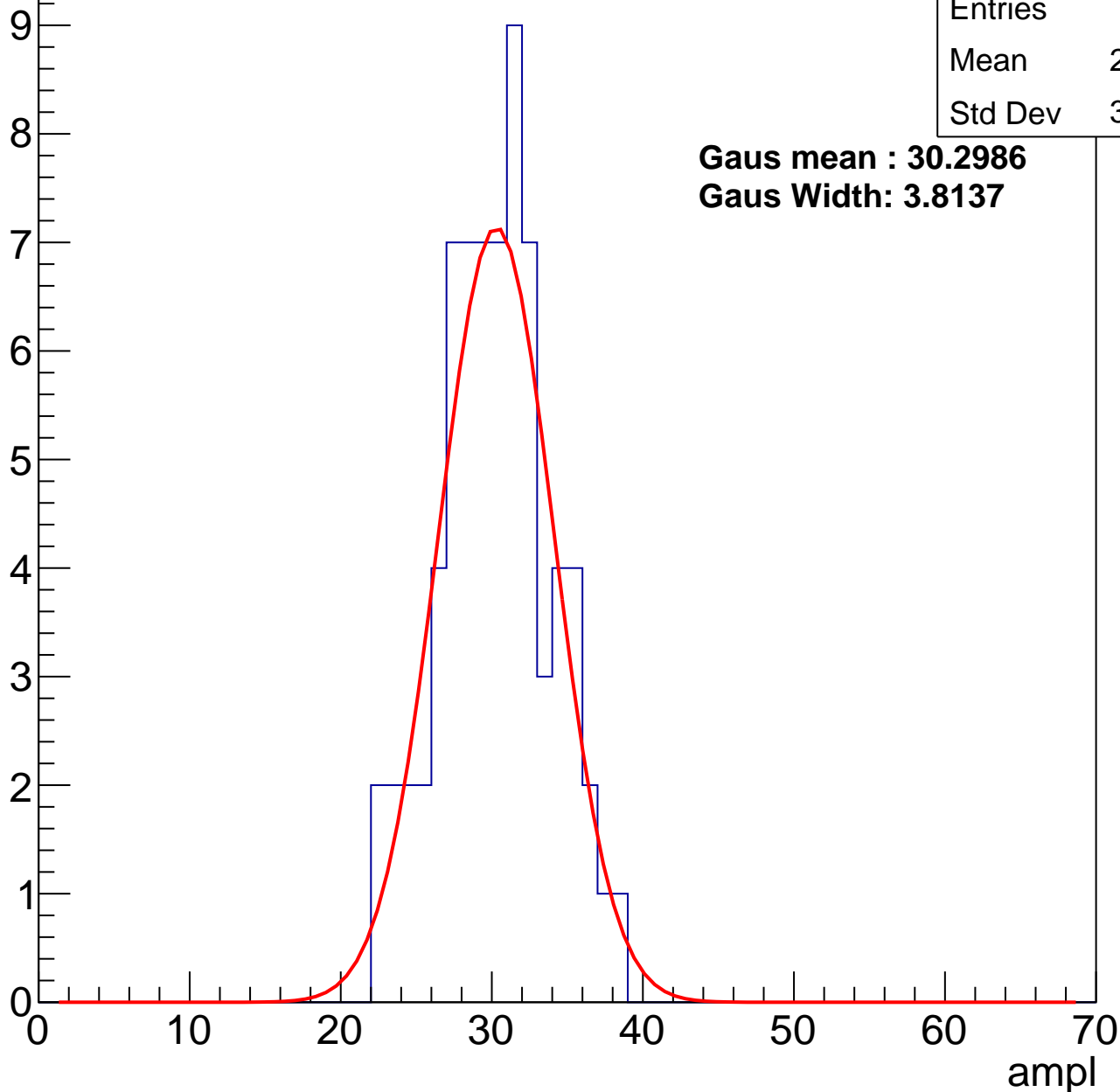
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29.79
Std Dev	3.599

**Gaus mean : 30.2986**

**Gaus Width: 3.8137**



# B1L003S, U22-ch99, adc1

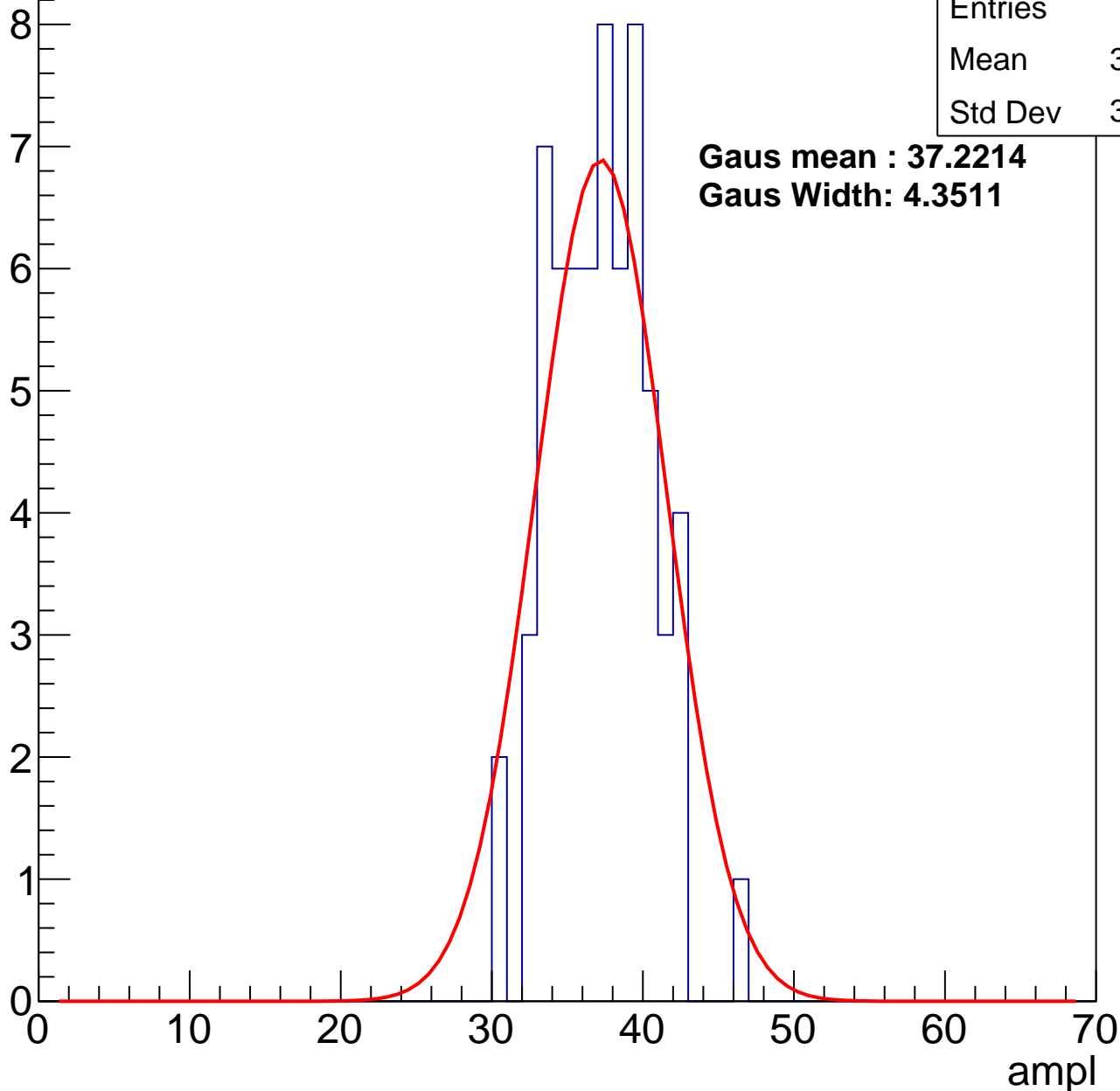
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.77
Std Dev	3.229

**Gaus mean : 37.2214**

**Gaus Width: 4.3511**



# B1L003S, U22-ch99, adc2

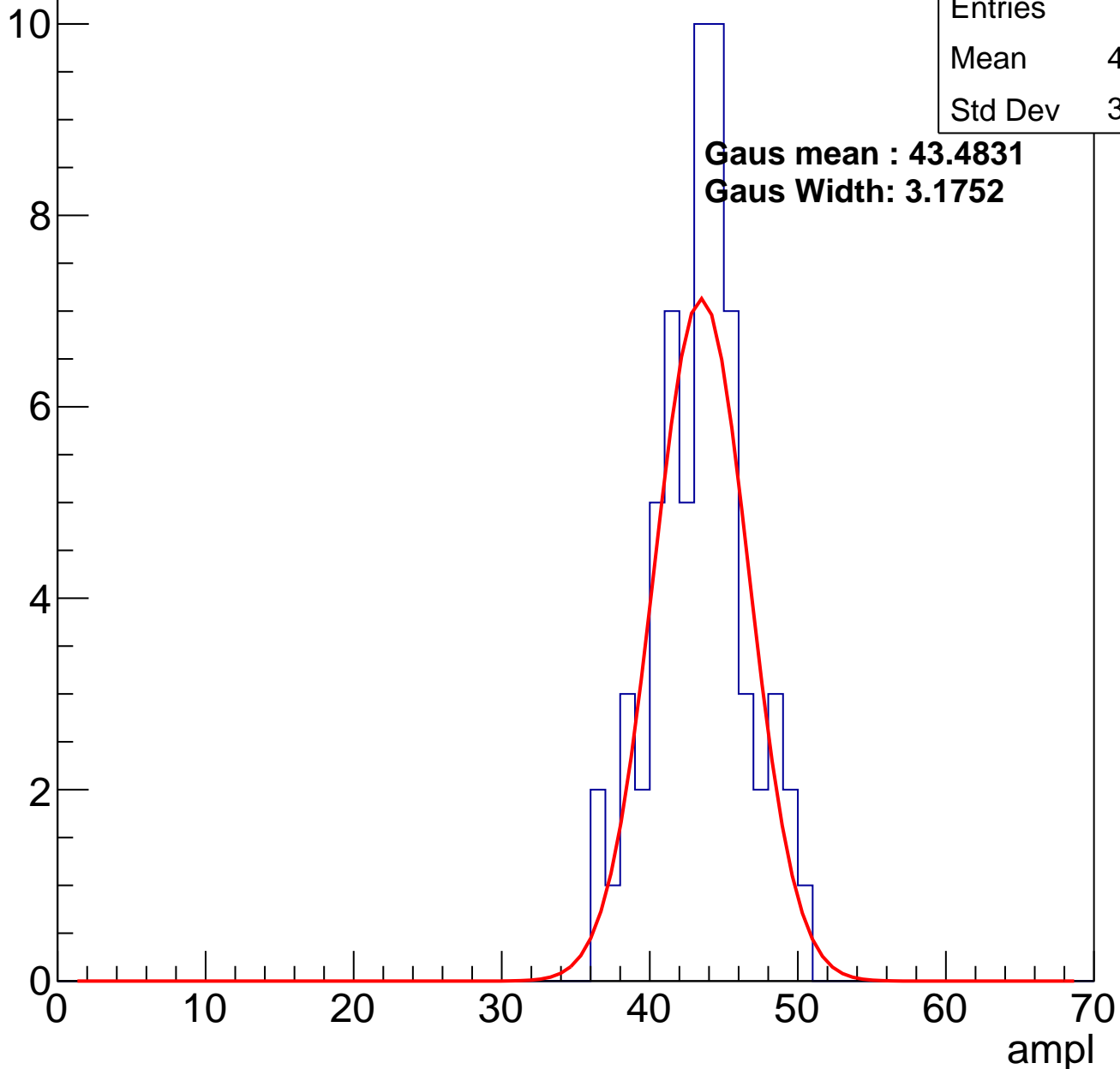
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	63
Mean	42.97
Std Dev	3.117

**Gaus mean : 43.4831**

**Gaus Width: 3.1752**

Entry

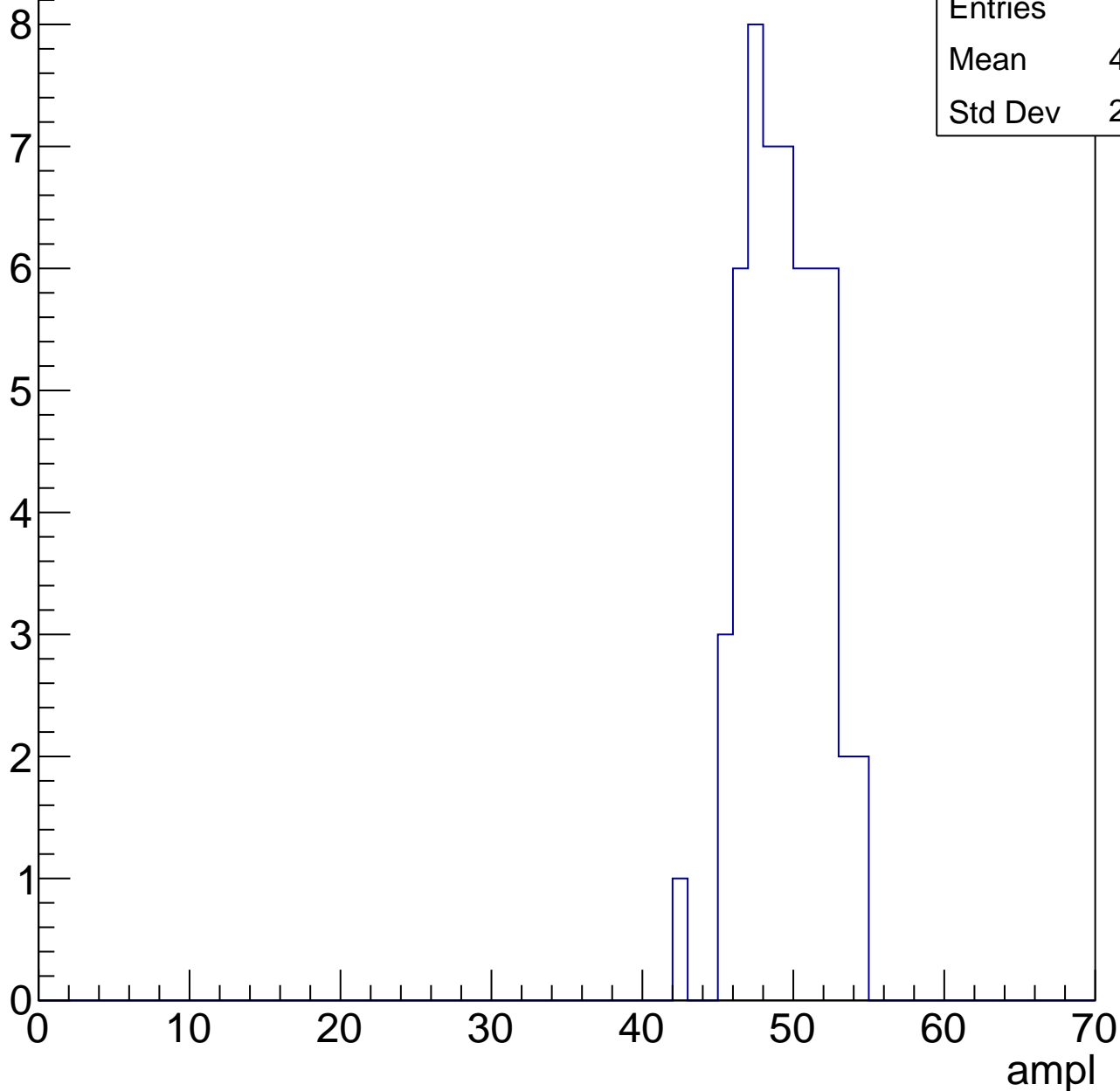


# B1L003S, U22-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.89
Std Dev	2.565



# B1L003S, U22-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

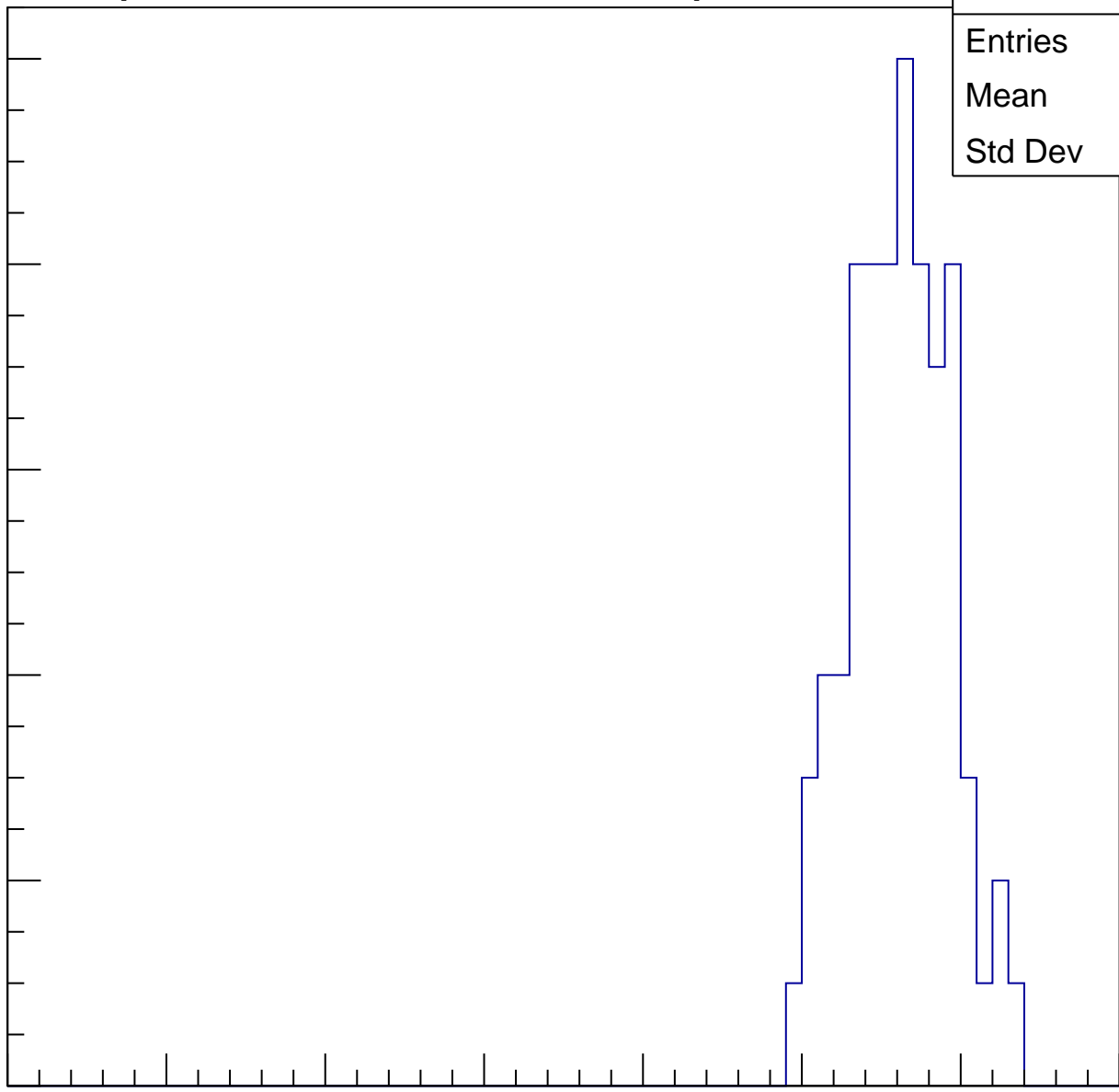
Entries	76
Mean	55.64
Std Dev	3.089

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

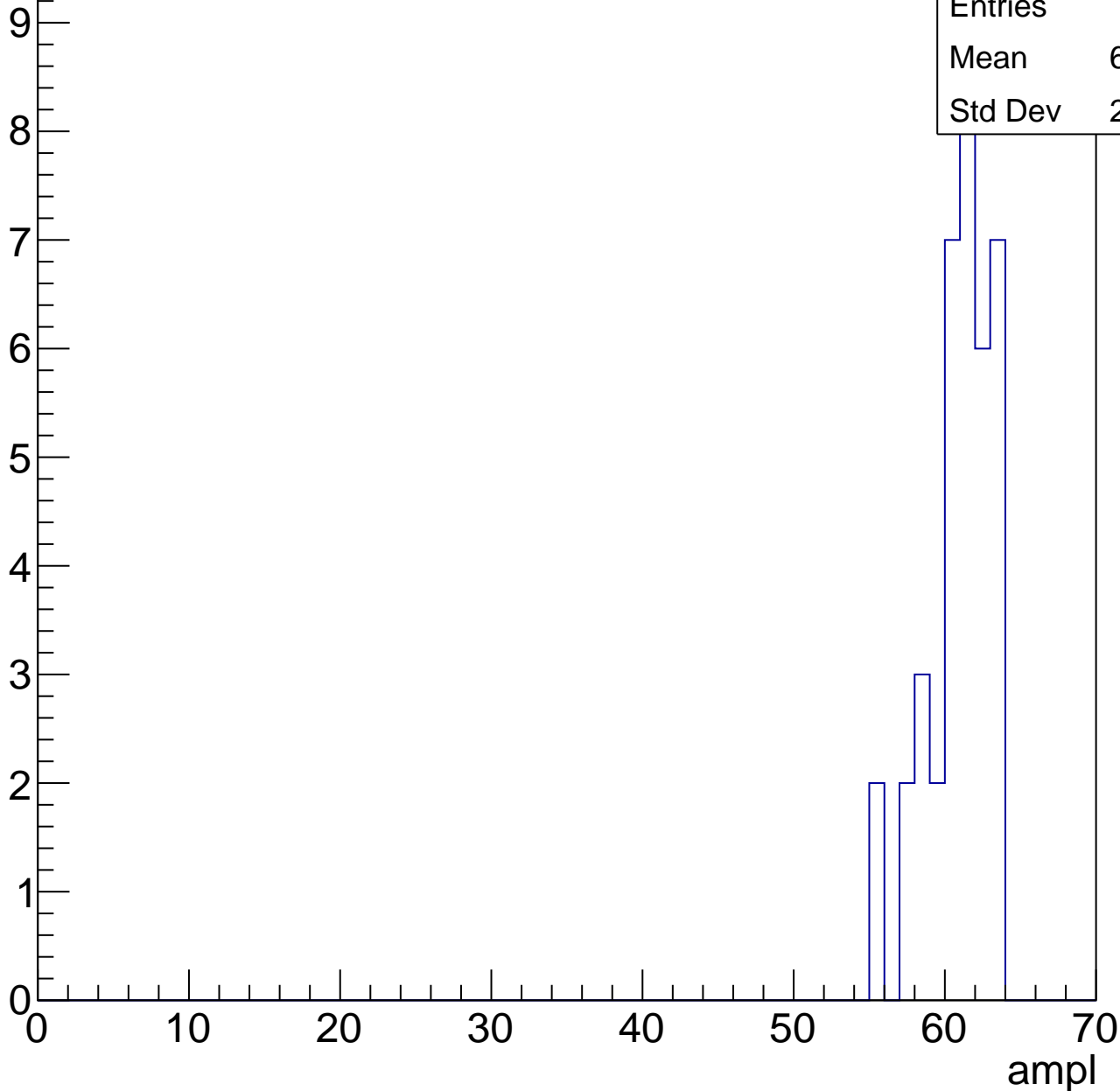


# B1L003S, U22-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

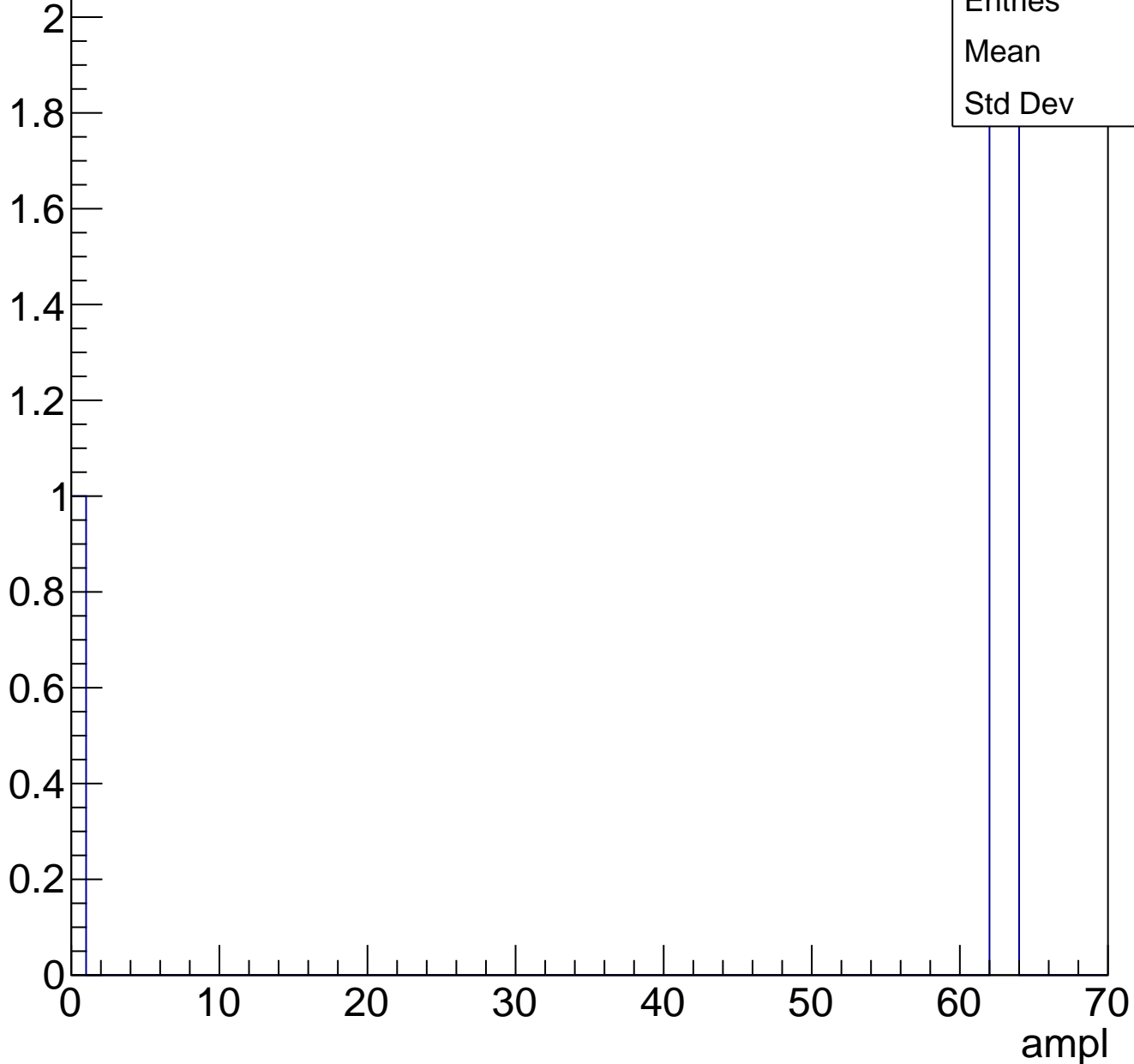
Entries	38
Mean	60.47
Std Dev	2.112



# B1L003S, U22-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	77
Mean	28.64
Std Dev	3.486

**Gaus mean : 29.2575**

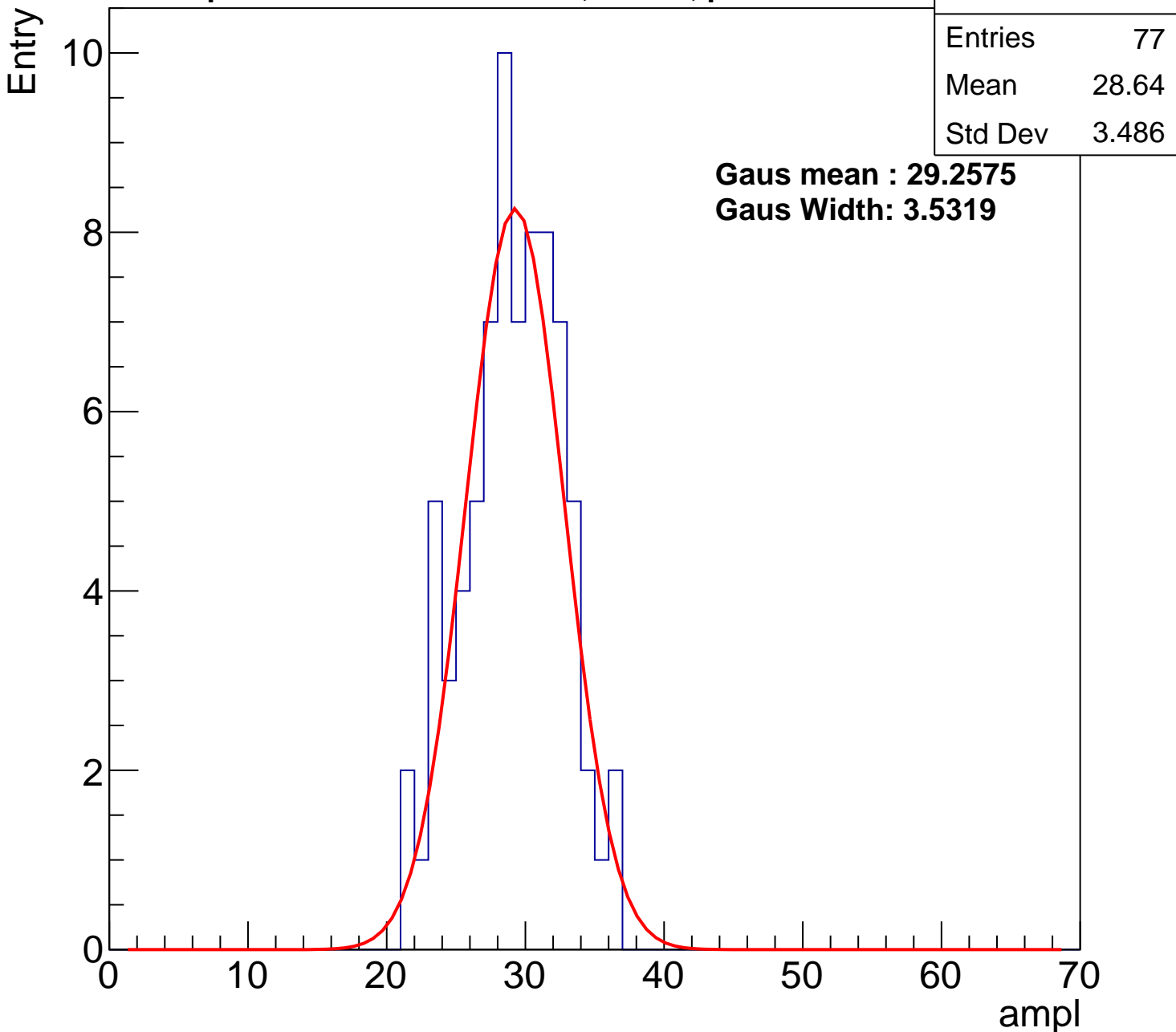
**Gaus Width: 3.5319**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch100, adc1

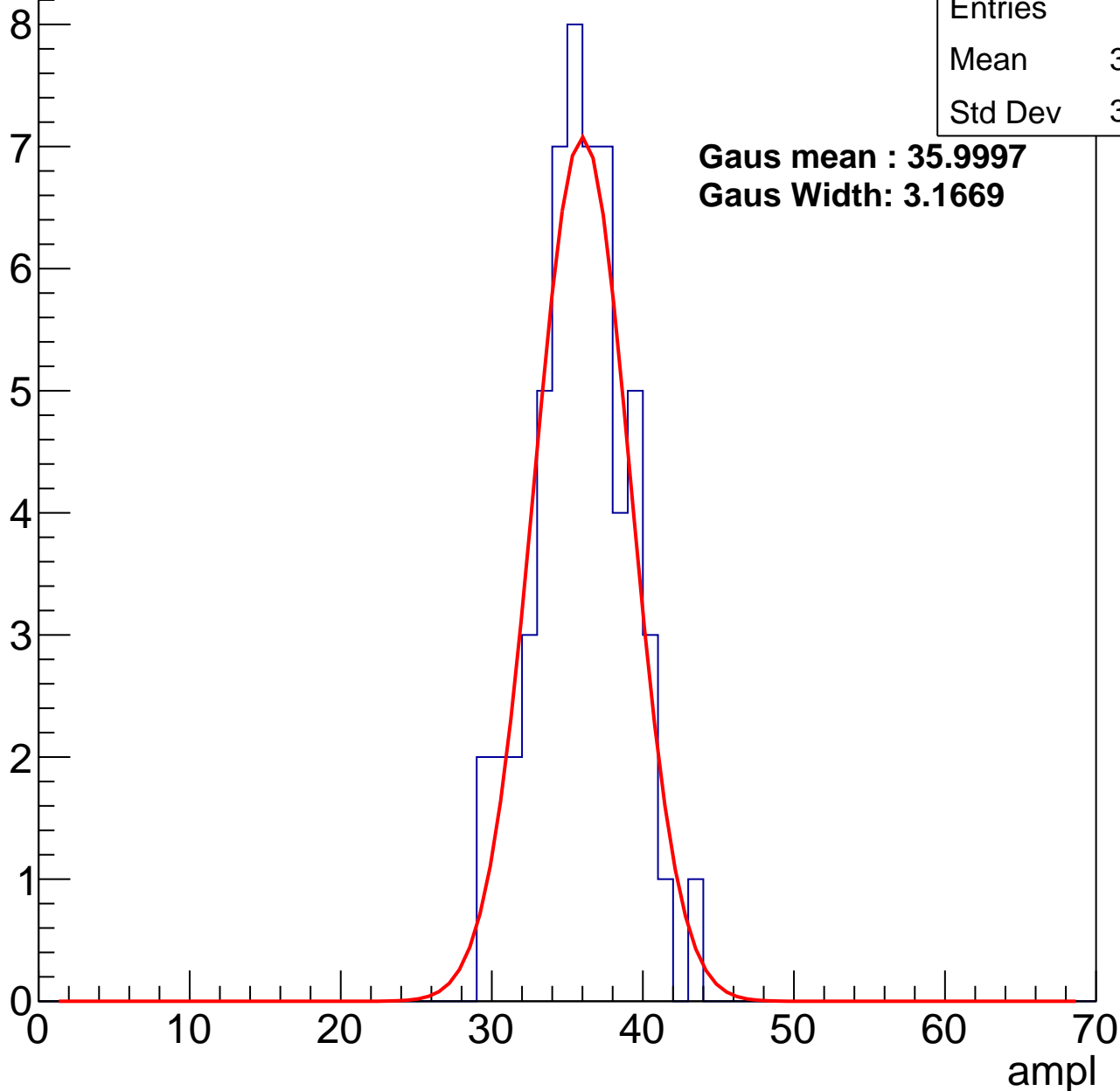
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.46
Std Dev	3.027

**Gaus mean : 35.9997**

**Gaus Width: 3.1669**



# B1L003S, U22-ch100, adc2

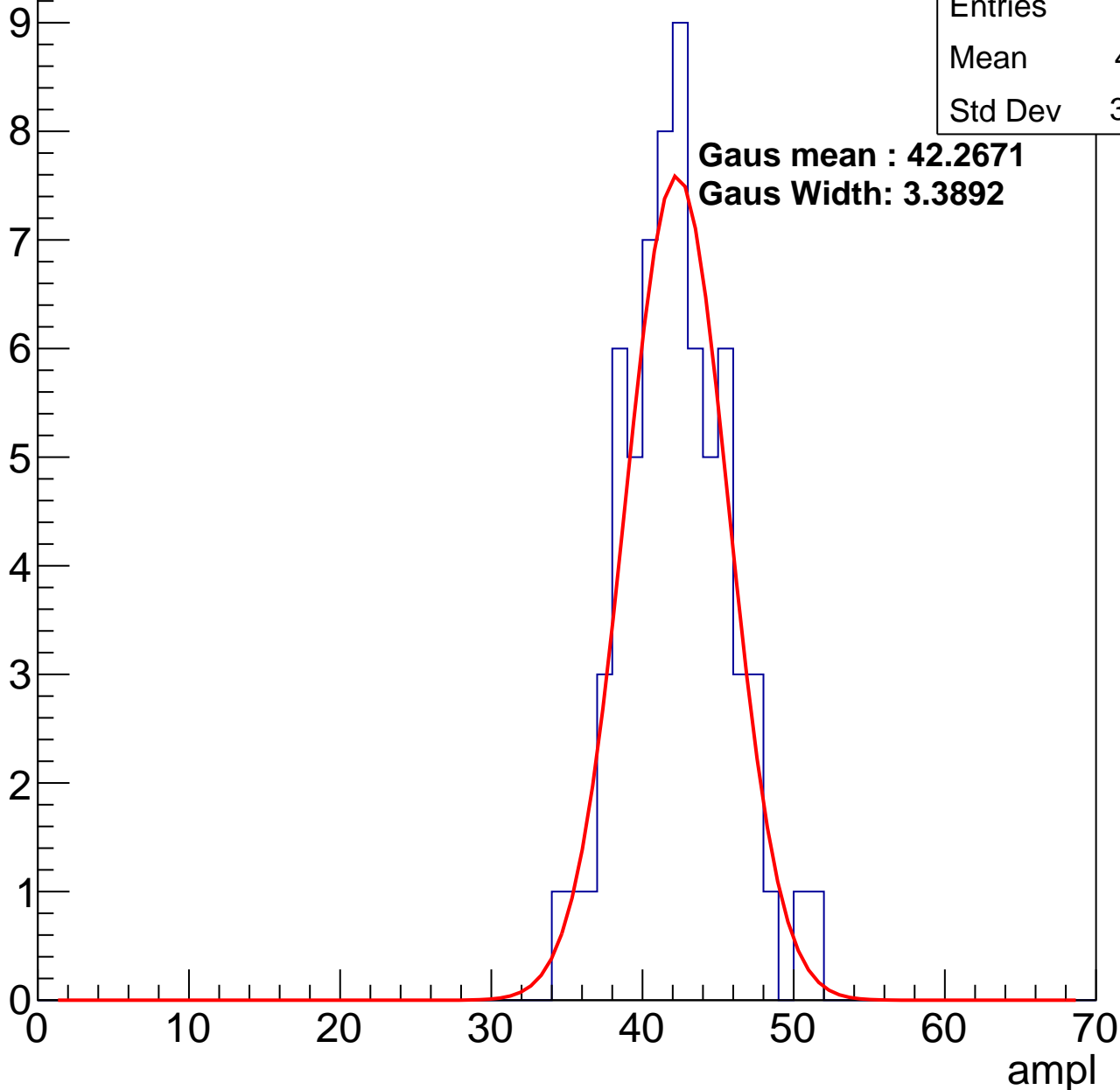
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.81
Std Dev	3.417

**Gaus mean : 42.2671**

**Gaus Width: 3.3892**

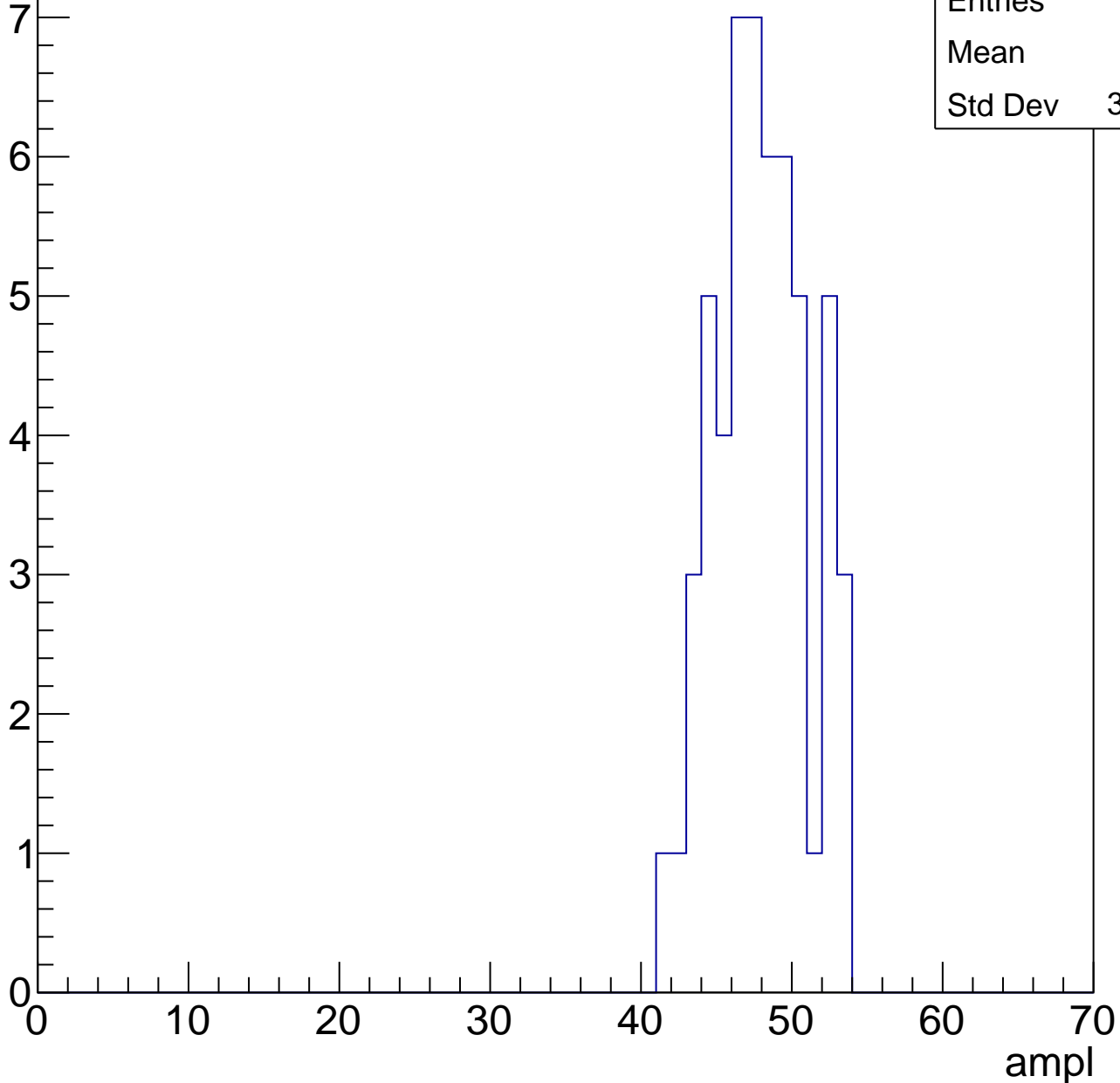


# B1L003S, U22-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	47.5
Std Dev	3.005

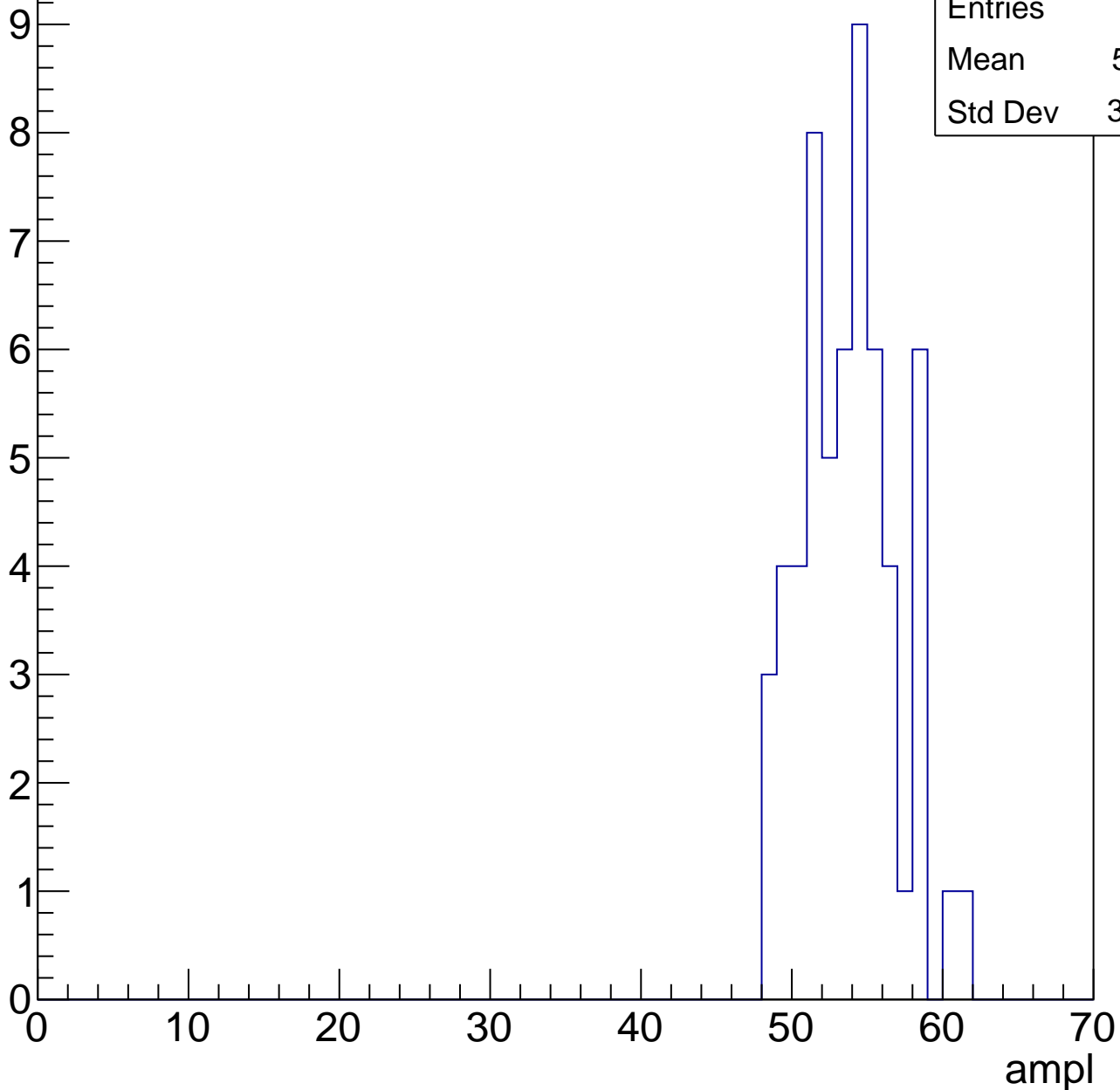


# B1L003S, U22-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

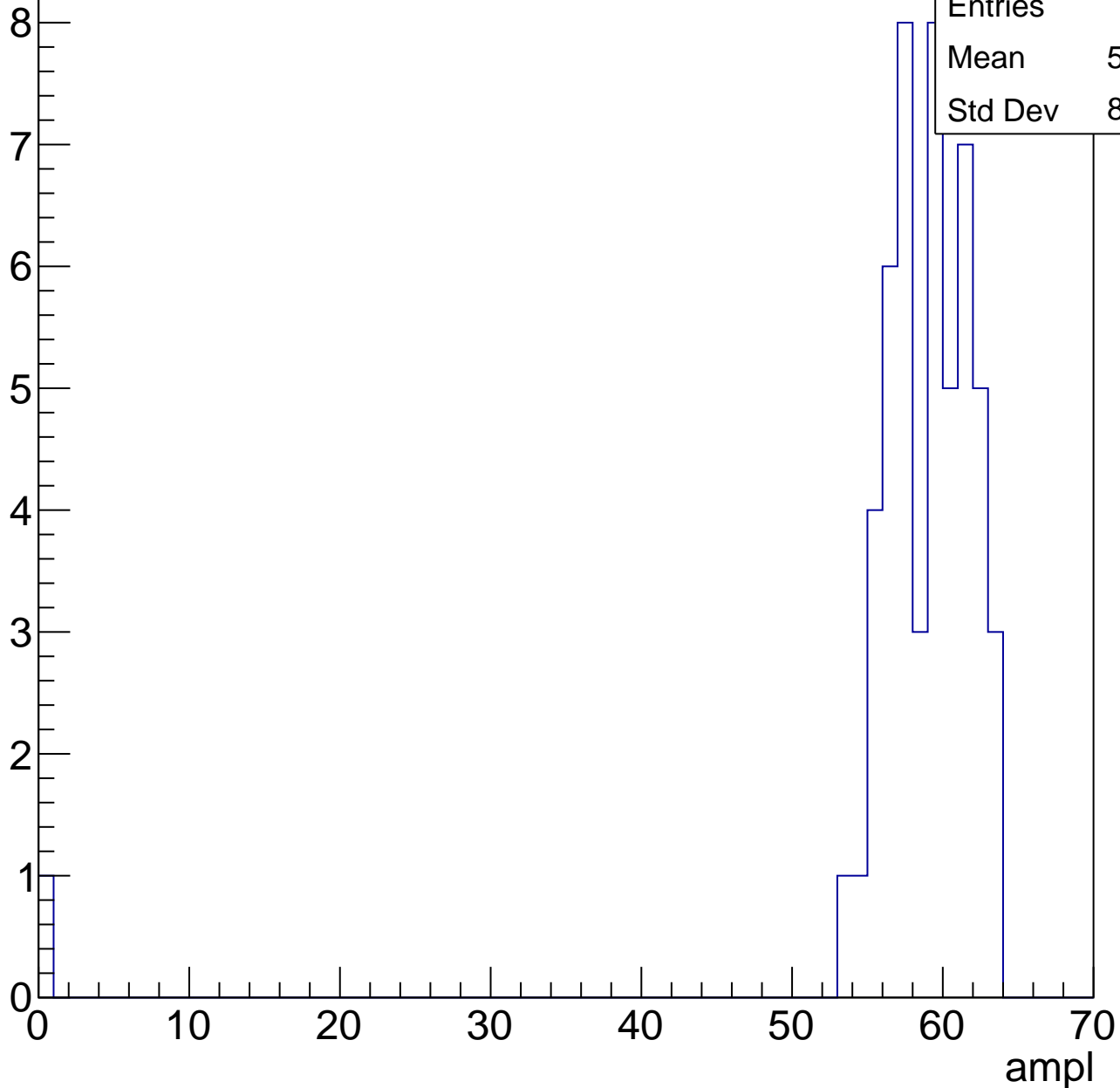
Entries	58
Mean	53.31
Std Dev	3.092



# B1L003S, U22-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

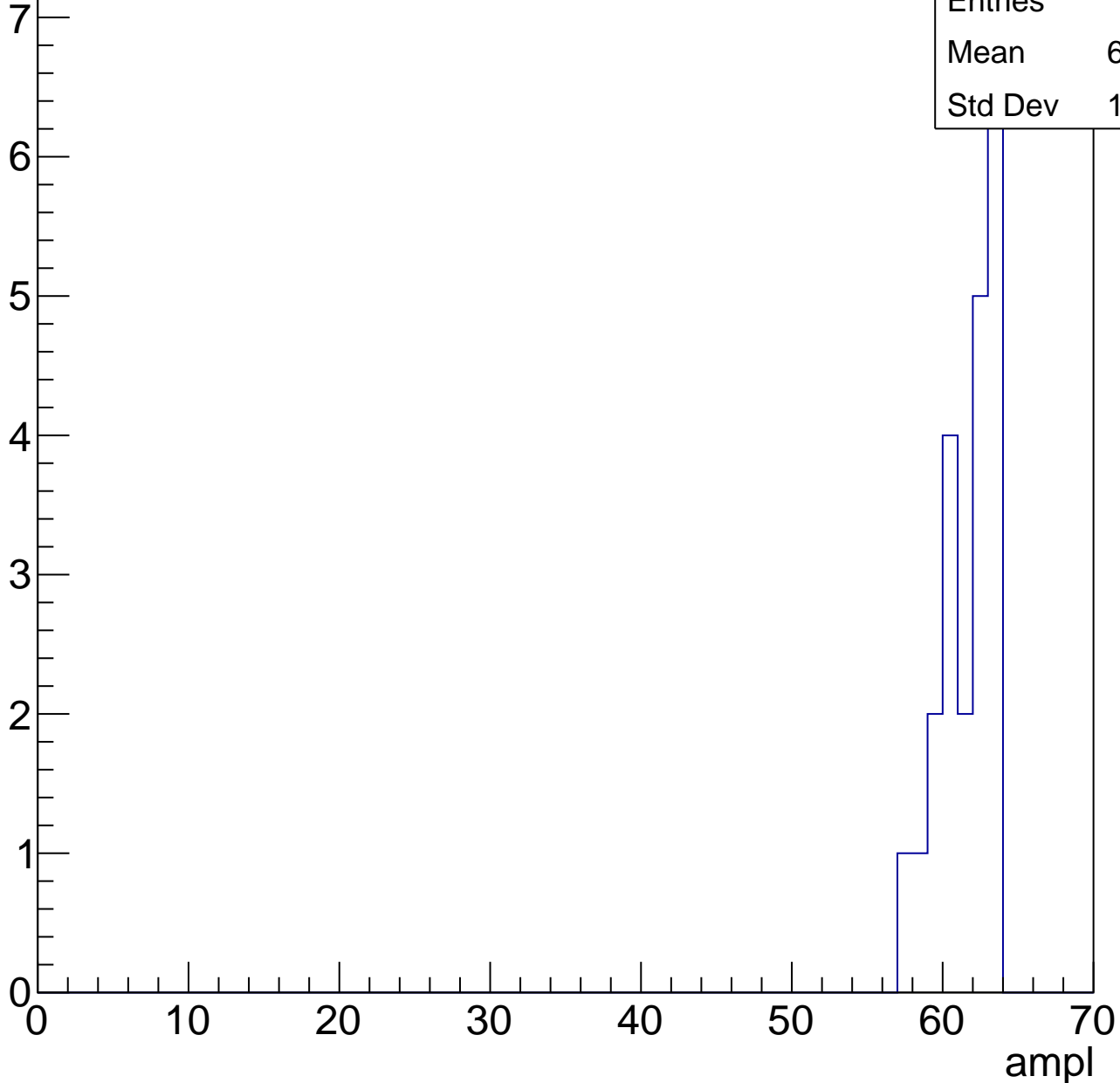


# B1L003S, U22-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	22
Mean	61.18
Std Dev	1.774





# B1L003S, U22-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch101, adc0

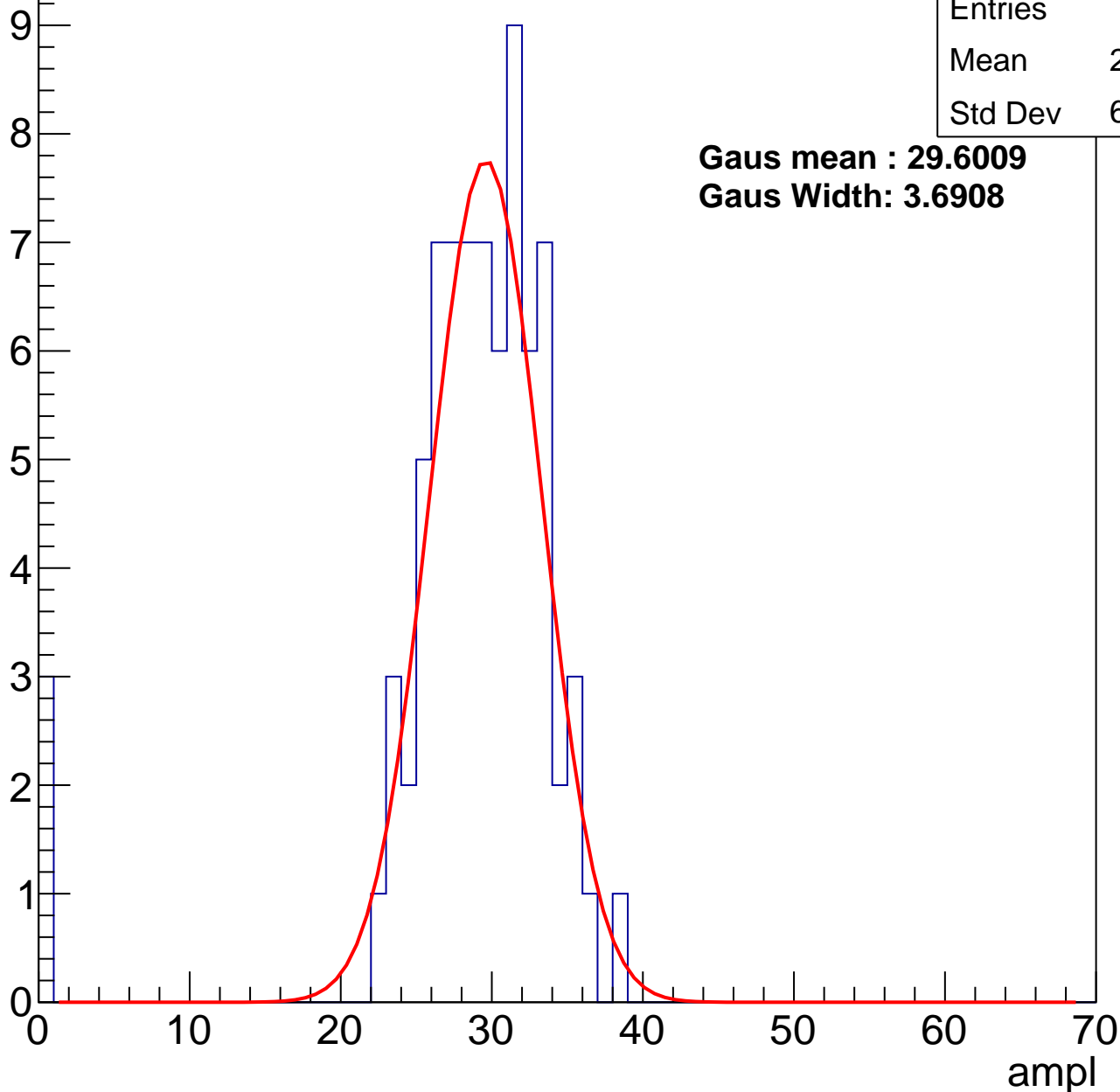
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	28.09
Std Dev	6.586

**Gaus mean : 29.6009**

**Gaus Width: 3.6908**



# B1L003S, U22-ch101, adc1

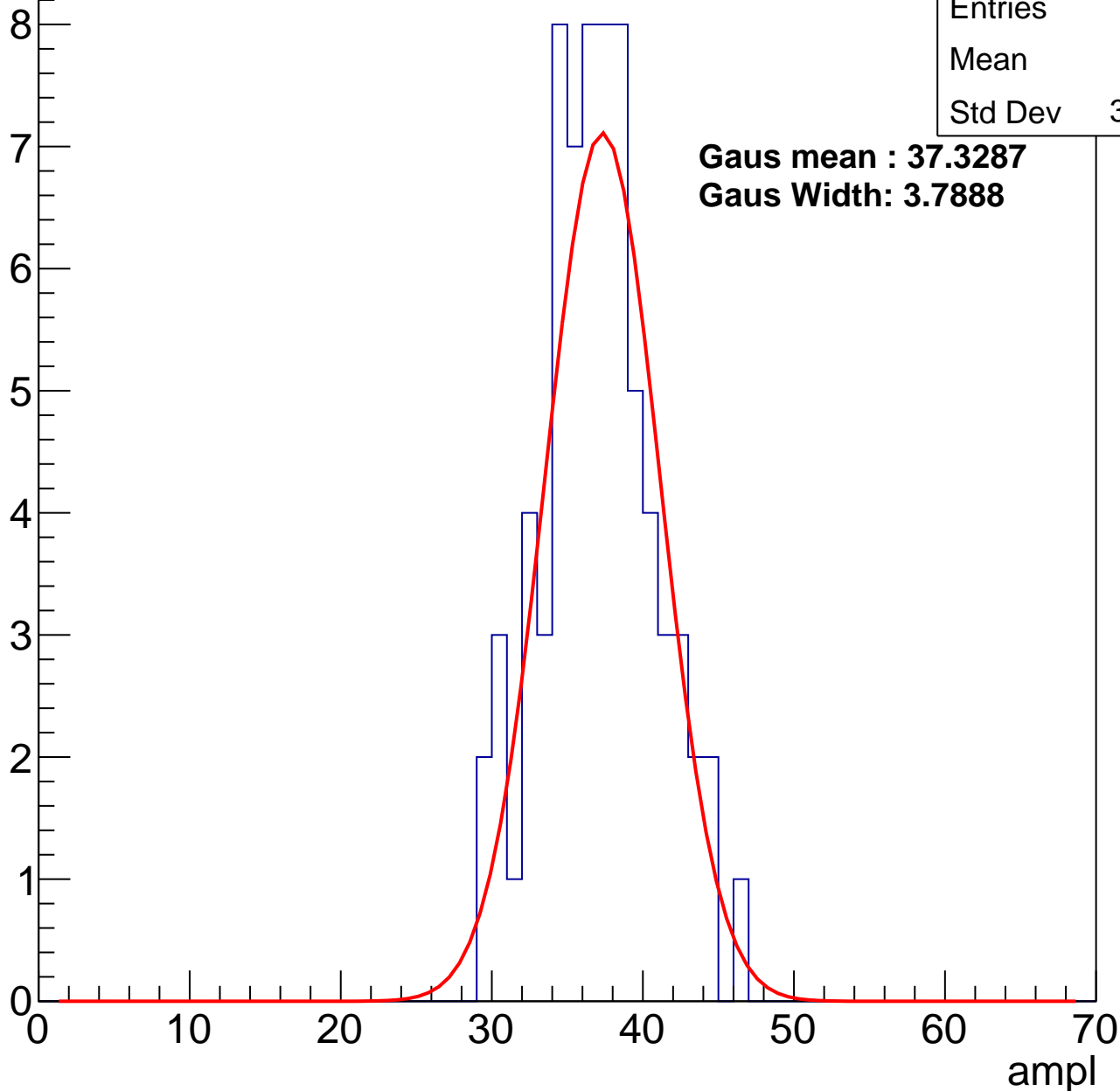
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	36.6
Std Dev	3.714

**Gaus mean : 37.3287**

**Gaus Width: 3.7888**



# B1L003S, U22-ch101, adc2

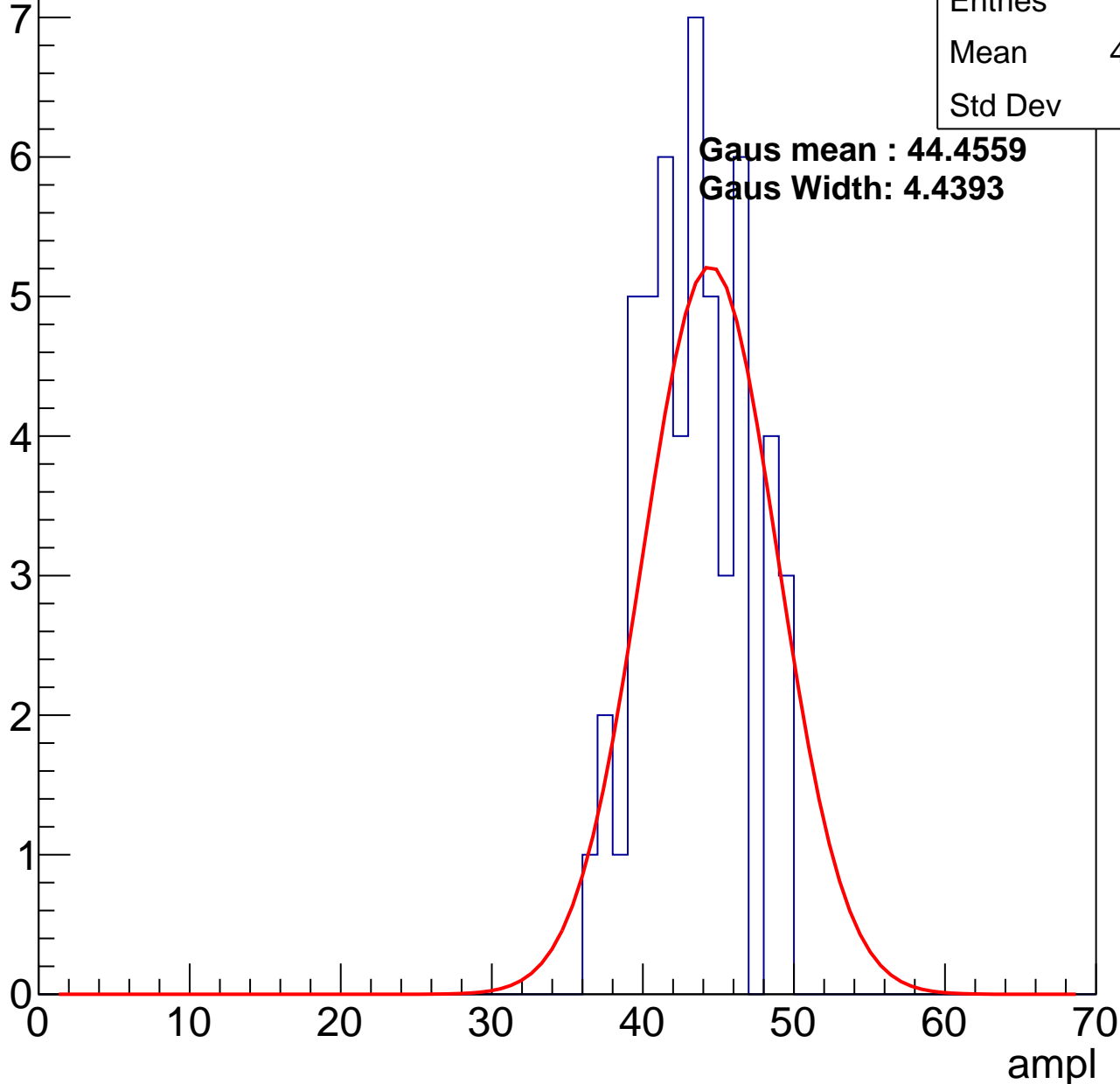
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.85
Std Dev	3.33

**Gaus mean : 44.4559**

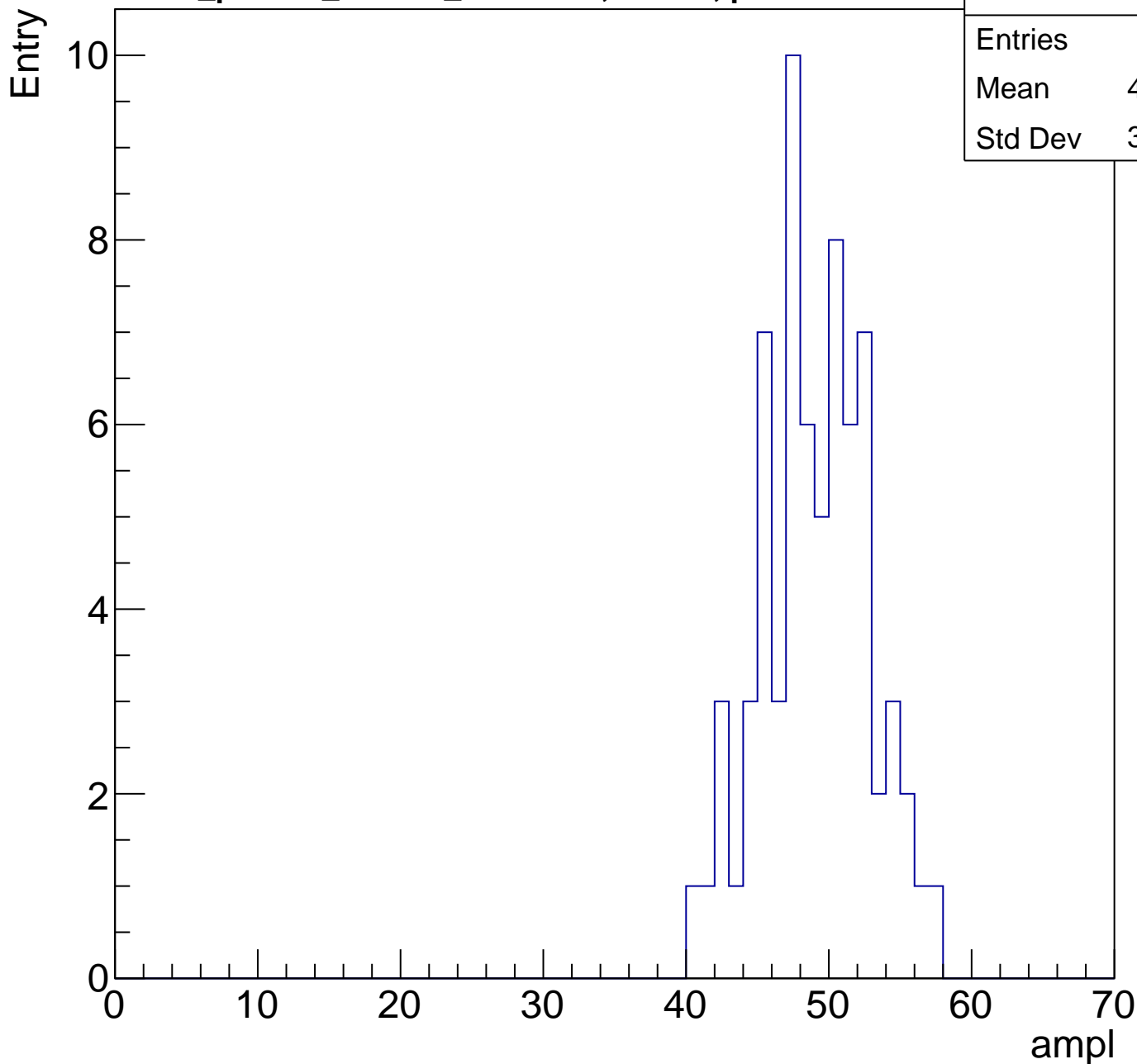
**Gaus Width: 4.4393**



# B1L003S, U22-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	48.56
Std Dev	3.706

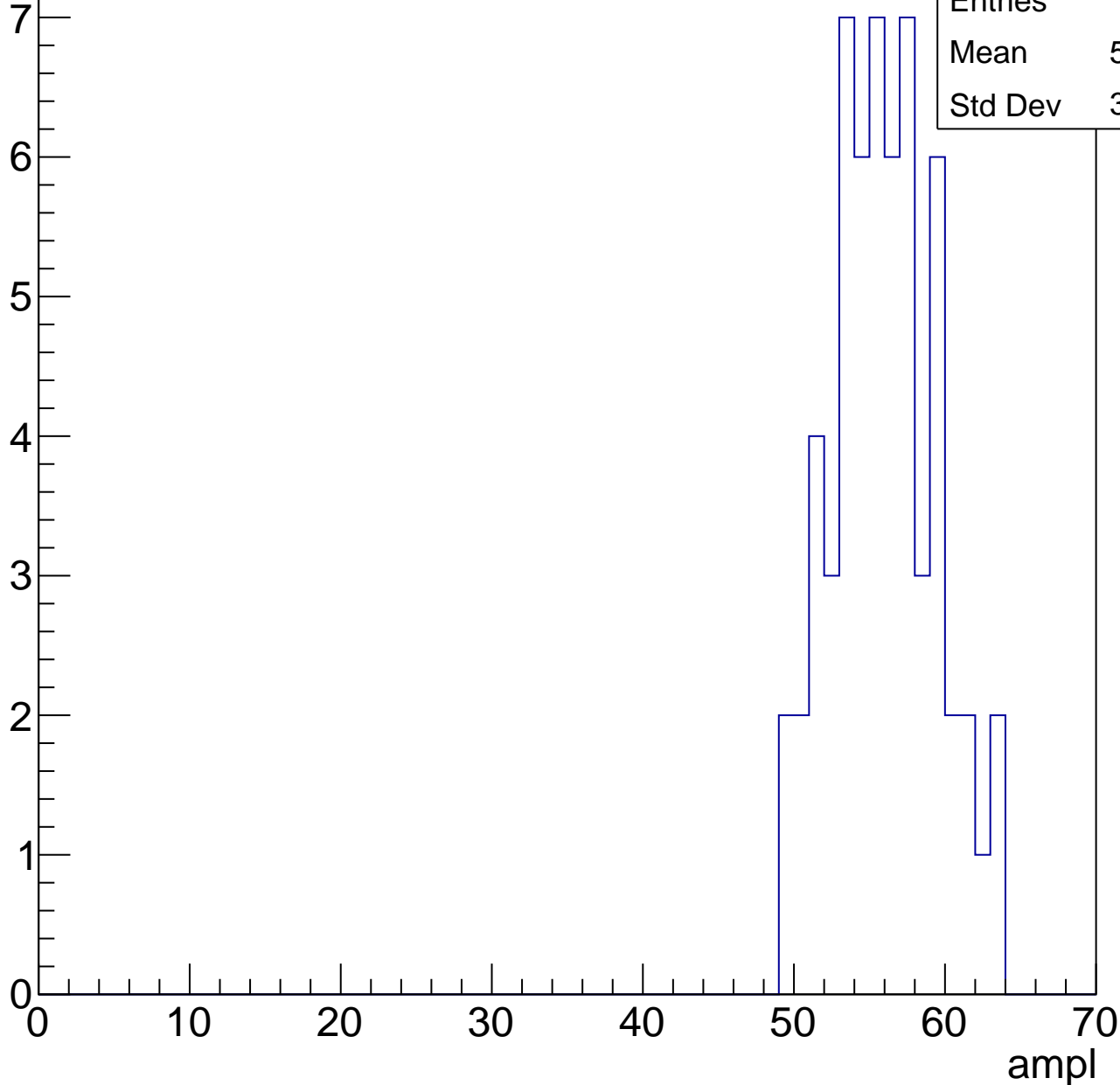


# B1L003S, U22-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	55.52
Std Dev	3.384

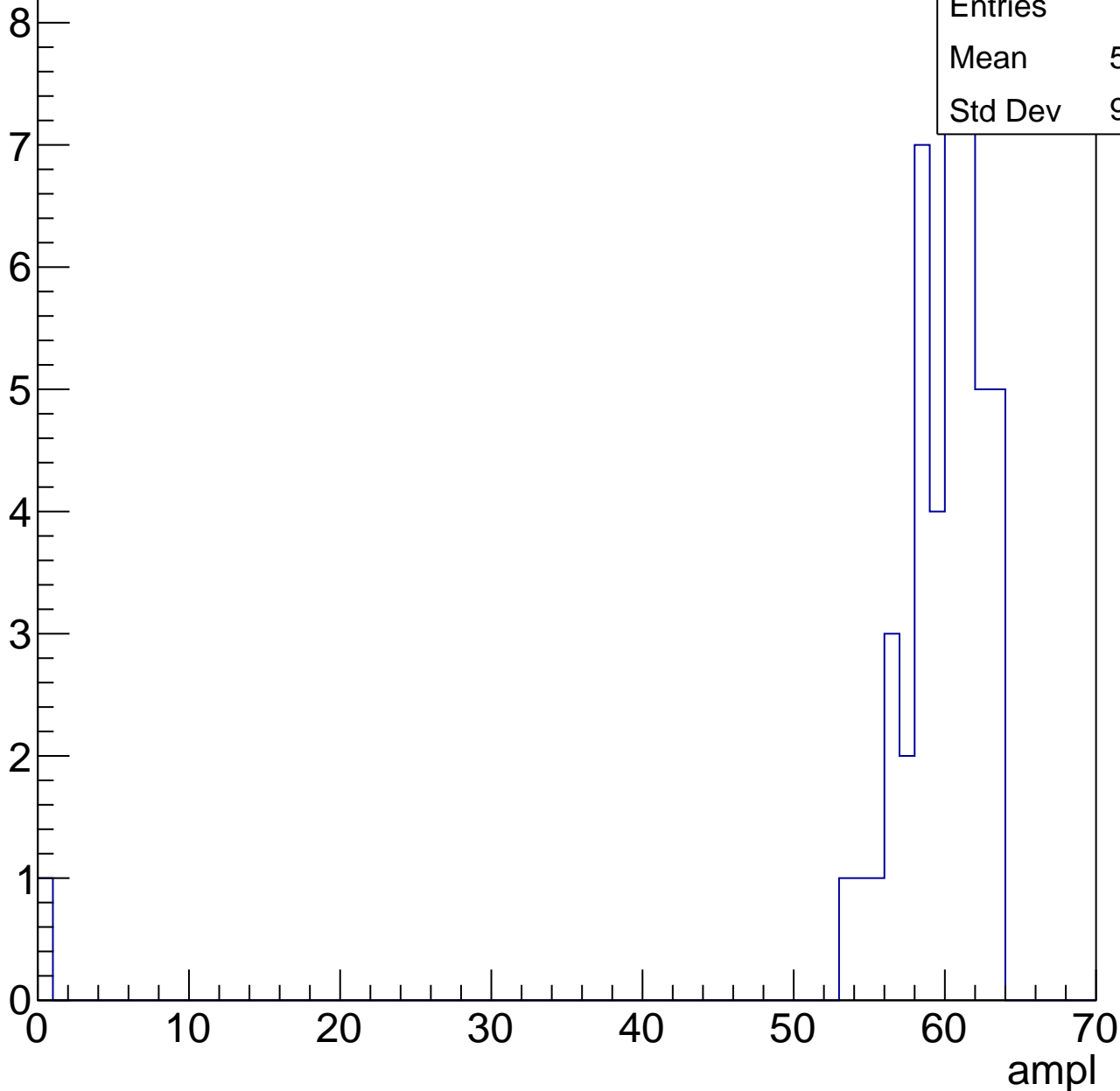


# B1L003S, U22-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

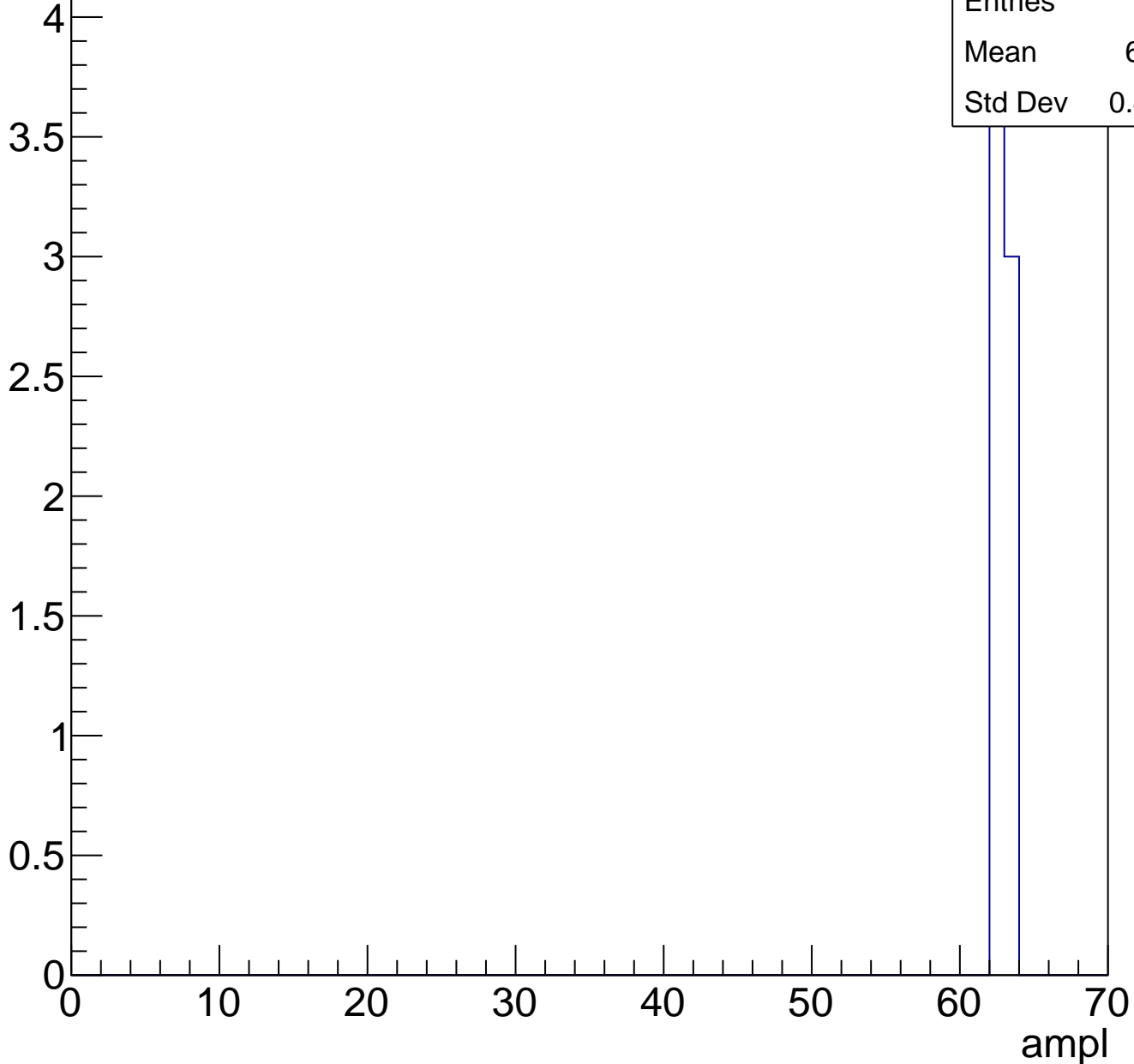
Entries	46
Mean	58.24
Std Dev	9.015



# B1L003S, U22-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	7
Mean	62.43
Std Dev	0.4949



# B1L003S, U22-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch102, adc0

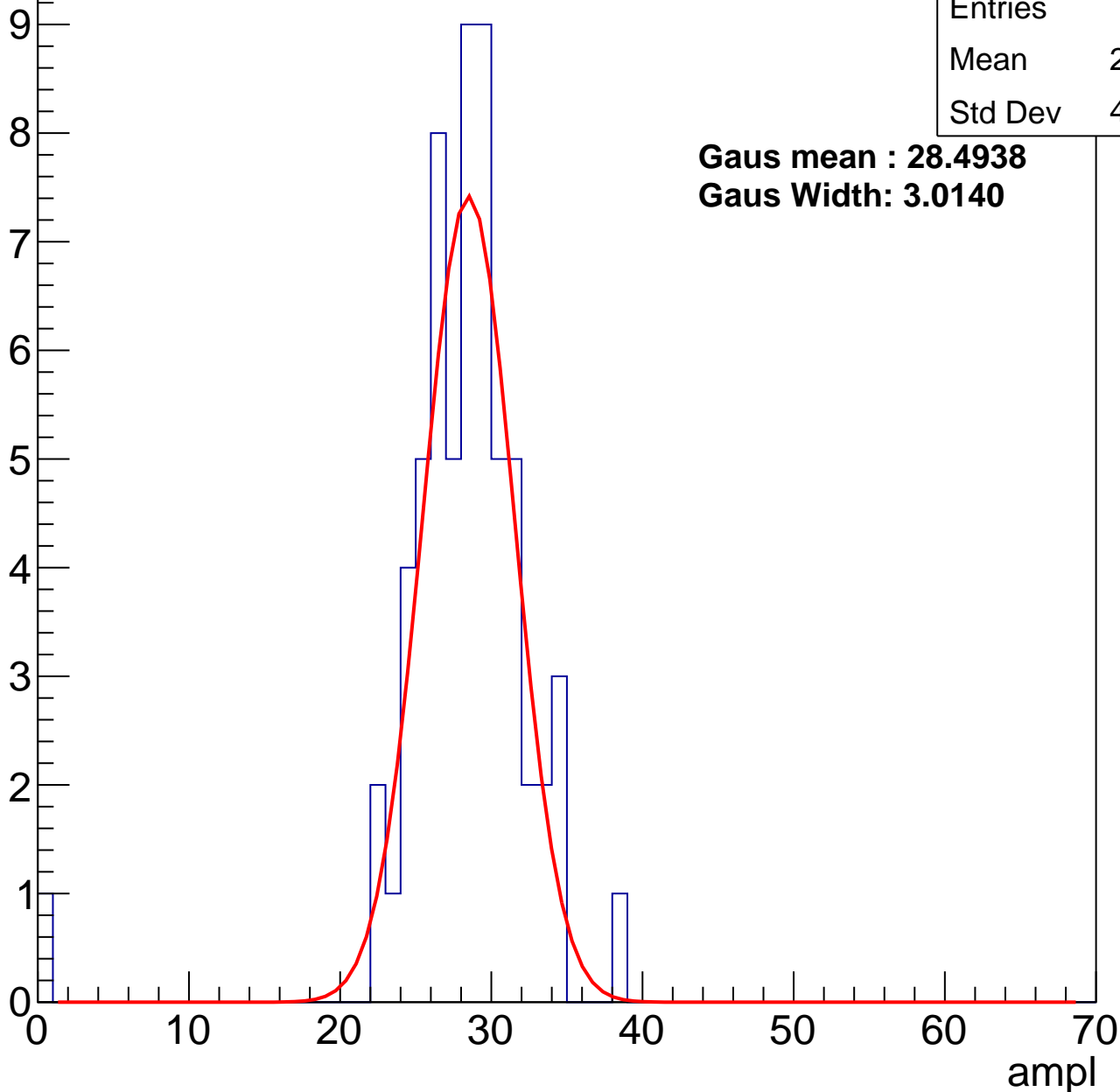
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	27.73
Std Dev	4.729

**Gaus mean : 28.4938**

**Gaus Width: 3.0140**



# B1L003S, U22-ch102, adc1

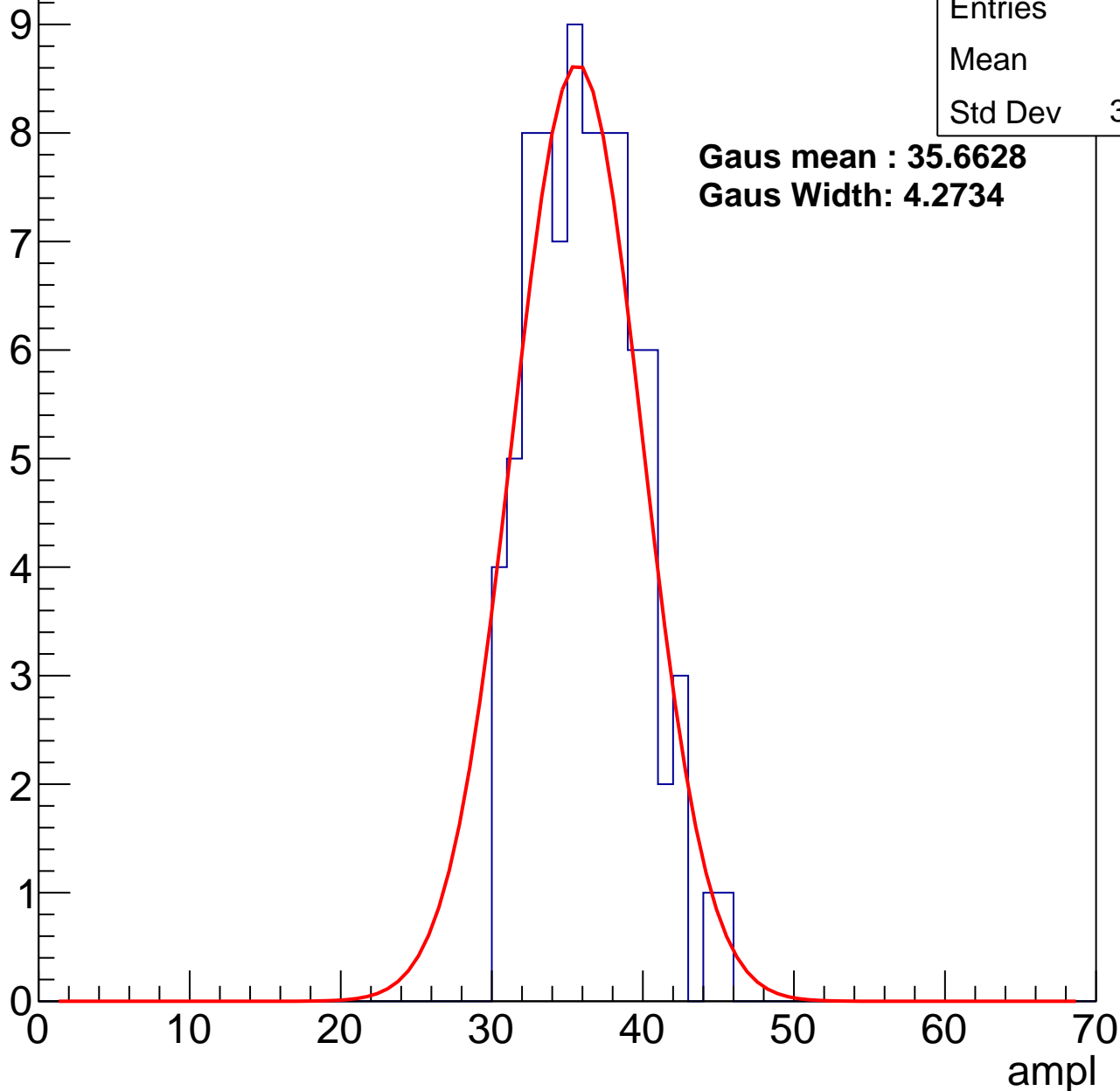
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	84
Mean	35.8
Std Dev	3.443

**Gaus mean : 35.6628**

**Gaus Width: 4.2734**



# B1L003S, U22-ch102, adc2

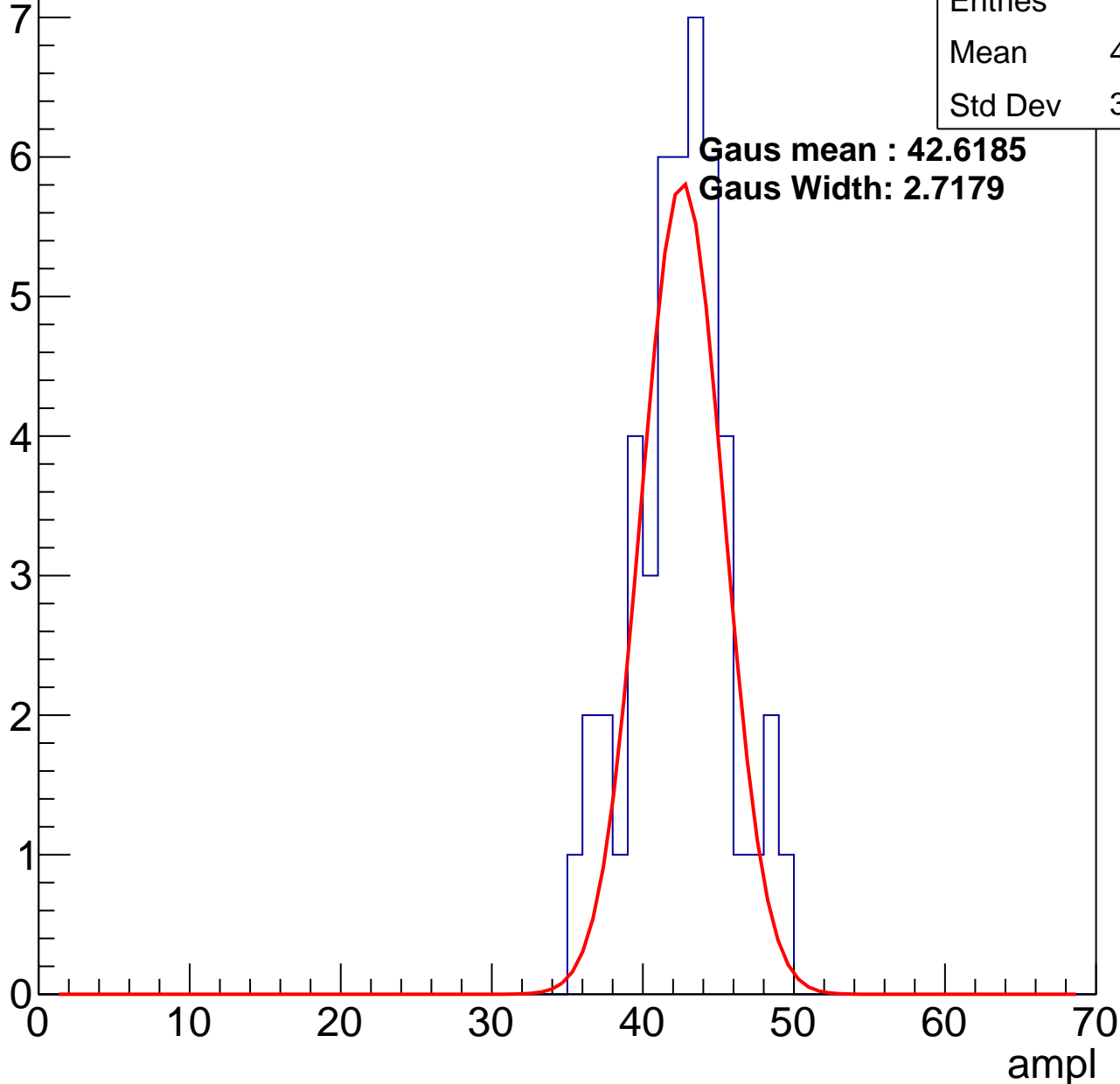
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	42.04
Std Dev	3.162

**Gaus mean : 42.6185**

**Gaus Width: 2.7179**

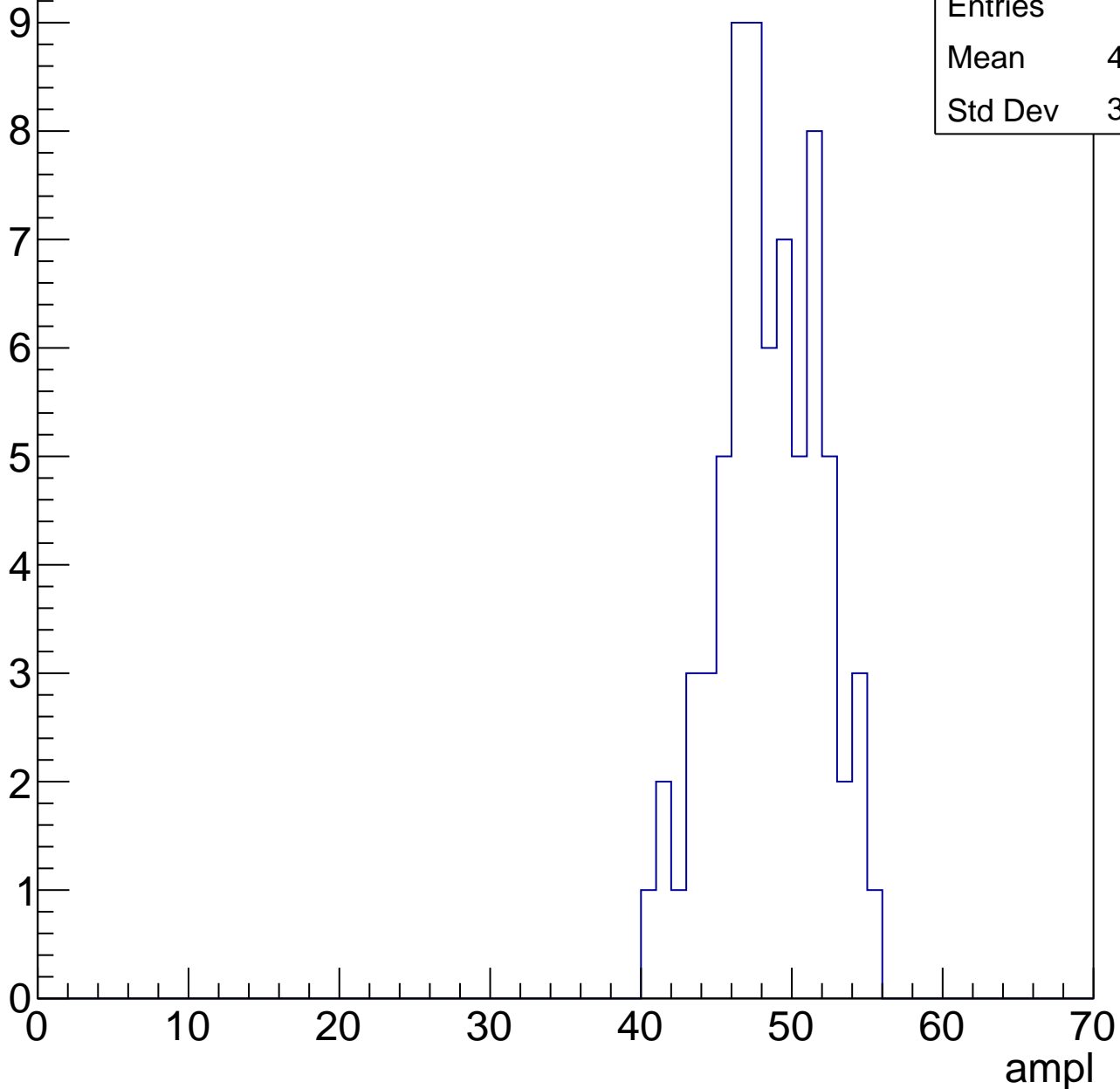


# B1L003S, U22-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	47.99
Std Dev	3.374

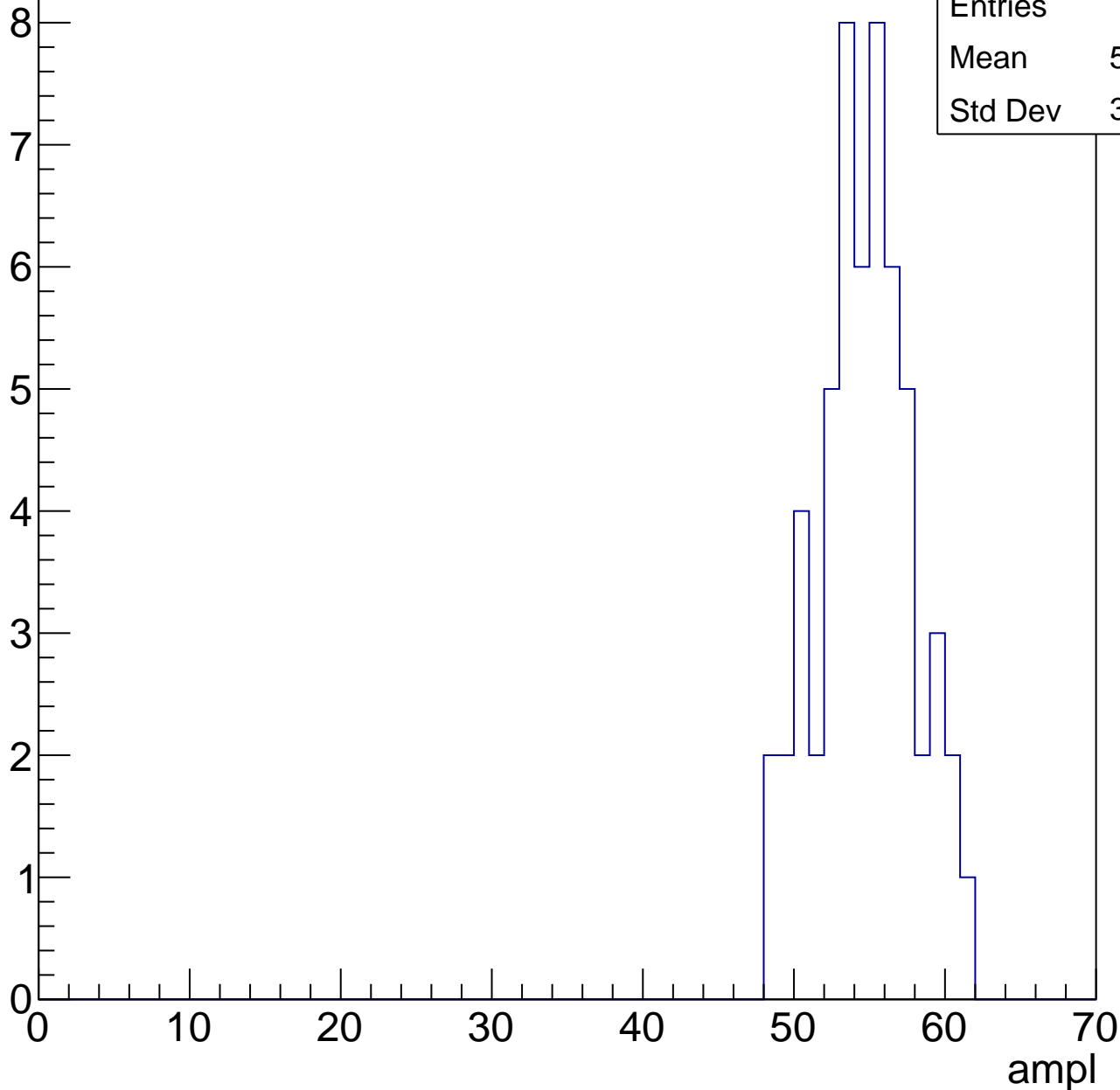


# B1L003S, U22-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.27
Std Dev	3.085

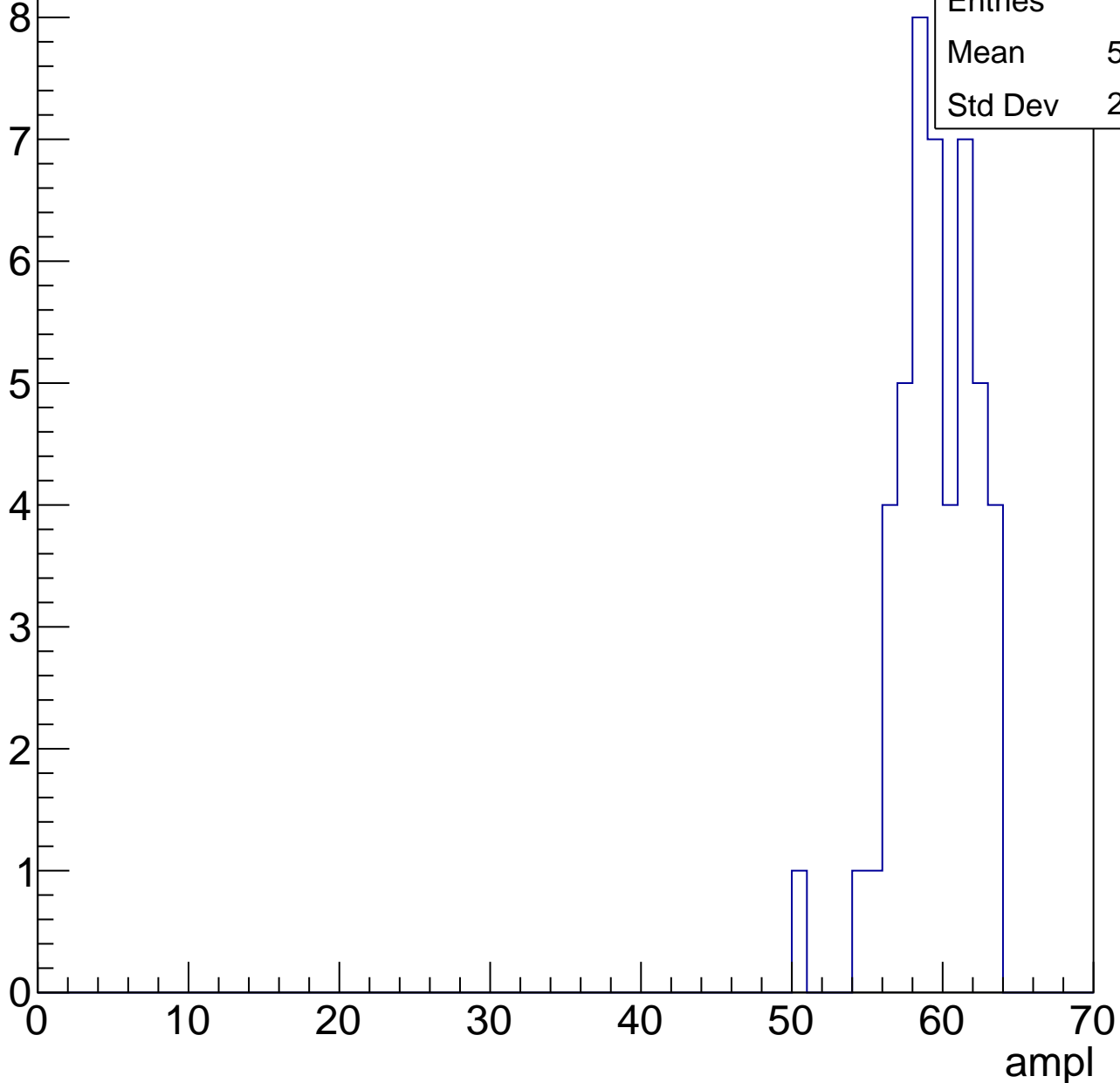


# B1L003S, U22-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	59.02
Std Dev	2.638

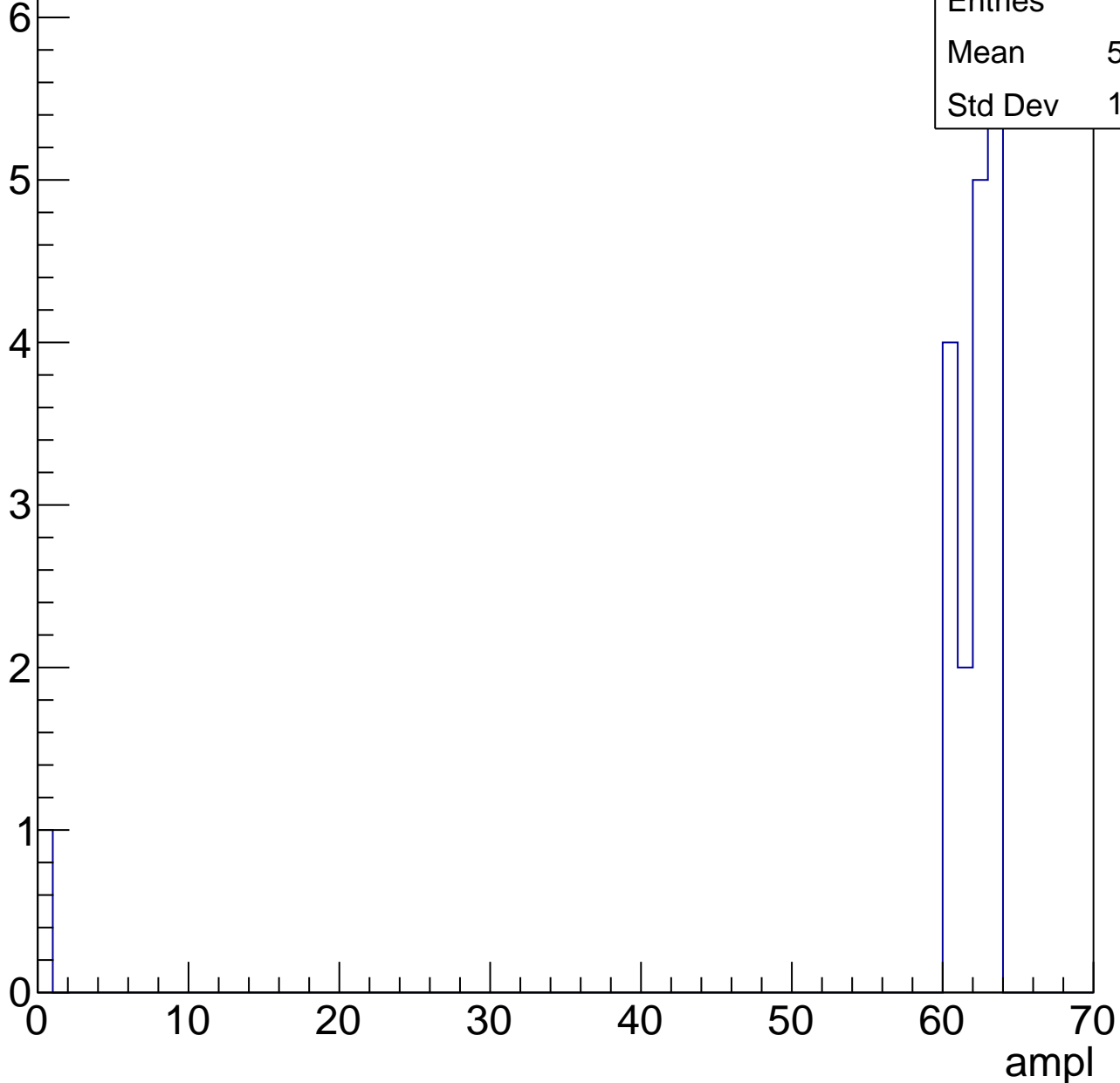


# B1L003S, U22-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.33
Std Dev	14.19

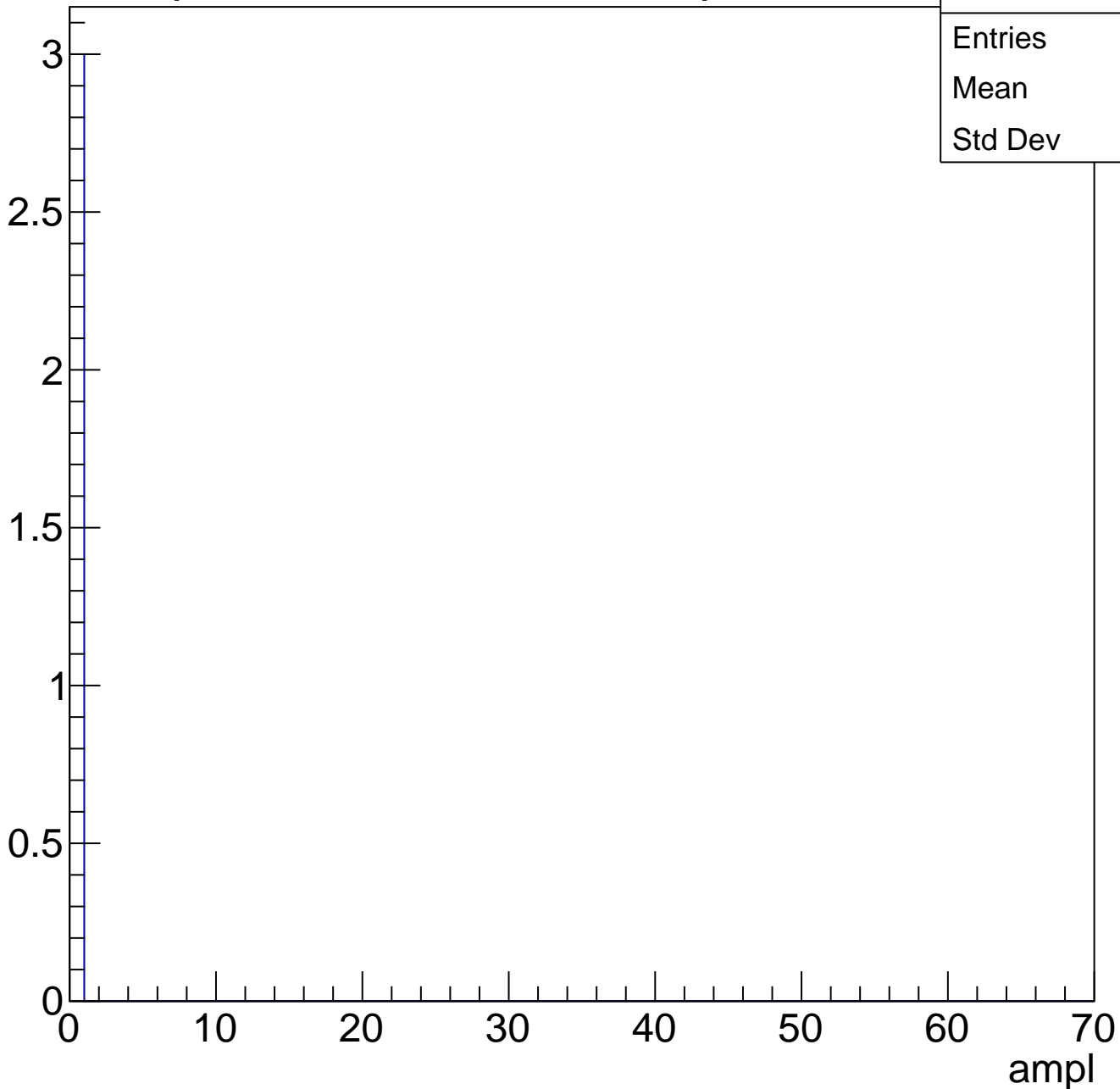




# B1L003S, U22-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

# B1L003S, U22-ch103, adc0

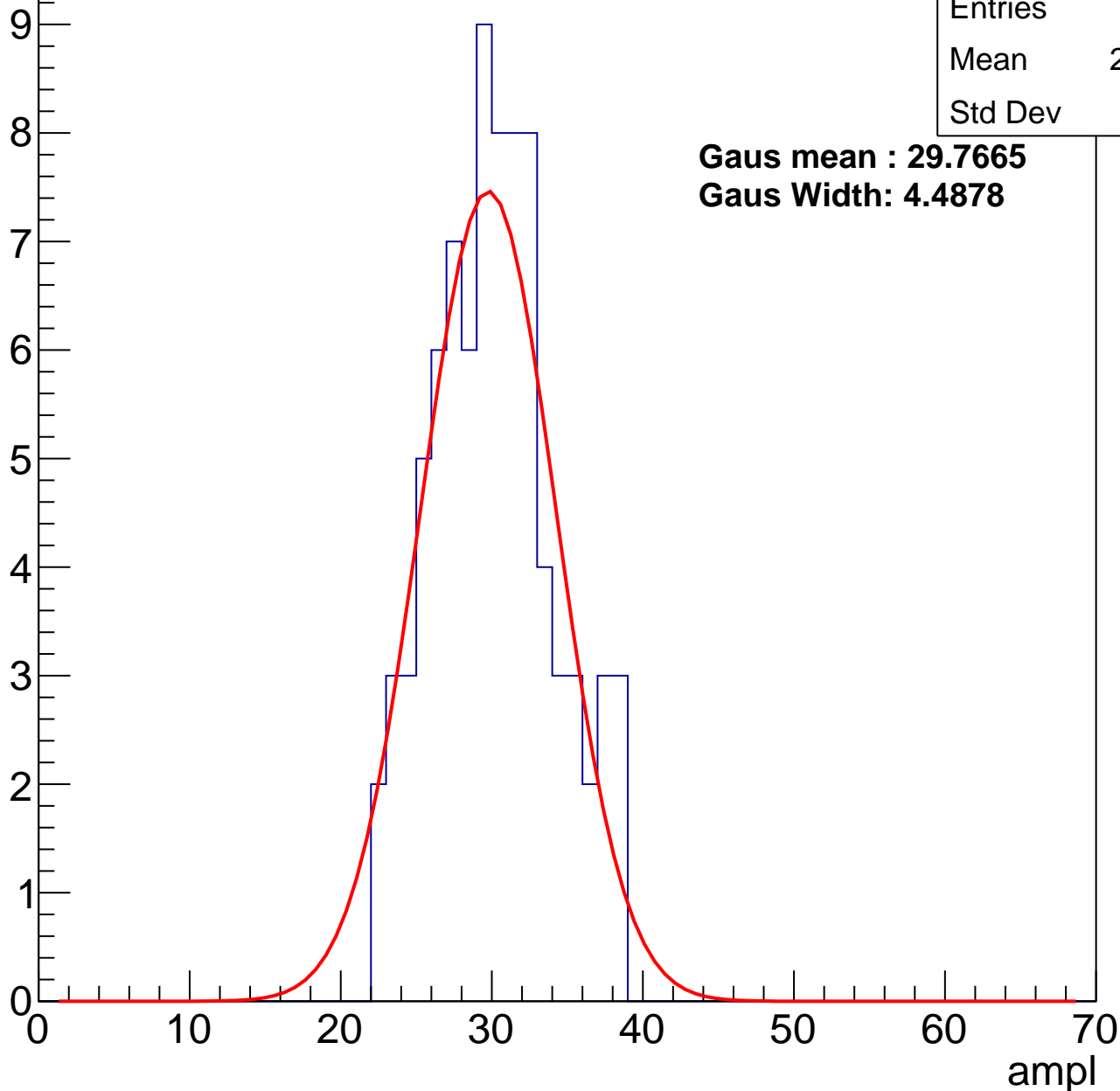
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	29.69
Std Dev	3.96

**Gaus mean : 29.7665**

**Gaus Width: 4.4878**



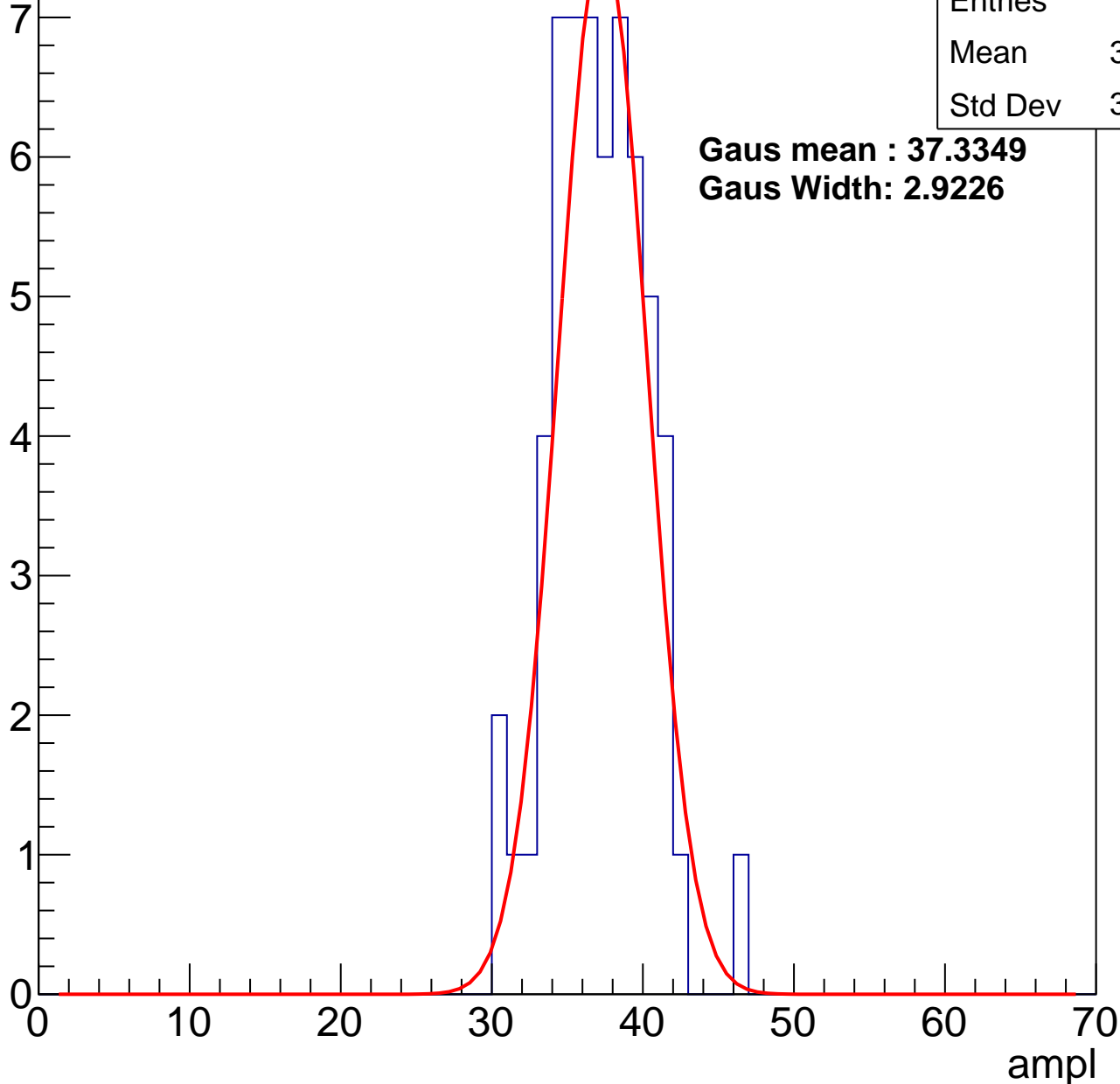
# B1L003S, U22-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.68
Std Dev	3.083

**Gaus mean : 37.3349**  
**Gaus Width: 2.9226**



# B1L003S, U22-ch103, adc2

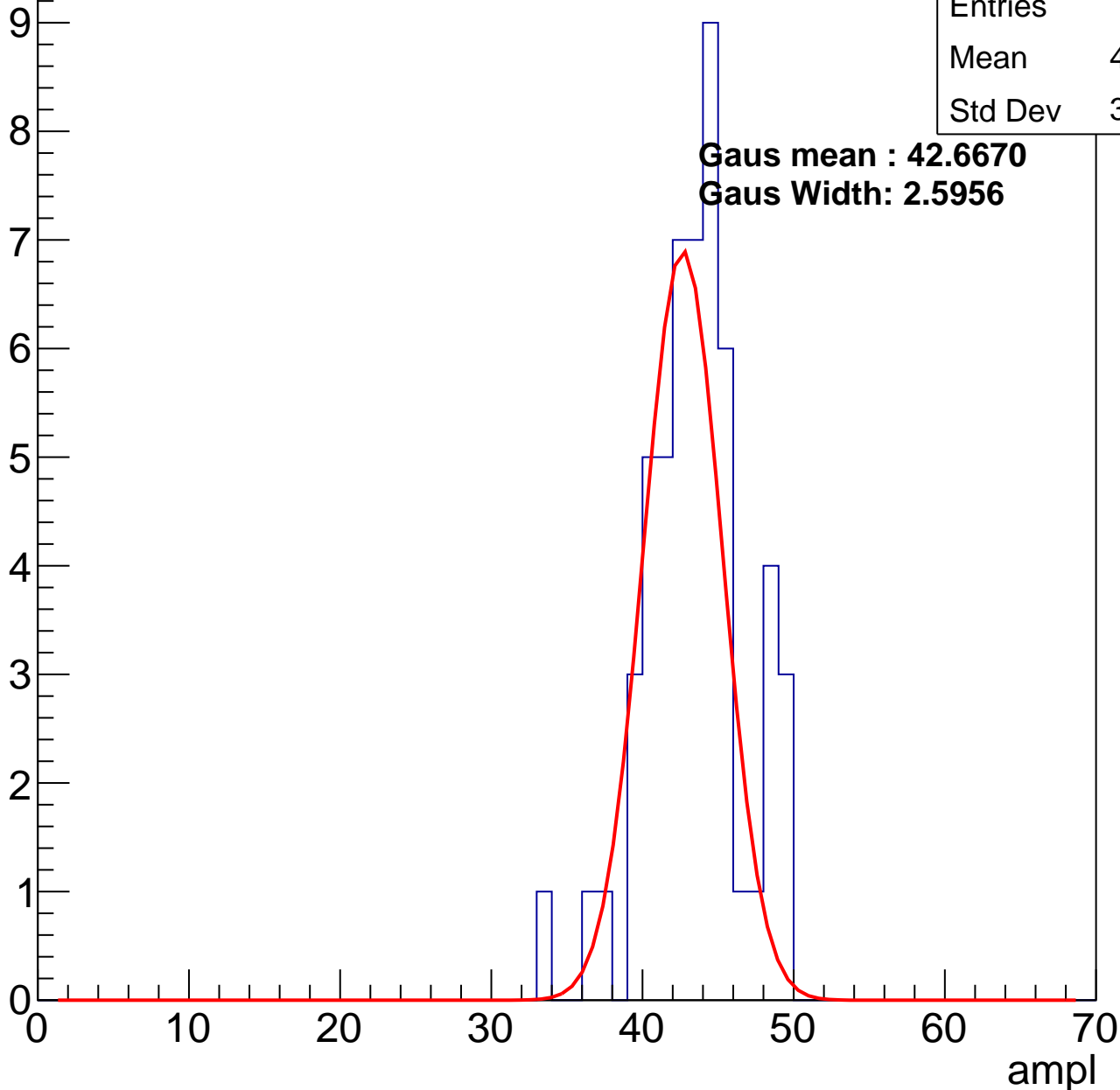
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.98
Std Dev	3.252

**Gaus mean : 42.6670**

**Gaus Width: 2.5956**

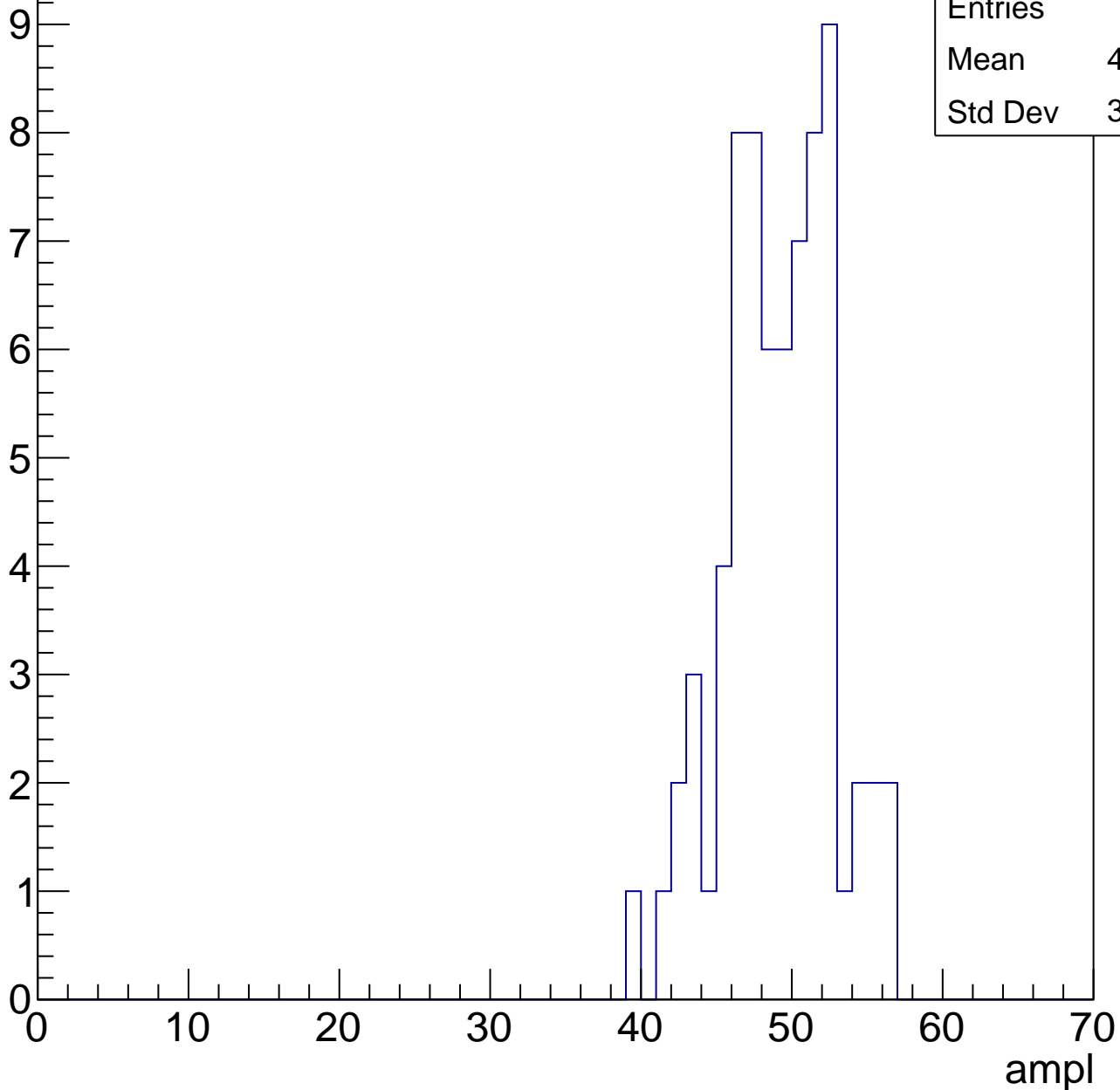


# B1L003S, U22-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	48.62
Std Dev	3.589

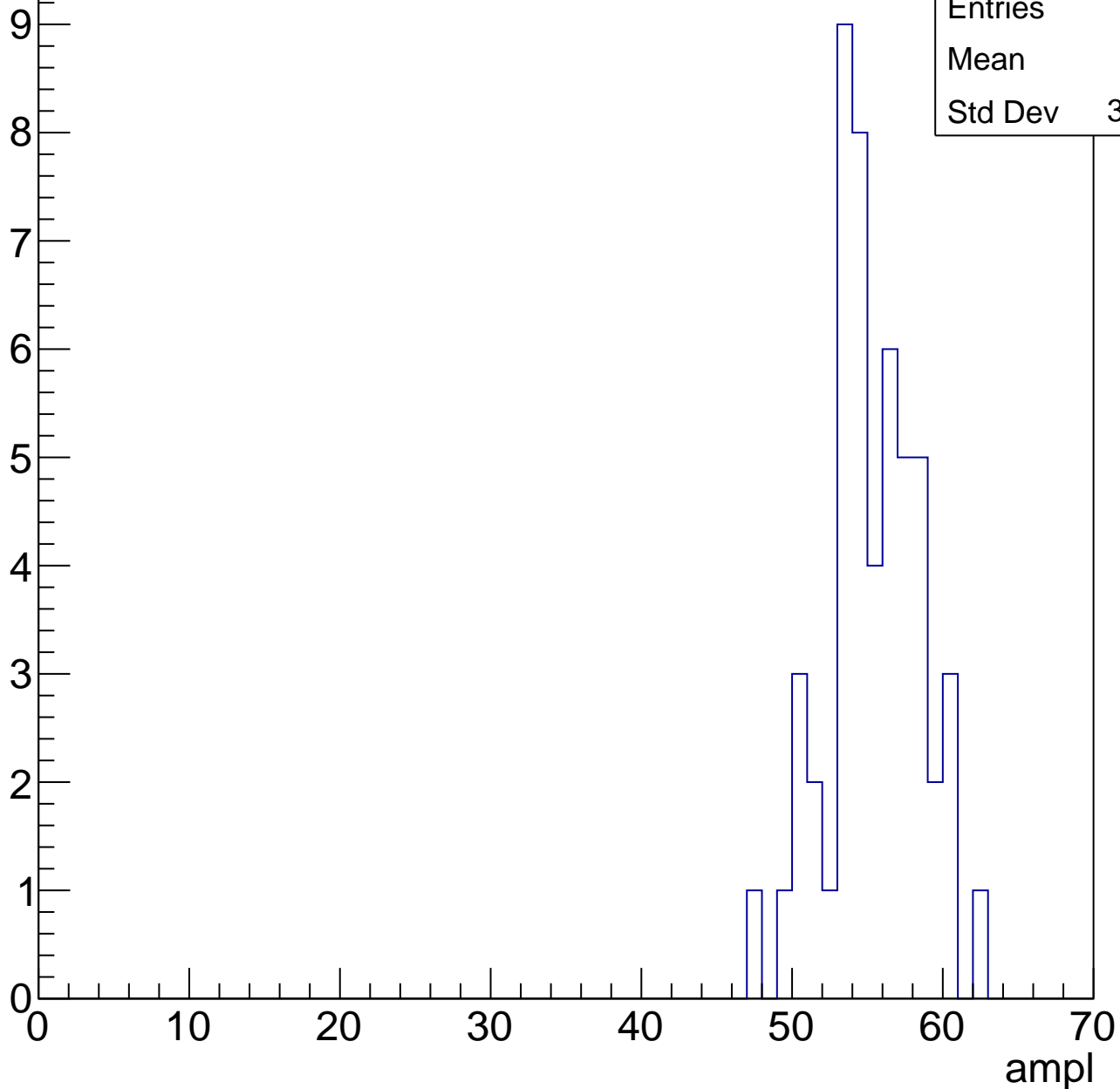


# B1L003S, U22-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.9
Std Dev	3.089

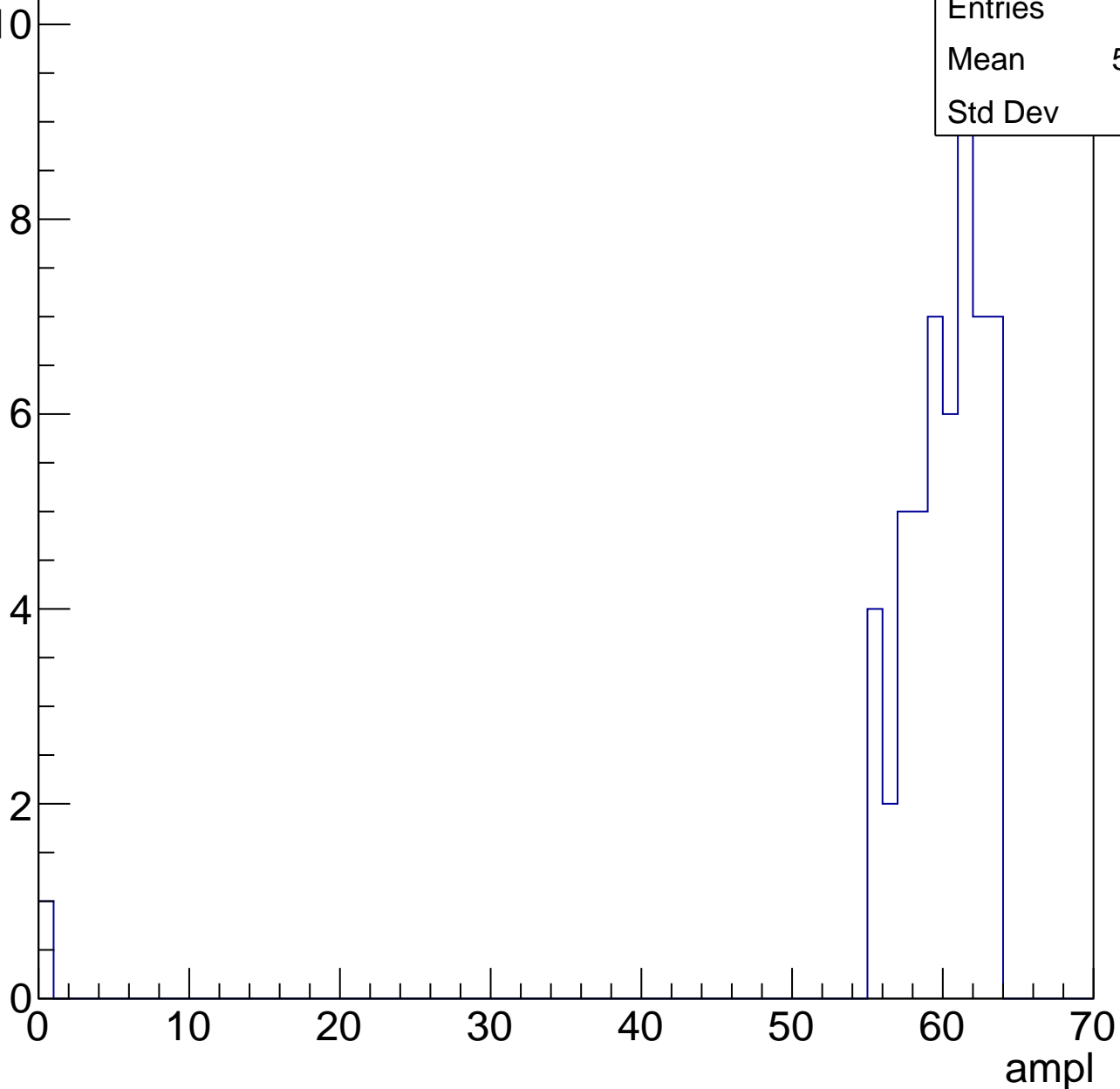


# B1L003S, U22-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

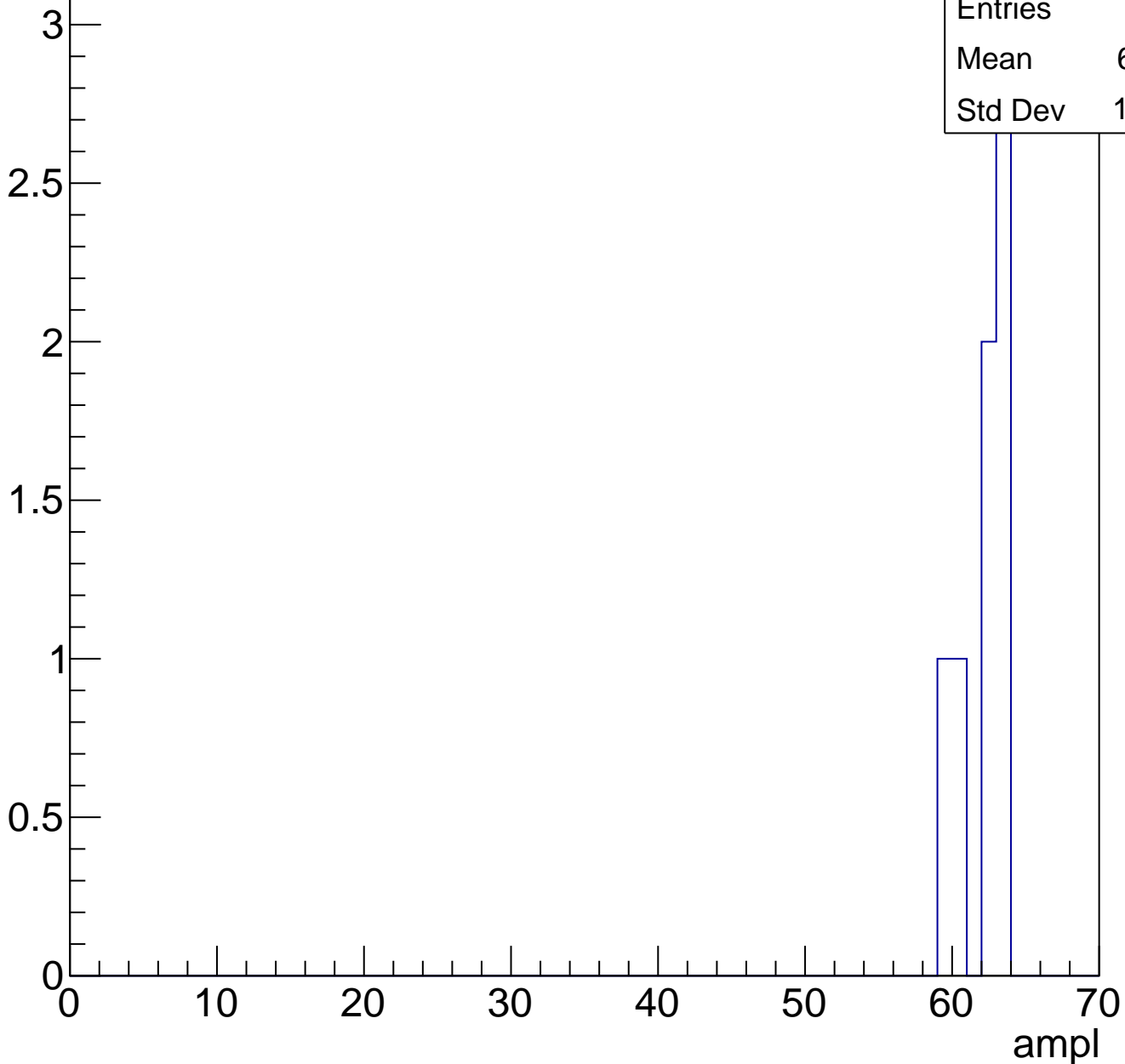
Entries	54
Mean	58.61
Std Dev	8.39



# B1L003S, U22-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch104, adc0

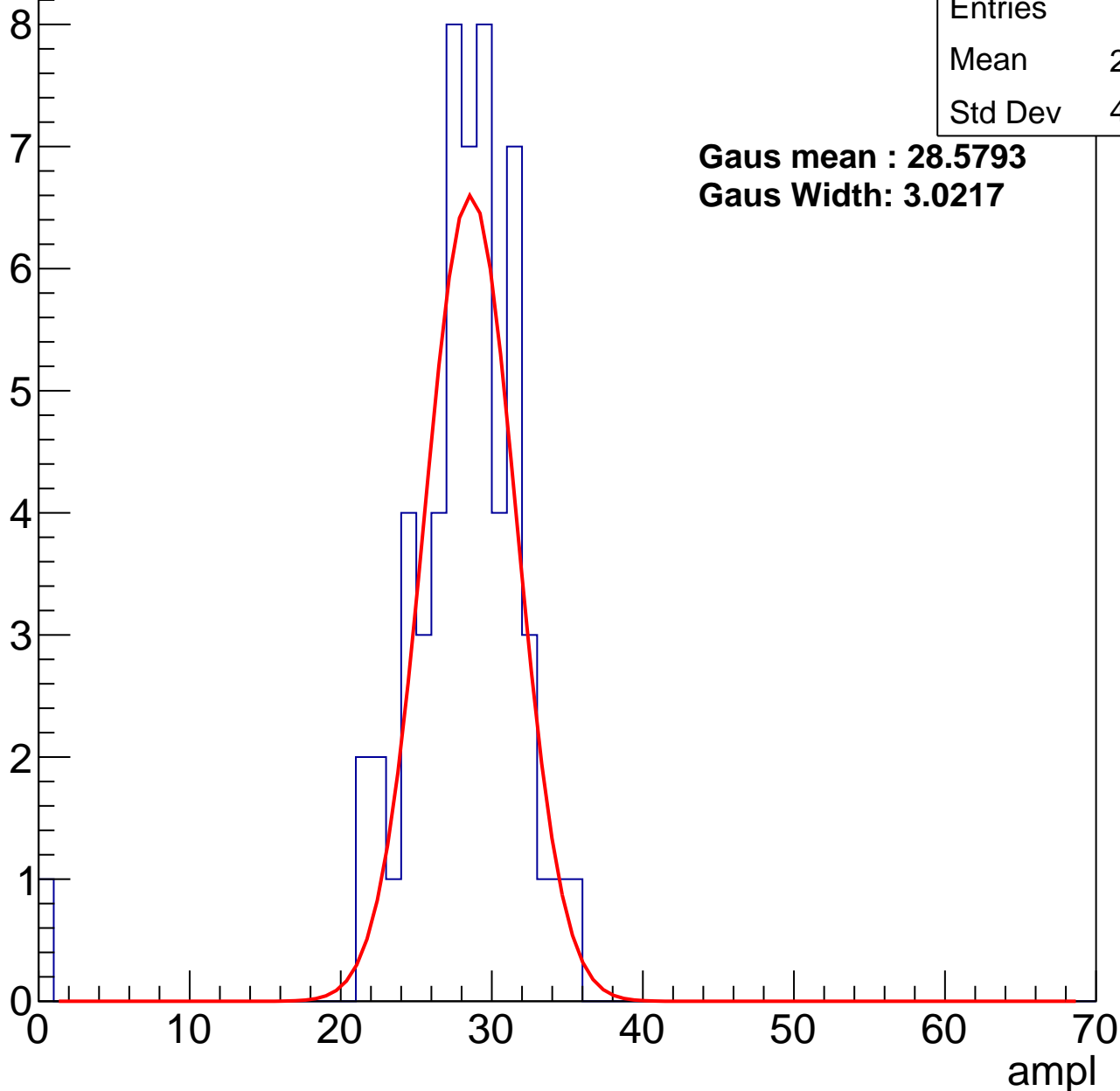
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	27.42
Std Dev	4.812

**Gaus mean : 28.5793**

**Gaus Width: 3.0217**



# B1L003S, U22-ch104, adc1

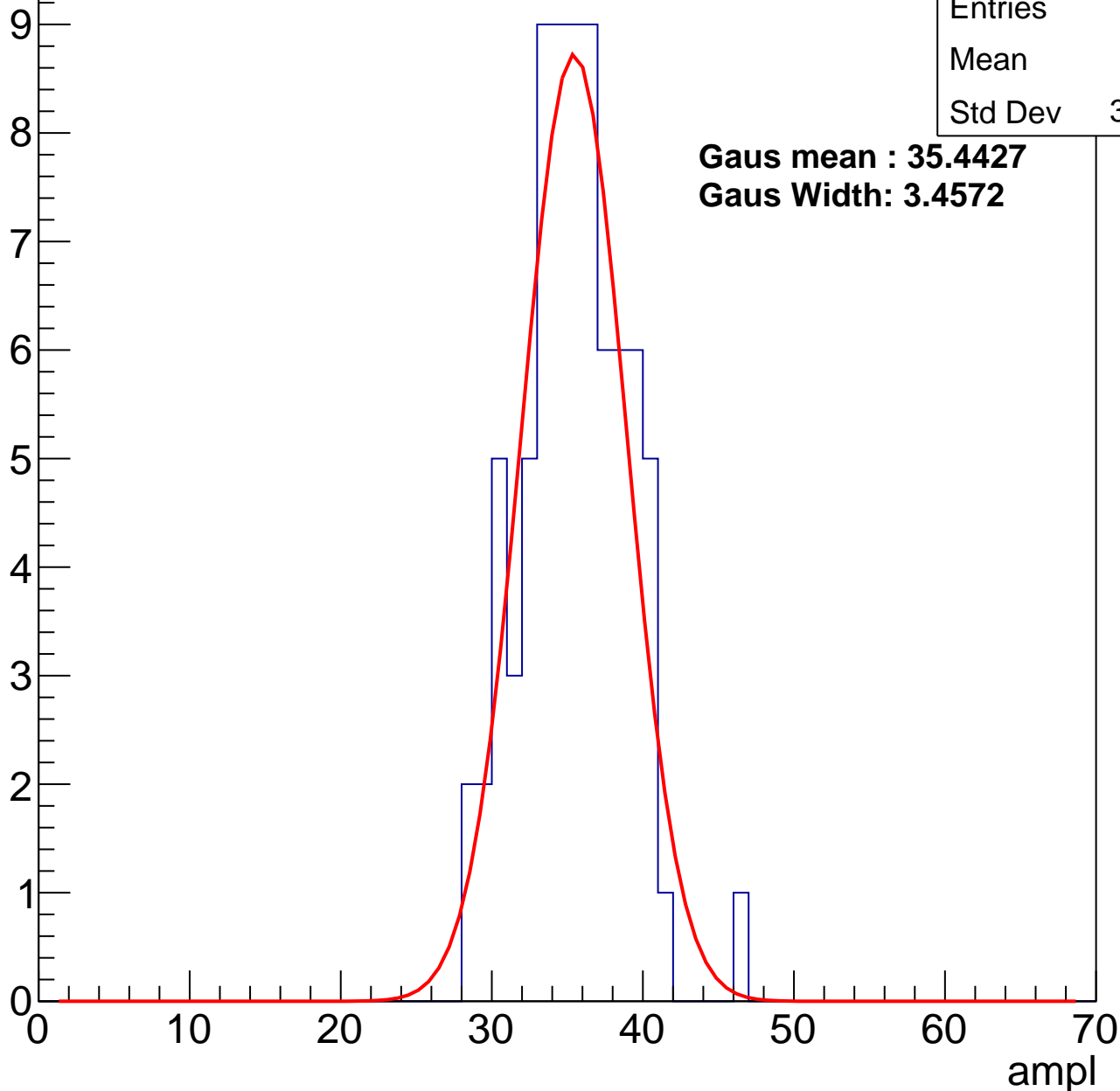
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	35
Std Dev	3.393

**Gaus mean : 35.4427**

**Gaus Width: 3.4572**



# B1L003S, U22-ch104, adc2

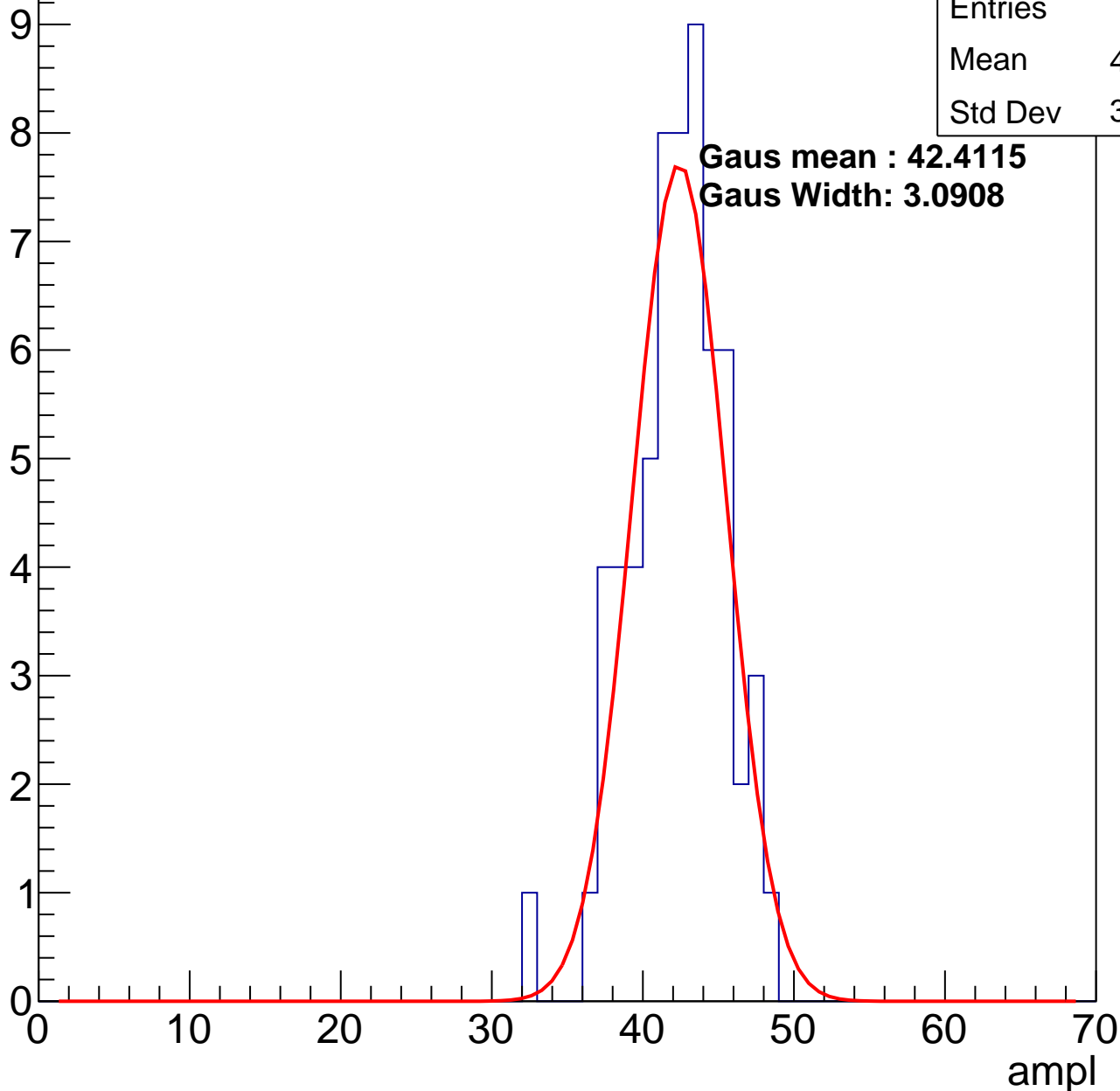
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	41.77
Std Dev	3.087

**Gaus mean : 42.4115**

**Gaus Width: 3.0908**

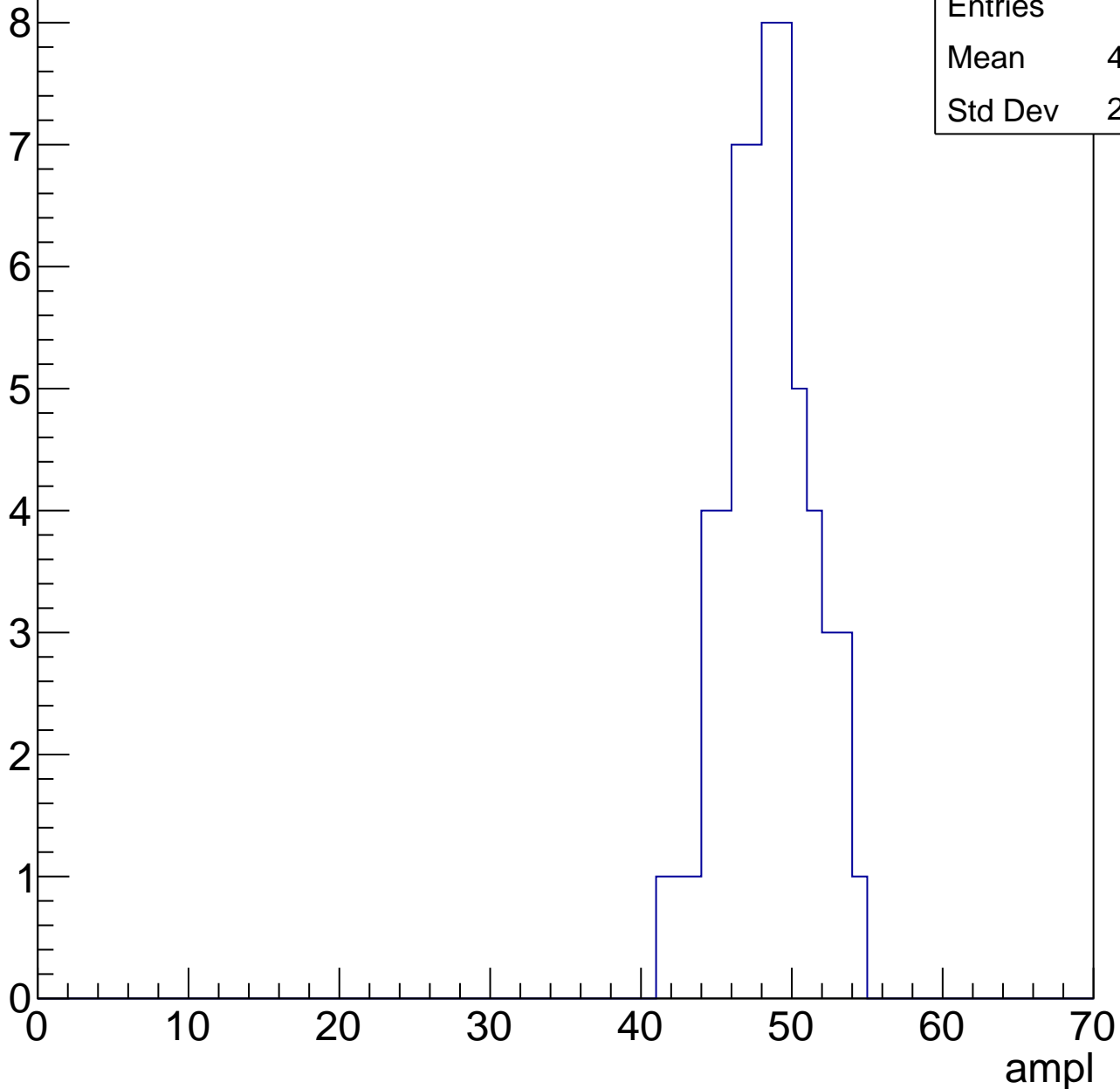


# B1L003S, U22-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.93
Std Dev	2.865

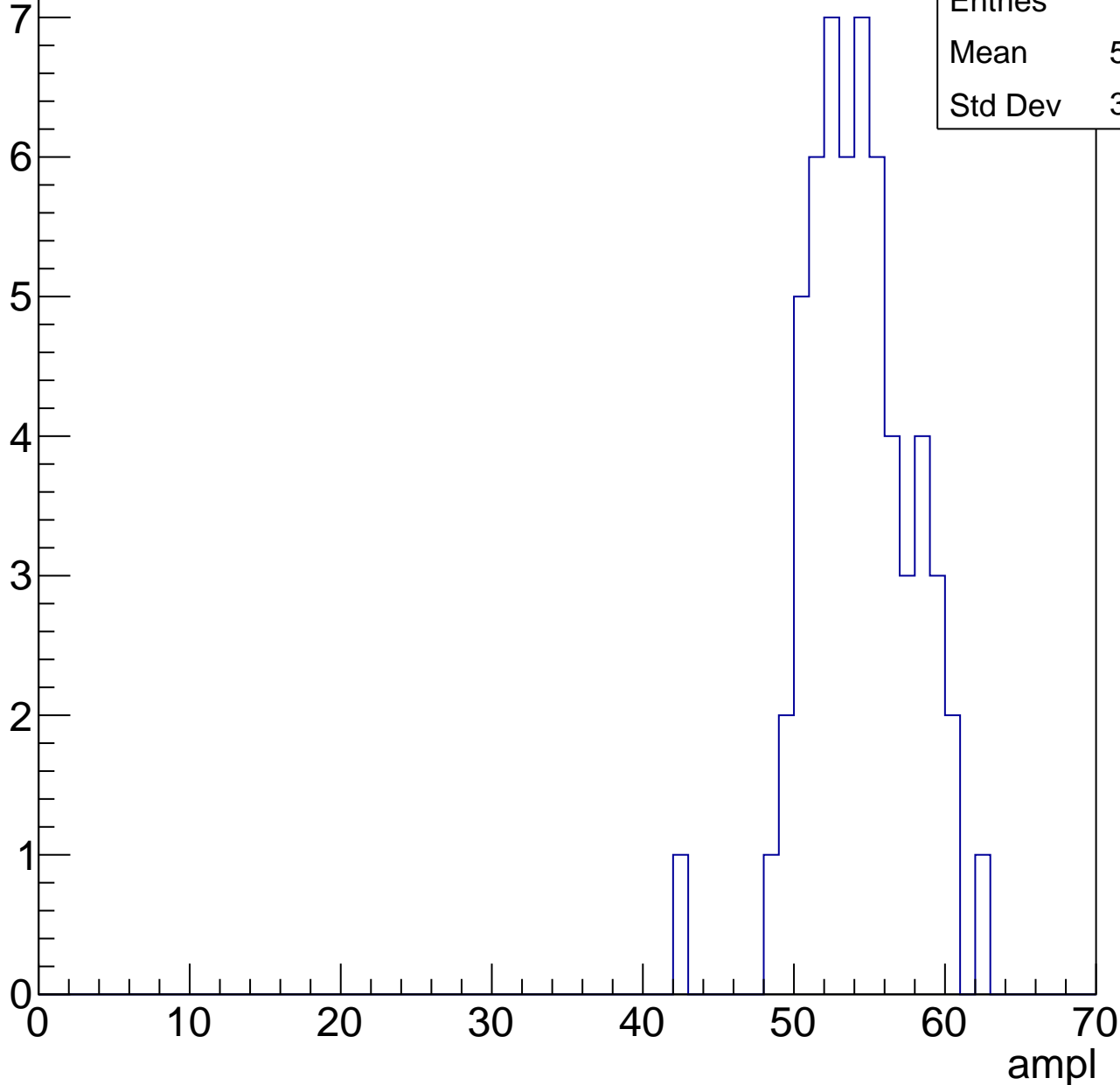


# B1L003S, U22-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.79
Std Dev	3.522

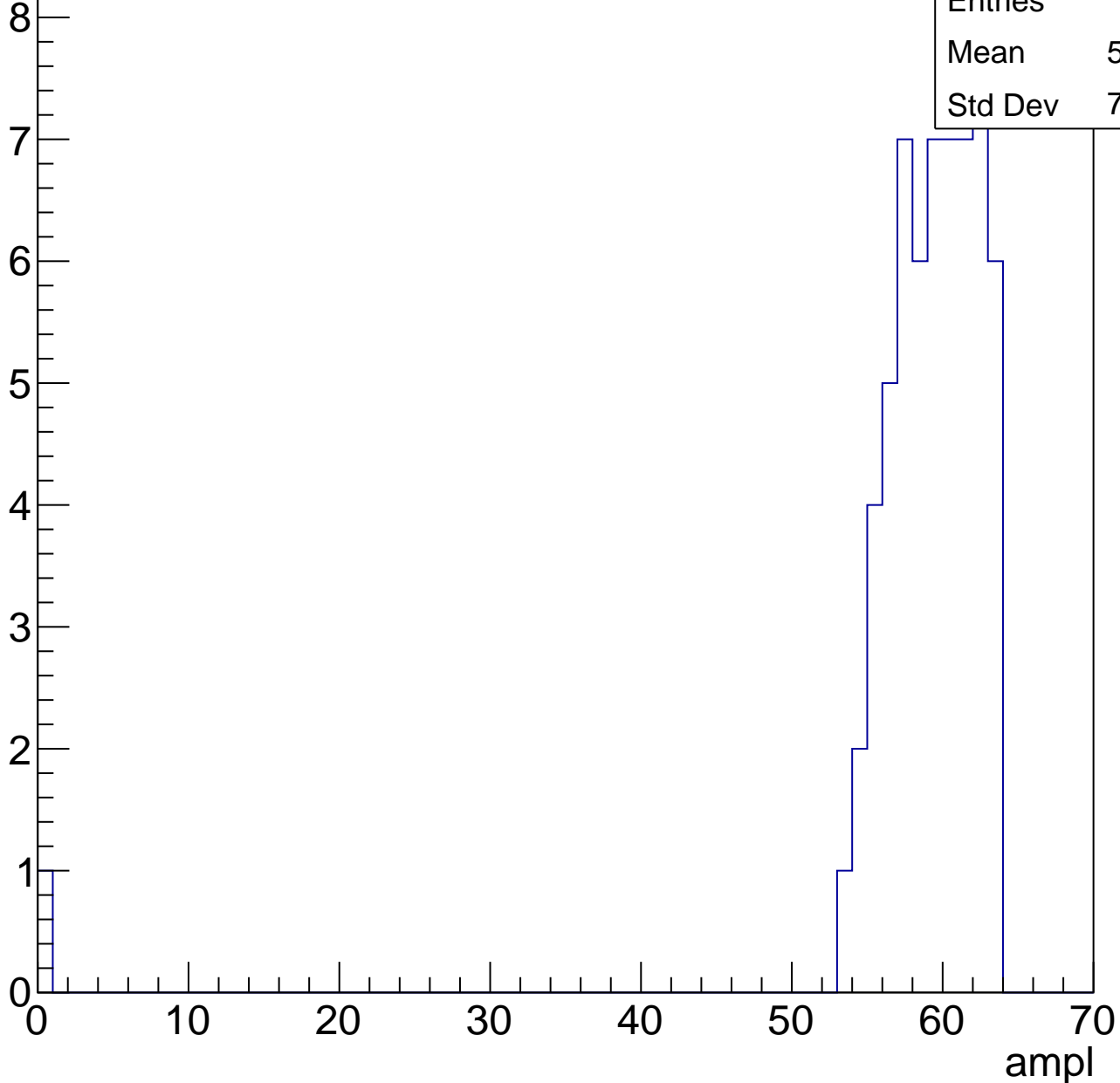


# B1L003S, U22-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

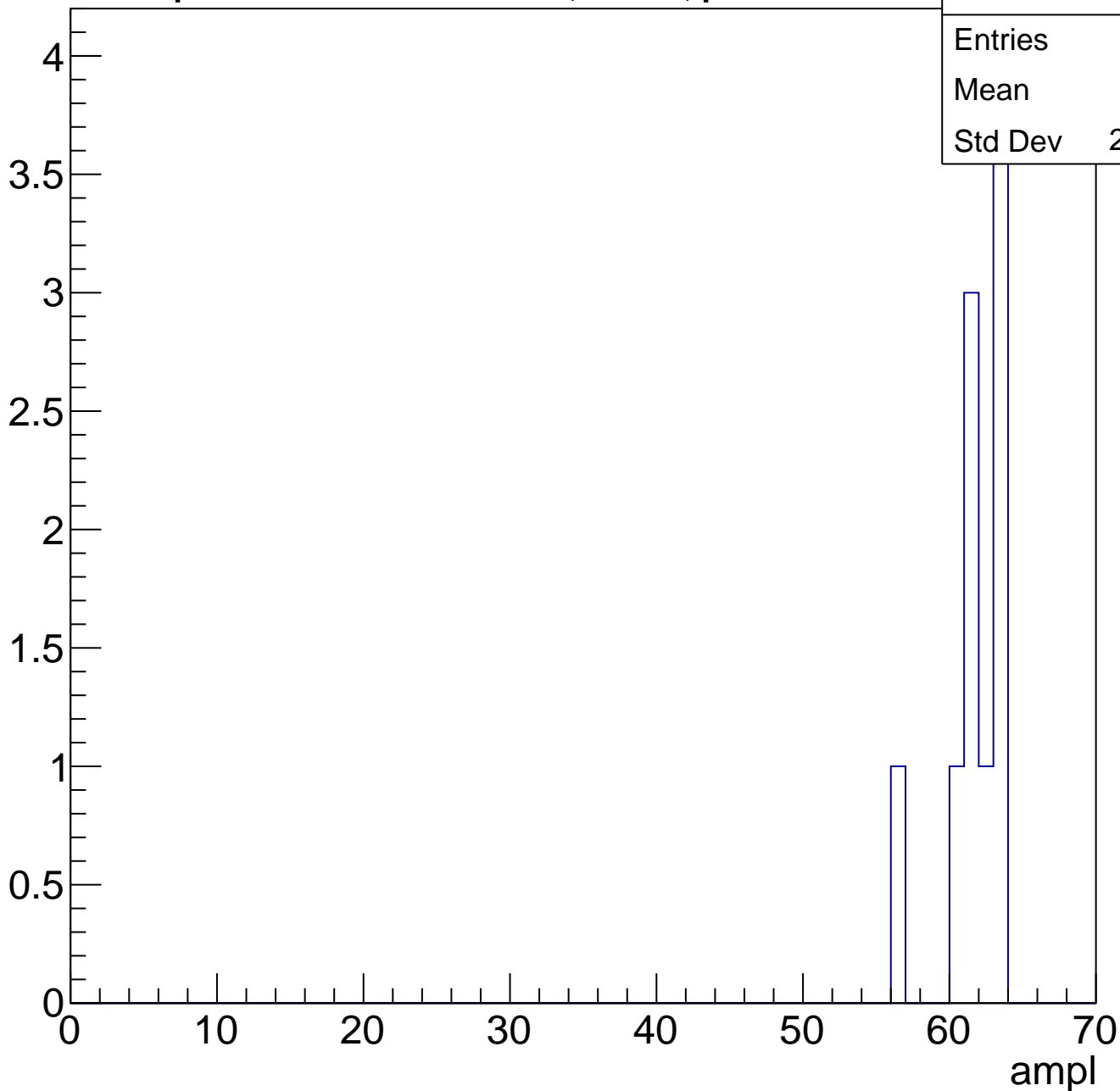
Entries	61
Mean	58.07
Std Dev	7.955



# B1L003S, U22-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

# B1L003S, U22-ch105, adc0

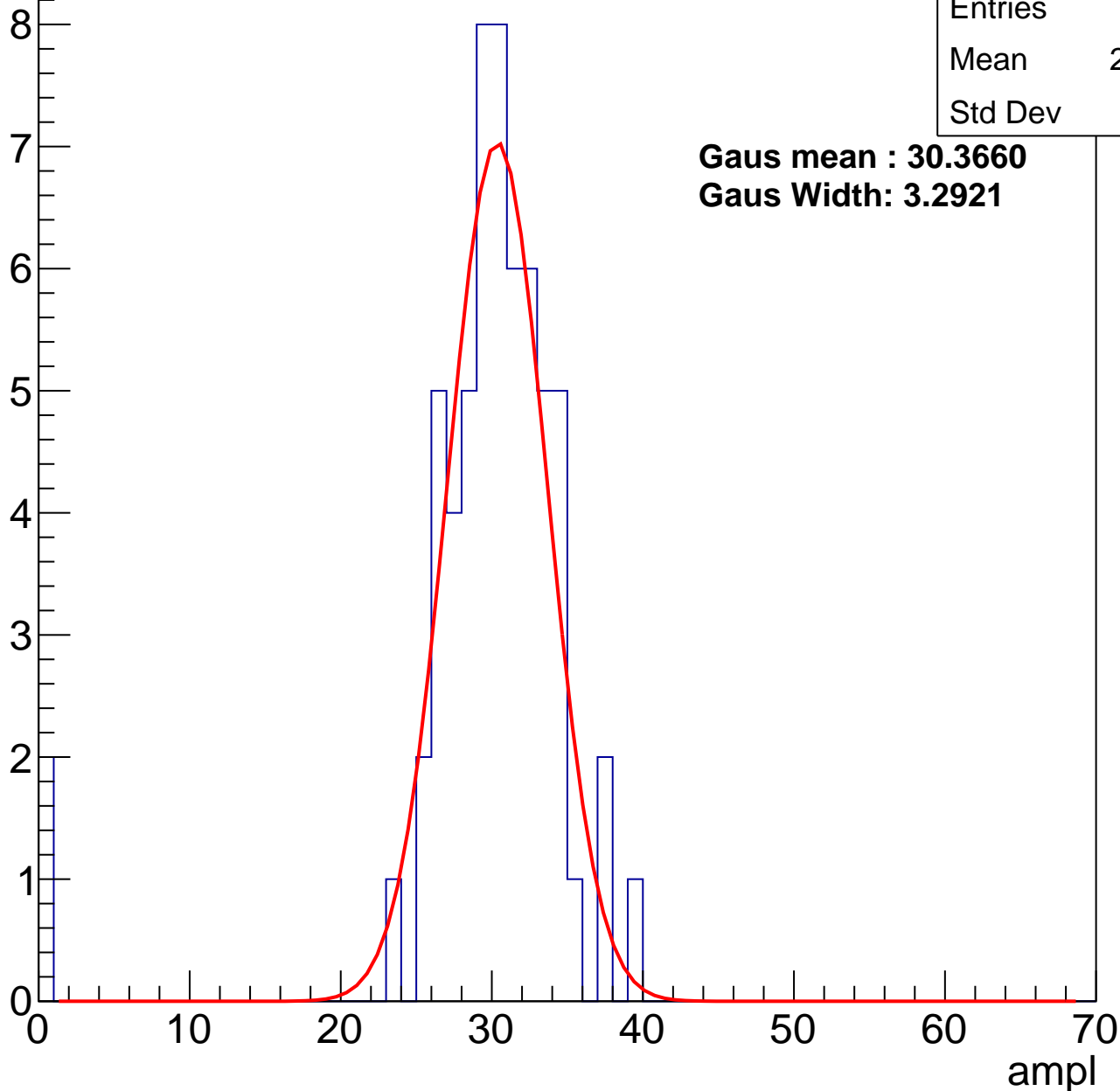
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	29.25
Std Dev	6.23

**Gaus mean : 30.3660**

**Gaus Width: 3.2921**



# B1L003S, U22-ch105, adc1

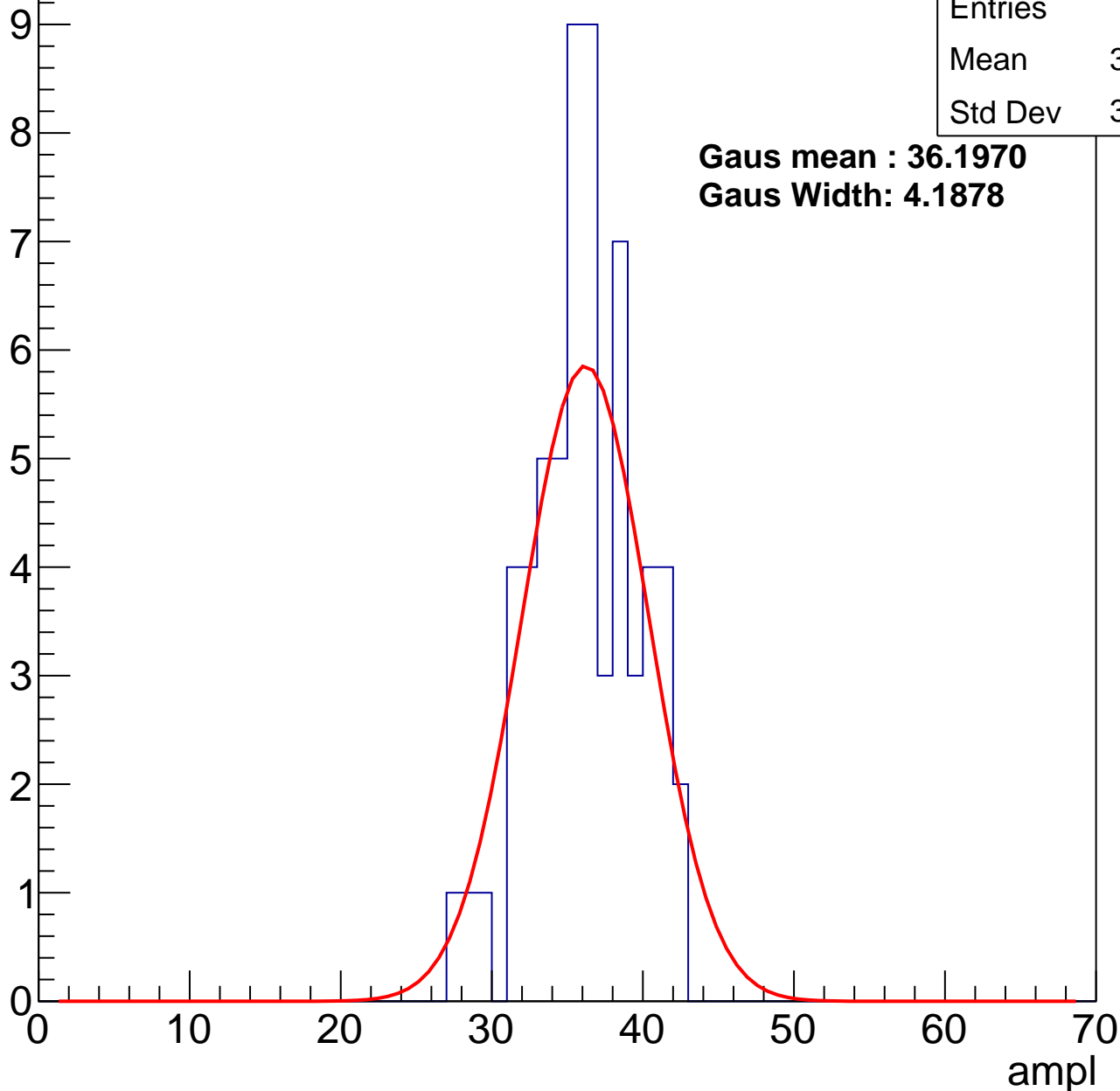
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	35.68
Std Dev	3.407

**Gaus mean : 36.1970**

**Gaus Width: 4.1878**



# B1L003S, U22-ch105, adc2

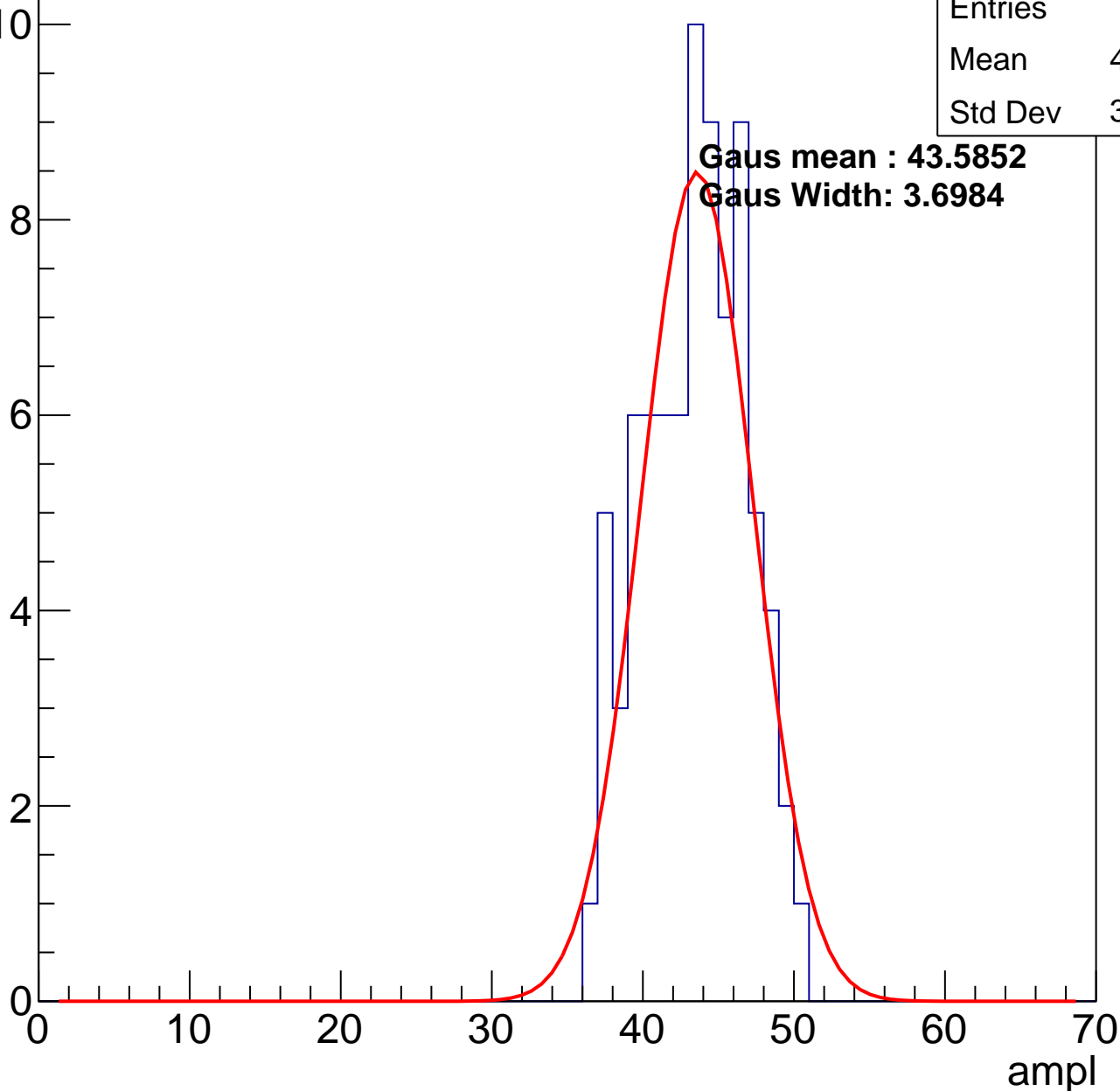
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	42.96
Std Dev	3.359

**Gaus mean : 43.5852**

**Gaus Width: 3.6984**

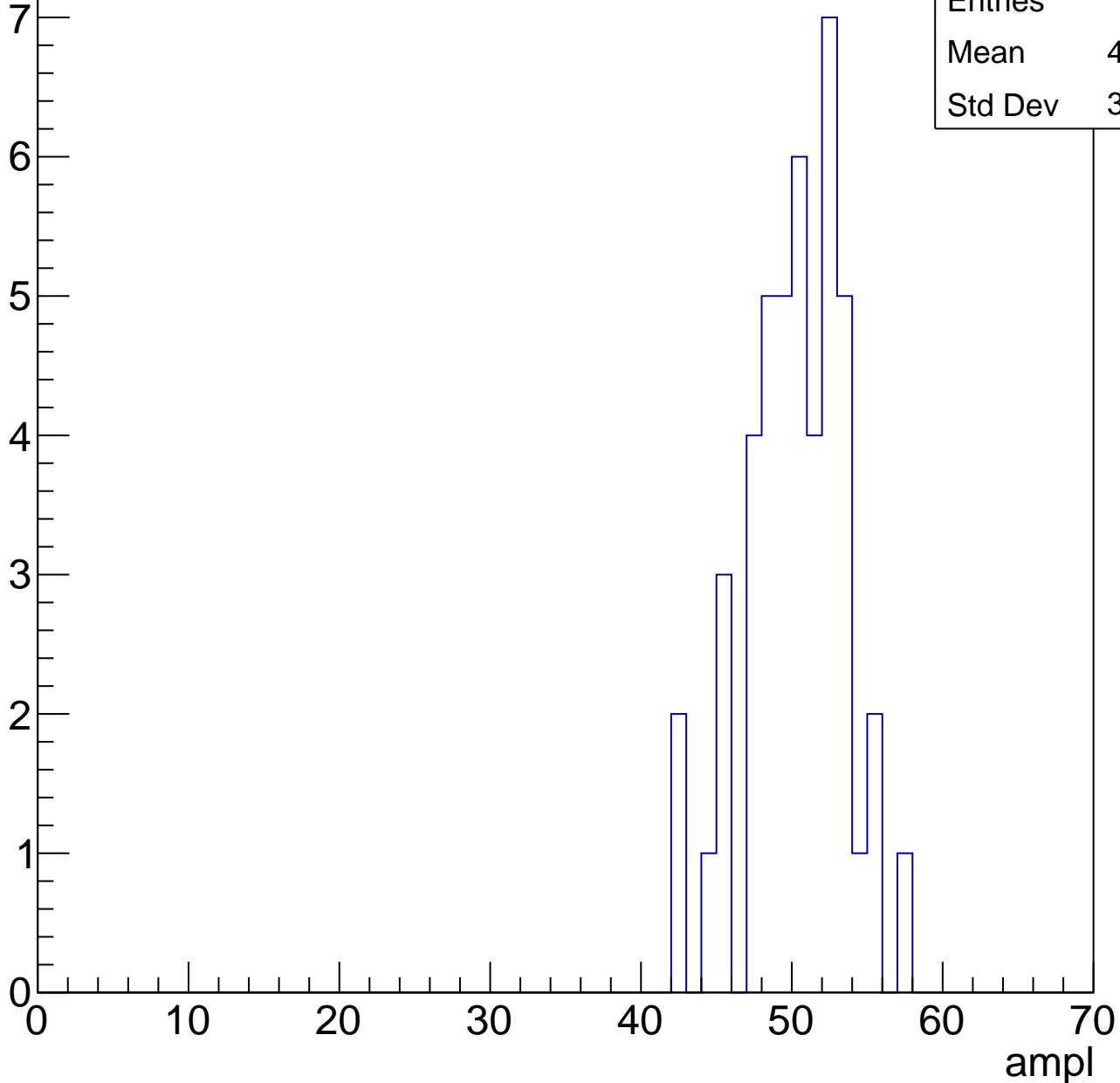


# B1L003S, U22-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	49.78
Std Dev	3.263

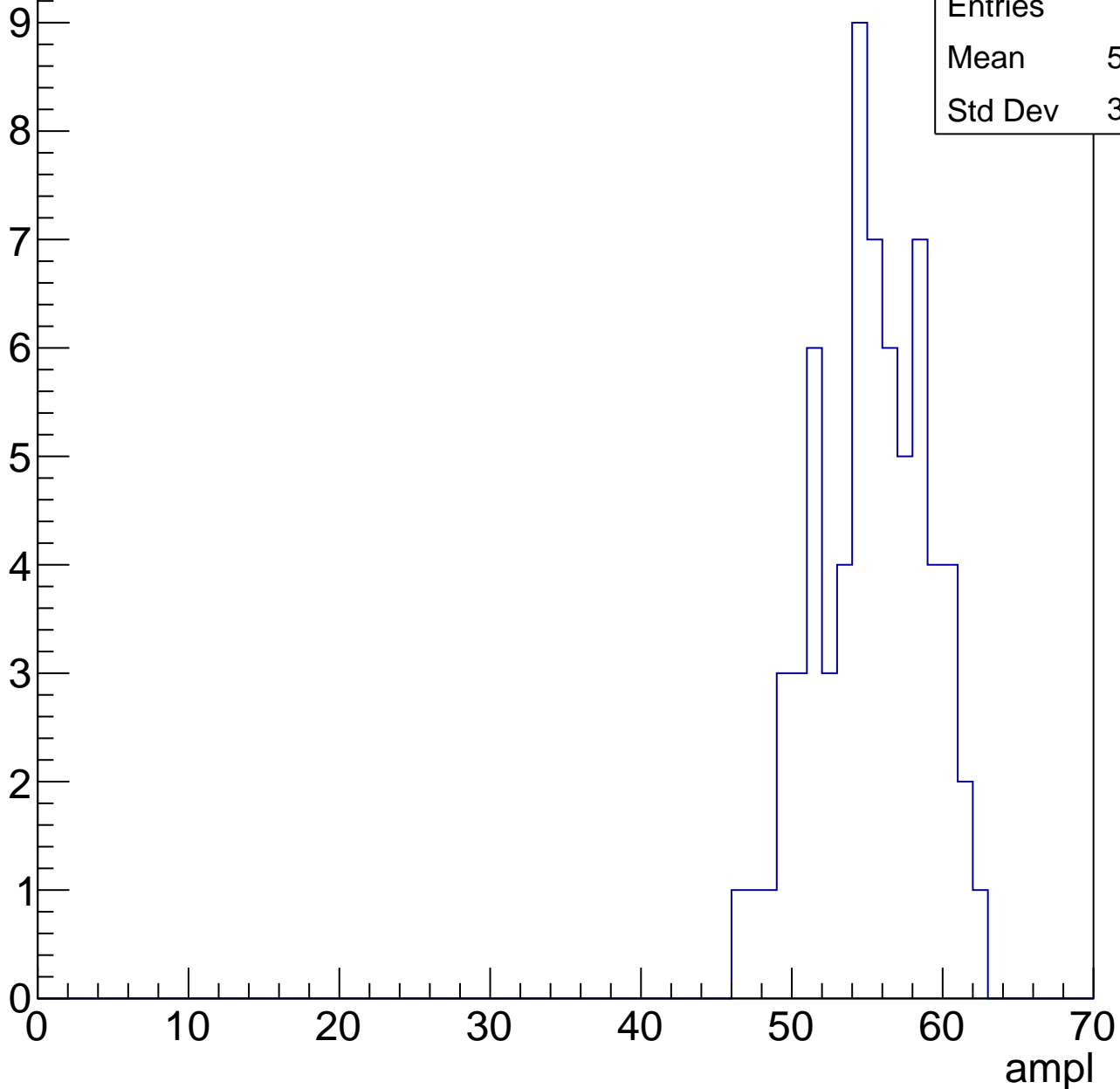


# B1L003S, U22-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	54.78
Std Dev	3.656

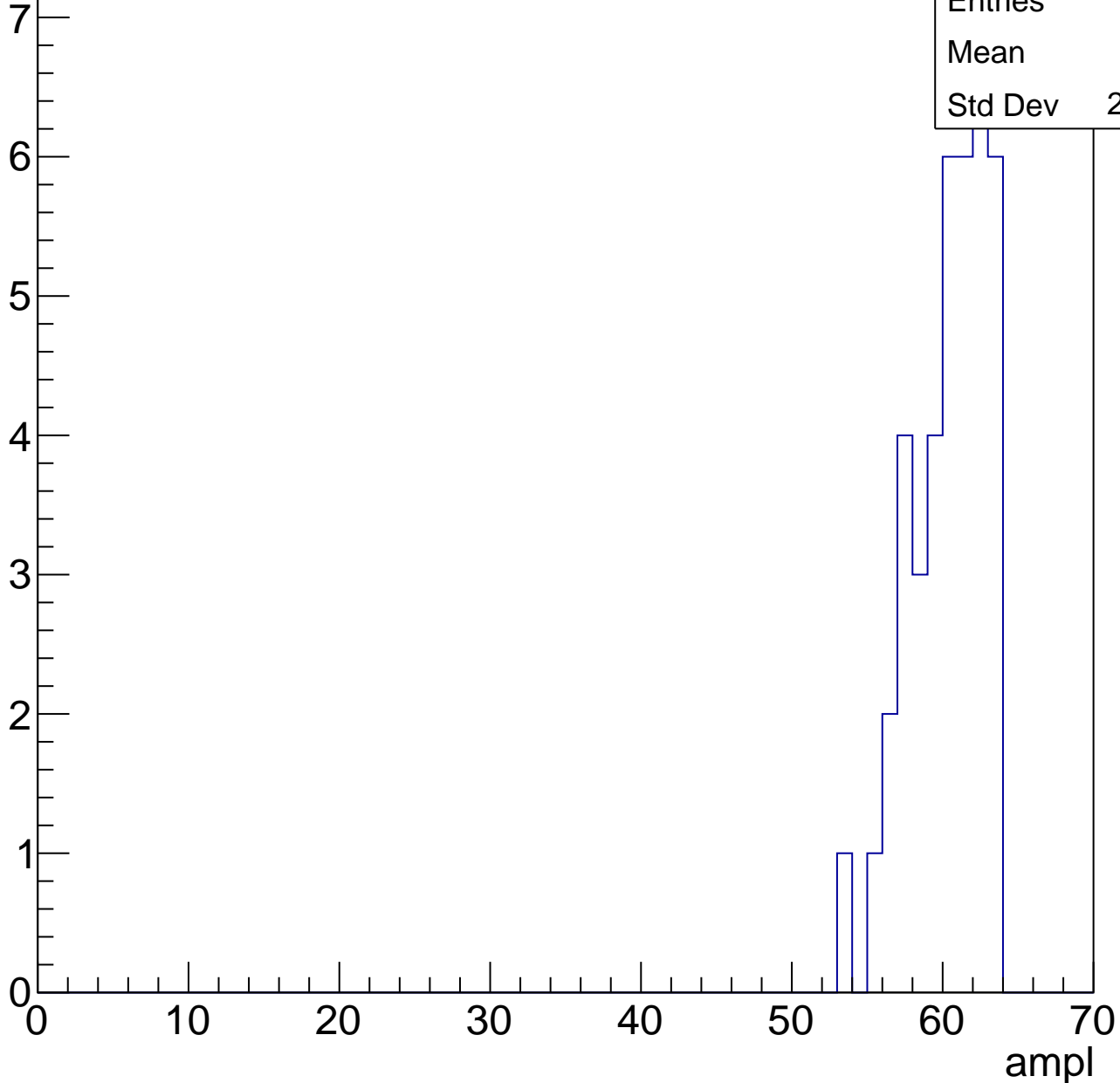


# B1L003S, U22-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

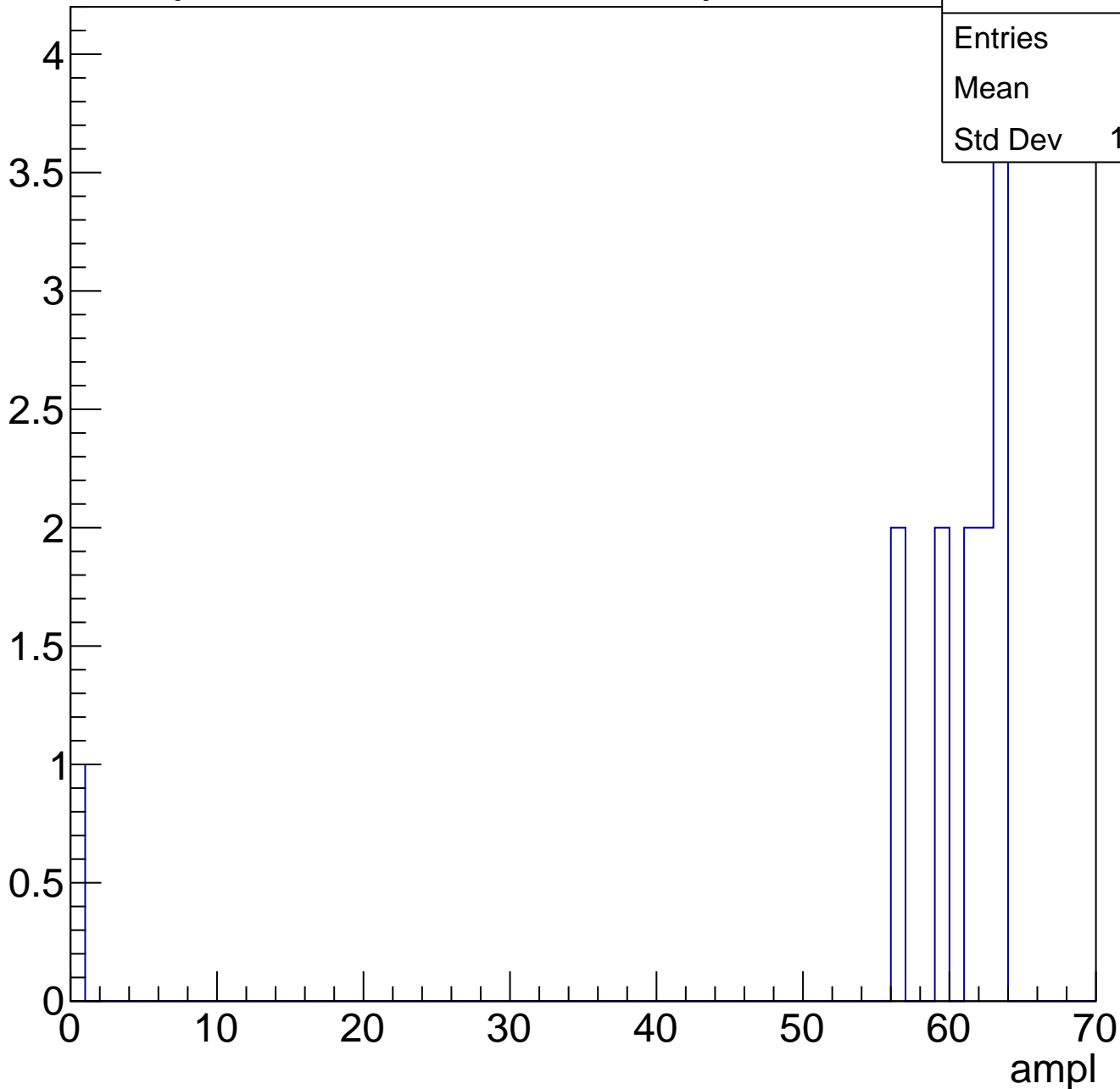
Entries	40
Mean	59.9
Std Dev	2.478



# B1L003S, U22-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch106, adc0

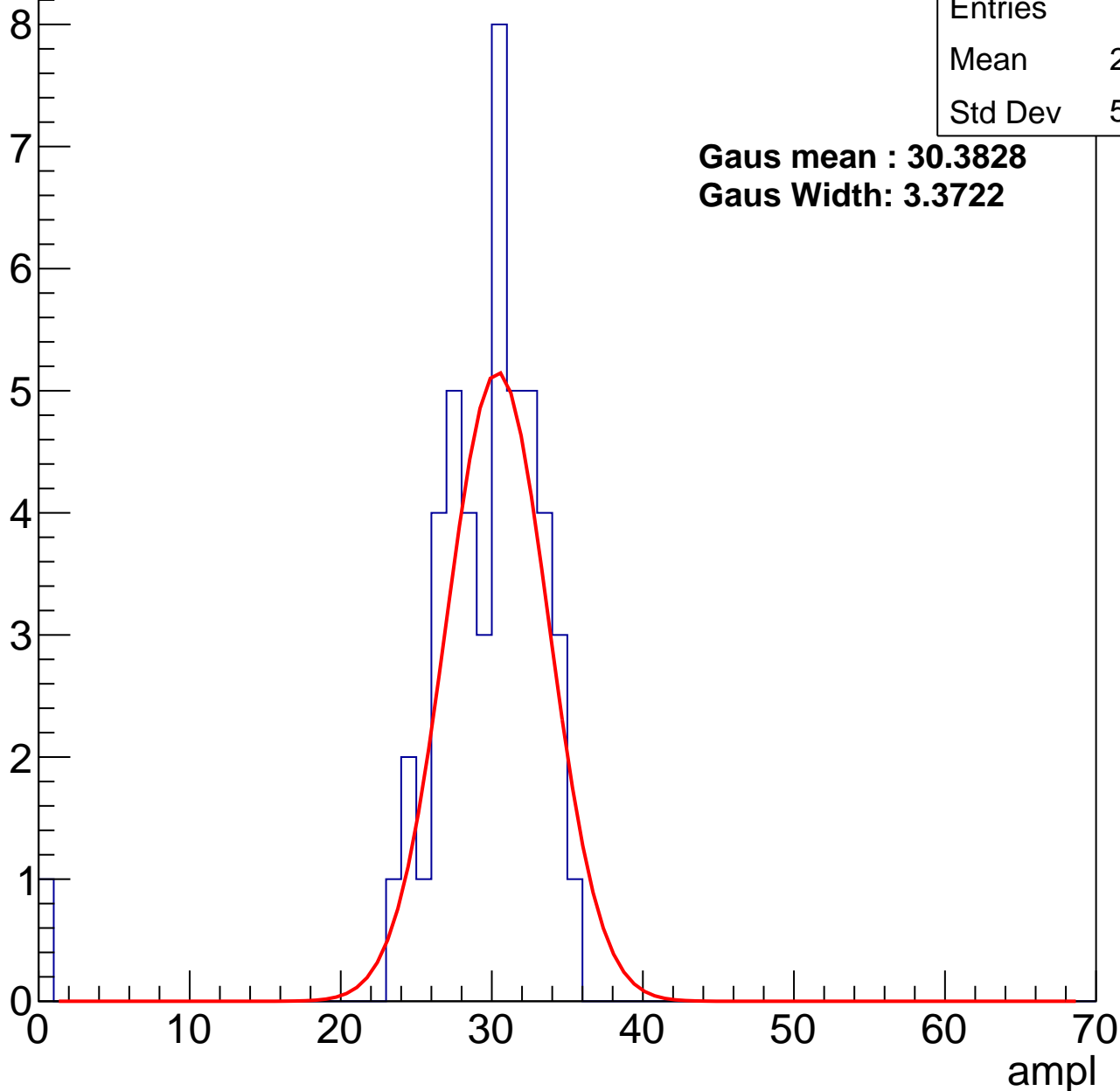
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	28.89
Std Dev	5.158

**Gaus mean : 30.3828**

**Gaus Width: 3.3722**



# B1L003S, U22-ch106, adc1

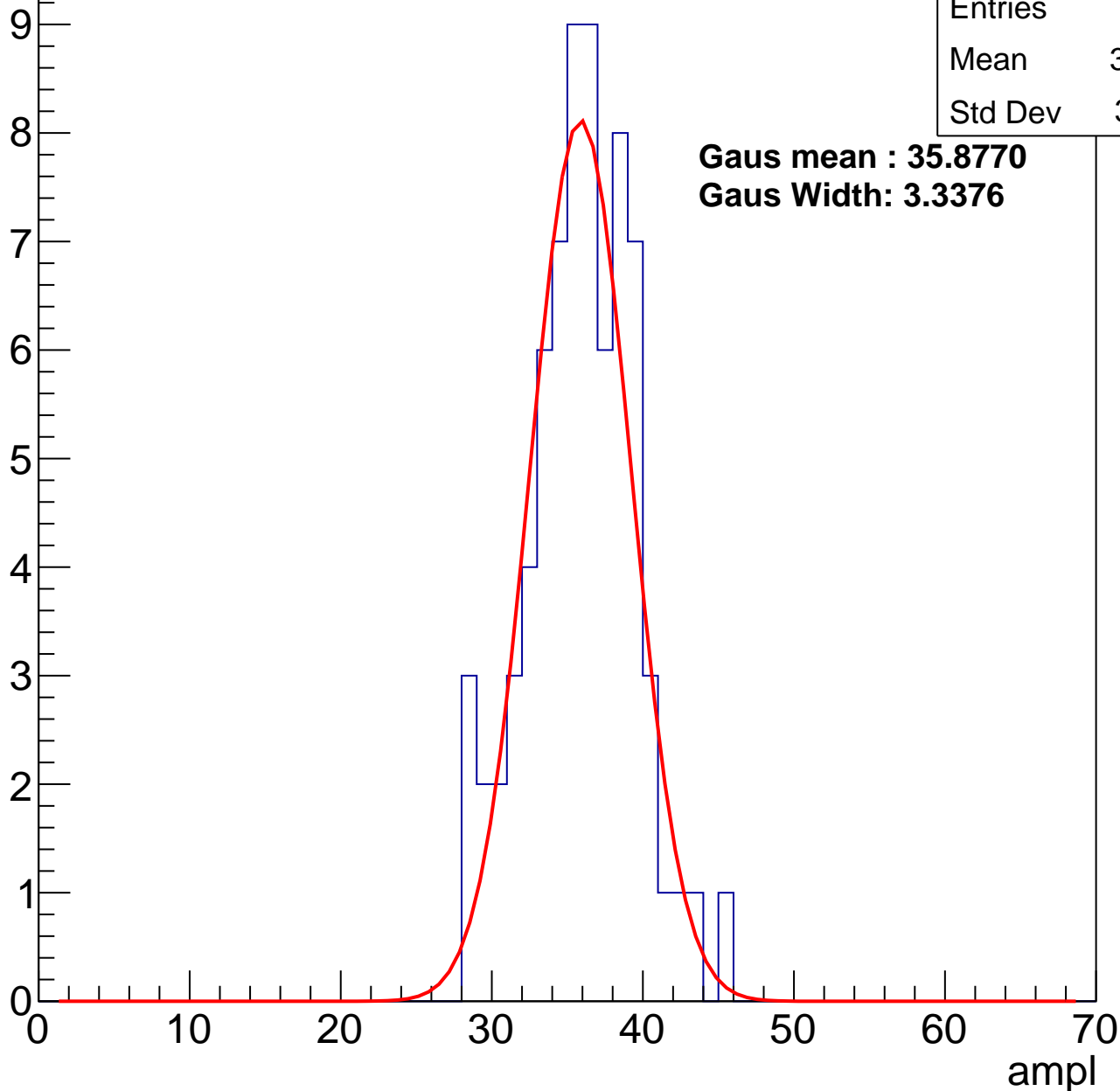
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.45
Std Dev	3.531

**Gaus mean : 35.8770**

**Gaus Width: 3.3376**



# B1L003S, U22-ch106, adc2

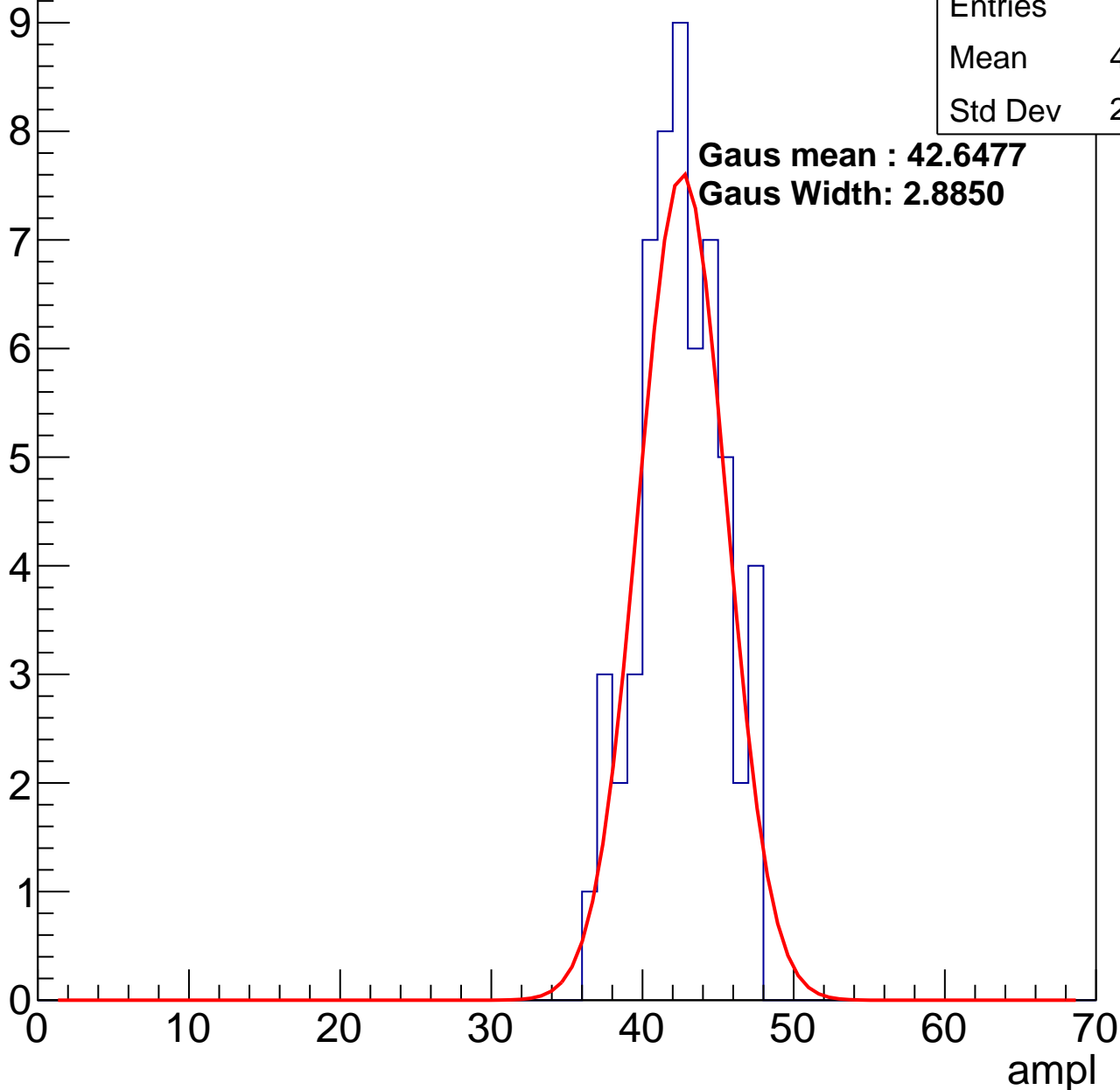
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.05
Std Dev	2.704

**Gaus mean : 42.6477**

**Gaus Width: 2.8850**

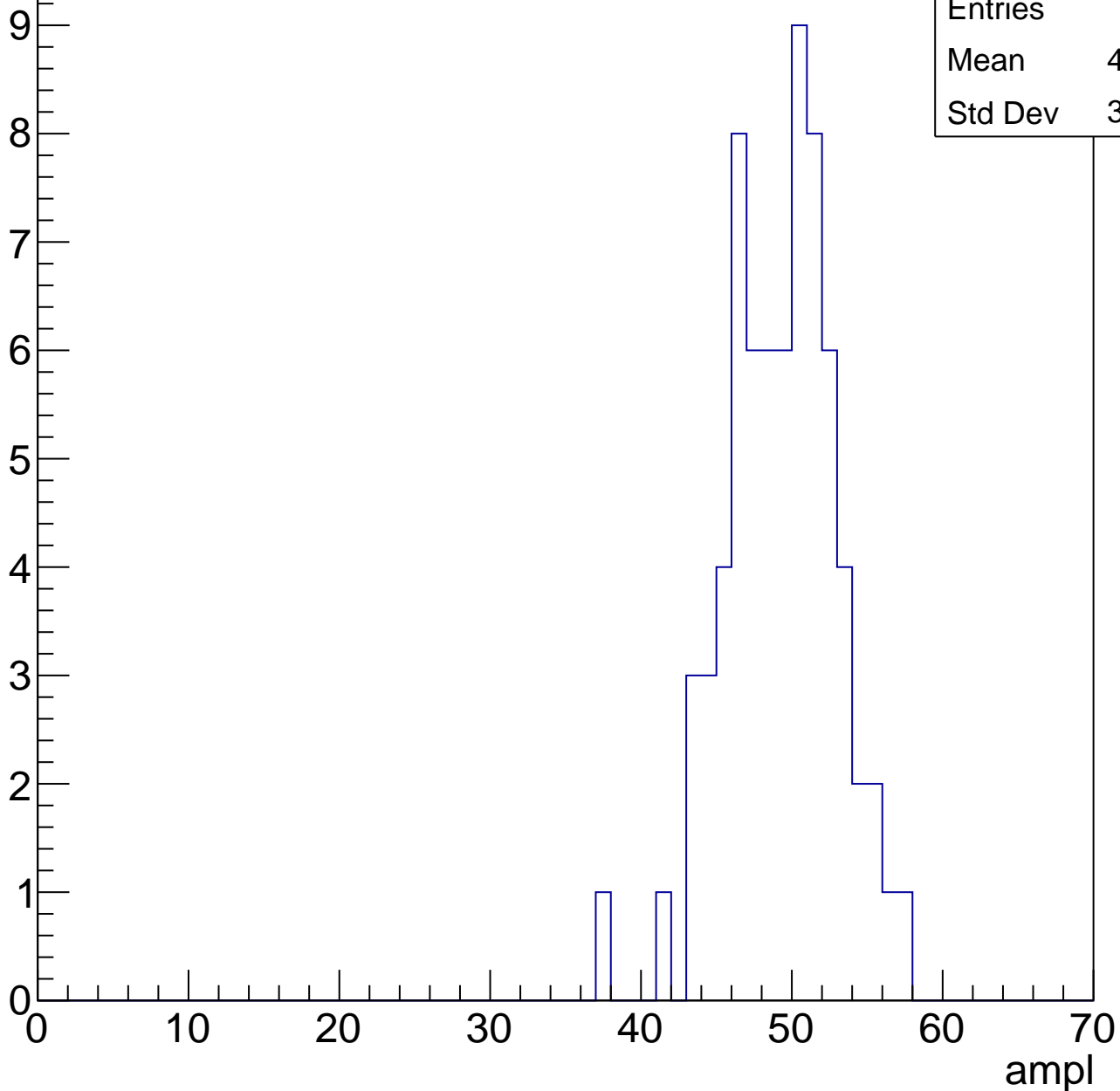


# B1L003S, U22-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	48.79
Std Dev	3.654

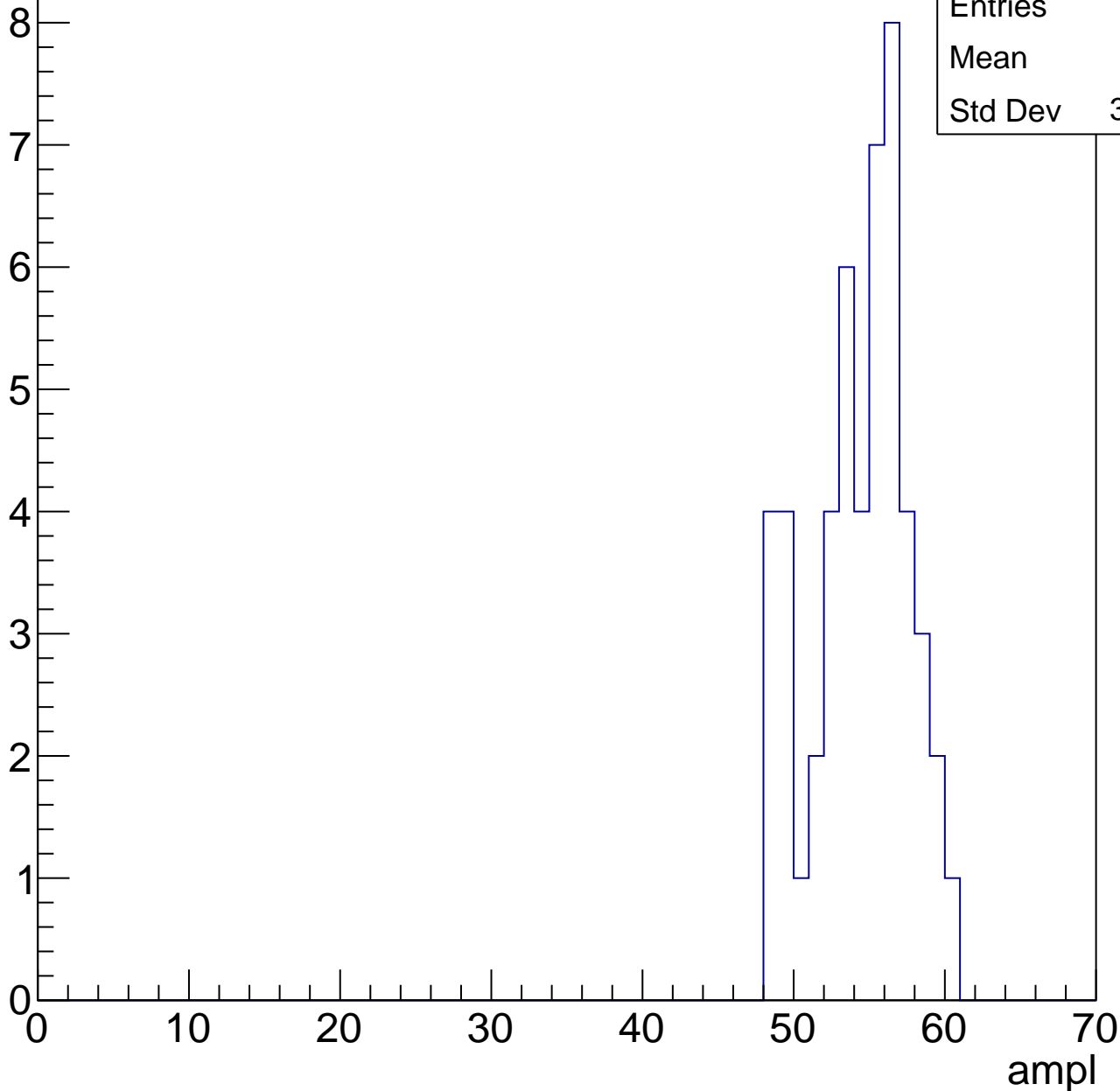


# B1L003S, U22-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	53.9
Std Dev	3.189

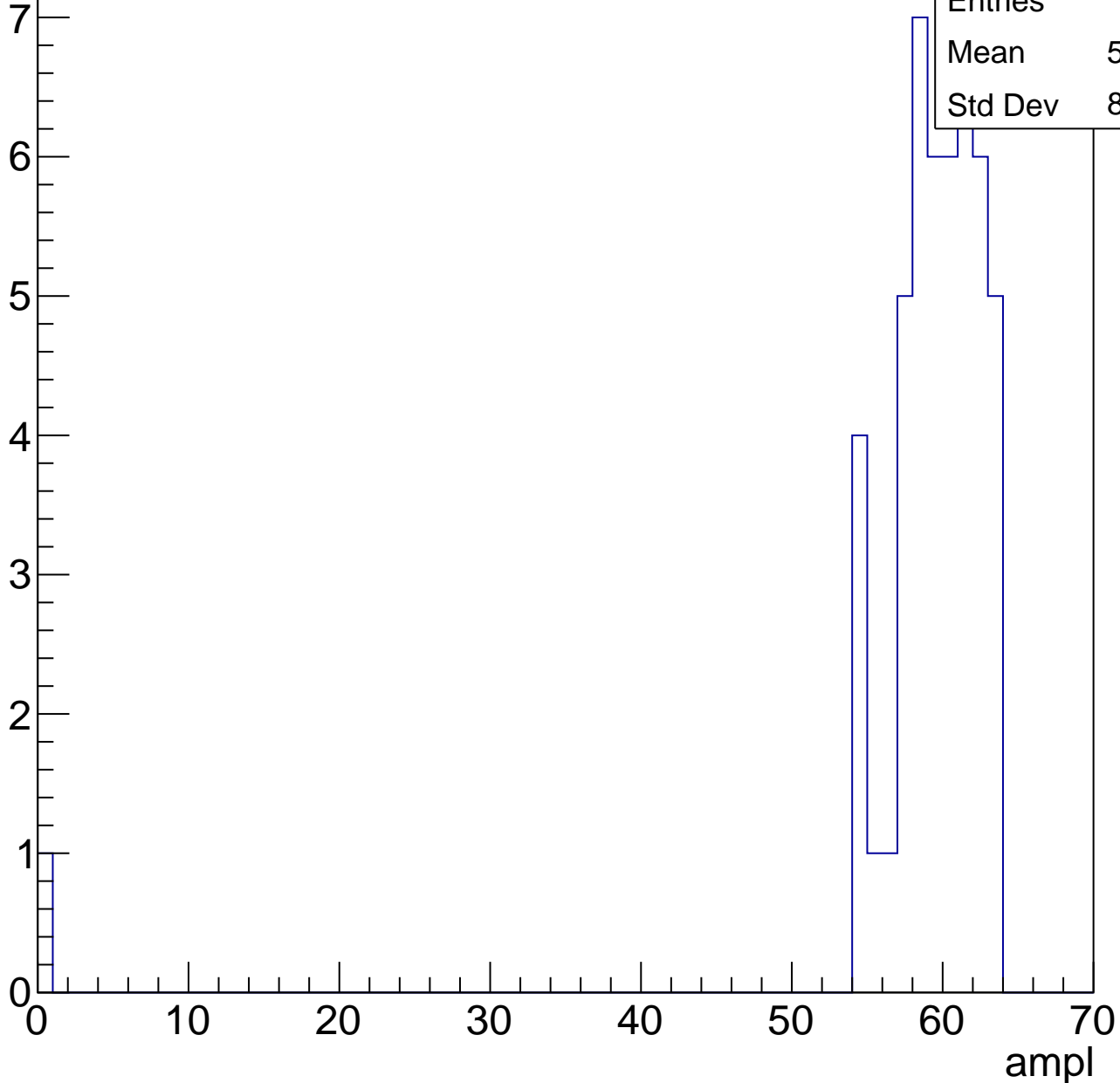


# B1L003S, U22-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.08
Std Dev	8.759



# B1L003S, U22-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

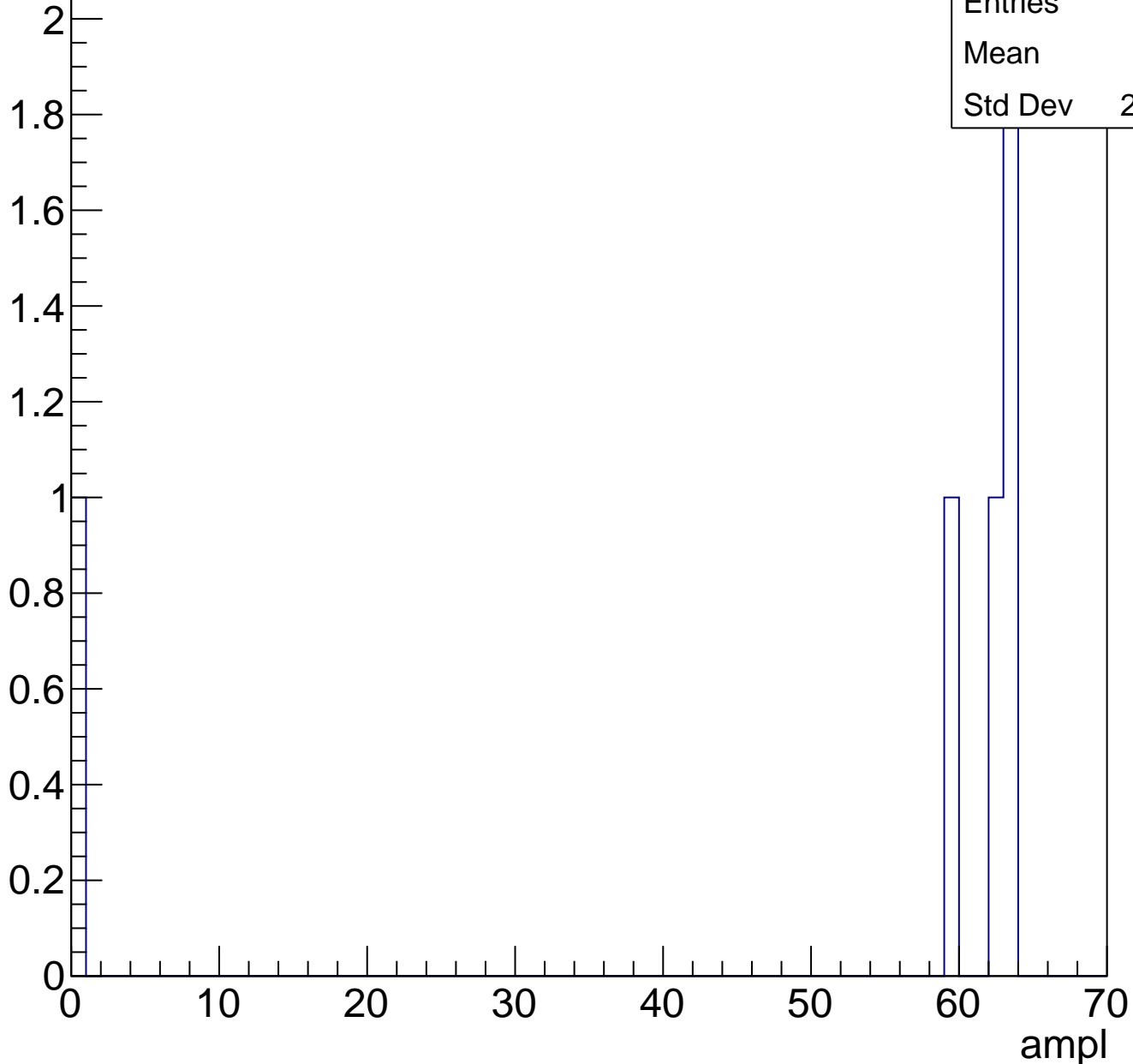
Entries	13
Mean	61.31
Std Dev	1.264



# B1L003S, U22-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch107, adc0

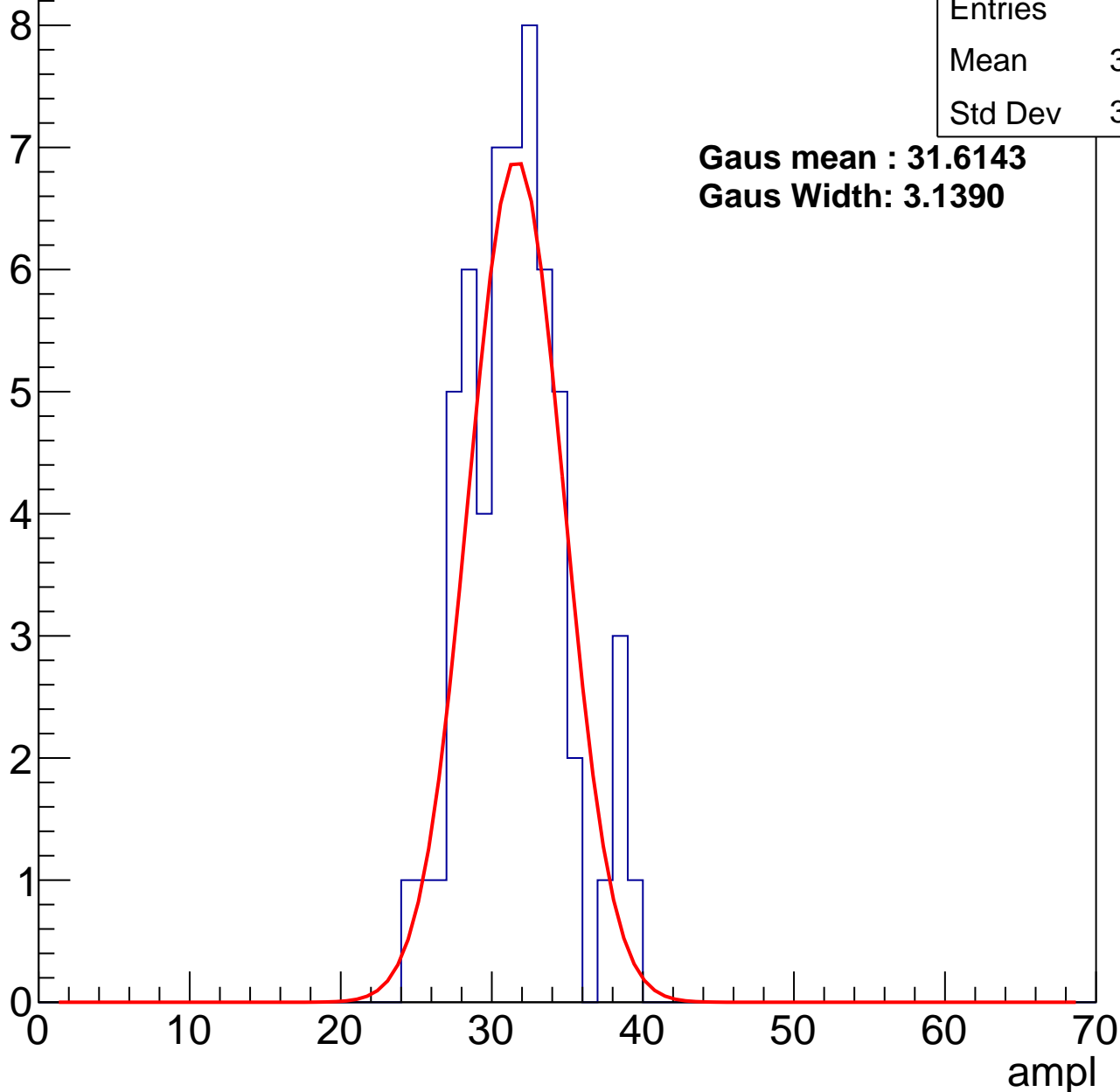
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	31.12
Std Dev	3.275

**Gaus mean : 31.6143**

**Gaus Width: 3.1390**



# B1L003S, U22-ch107, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	64
Mean	36.94
Std Dev	2.994

**Gaus mean : 37.3385**

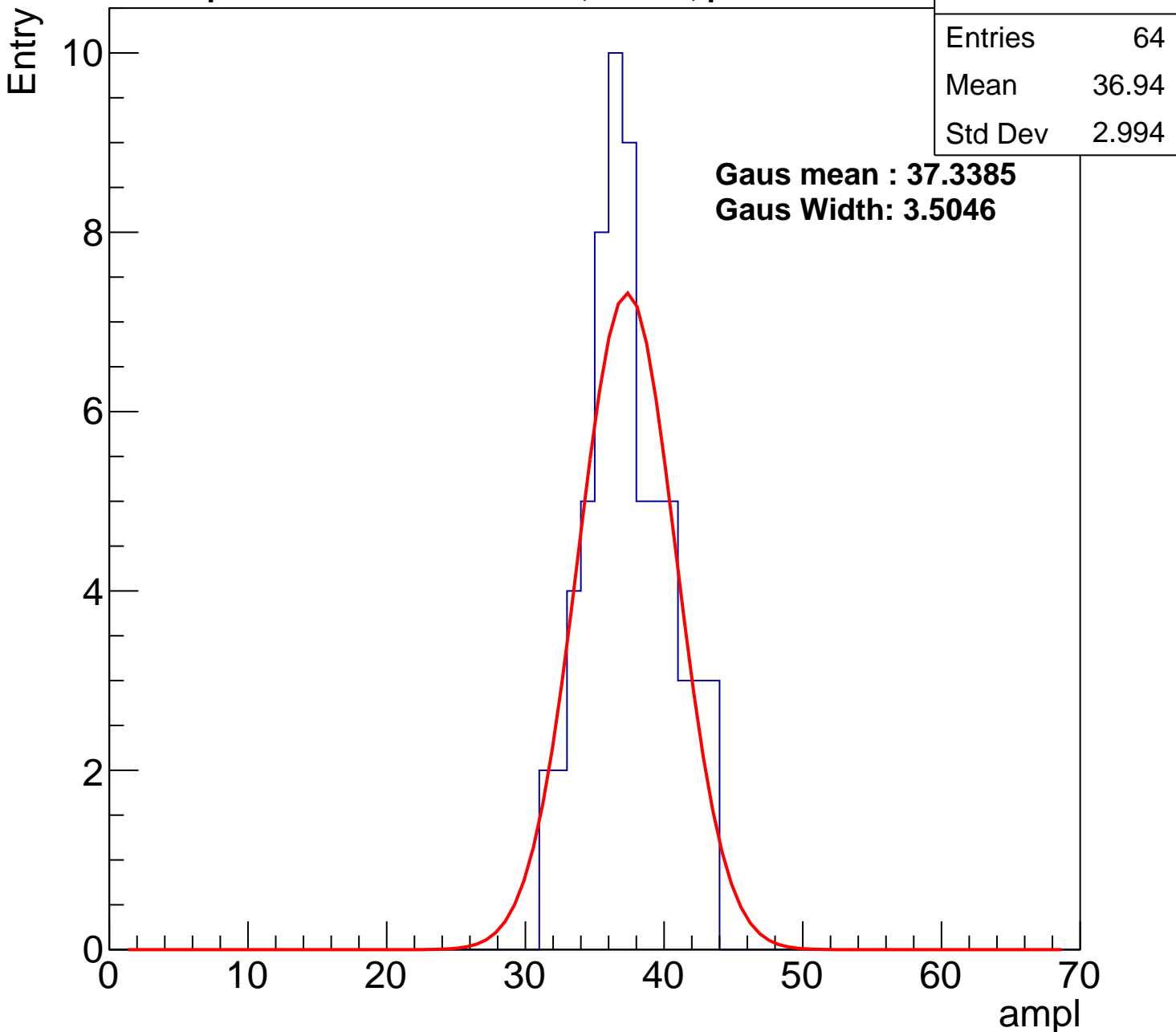
**Gaus Width: 3.5046**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L003S, U22-ch107, adc2

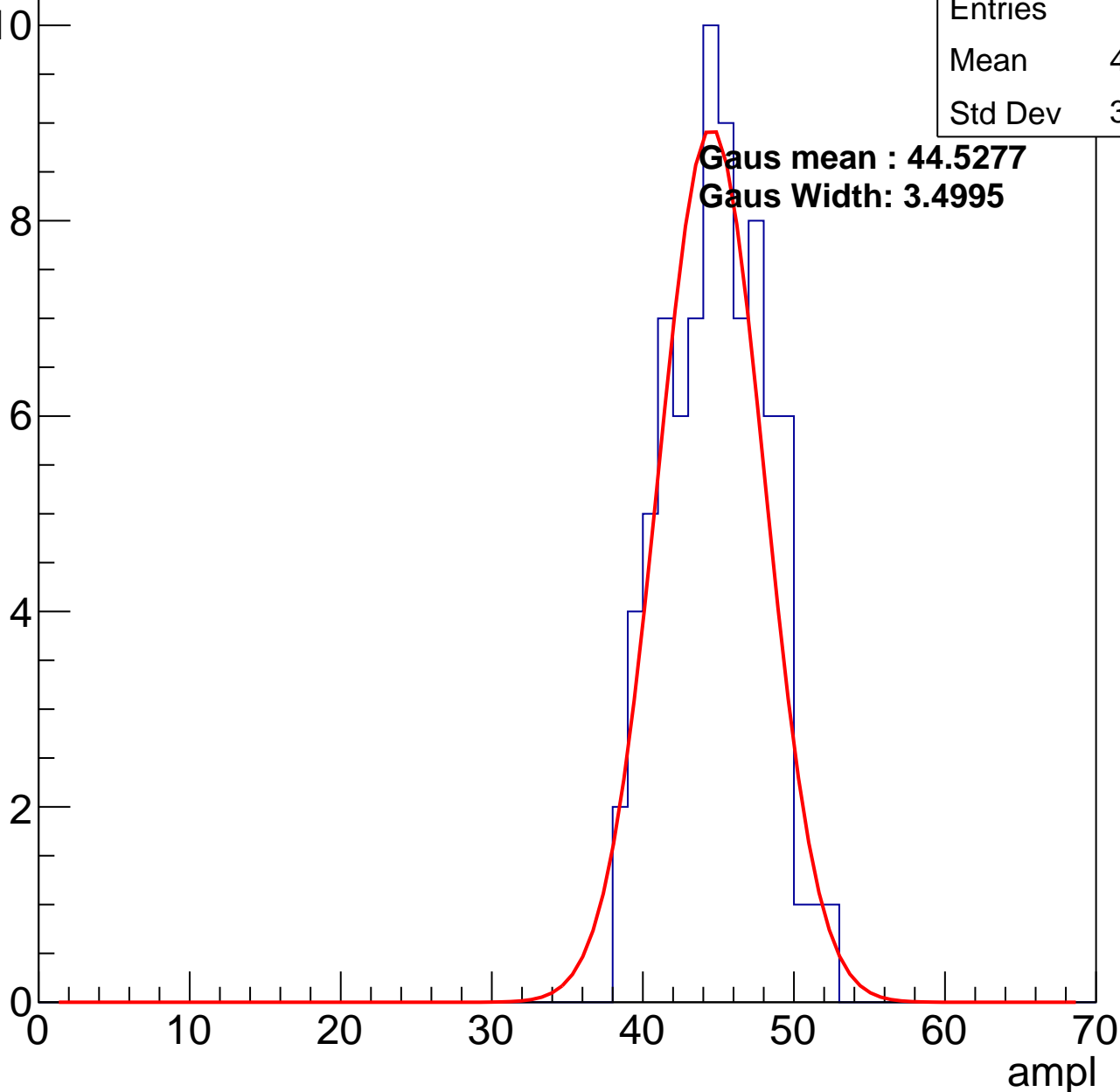
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	44.38
Std Dev	3.238

**Gaus mean : 44.5277**

**Gaus Width: 3.4995**

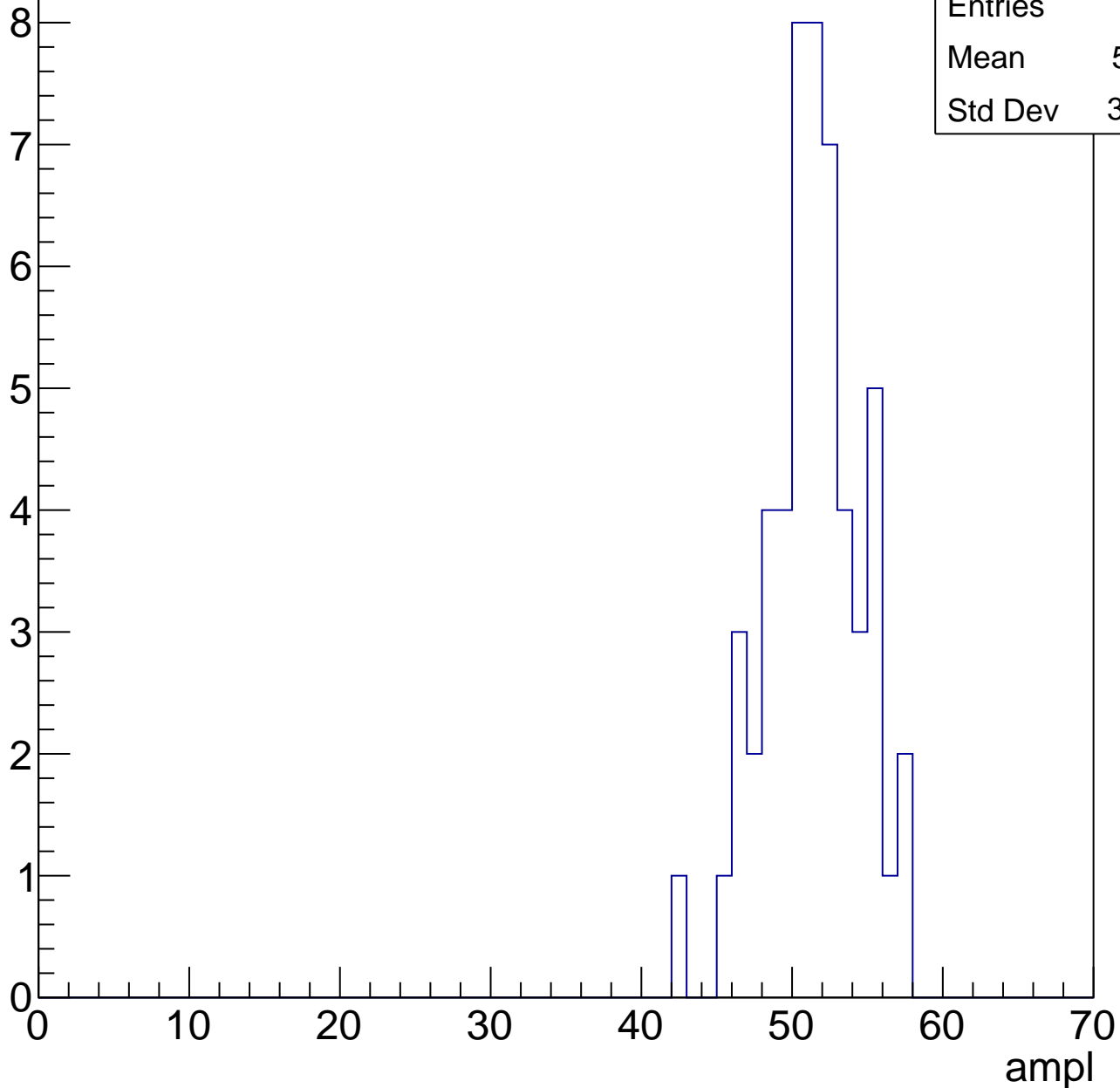


# B1L003S, U22-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	50.91
Std Dev	3.104

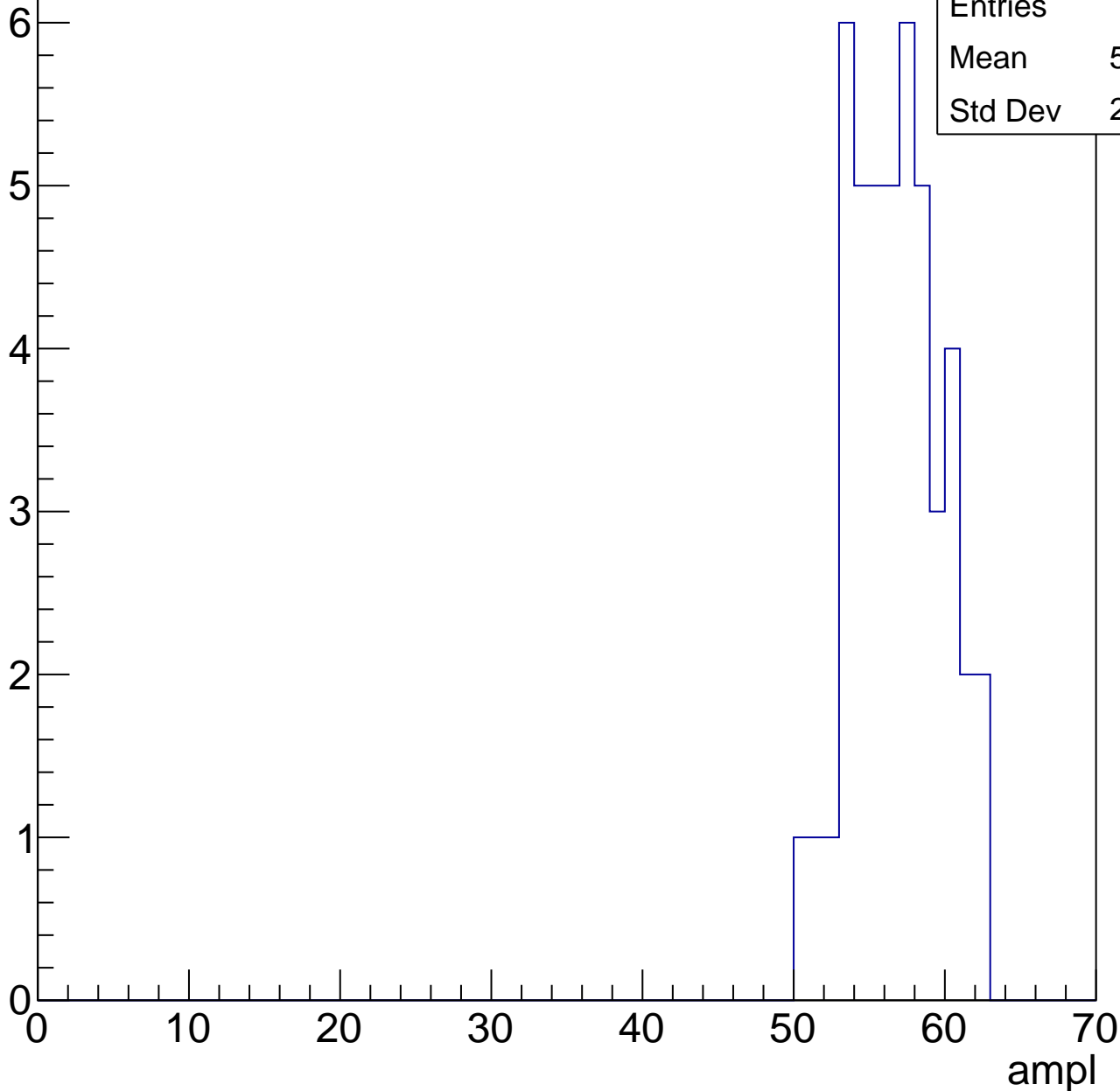


# B1L003S, U22-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	46
Mean	56.33
Std Dev	2.912

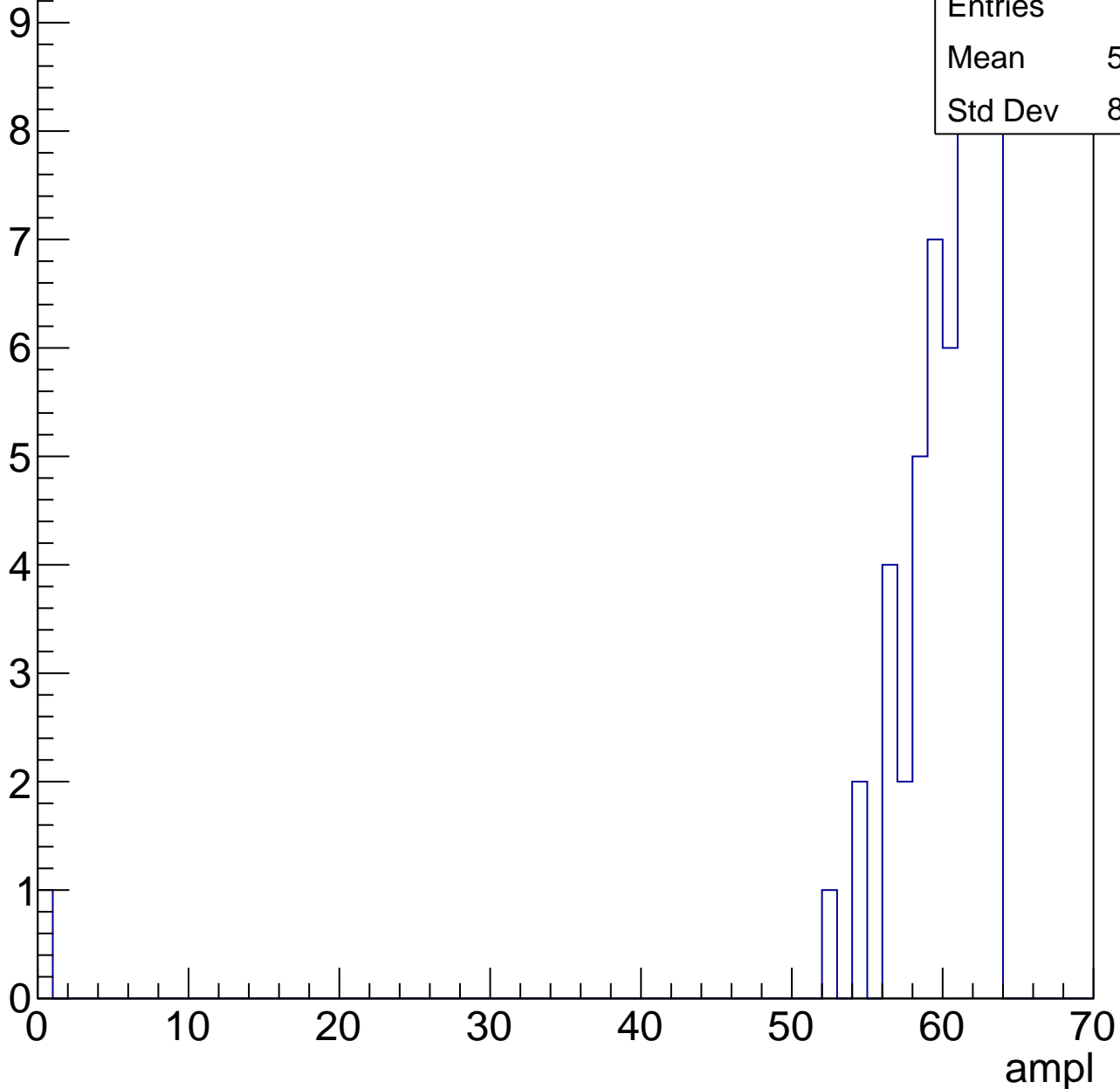


# B1L003S, U22-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	58.72
Std Dev	8.555



# B1L003S, U22-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0



# B1L003S, U22-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch108, adc0

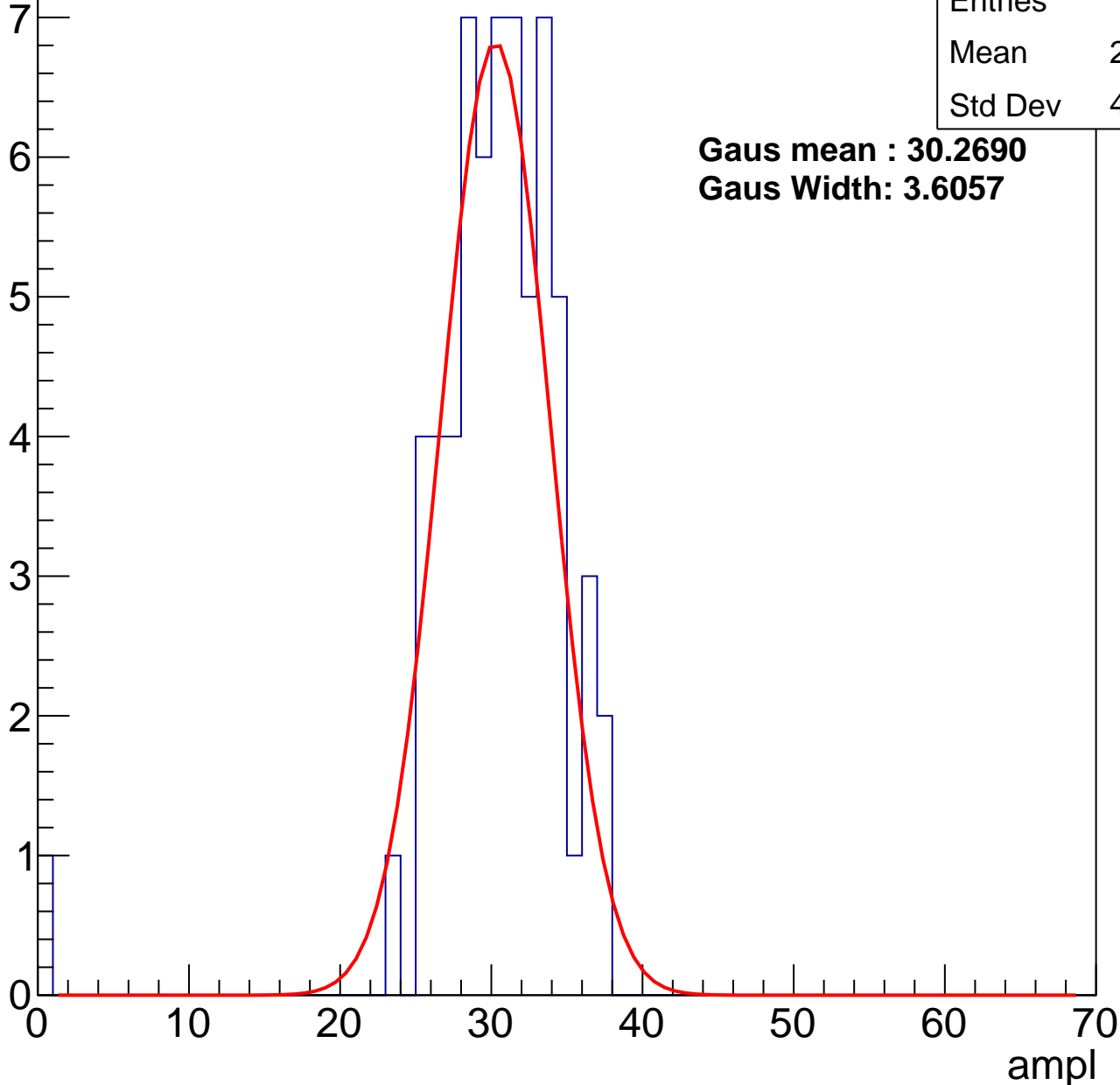
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.84
Std Dev	4.972

**Gaus mean : 30.2690**

**Gaus Width: 3.6057**



# B1L003S, U22-ch108, adc1

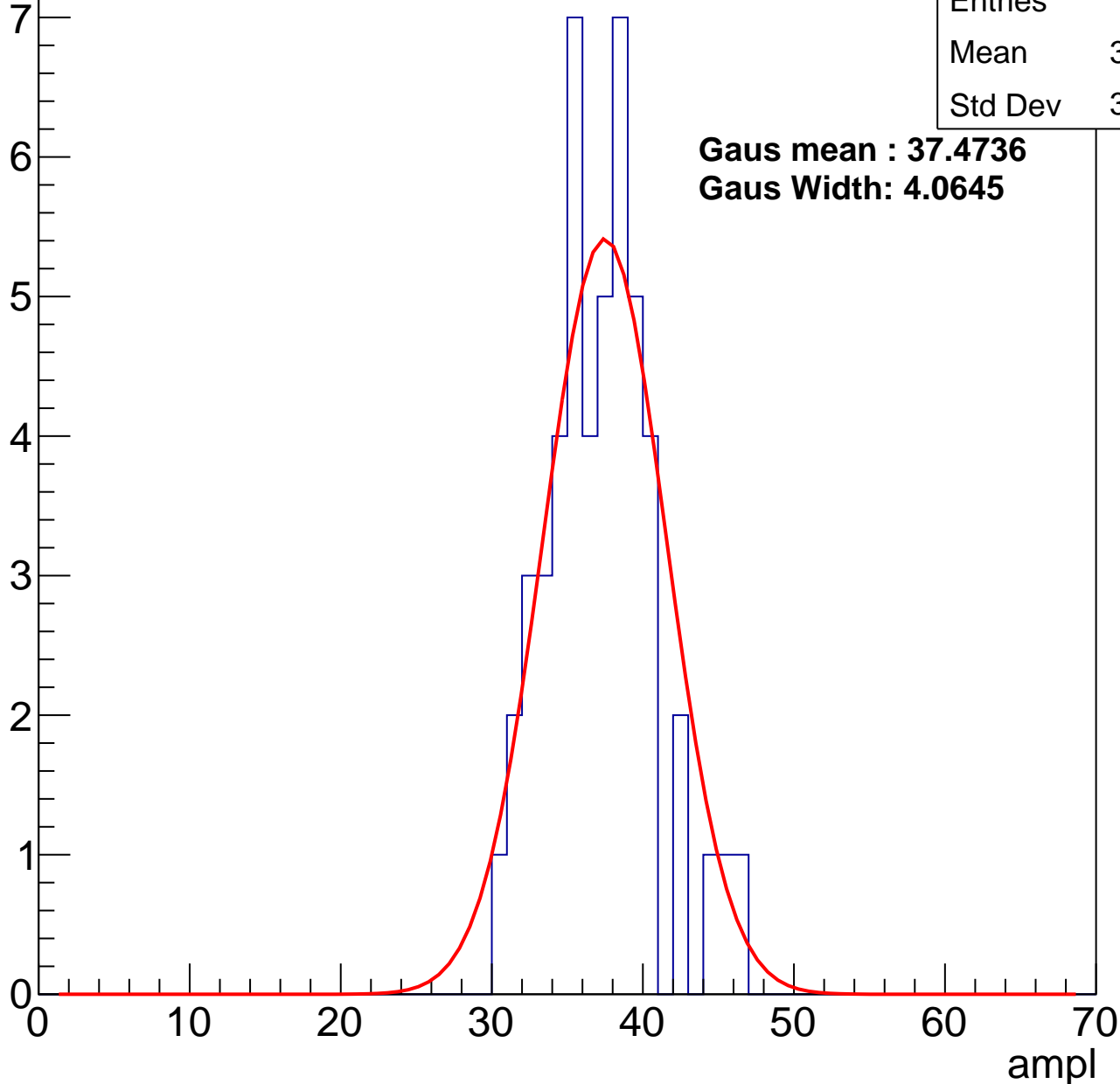
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	36.74
Std Dev	3.509

**Gaus mean : 37.4736**

**Gaus Width: 4.0645**



# B1L003S, U22-ch108, adc2

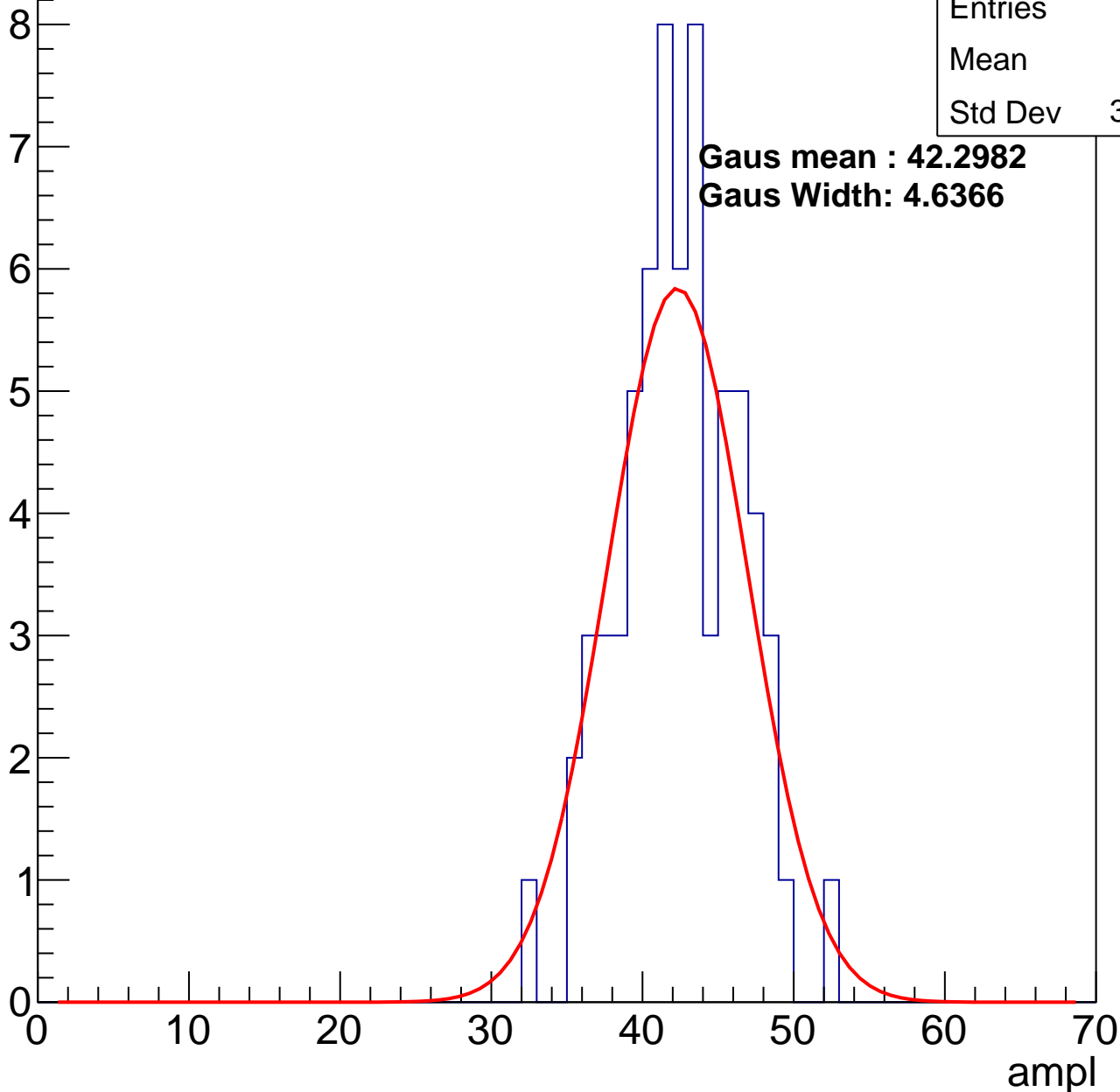
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42
Std Dev	3.879

**Gaus mean : 42.2982**

**Gaus Width: 4.6366**

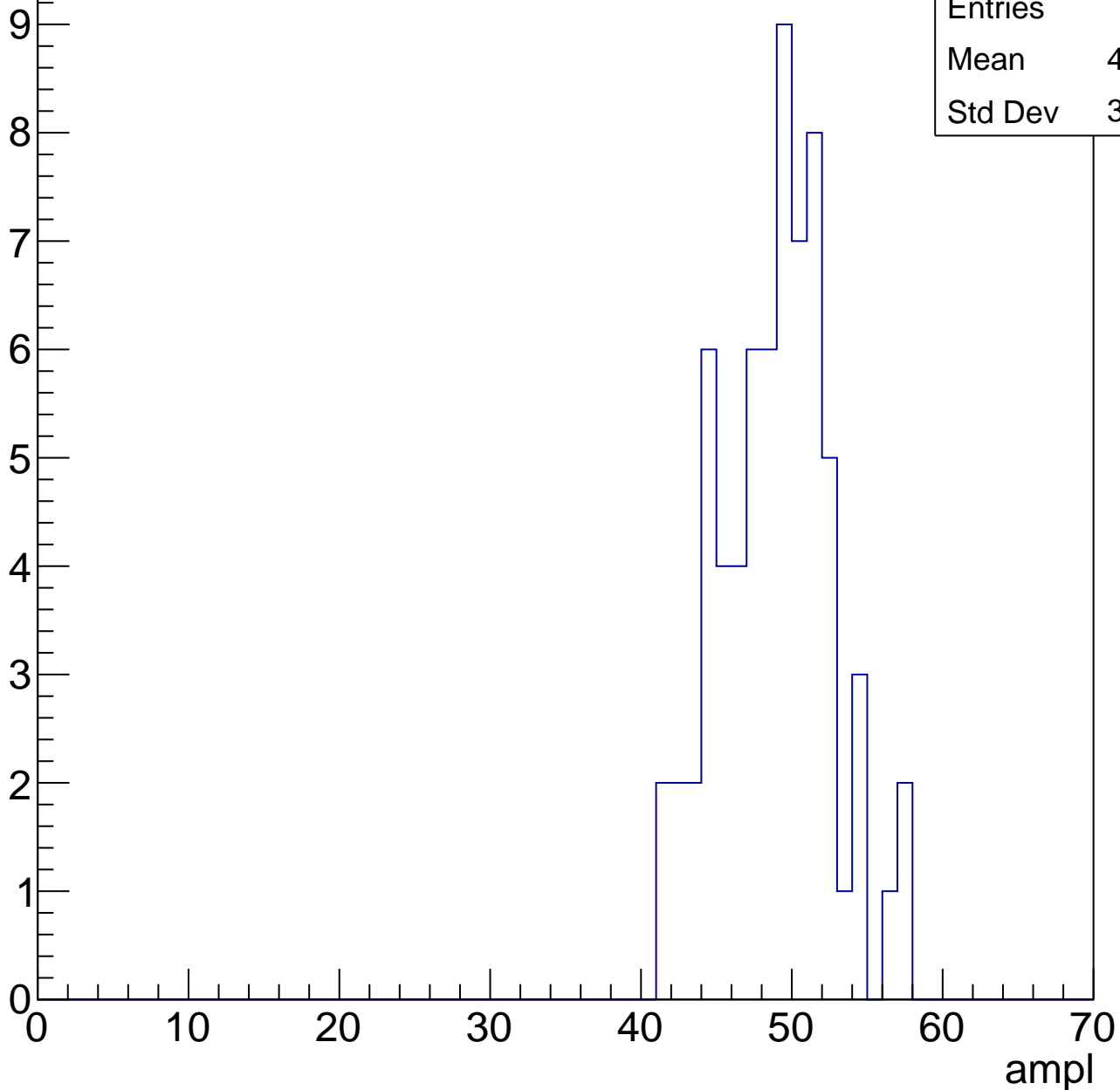


# B1L003S, U22-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	48.44
Std Dev	3.656



# B1L003S, U22-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

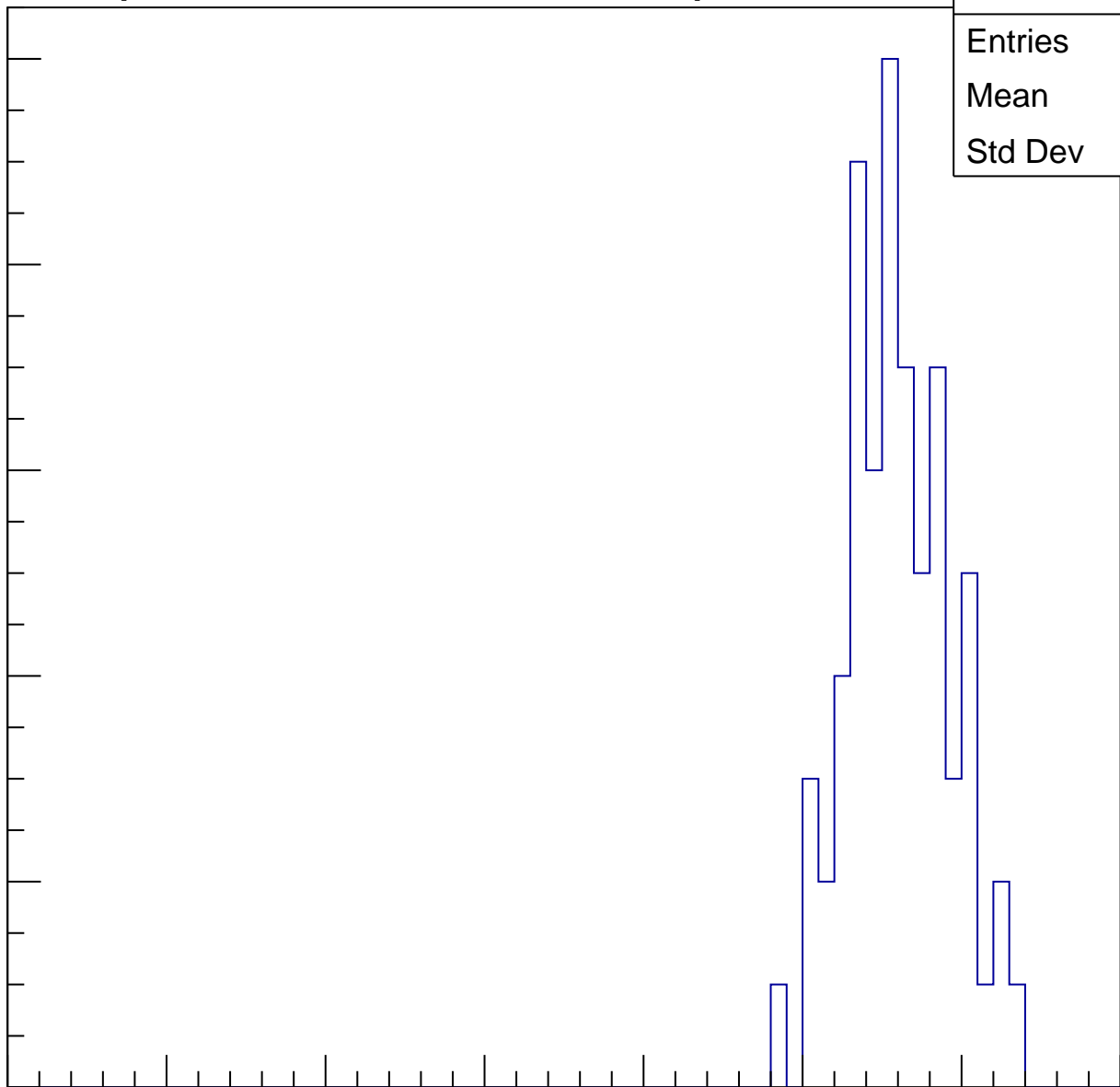
Entries	66
Mean	55.56
Std Dev	3.196

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

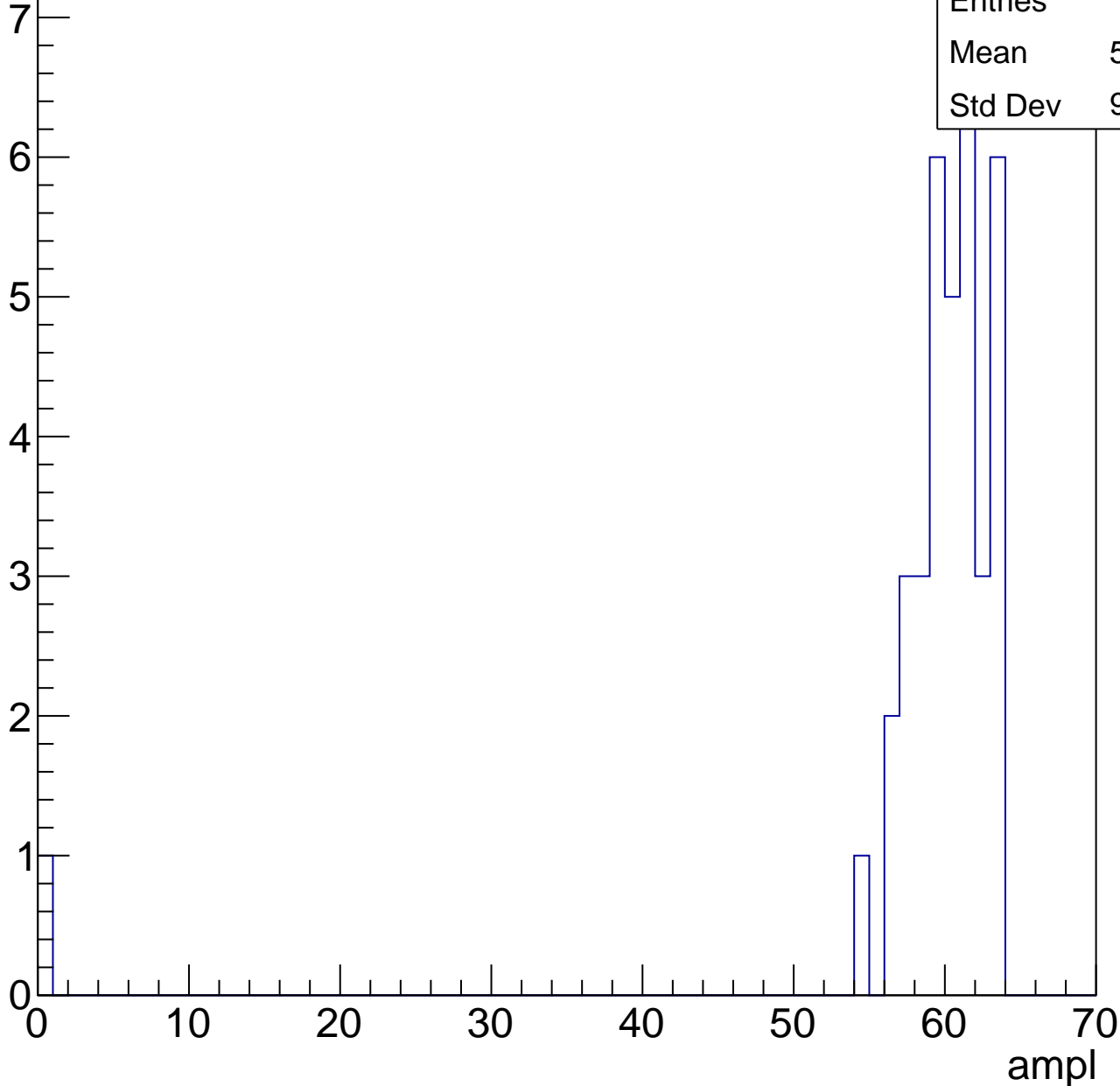


# B1L003S, U22-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	37
Mean	58.27
Std Dev	9.967

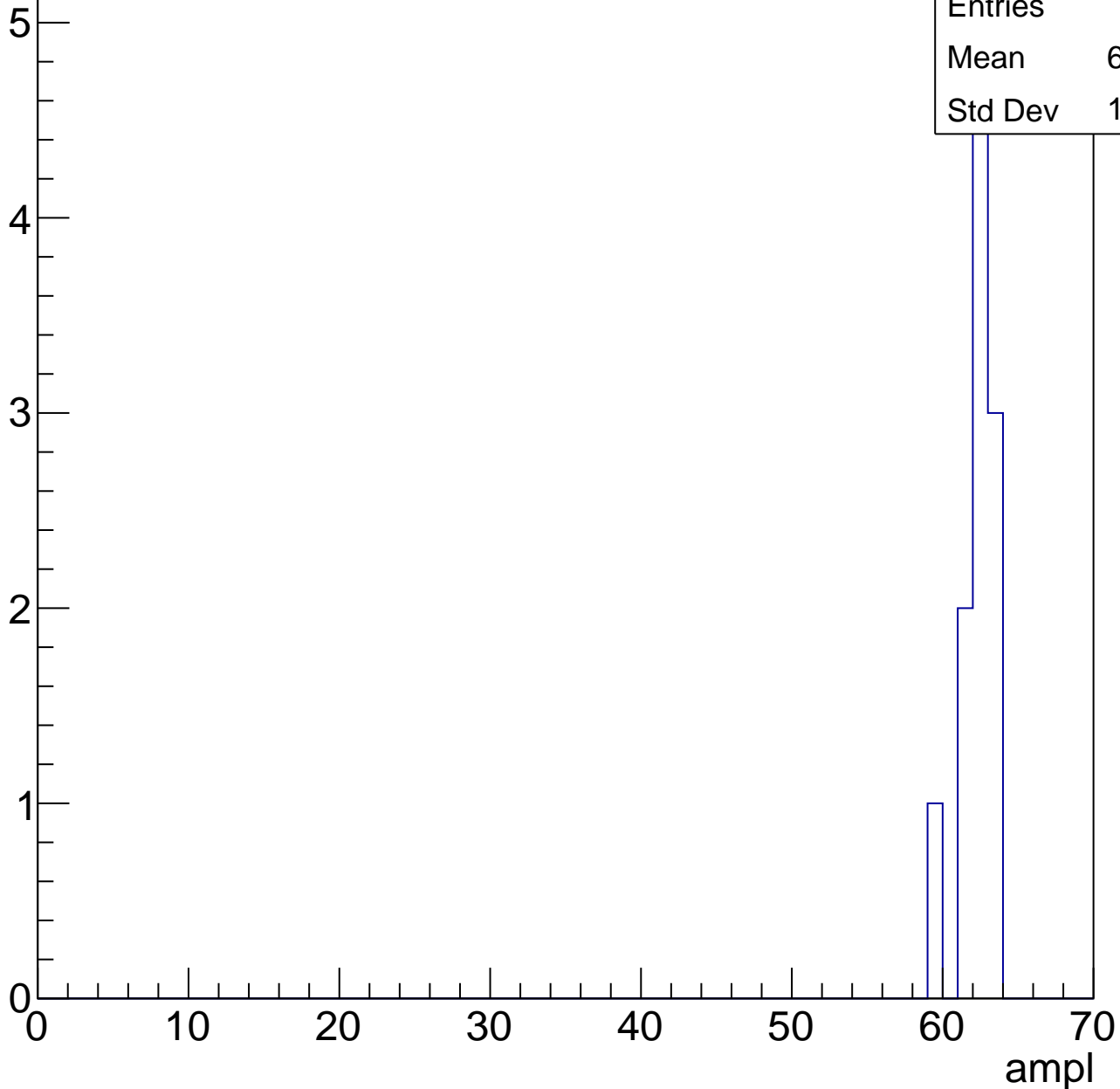


# B1L003S, U22-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	11
Mean	61.82
Std Dev	1.113

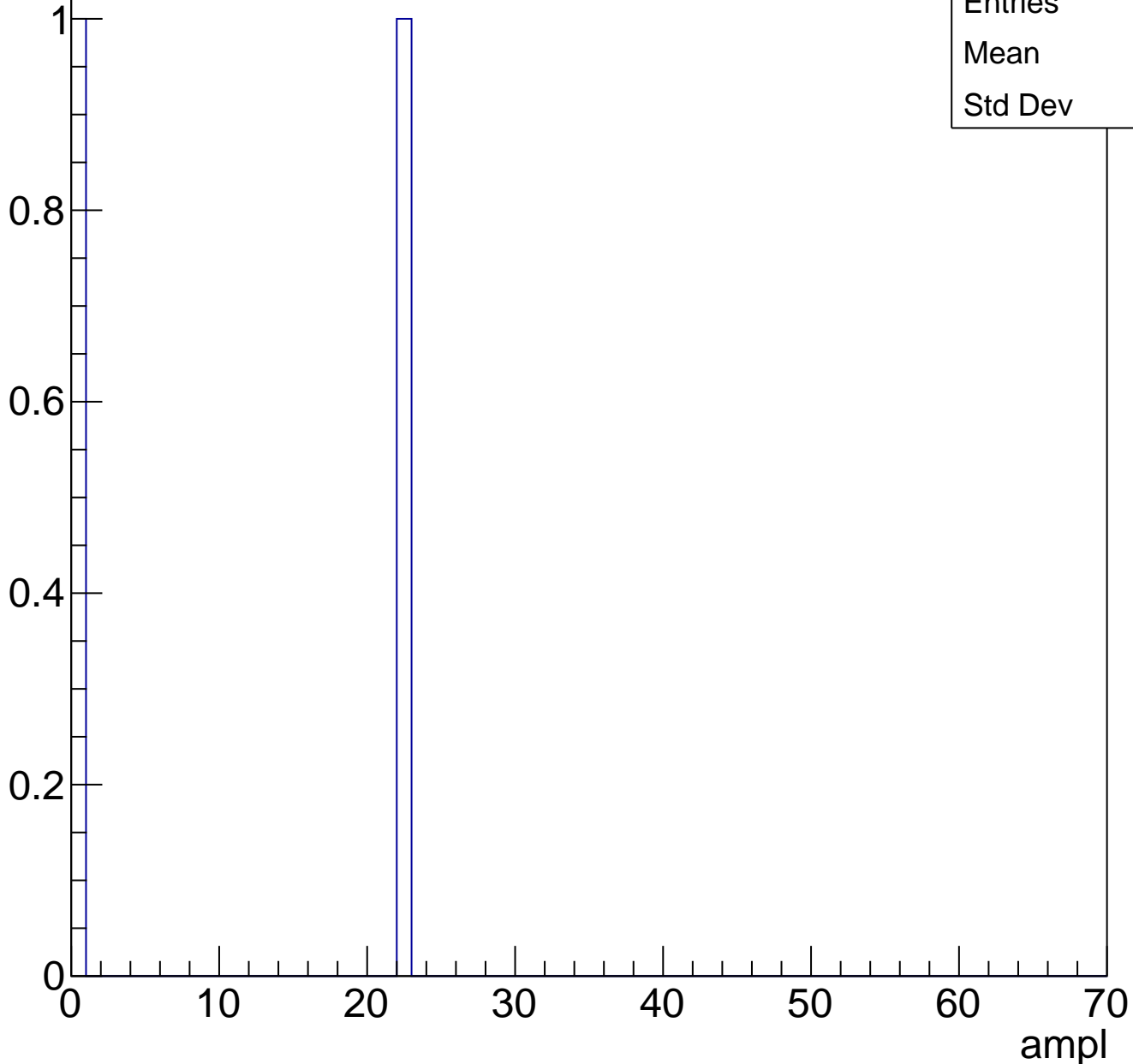




# B1L003S, U22-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

# B1L003S, U22-ch109, adc0

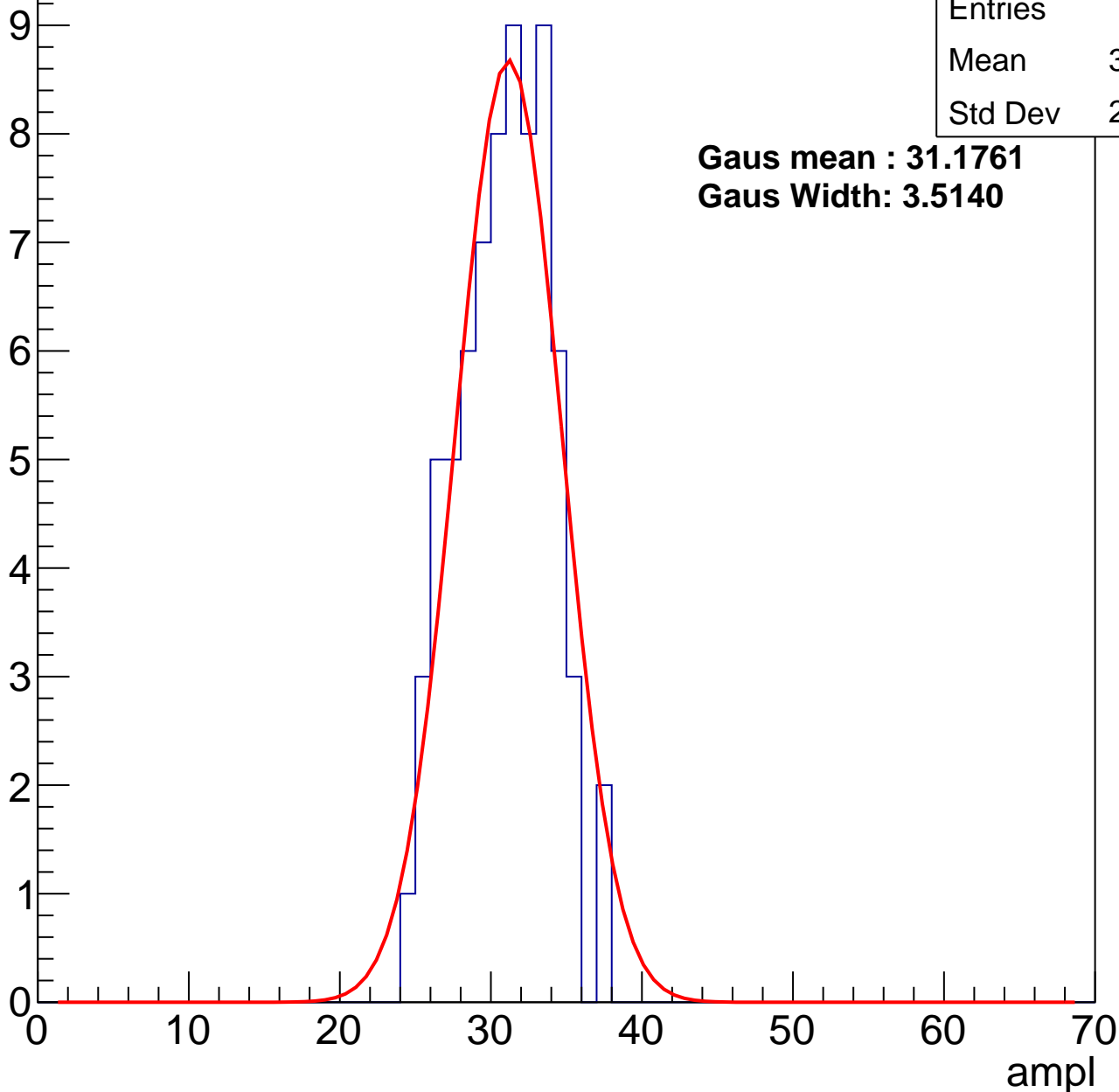
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.42
Std Dev	2.994

**Gaus mean : 31.1761**

**Gaus Width: 3.5140**



# B1L003S, U22-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	65
Mean	37.25
Std Dev	3.177

**Gaus mean : 38.1879**

**Gaus Width: 2.8931**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B1L003S, U22-ch109, adc2

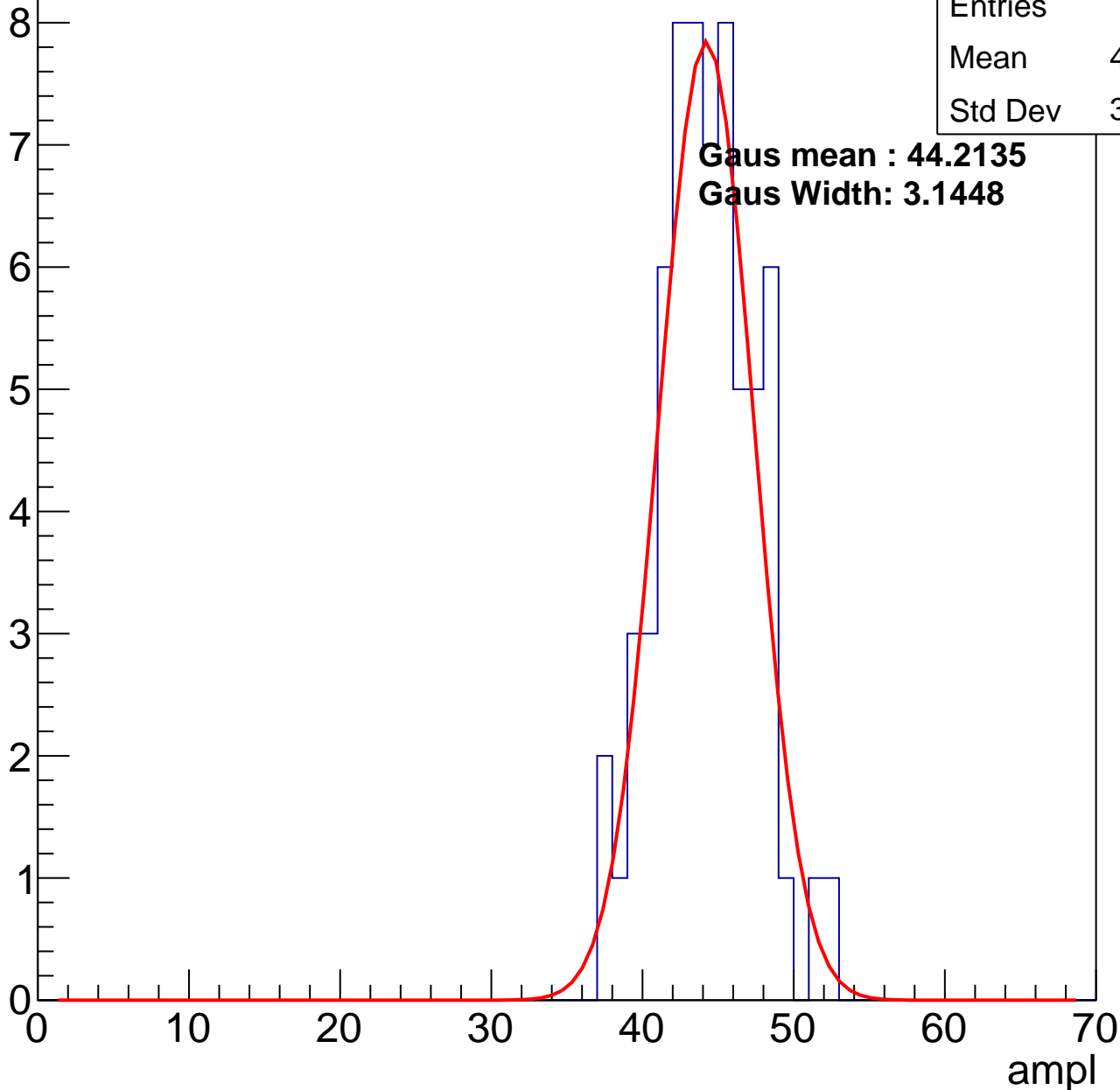
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.82
Std Dev	3.176

**Gaus mean : 44.2135**

**Gaus Width: 3.1448**

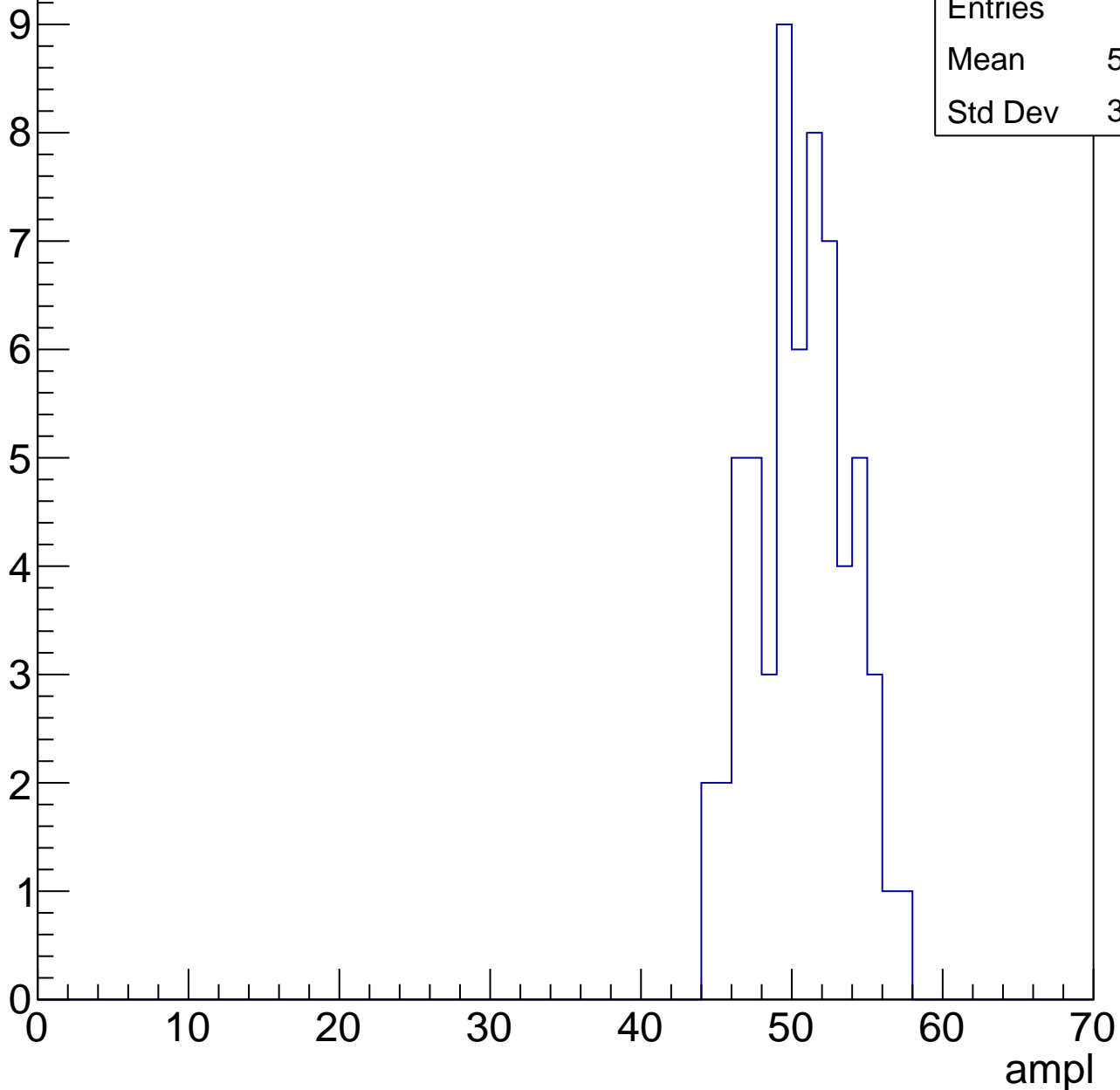


# B1L003S, U22-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	50.16
Std Dev	3.079

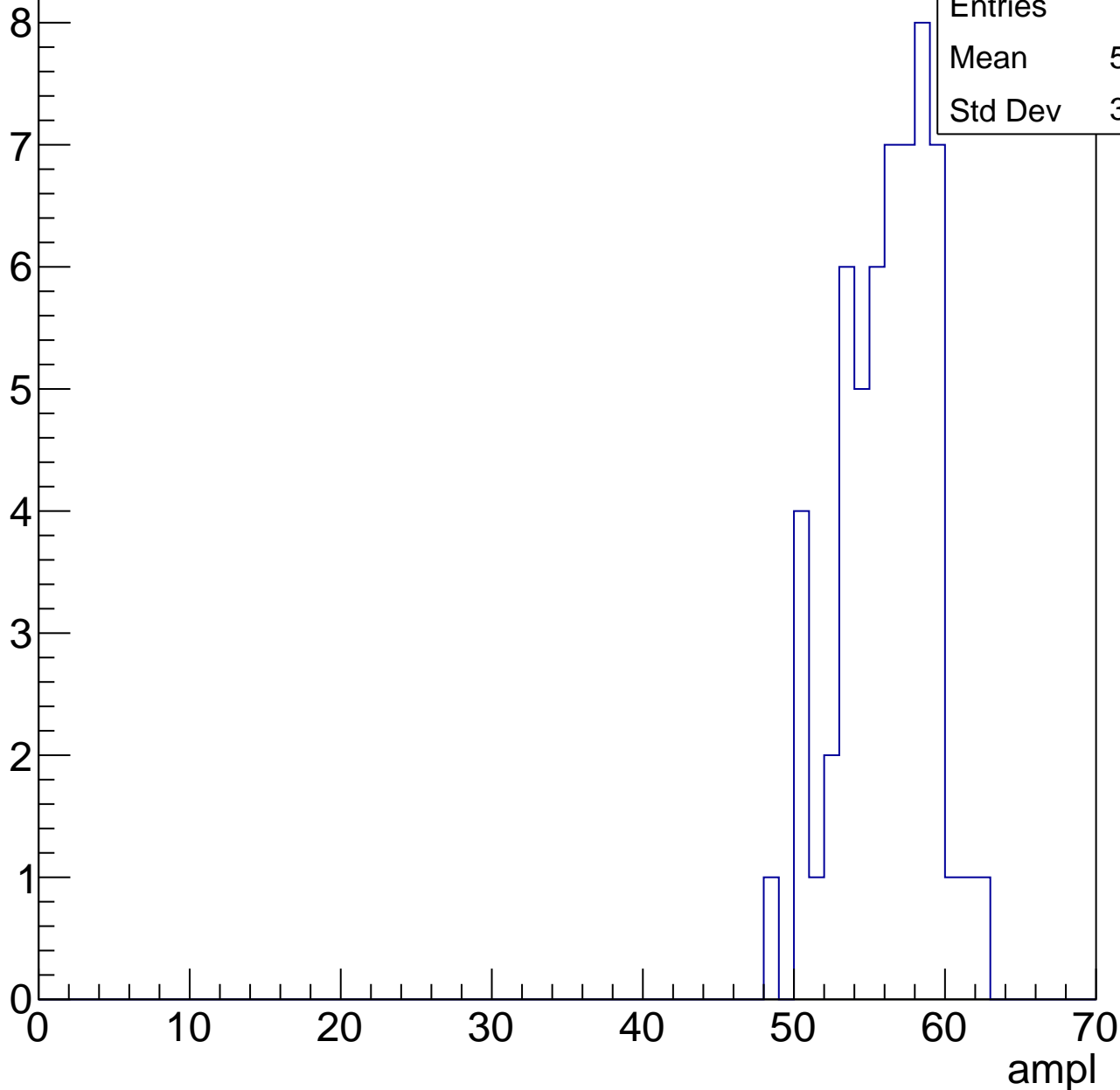


# B1L003S, U22-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

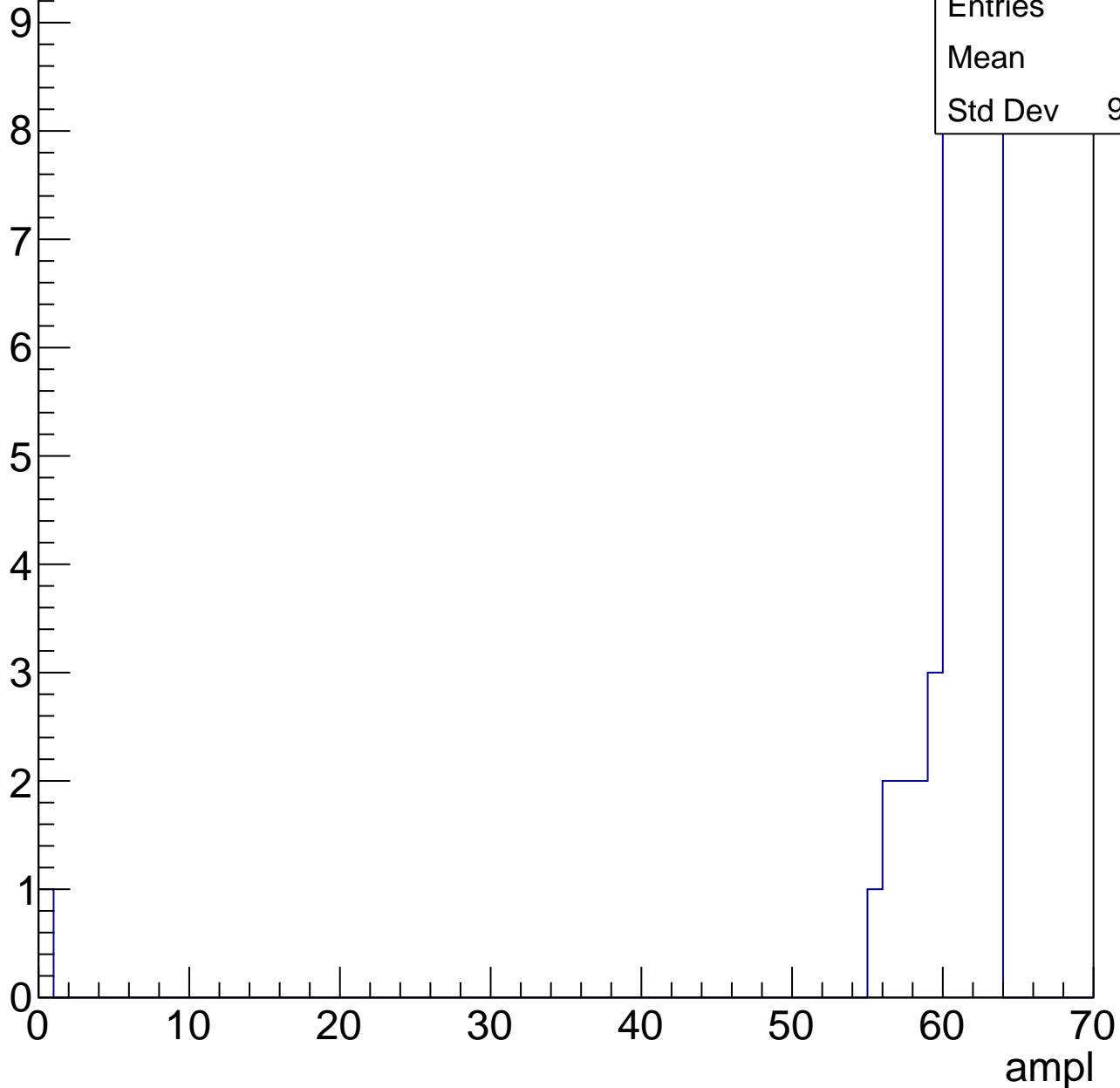
Entries	57
Mean	55.65
Std Dev	3.012



# B1L003S, U22-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L003S, U22-ch110, adc0

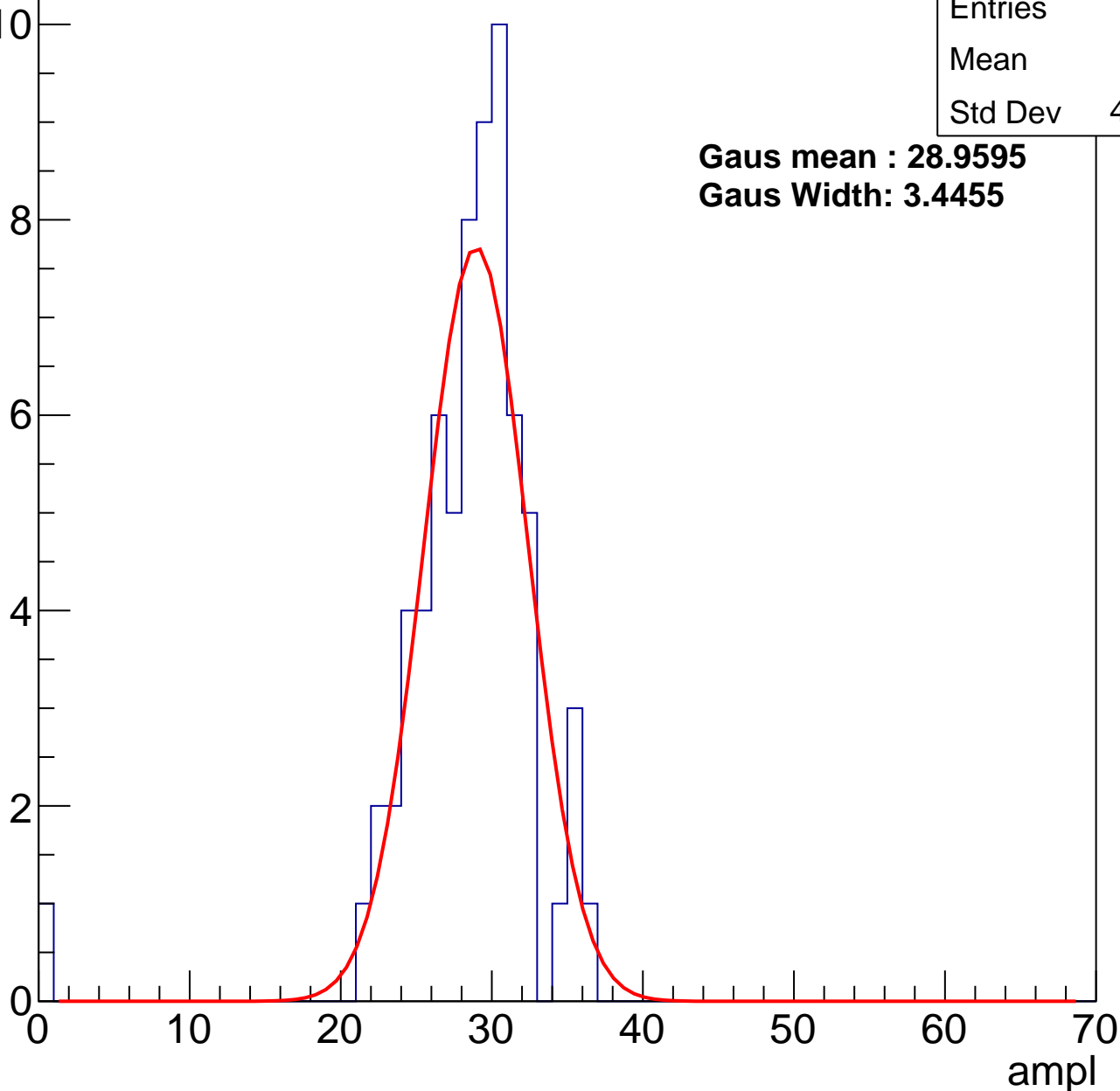
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28
Std Dev	4.719

**Gaus mean : 28.9595**

**Gaus Width: 3.4455**



# B1L003S, U22-ch110, adc1

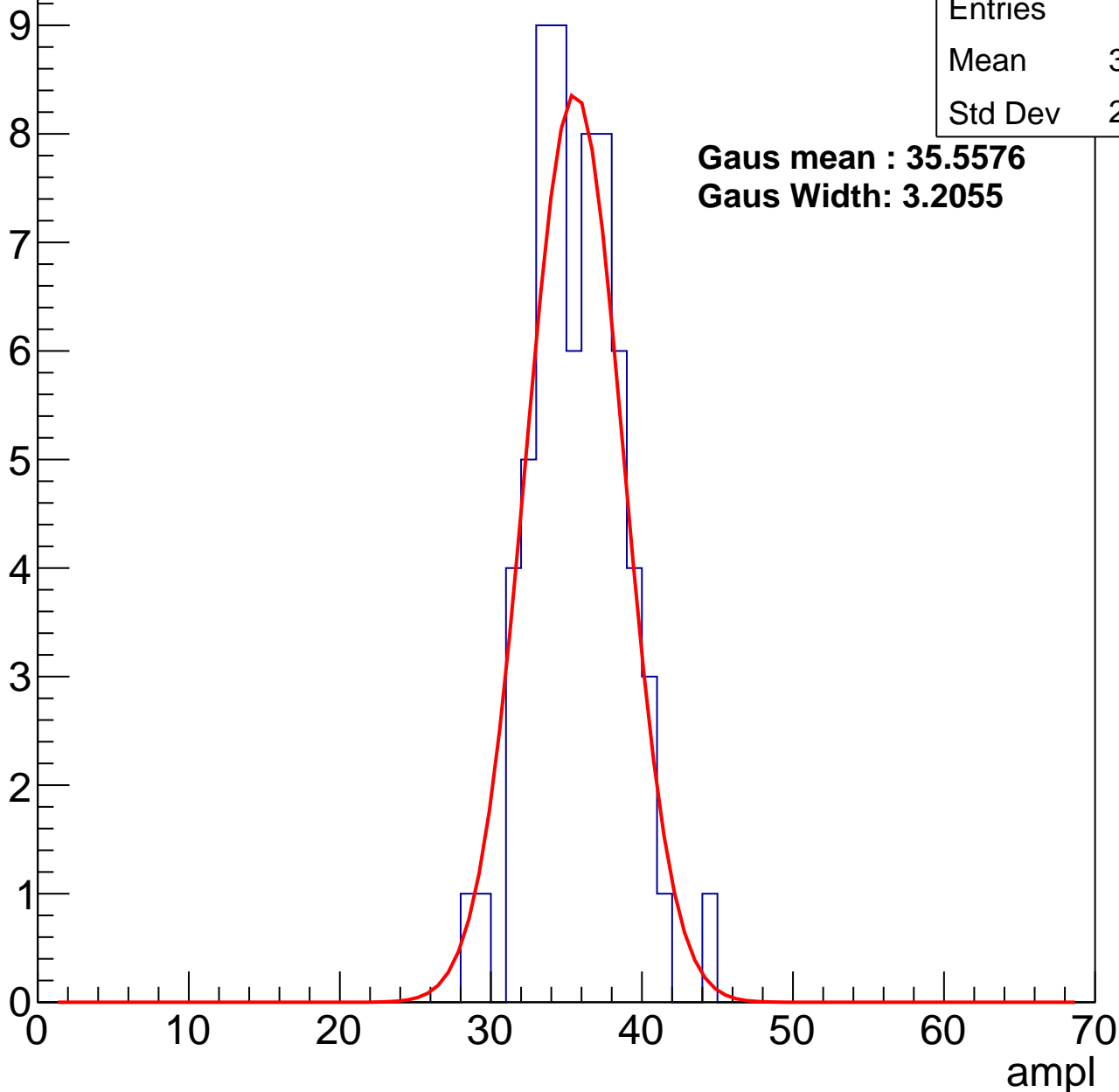
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.26
Std Dev	2.976

**Gaus mean : 35.5576**

**Gaus Width: 3.2055**



# B1L003S, U22-ch110, adc2

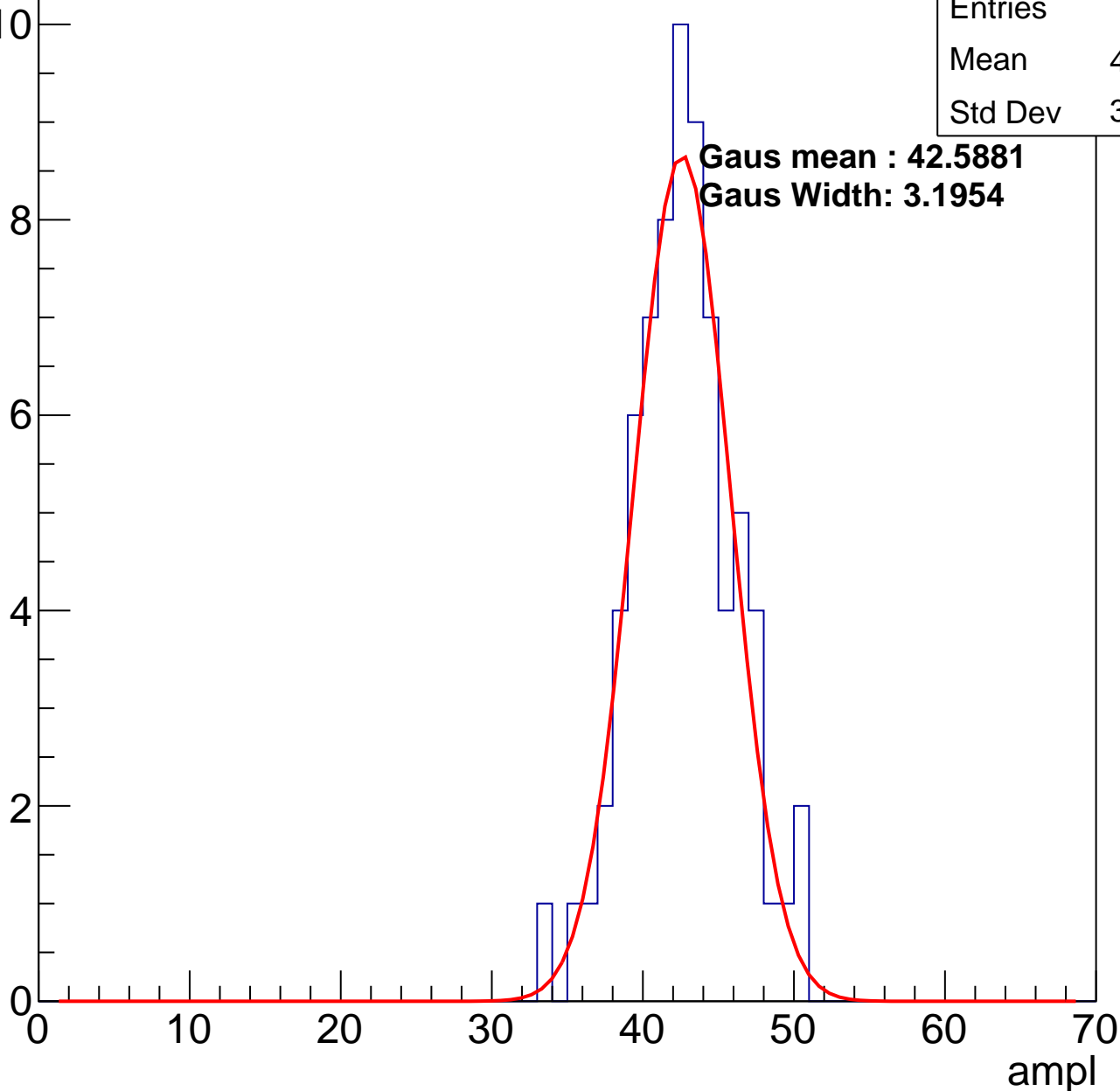
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.22
Std Dev	3.377

**Gaus mean : 42.5881**

**Gaus Width: 3.1954**

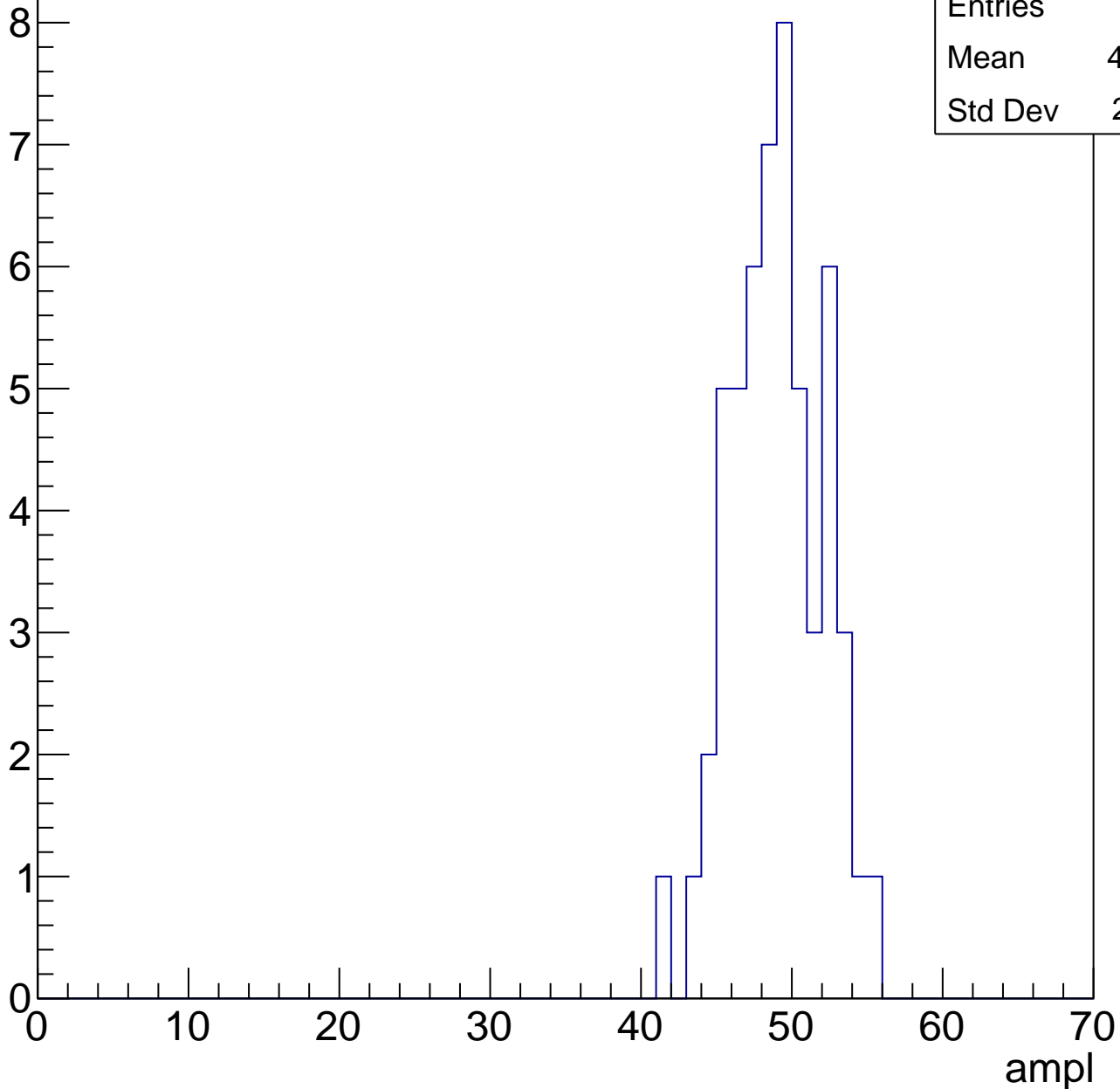


# B1L003S, U22-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.52
Std Dev	2.961

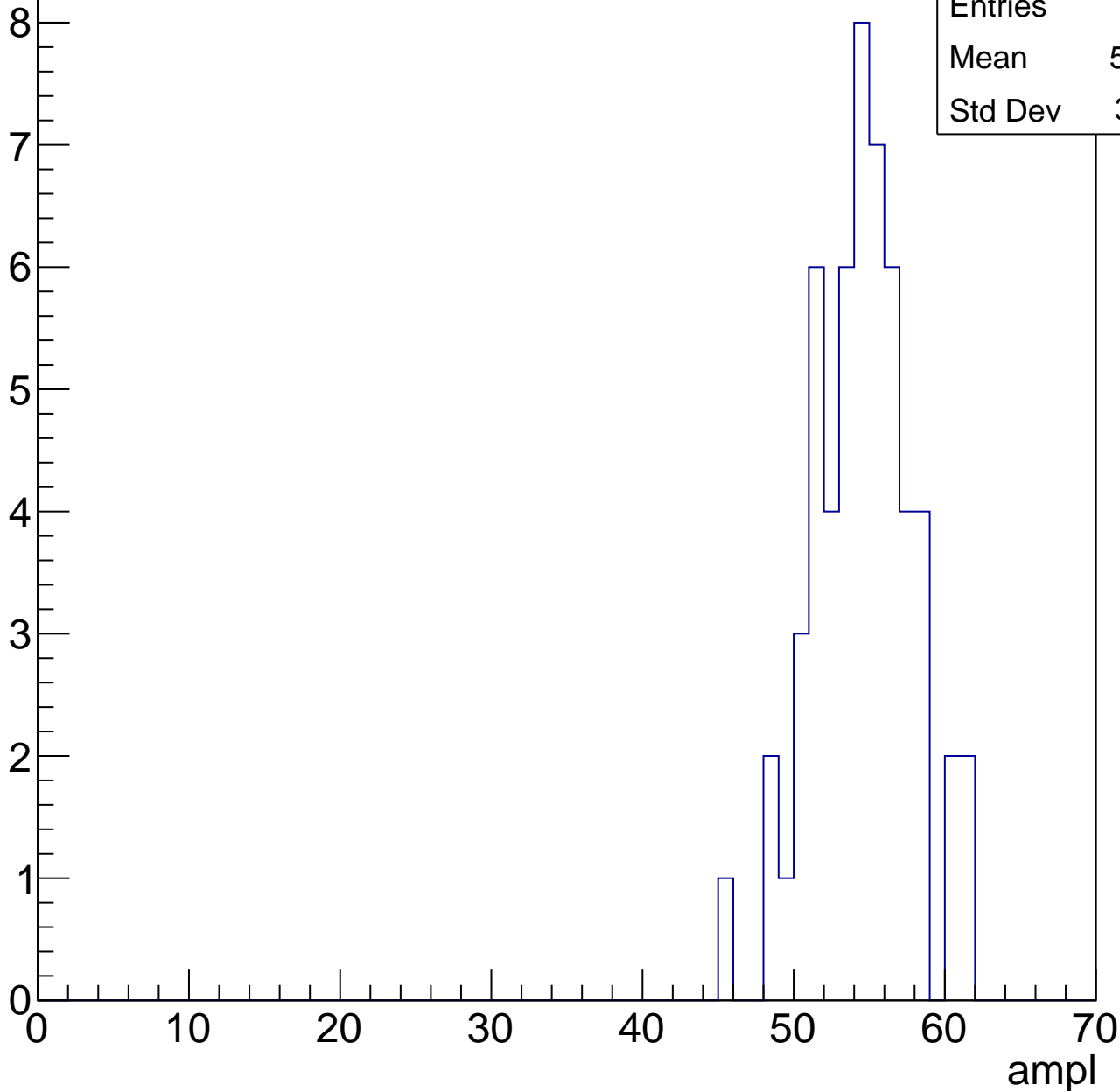


# B1L003S, U22-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	54.05
Std Dev	3.281

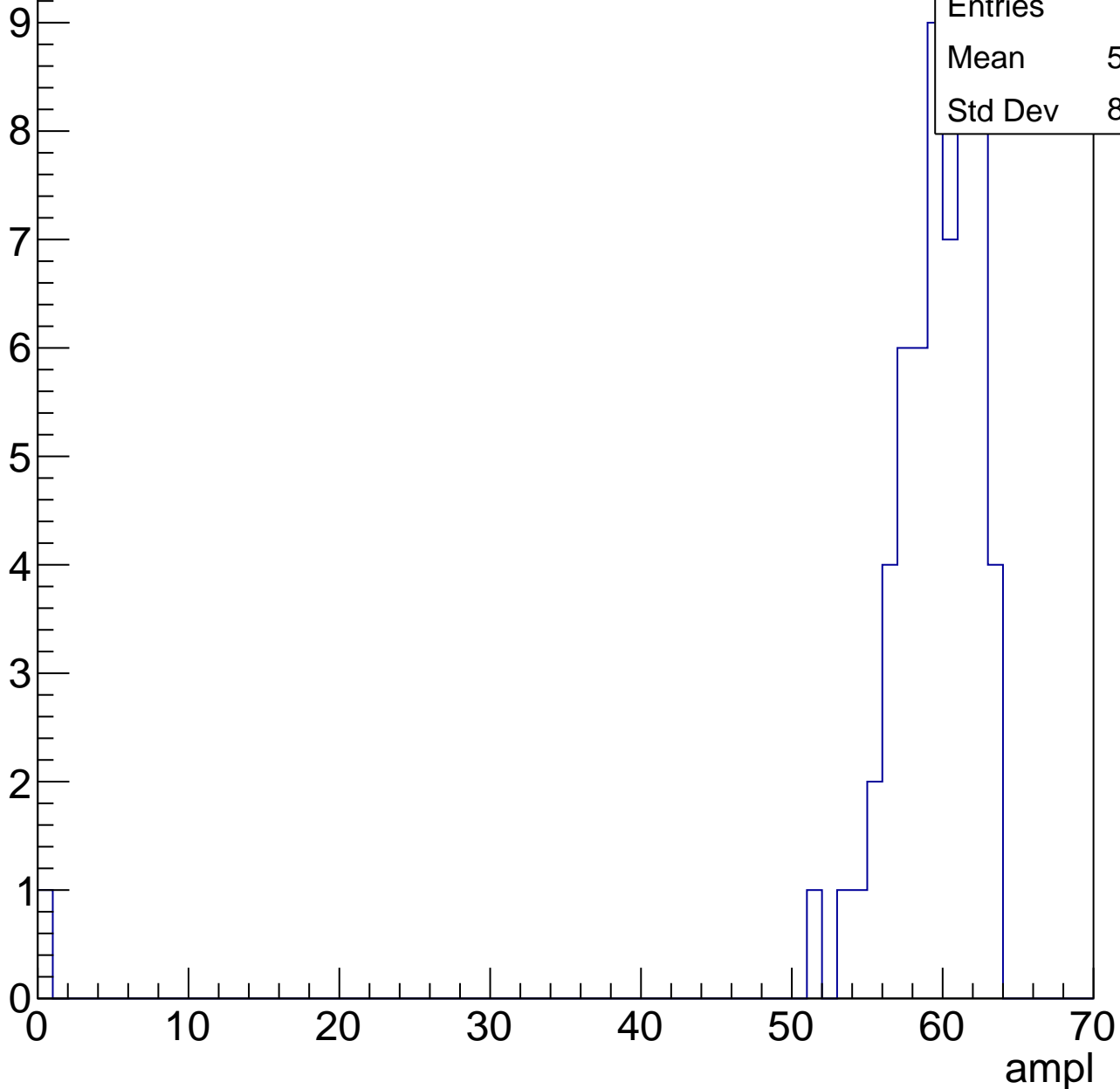


# B1L003S, U22-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.09
Std Dev	8.127

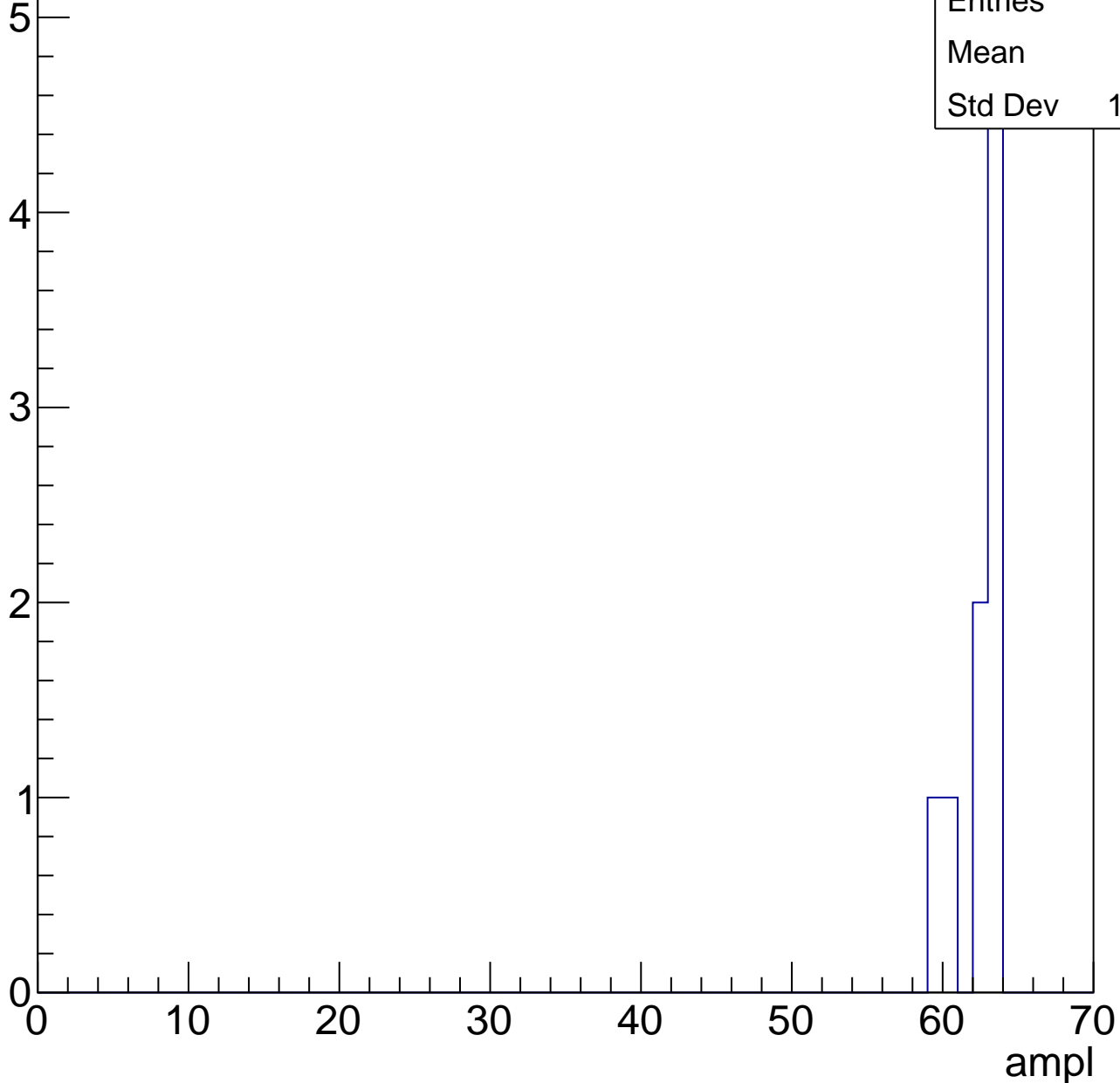


# B1L003S, U22-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	9
Mean	62
Std Dev	1.414





# B1L003S, U22-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L003S, U22-ch111, adc0

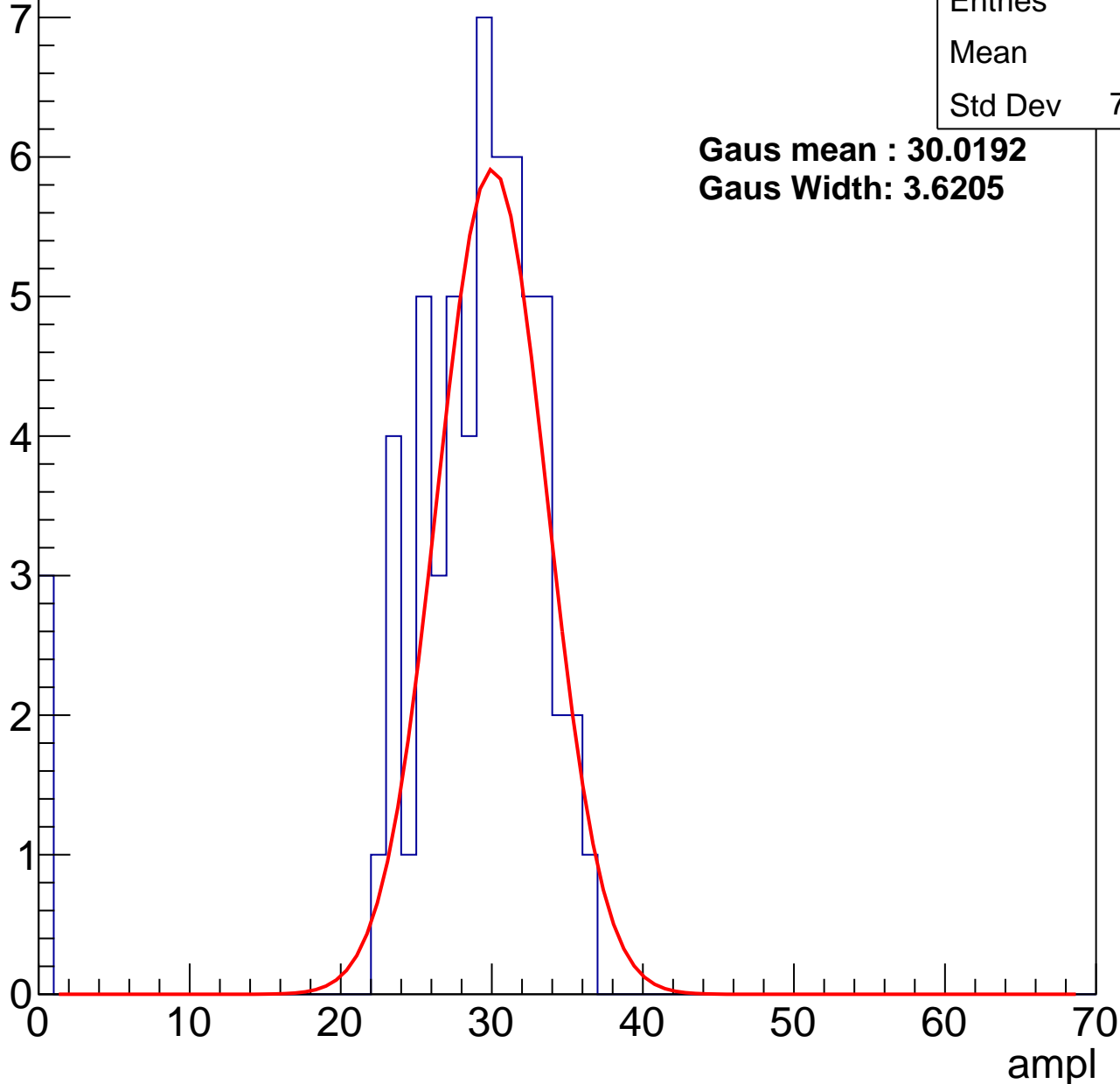
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	27.6
Std Dev	7.165

**Gaus mean : 30.0192**

**Gaus Width: 3.6205**



# B1L003S, U22-ch111, adc1

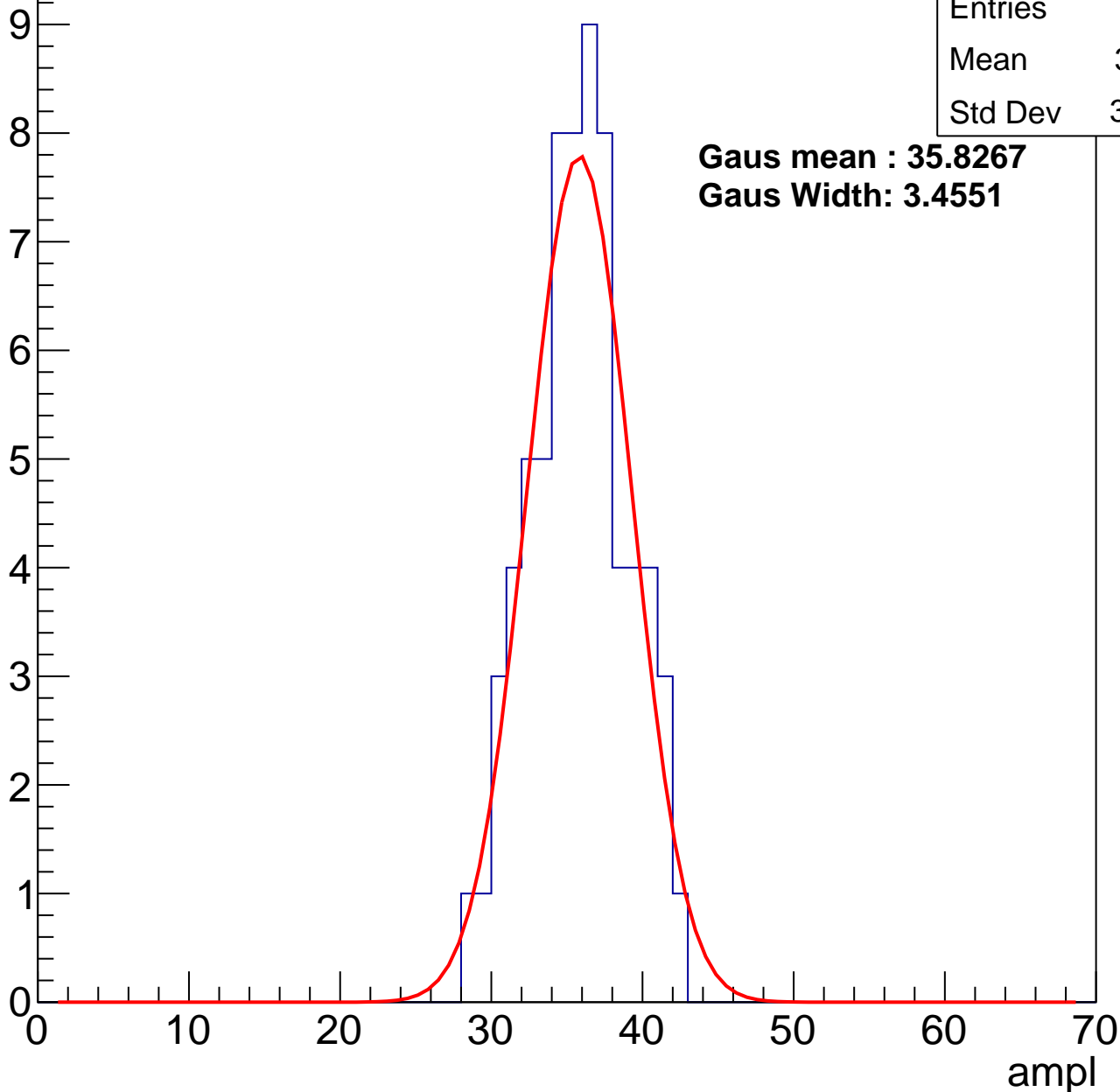
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.31
Std Dev	3.182

**Gaus mean : 35.8267**

**Gaus Width: 3.4551**



# B1L003S, U22-ch111, adc2

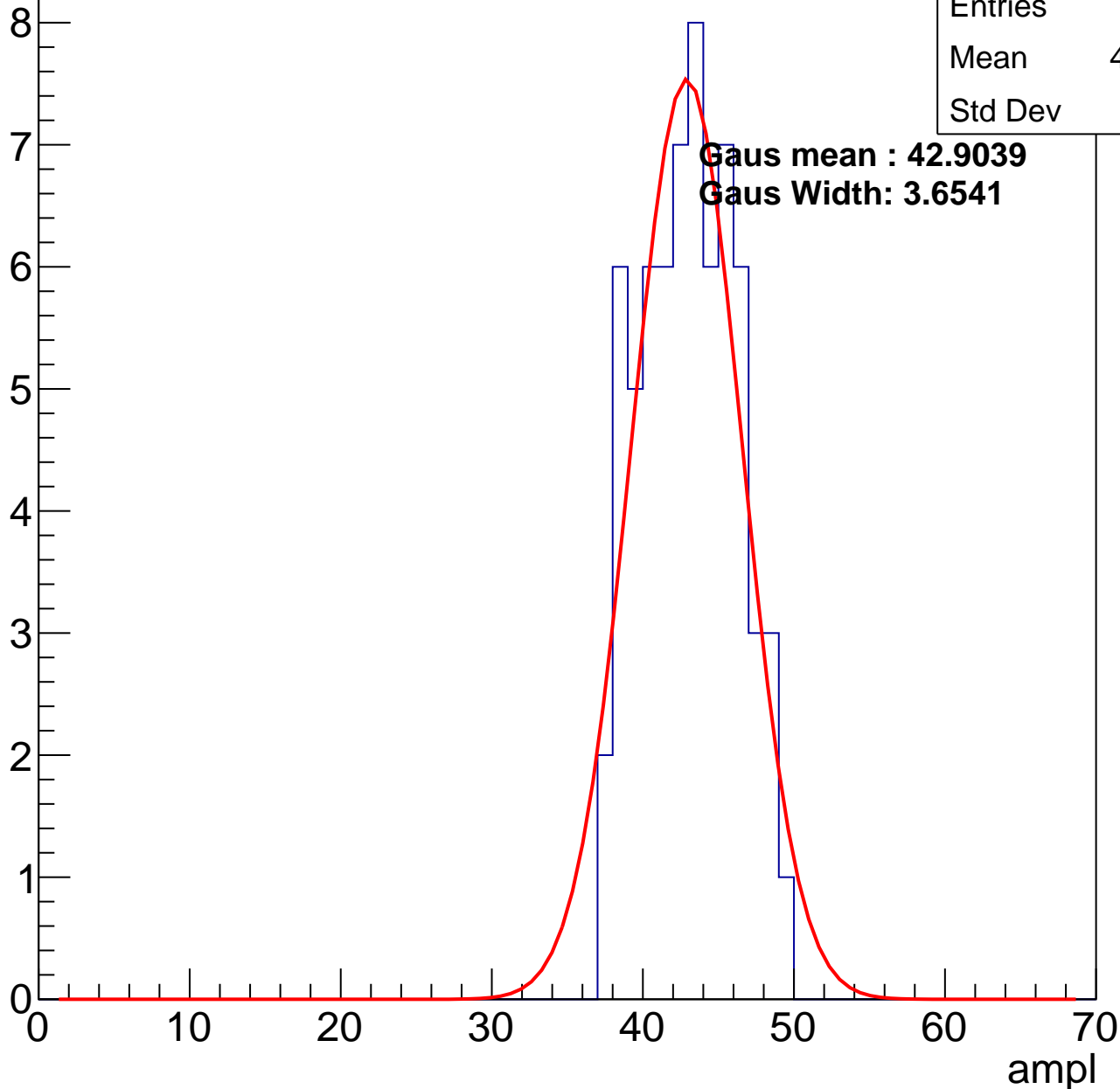
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.58
Std Dev	3.07

**Gaus mean : 42.9039**

**Gaus Width: 3.6541**



# B1L003S, U22-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

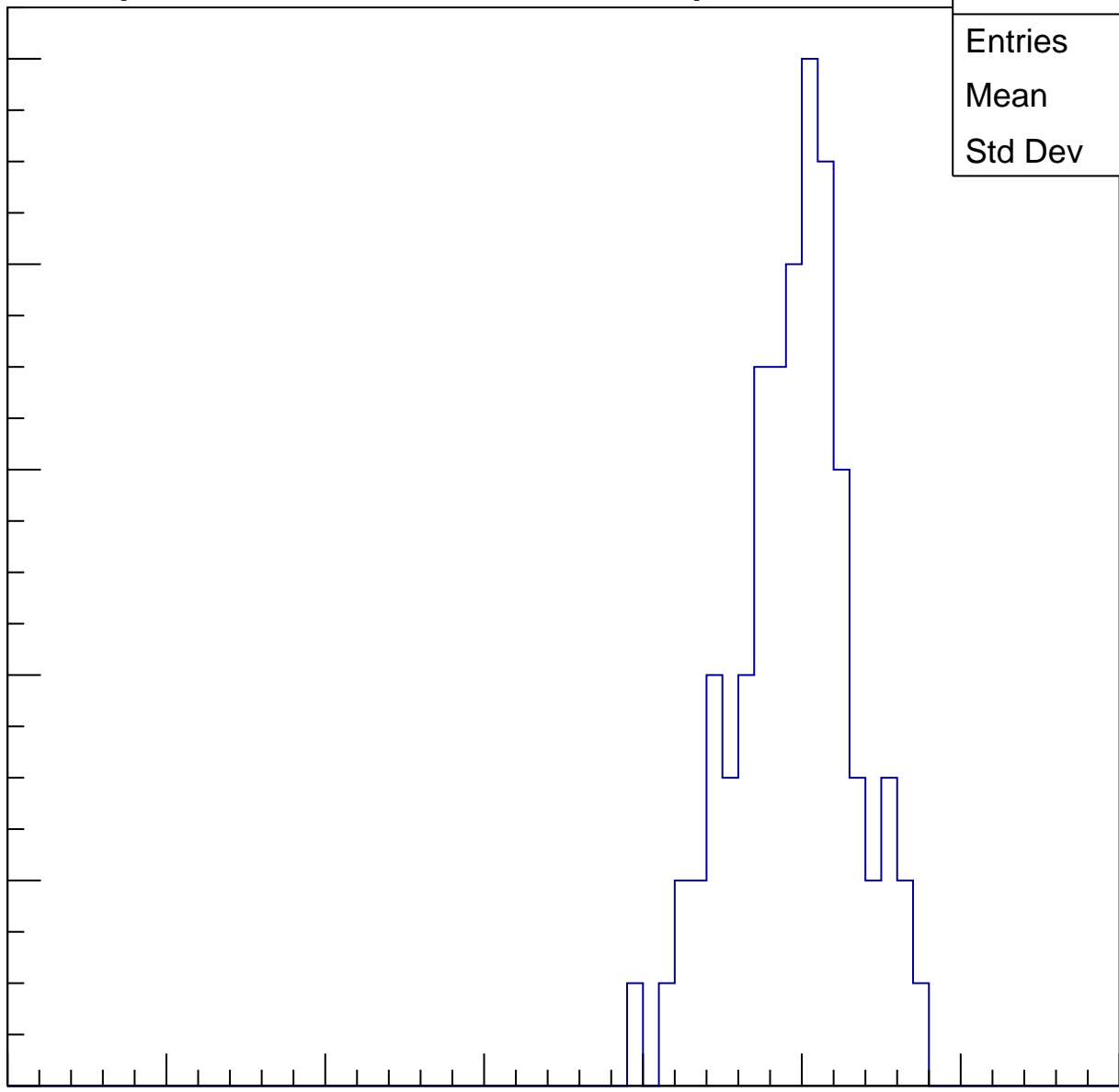
Entries	75
Mean	48.99
Std Dev	3.69

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

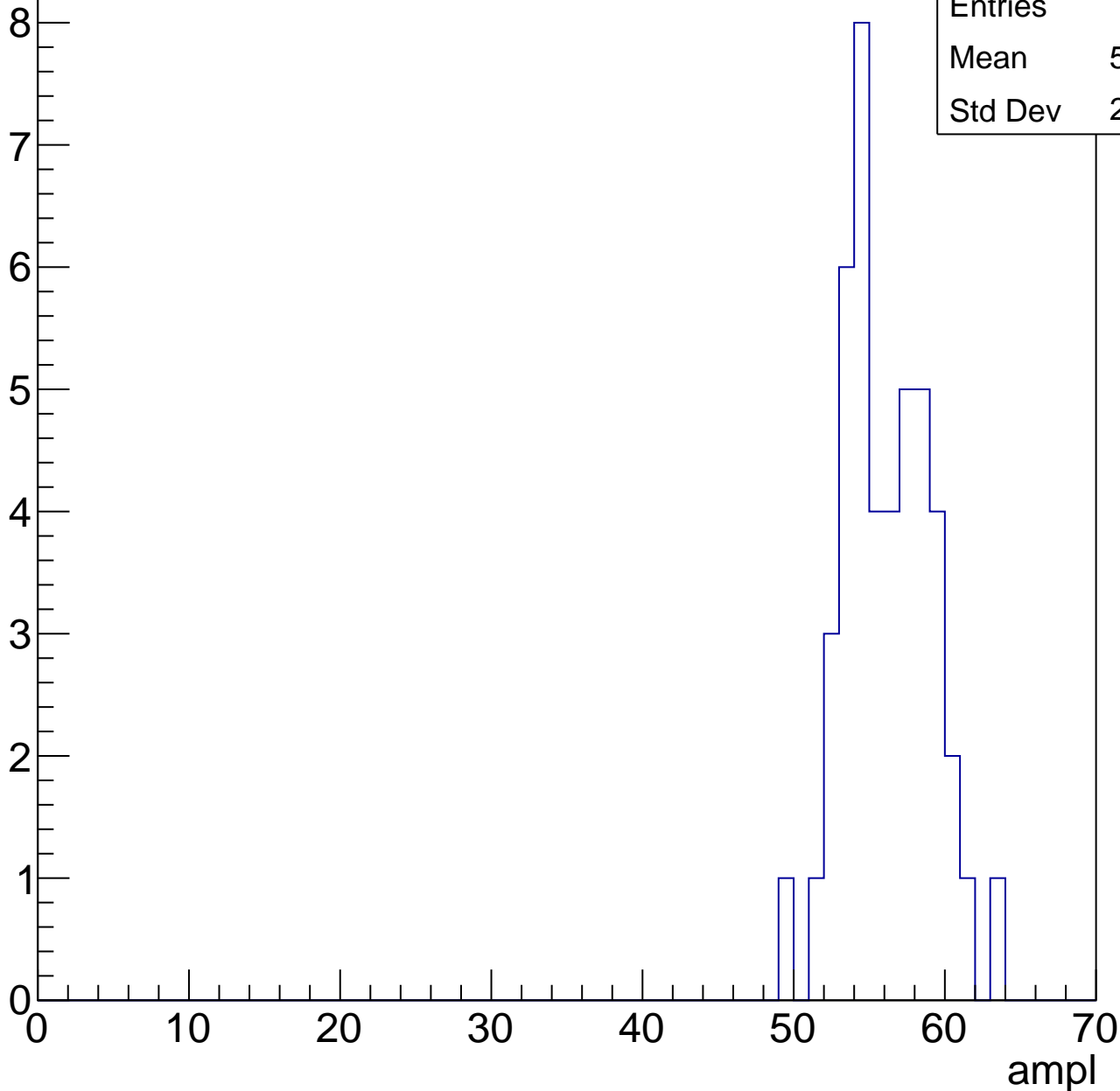


# B1L003S, U22-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	45
Mean	55.67
Std Dev	2.883

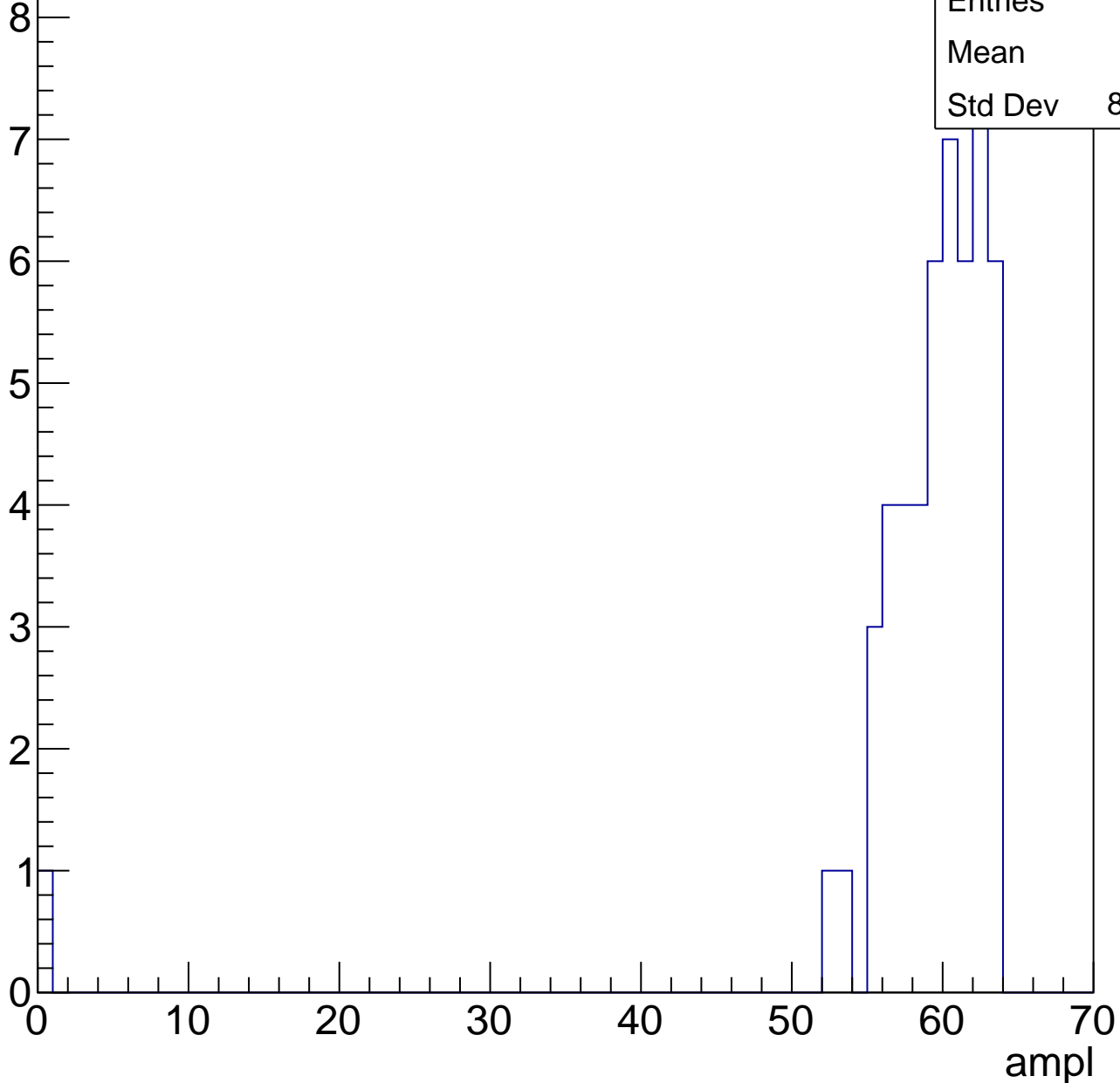


# B1L003S, U22-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

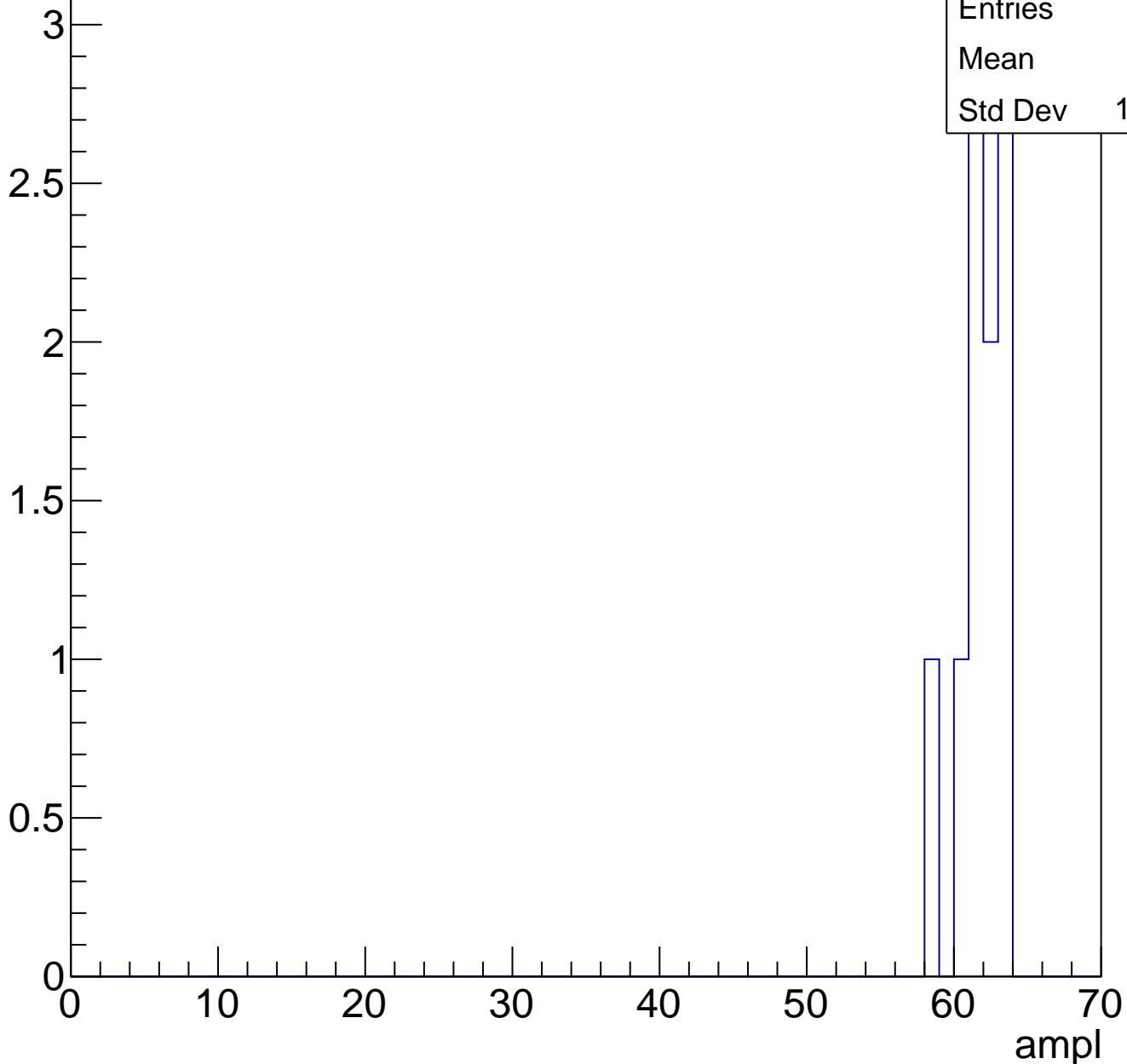
Entries	51
Mean	58.2
Std Dev	8.673



# B1L003S, U22-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch112, adc0

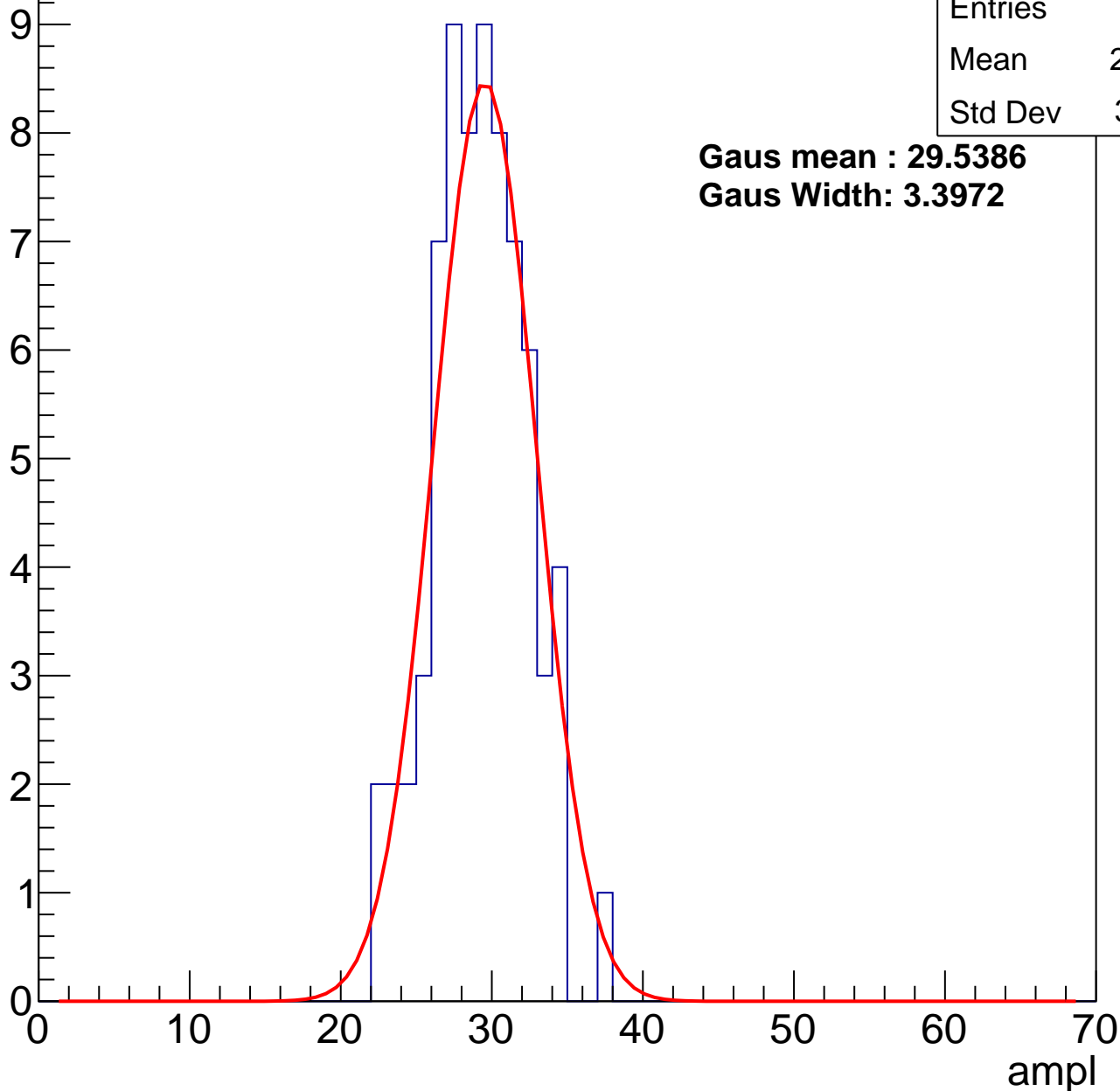
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	28.79
Std Dev	3.081

**Gaus mean : 29.5386**

**Gaus Width: 3.3972**



# B1L003S, U22-ch112, adc1

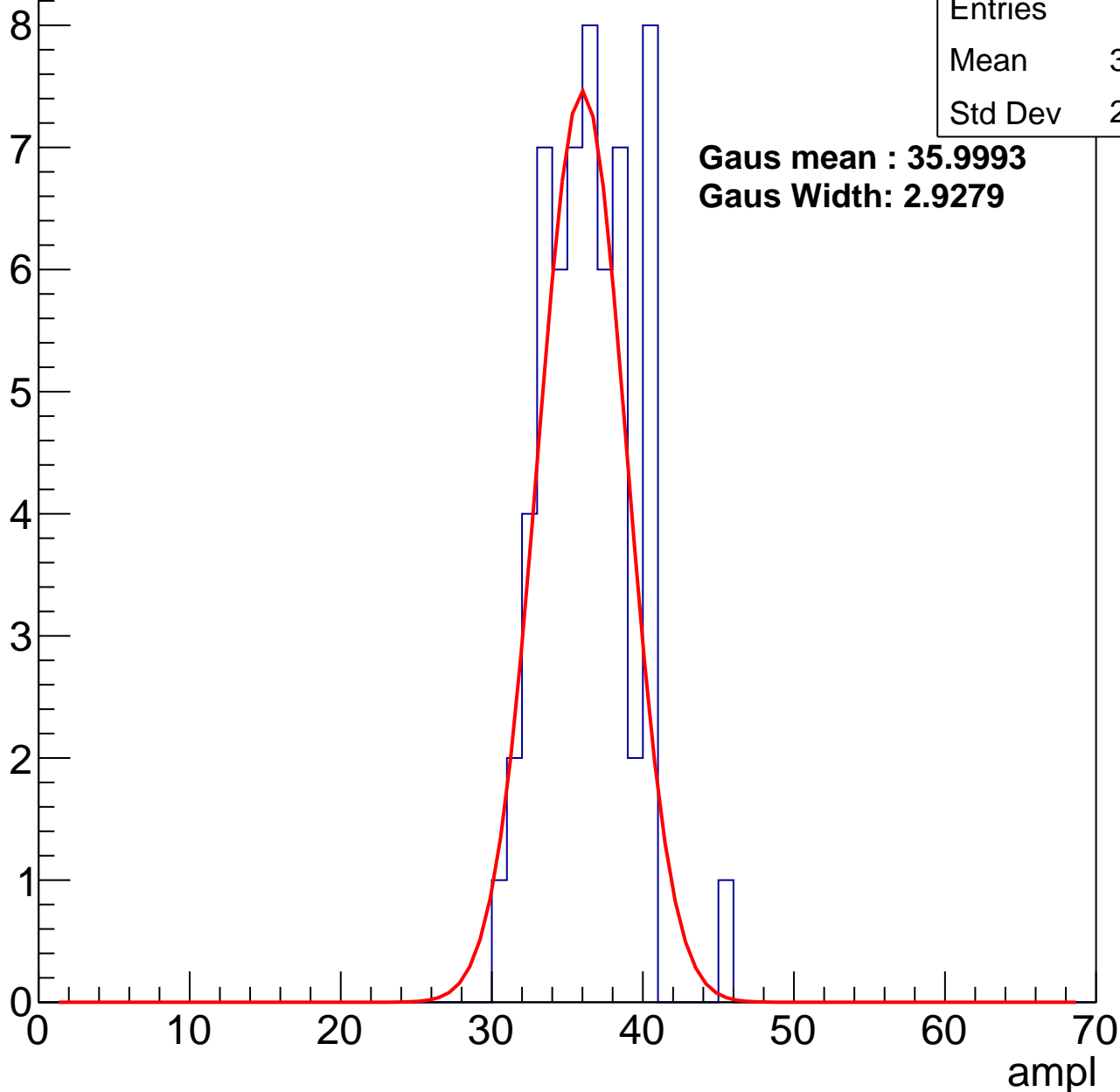
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.92
Std Dev	2.924

**Gaus mean : 35.9993**

**Gaus Width: 2.9279**



# B1L003S, U22-ch112, adc2

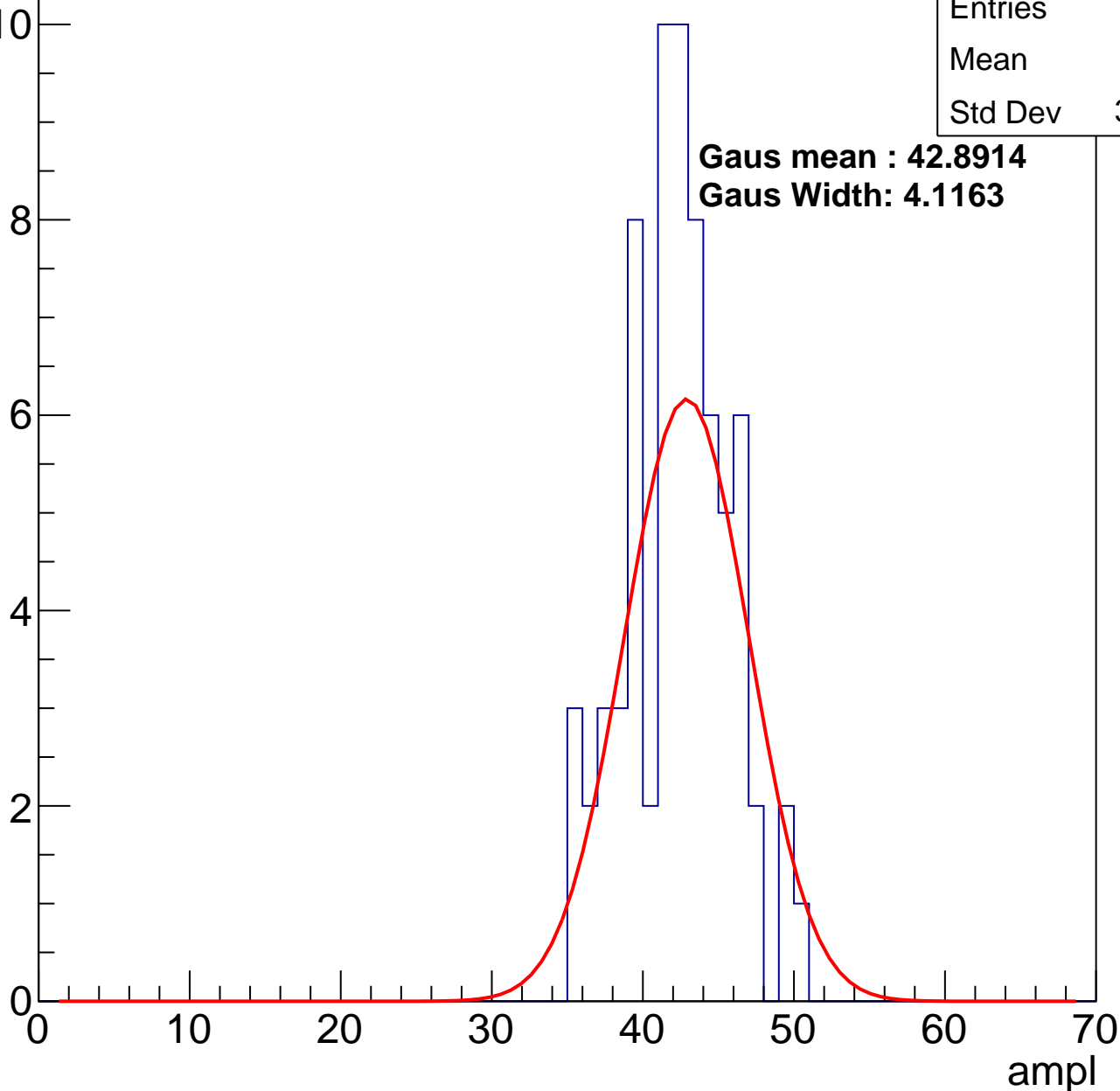
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	71
Mean	41.9
Std Dev	3.391

**Gaus mean : 42.8914**

**Gaus Width: 4.1163**

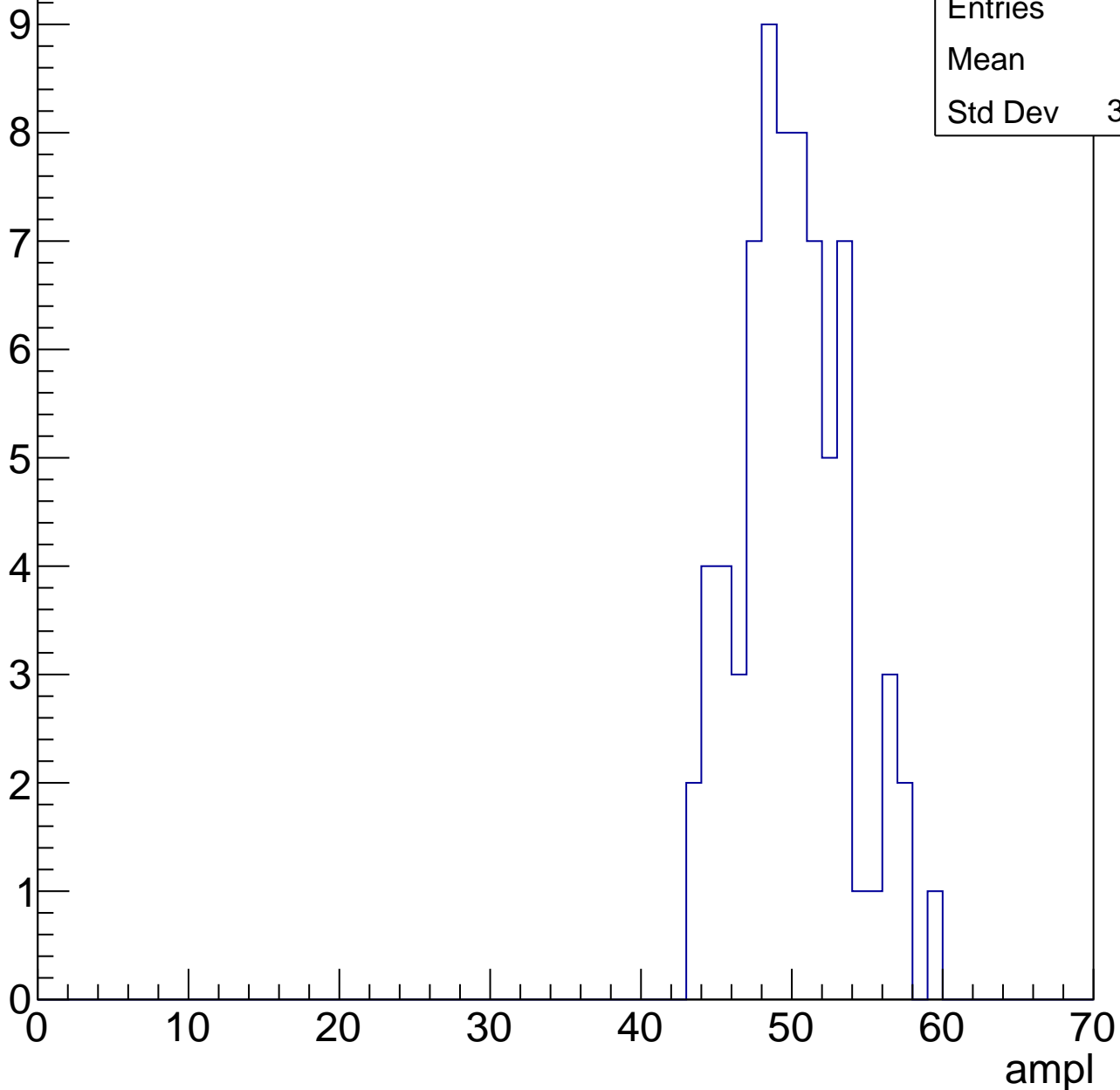


# B1L003S, U22-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	72
Mean	49.6
Std Dev	3.542

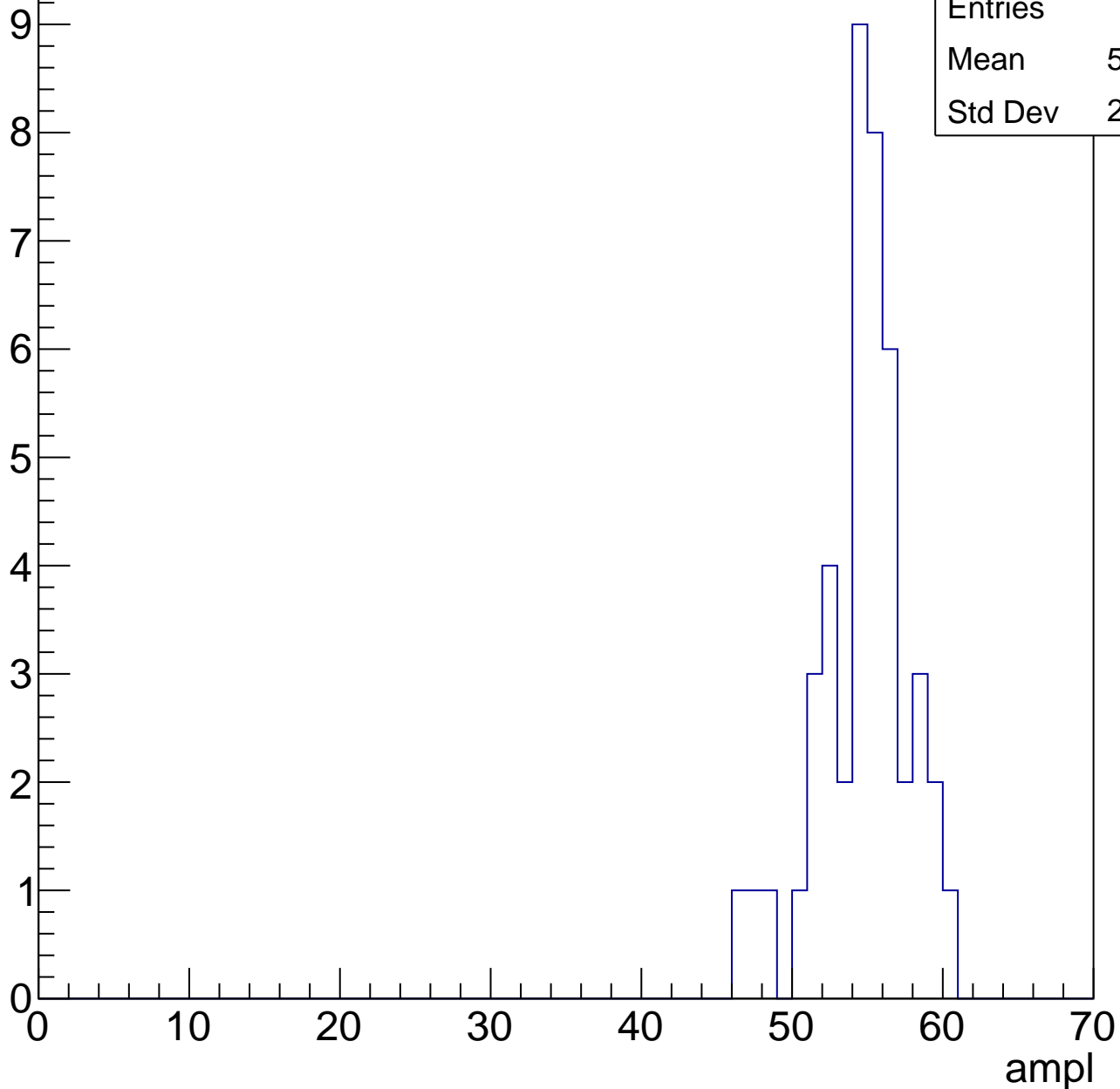


# B1L003S, U22-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	54.23
Std Dev	2.984

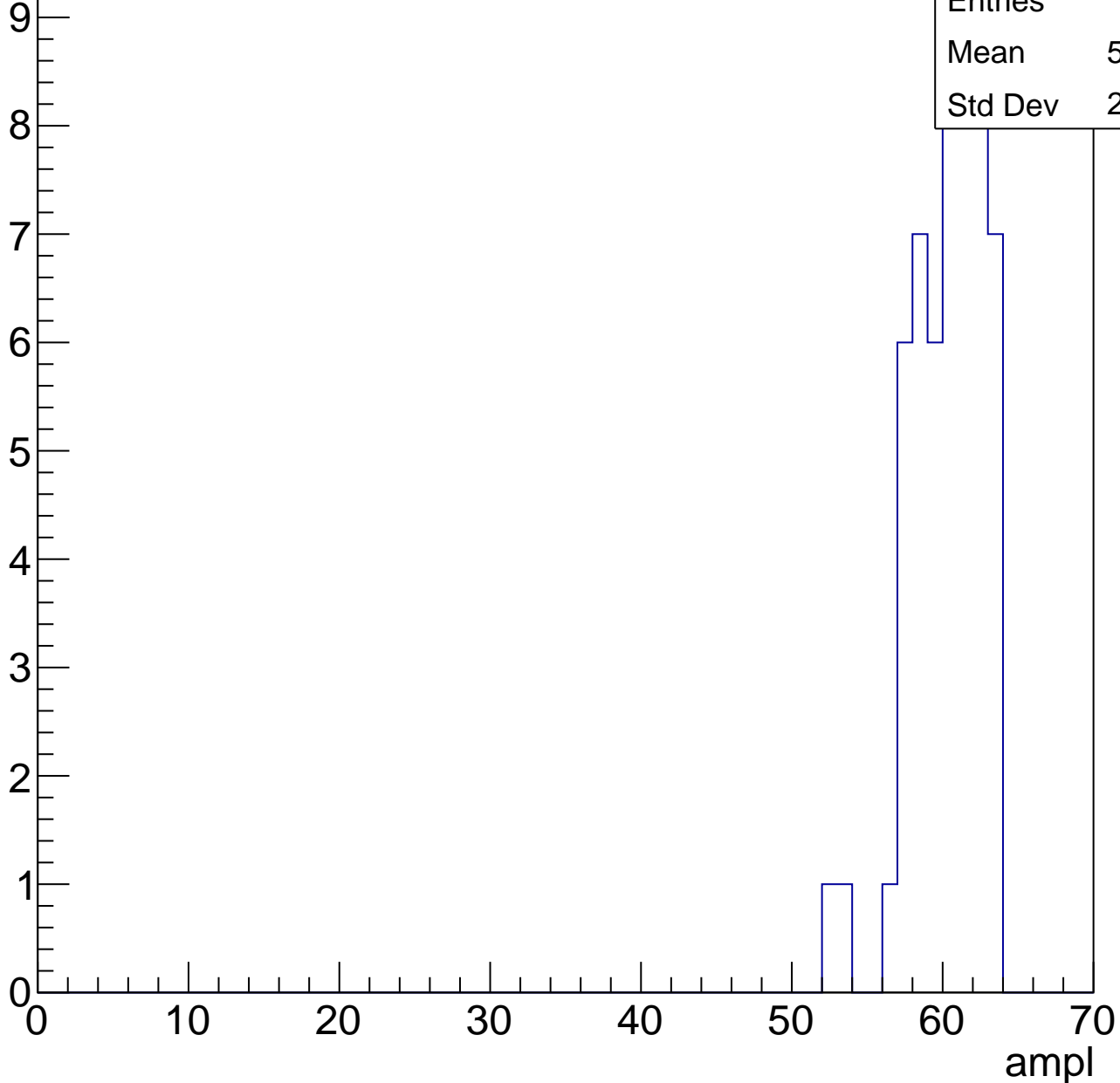


# B1L003S, U22-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

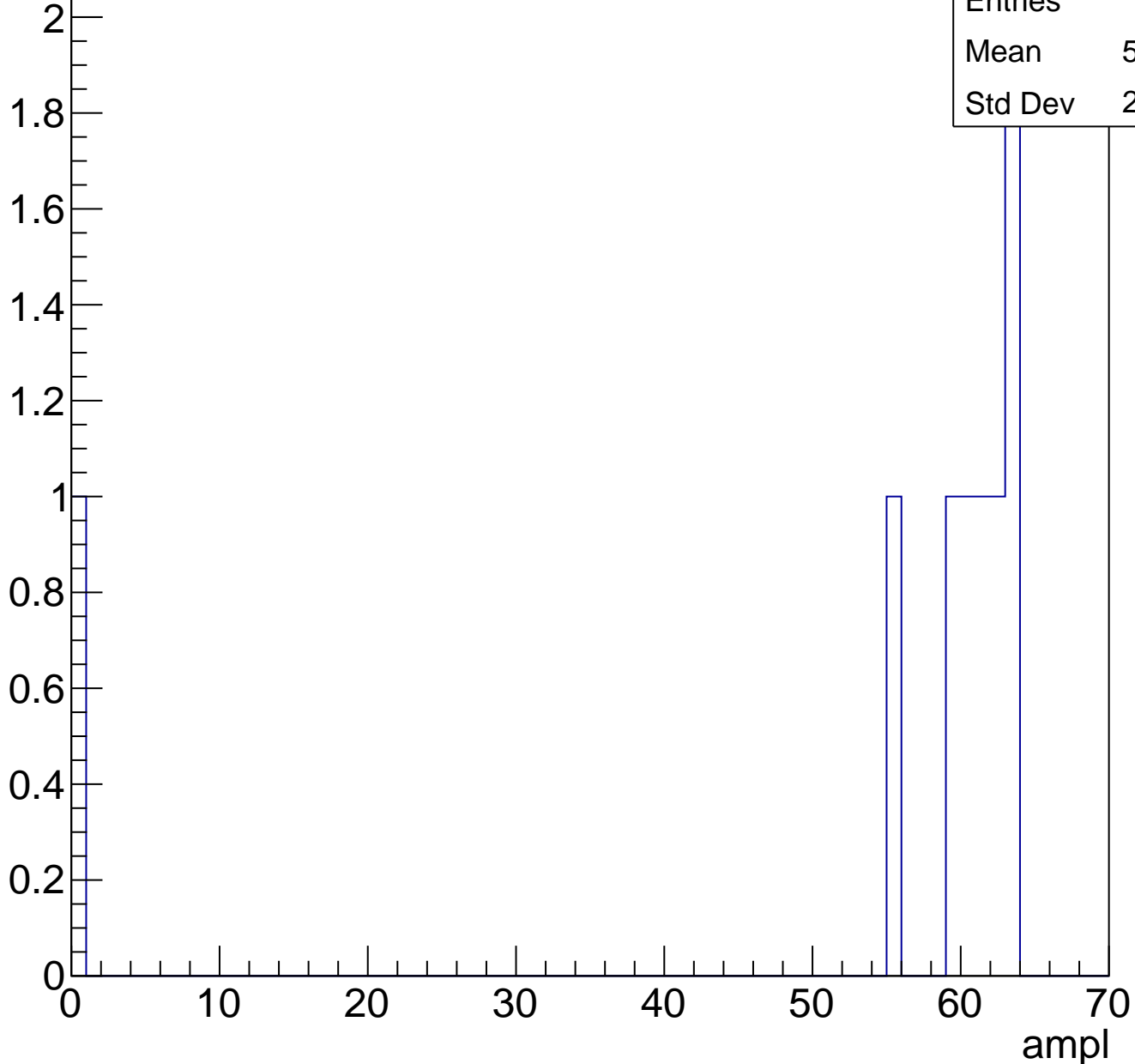
Entries	55
Mean	59.84
Std Dev	2.425



# B1L003S, U22-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	8
Mean	52.88
Std Dev	20.13



# B1L003S, U22-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L003S, U22-ch113, adc0

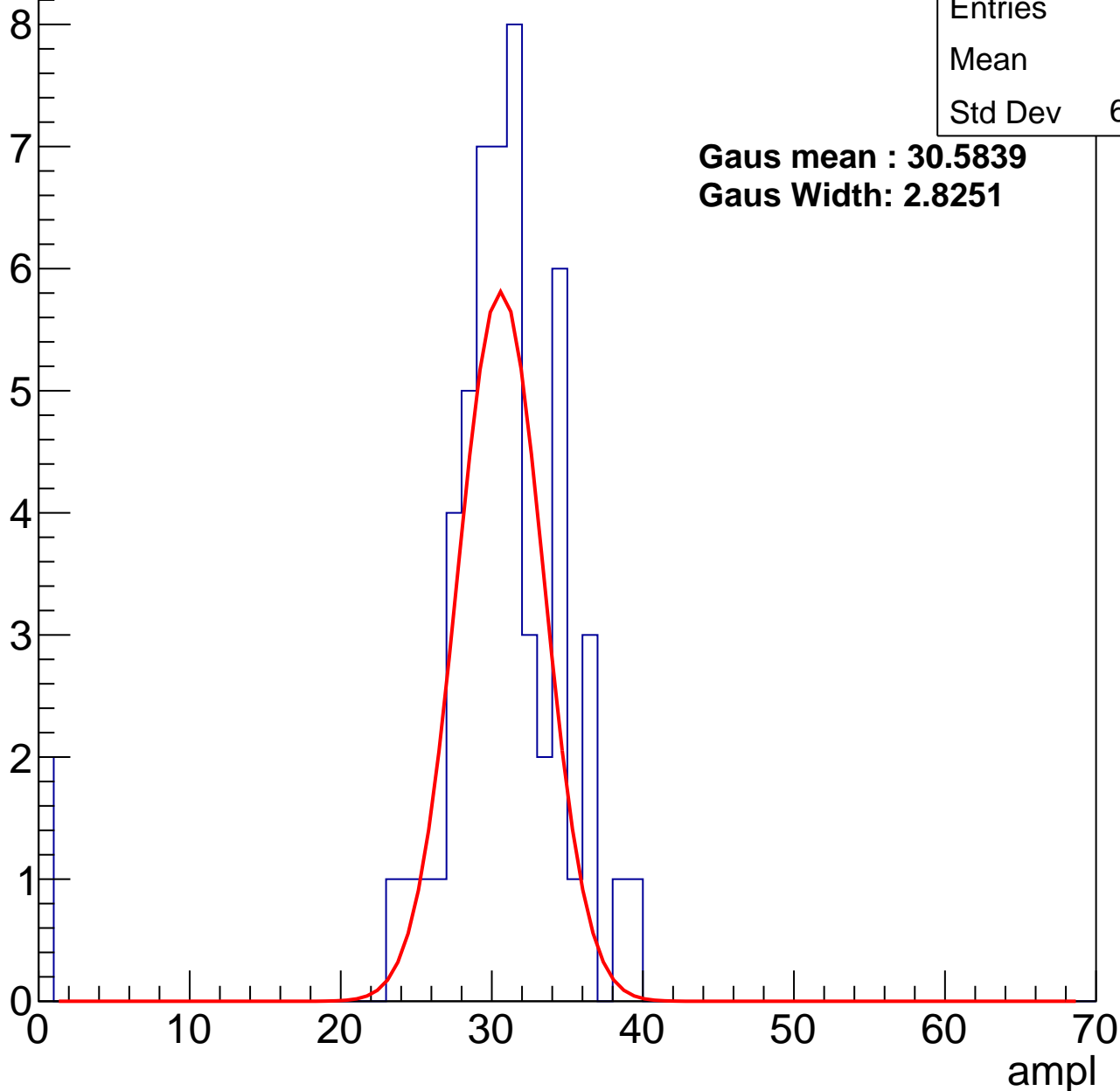
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	29.5
Std Dev	6.649

**Gaus mean : 30.5839**

**Gaus Width: 2.8251**



# B1L003S, U22-ch113, adc1

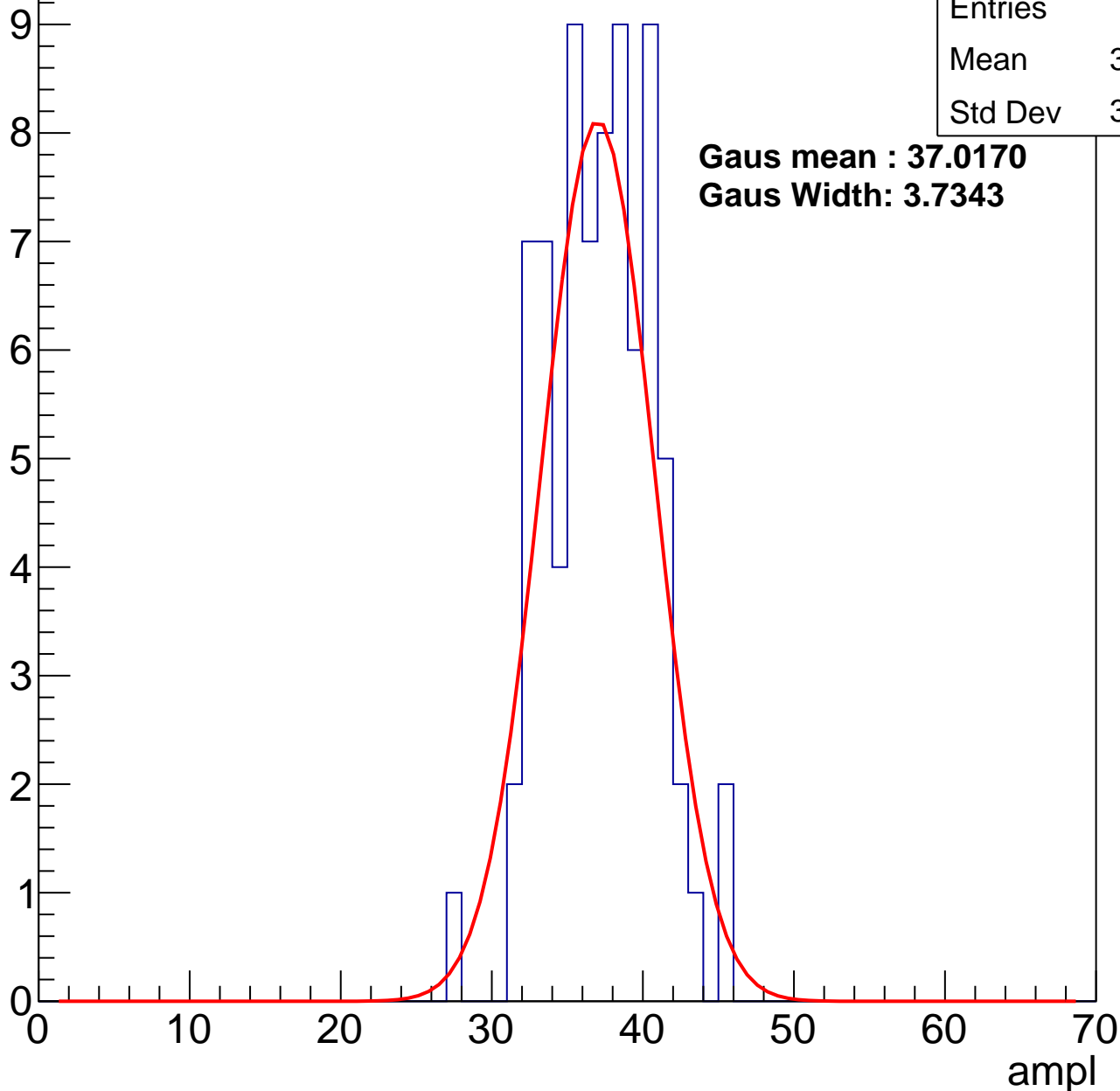
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	79
Mean	36.72
Std Dev	3.449

**Gaus mean : 37.0170**

**Gaus Width: 3.7343**



# B1L003S, U22-ch113, adc2

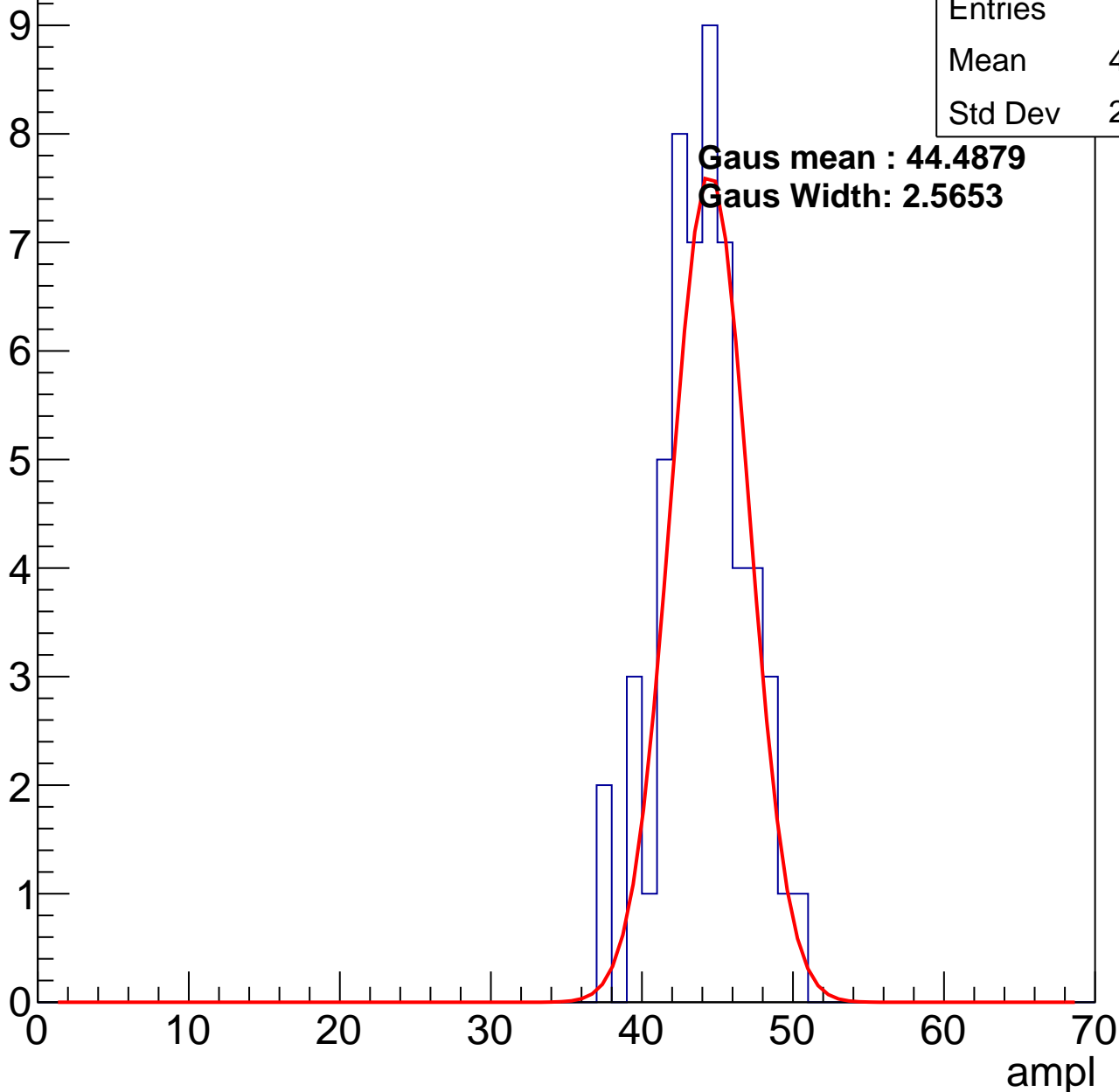
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	43.62
Std Dev	2.806

**Gaus mean : 44.4879**

**Gaus Width: 2.5653**

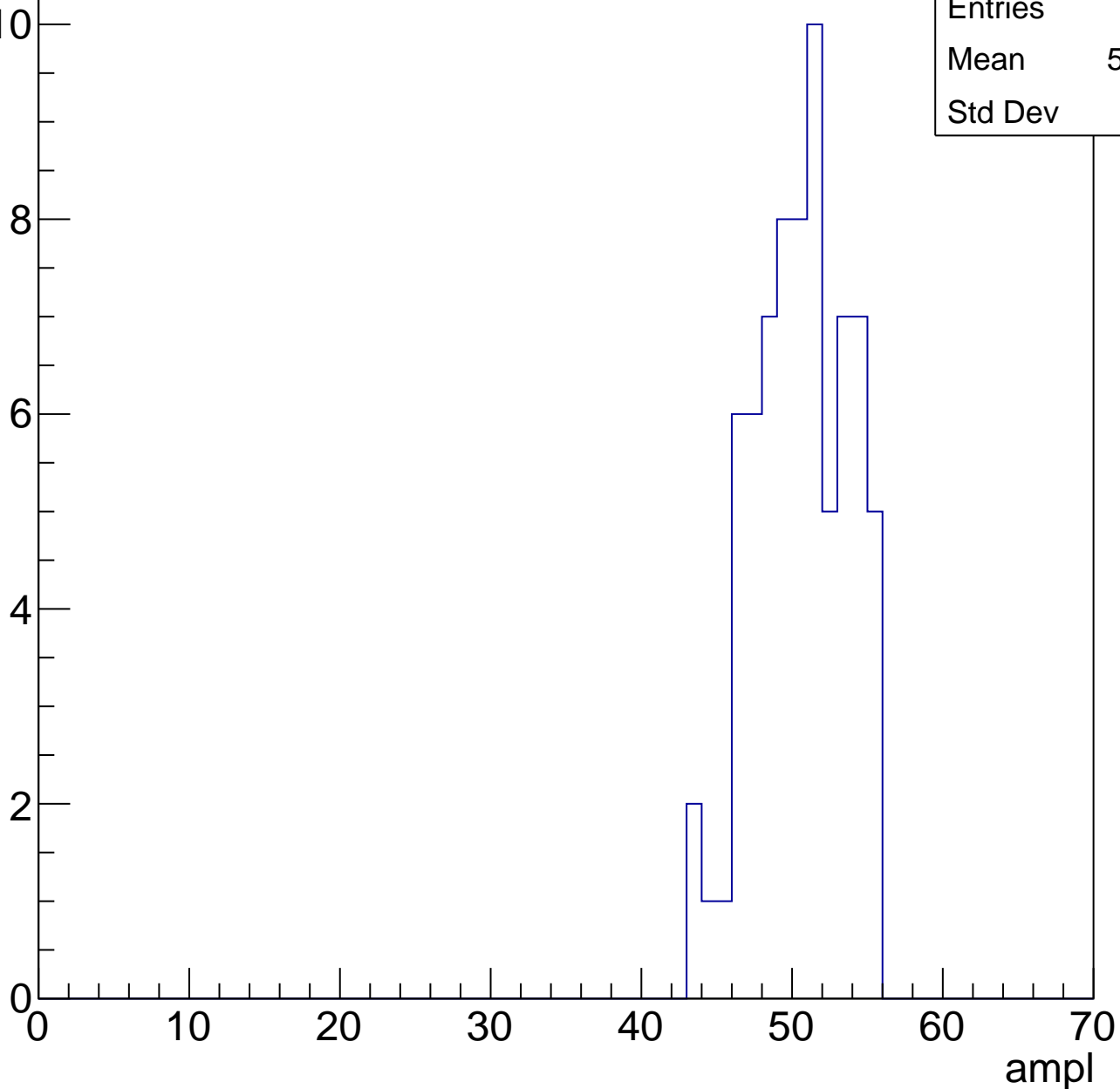


# B1L003S, U22-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	50.07
Std Dev	3.04

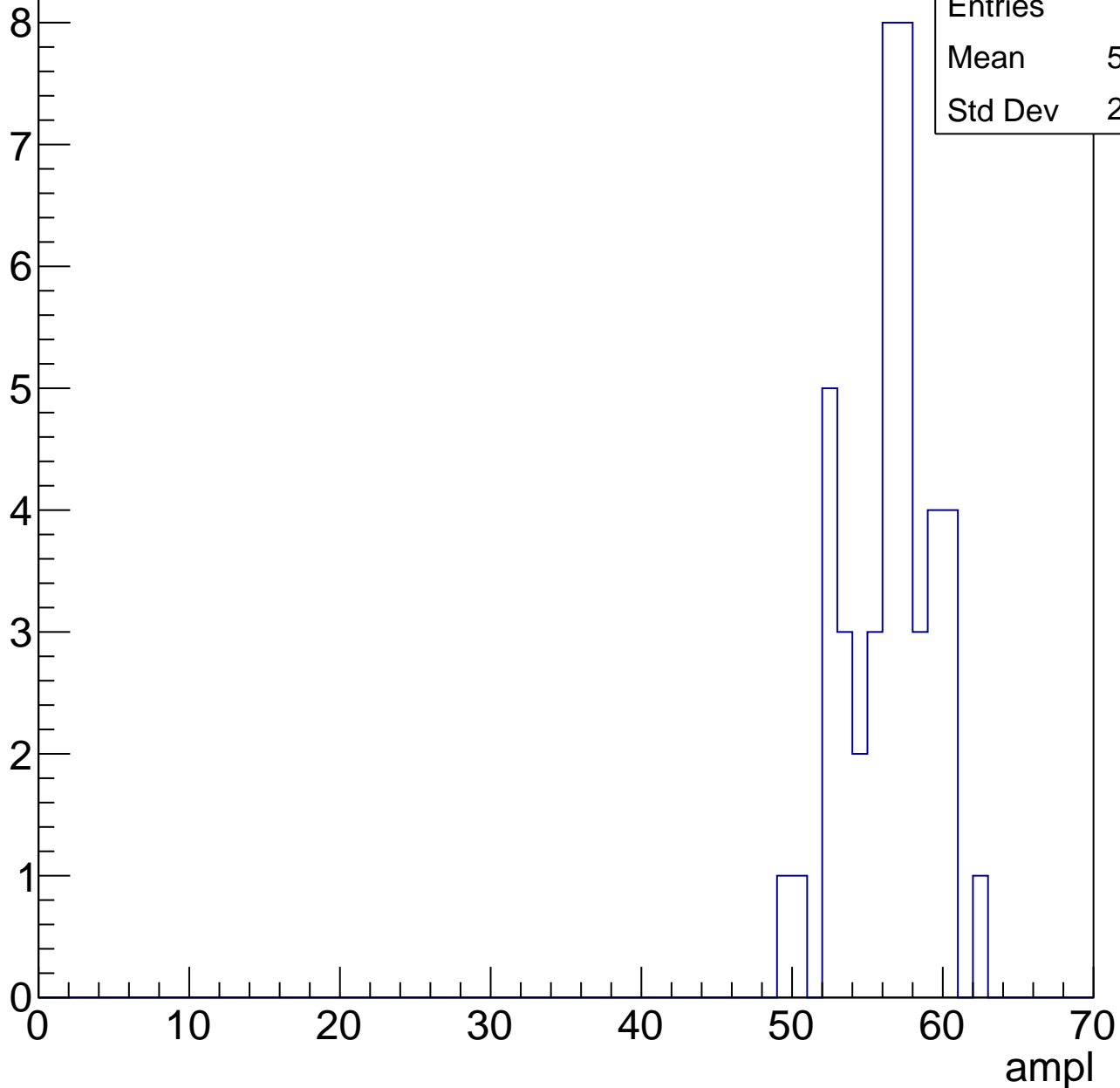


# B1L003S, U22-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.98
Std Dev	2.889

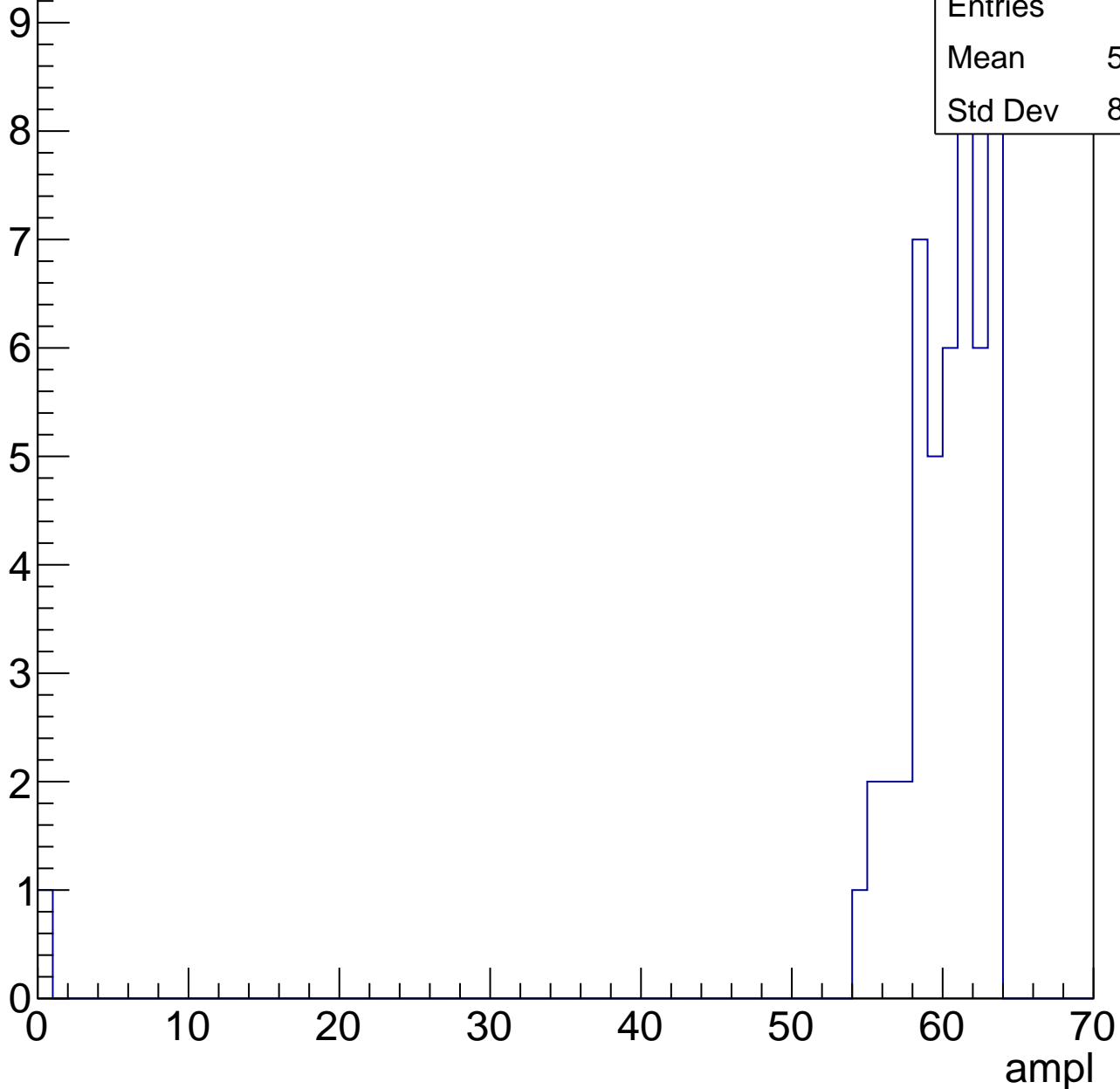


# B1L003S, U22-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

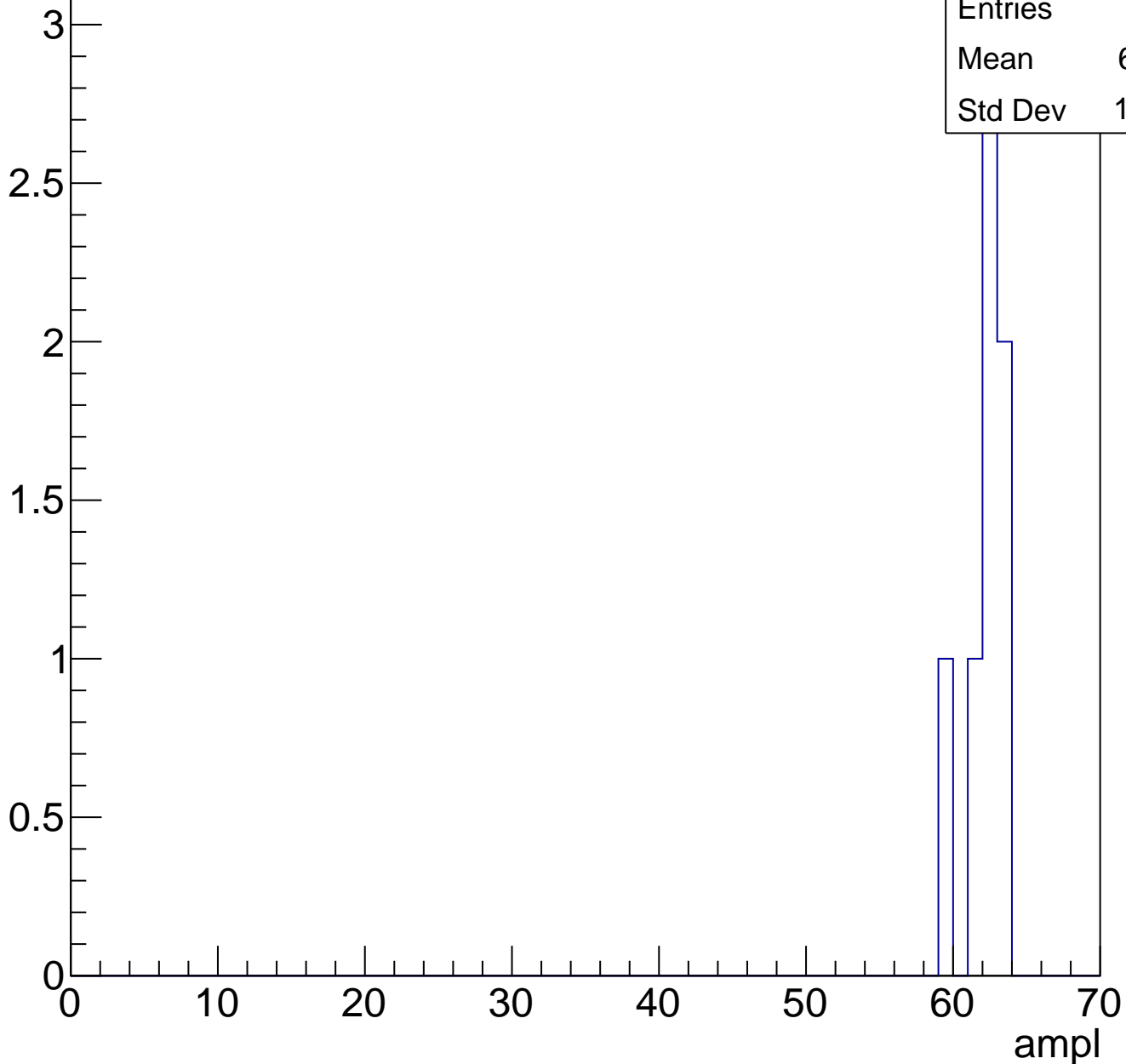
Entries	49
Mean	58.69
Std Dev	8.795



# B1L003S, U22-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	7
Mean	61.71
Std Dev	1.278



# B1L003S, U22-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

# B1L003S, U22-ch114, adc0

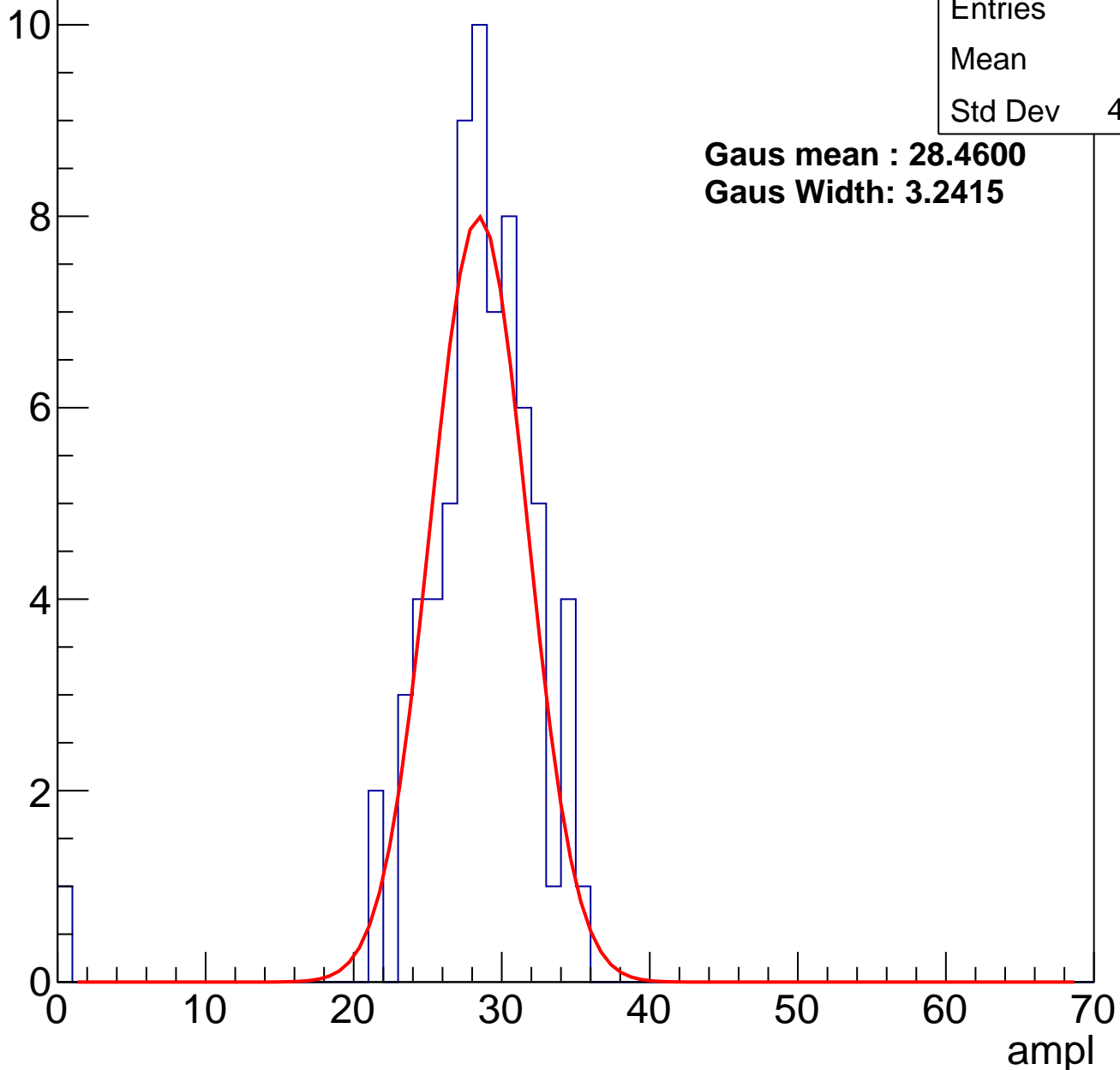
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	70
Mean	27.9
Std Dev	4.592

**Gaus mean : 28.4600**

**Gaus Width: 3.2415**

Entry



# B1L003S, U22-ch114, adc1

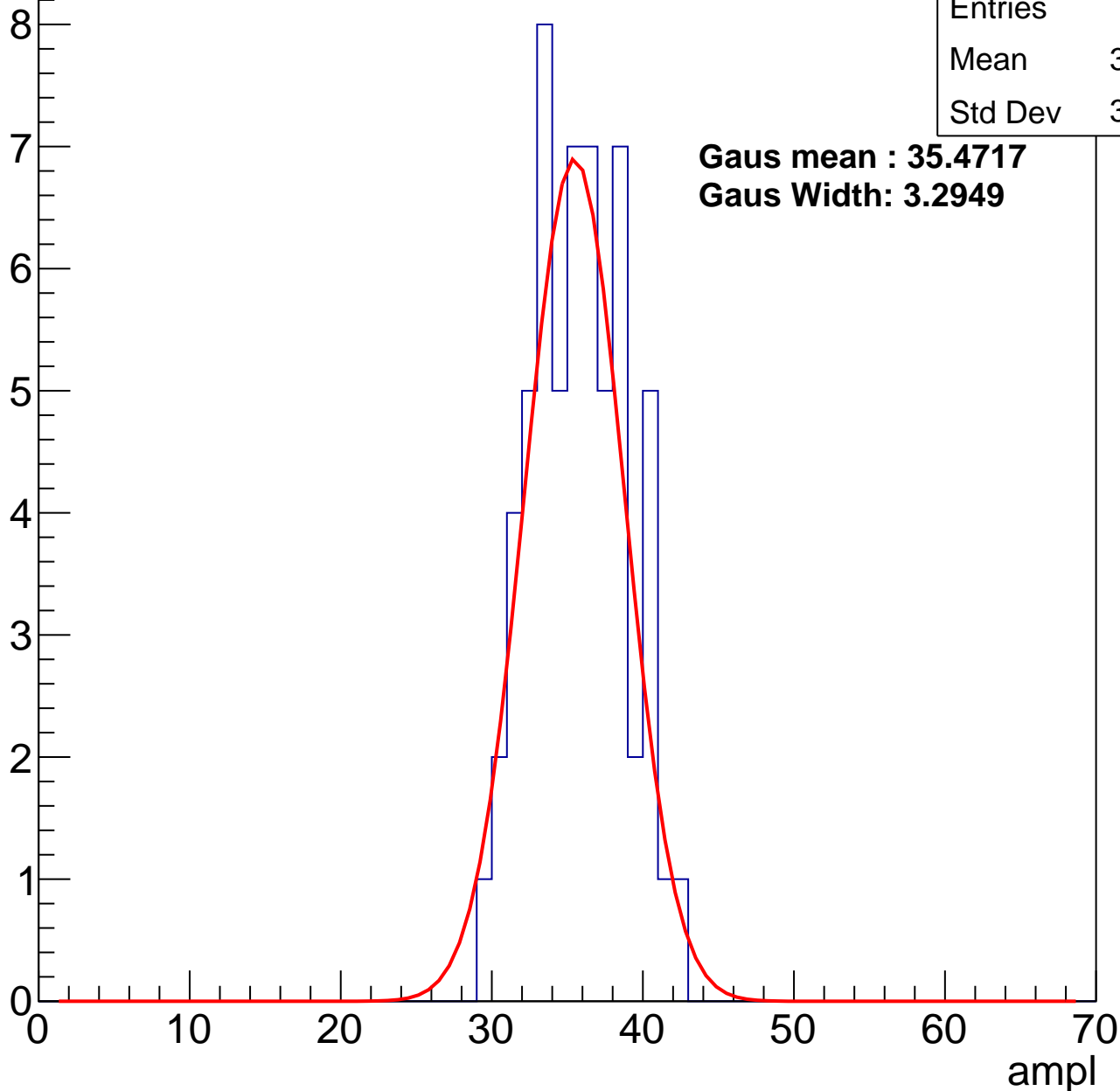
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.27
Std Dev	3.054

**Gaus mean : 35.4717**

**Gaus Width: 3.2949**



# B1L003S, U22-ch114, adc2

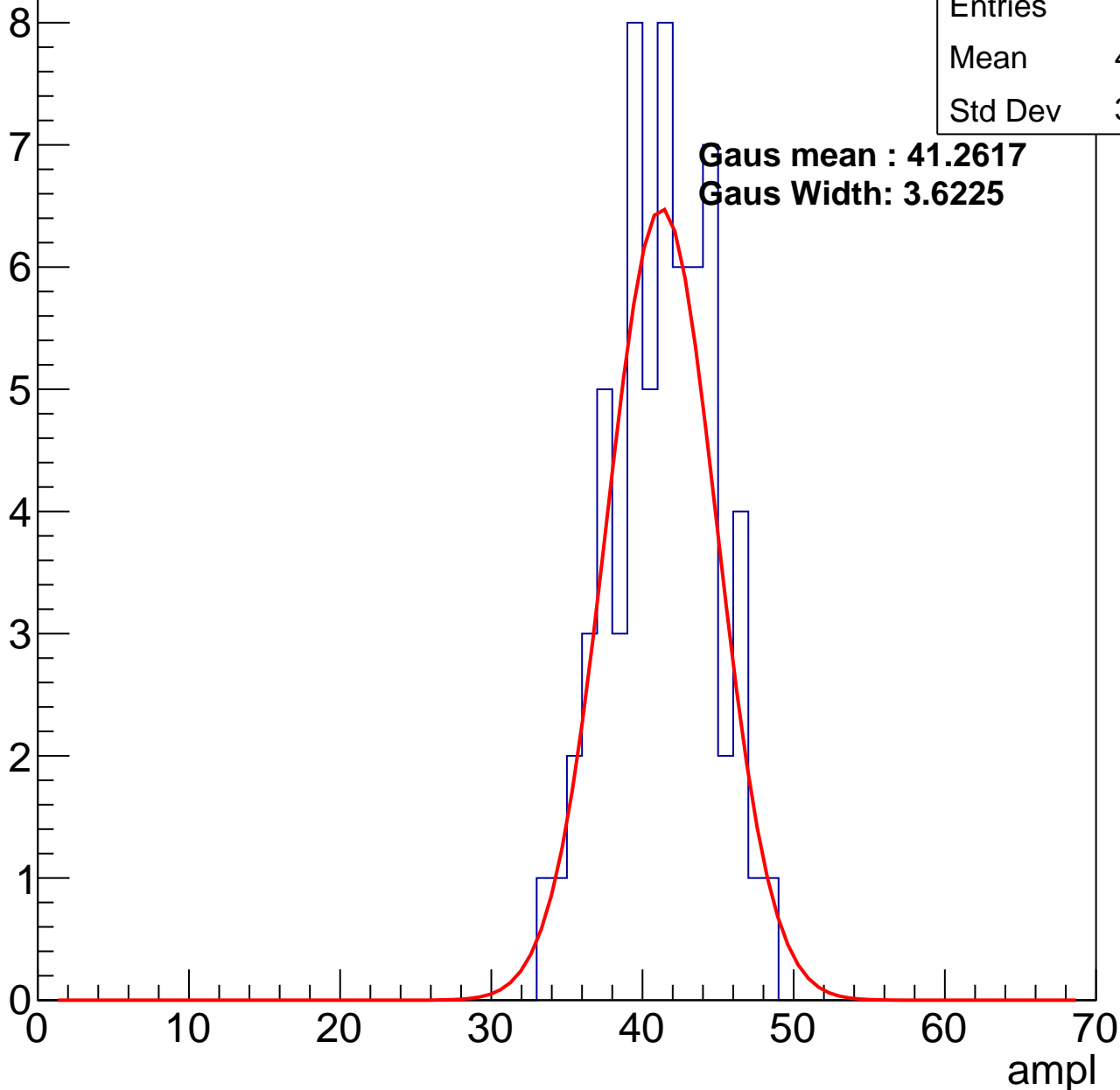
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	40.81
Std Dev	3.361

**Gaus mean : 41.2617**

**Gaus Width: 3.6225**

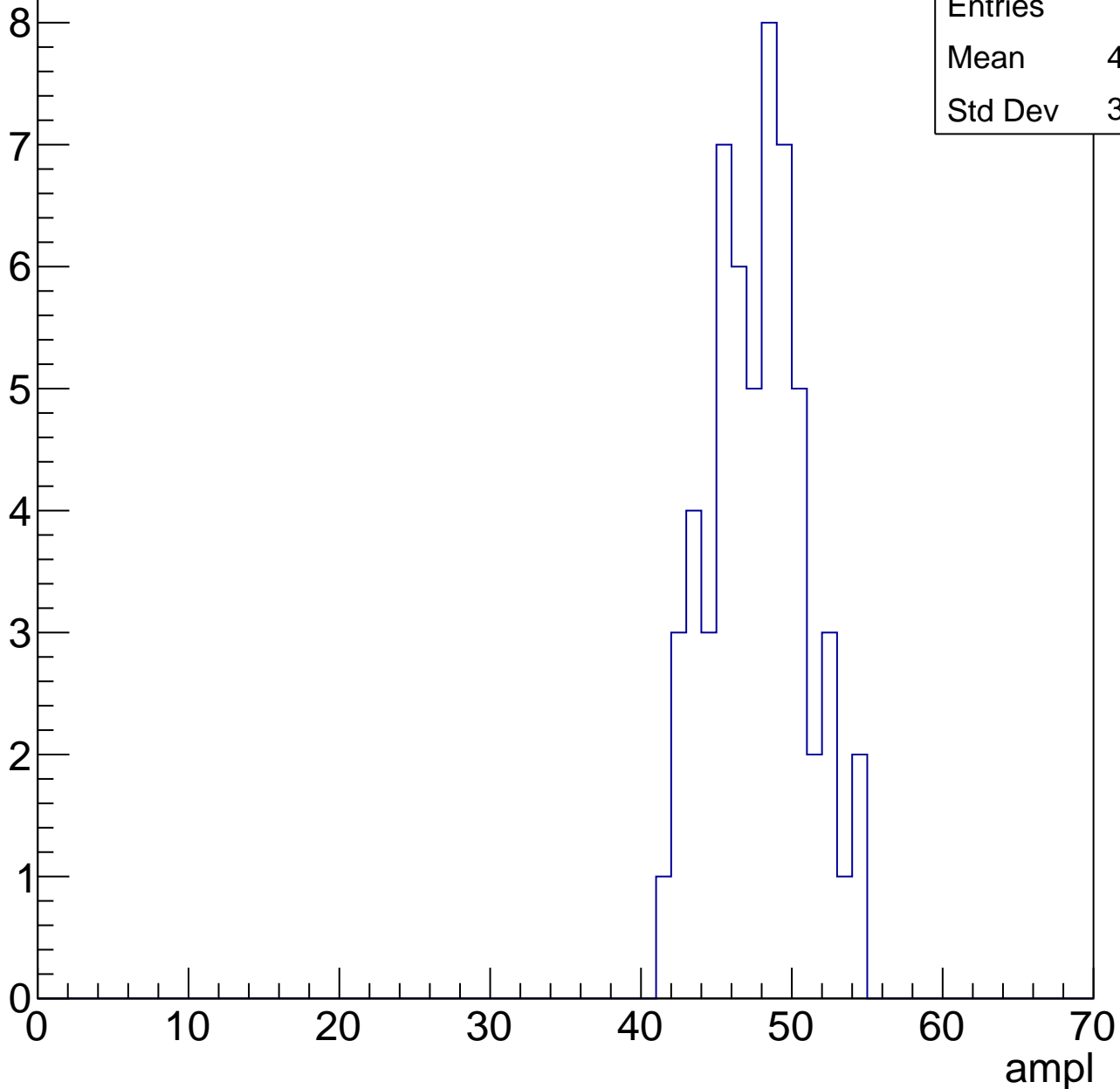


# B1L003S, U22-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	57
Mean	47.25
Std Dev	3.119

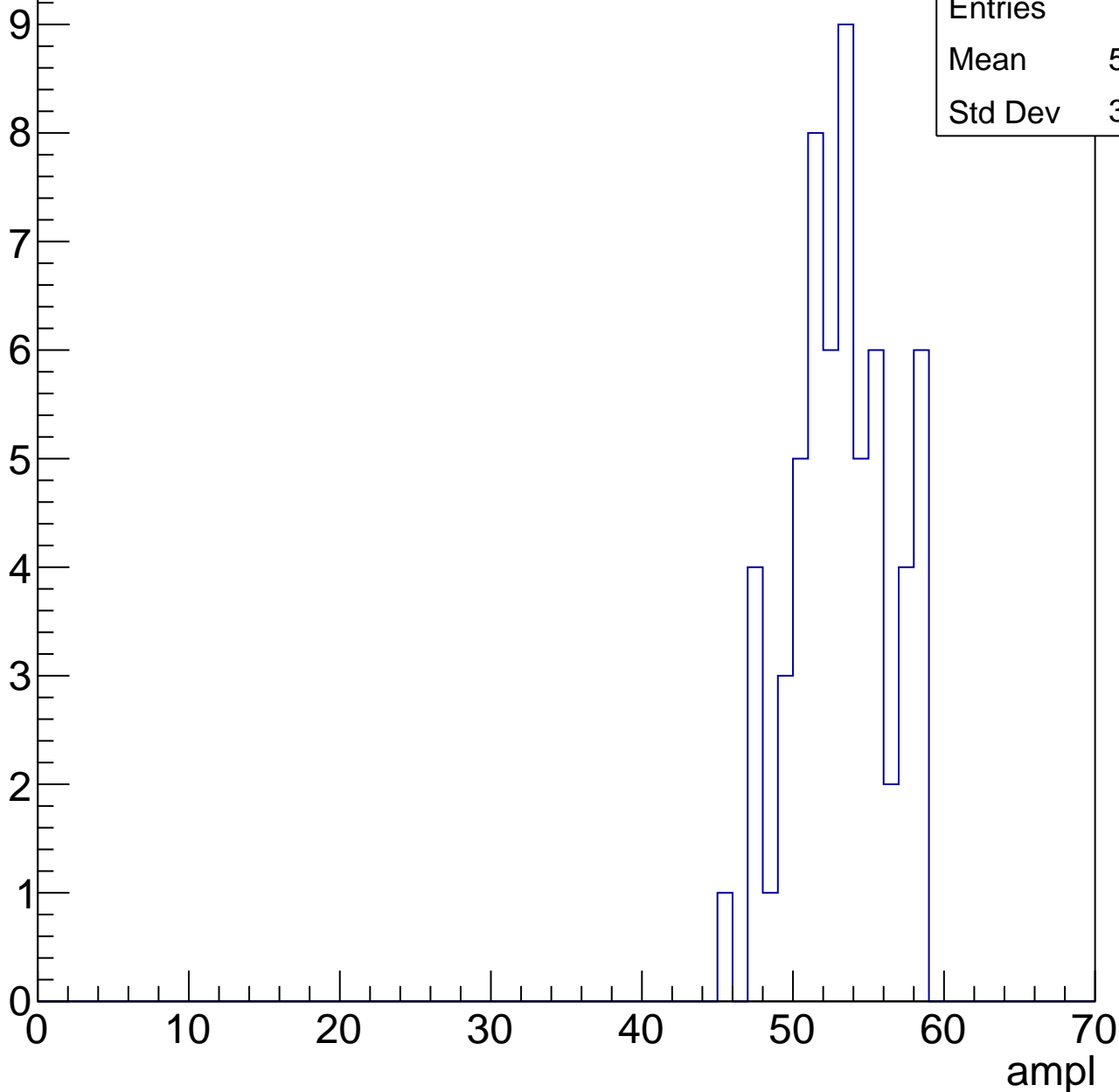


# B1L003S, U22-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	52.72
Std Dev	3.215

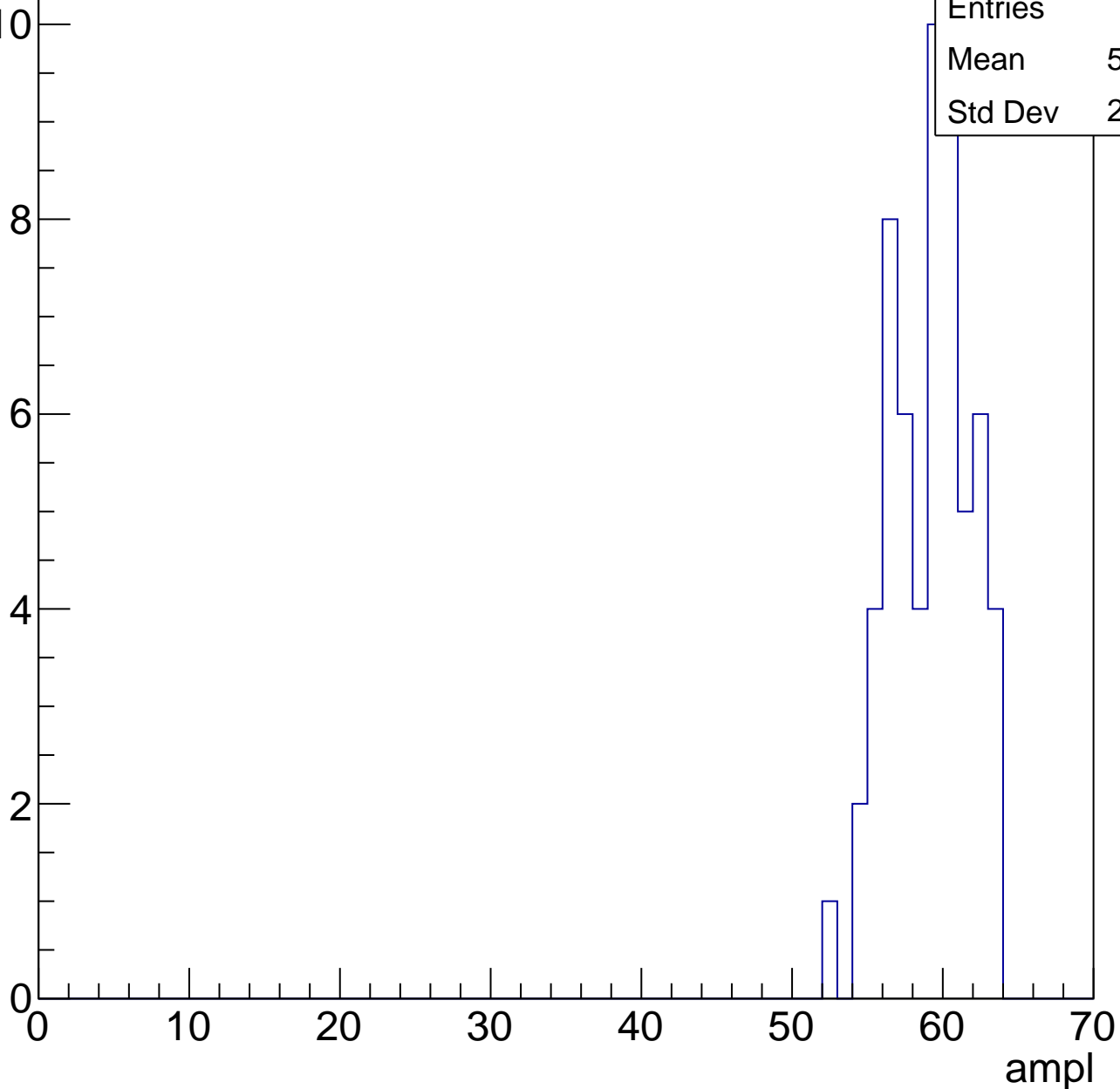


# B1L003S, U22-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	58.66
Std Dev	2.614

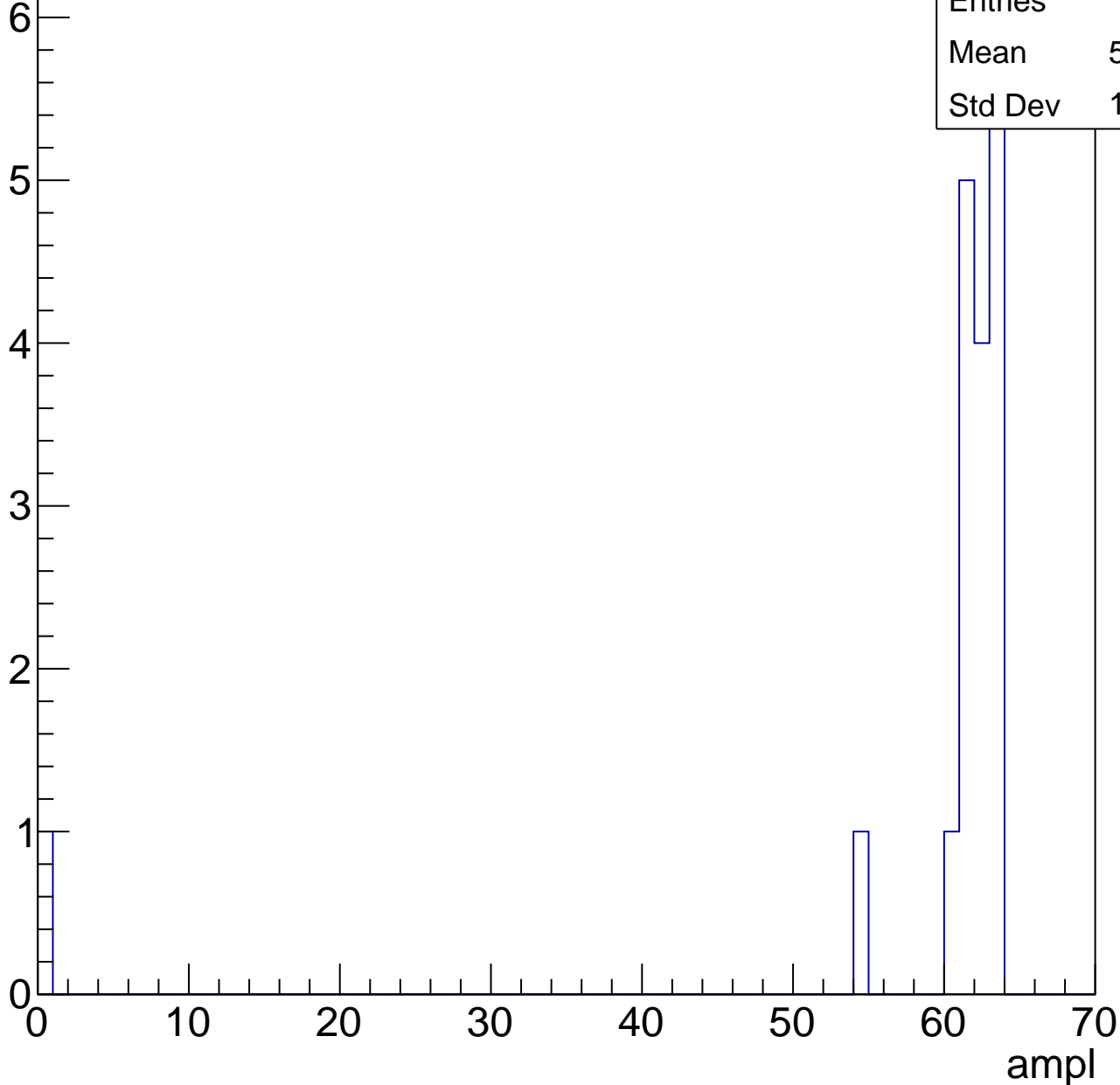


# B1L003S, U22-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	18
Mean	58.06
Std Dev	14.23





# B1L003S, U22-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch115, adc0

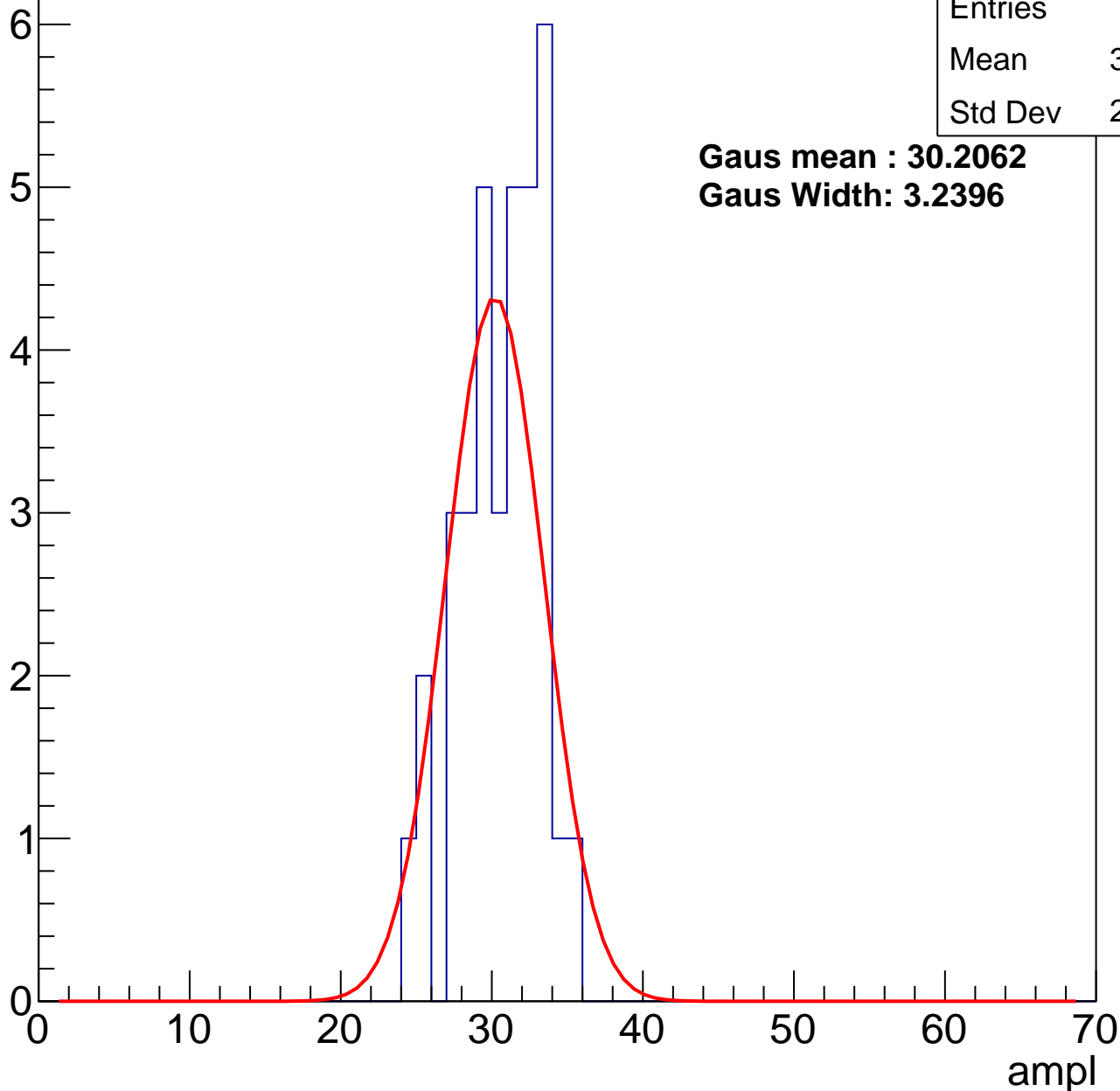
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	35
Mean	30.17
Std Dev	2.667

**Gaus mean : 30.2062**

**Gaus Width: 3.2396**



# B1L003S, U22-ch115, adc1

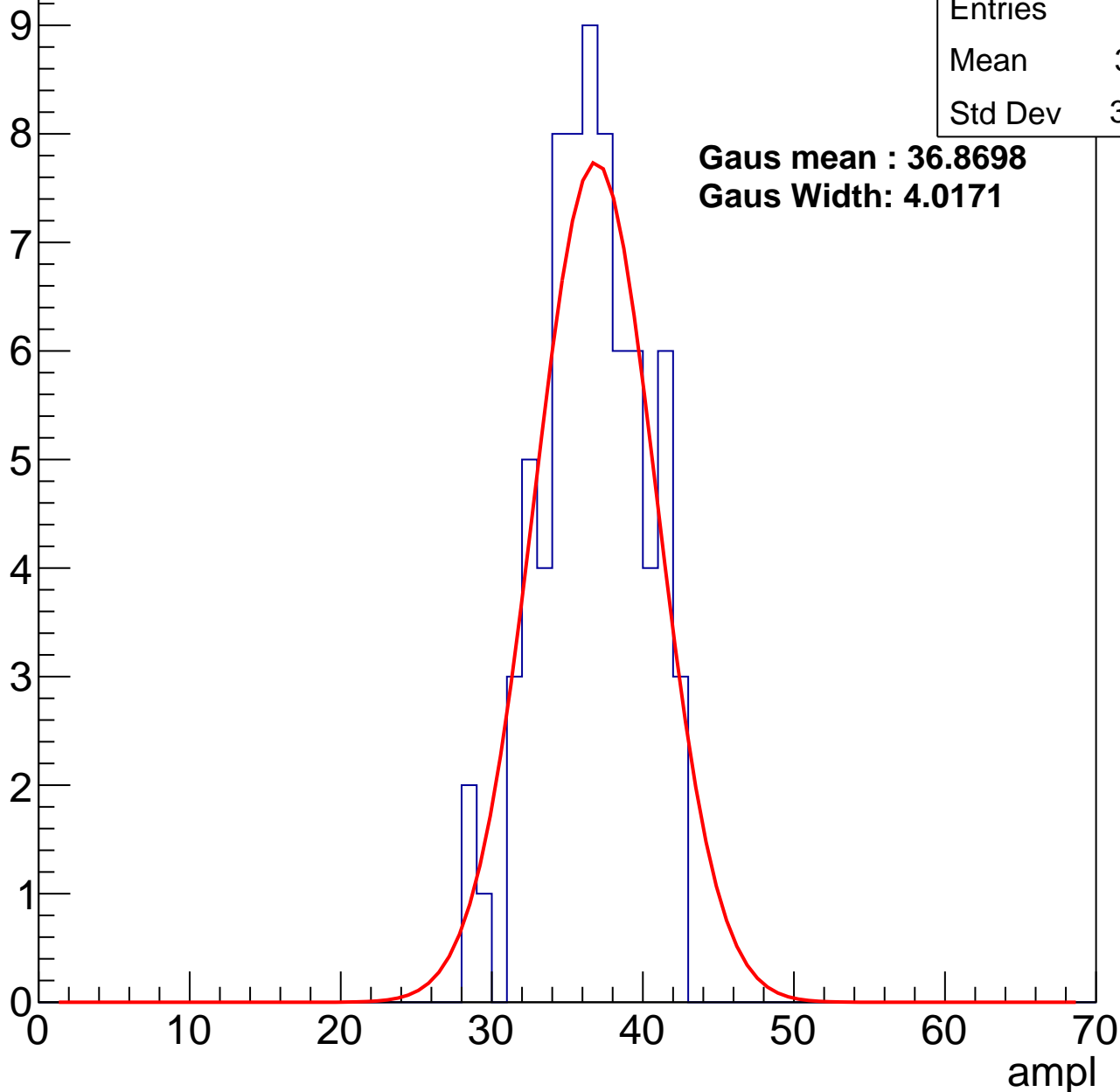
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	36.11
Std Dev	3.342

**Gaus mean : 36.8698**

**Gaus Width: 4.0171**



# B1L003S, U22-ch115, adc2

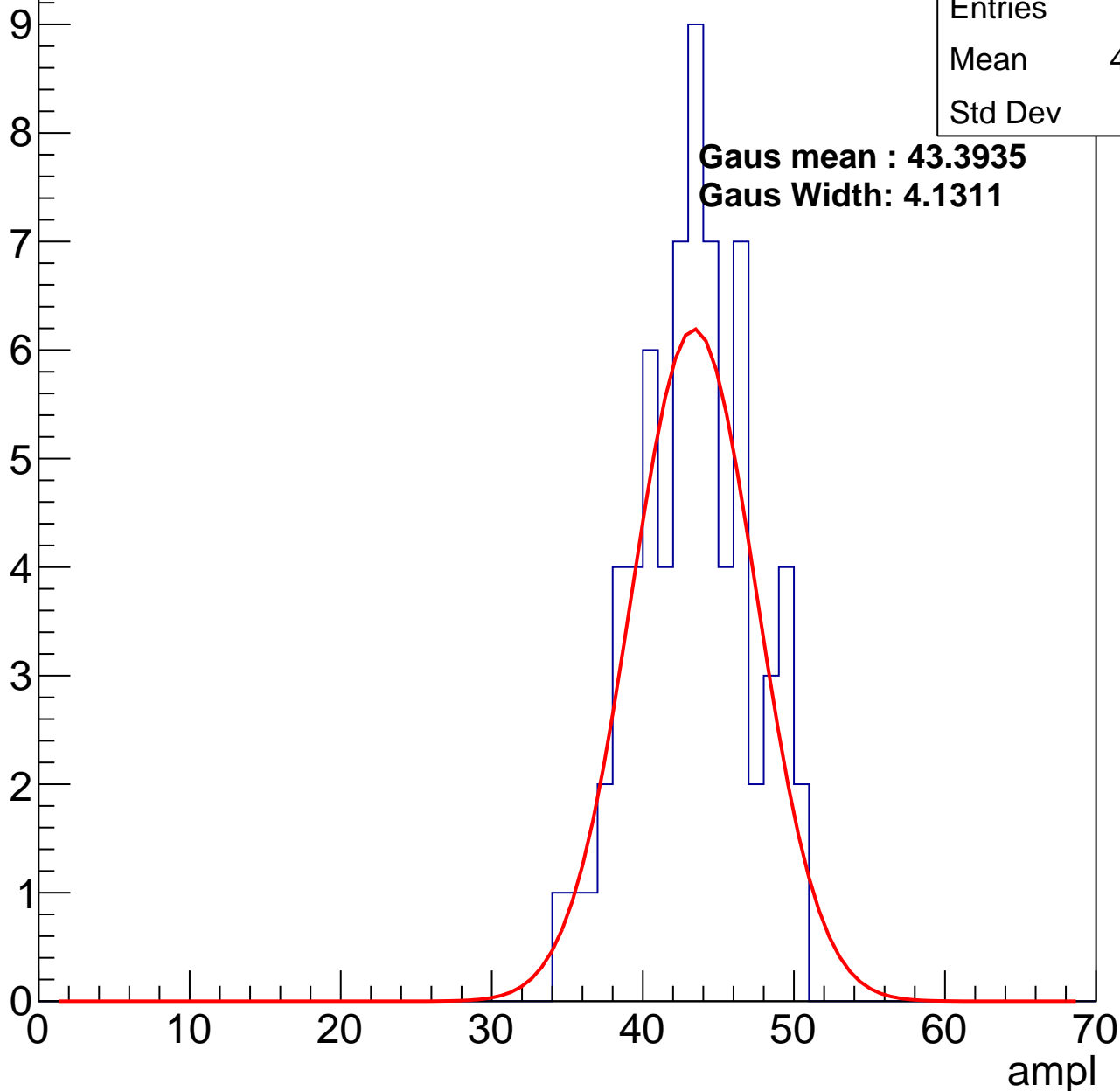
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	42.88
Std Dev	3.72

**Gaus mean : 43.3935**

**Gaus Width: 4.1311**

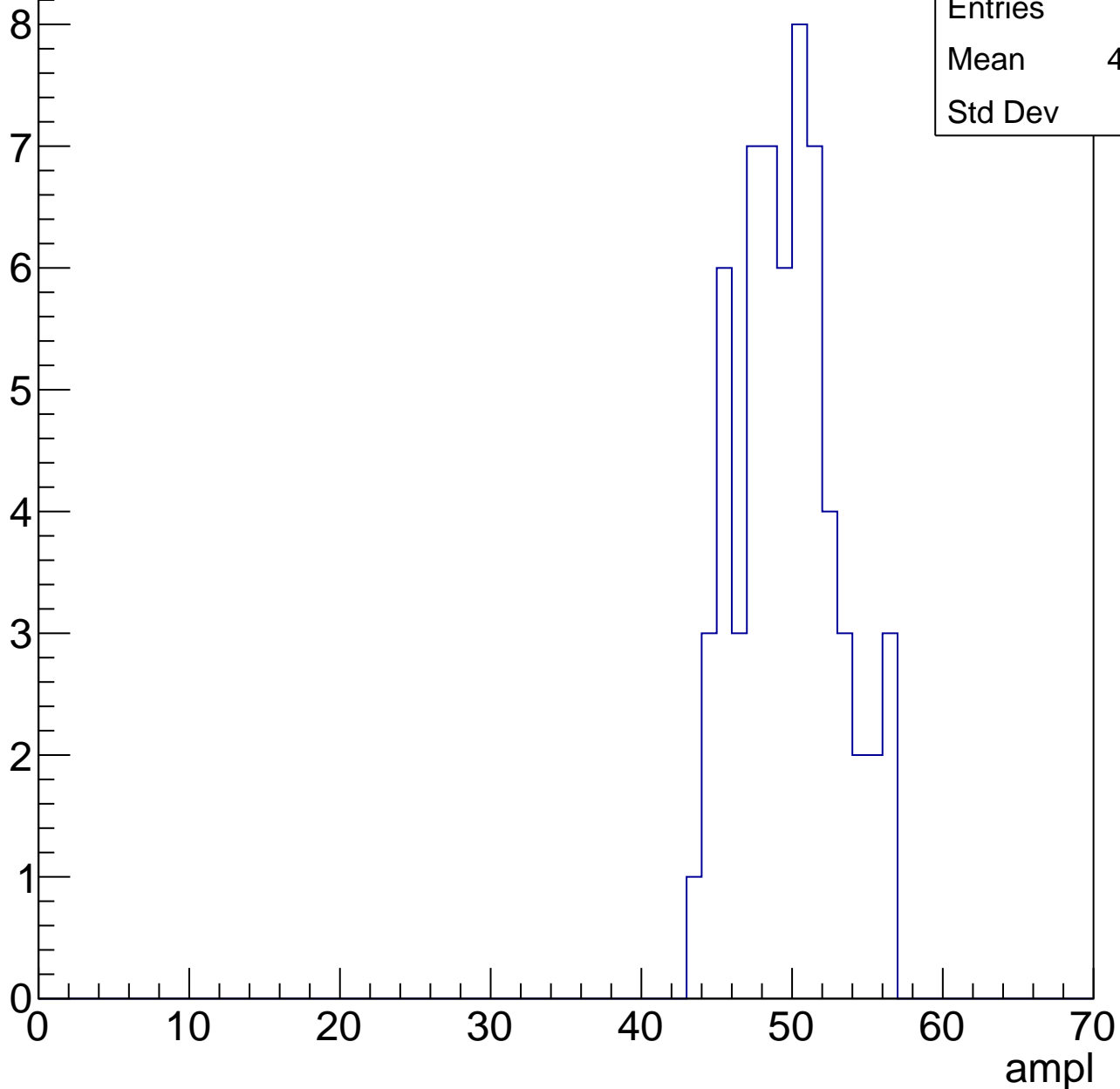


# B1L003S, U22-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.23
Std Dev	3.25

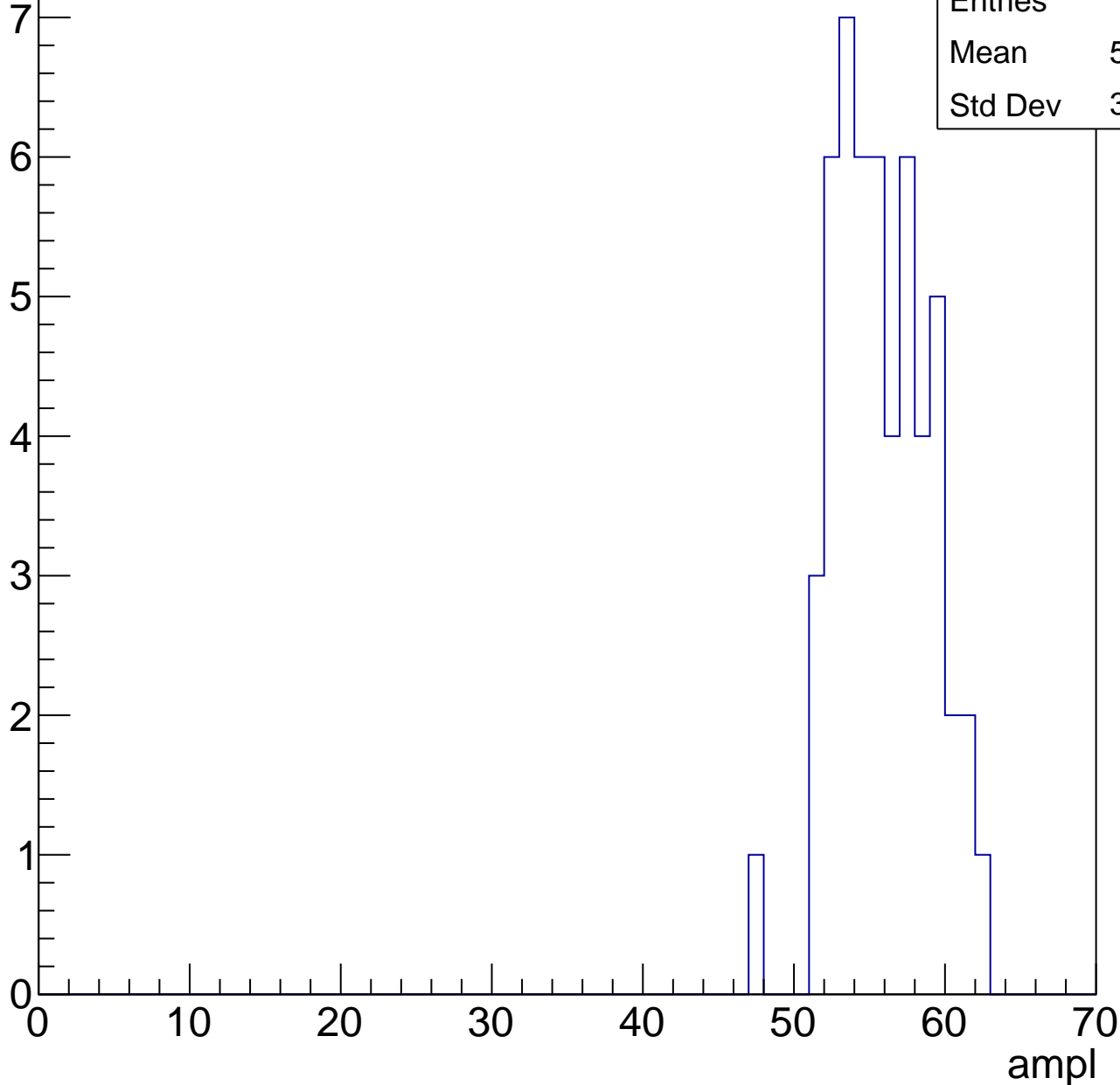


# B1L003S, U22-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.36
Std Dev	3.097

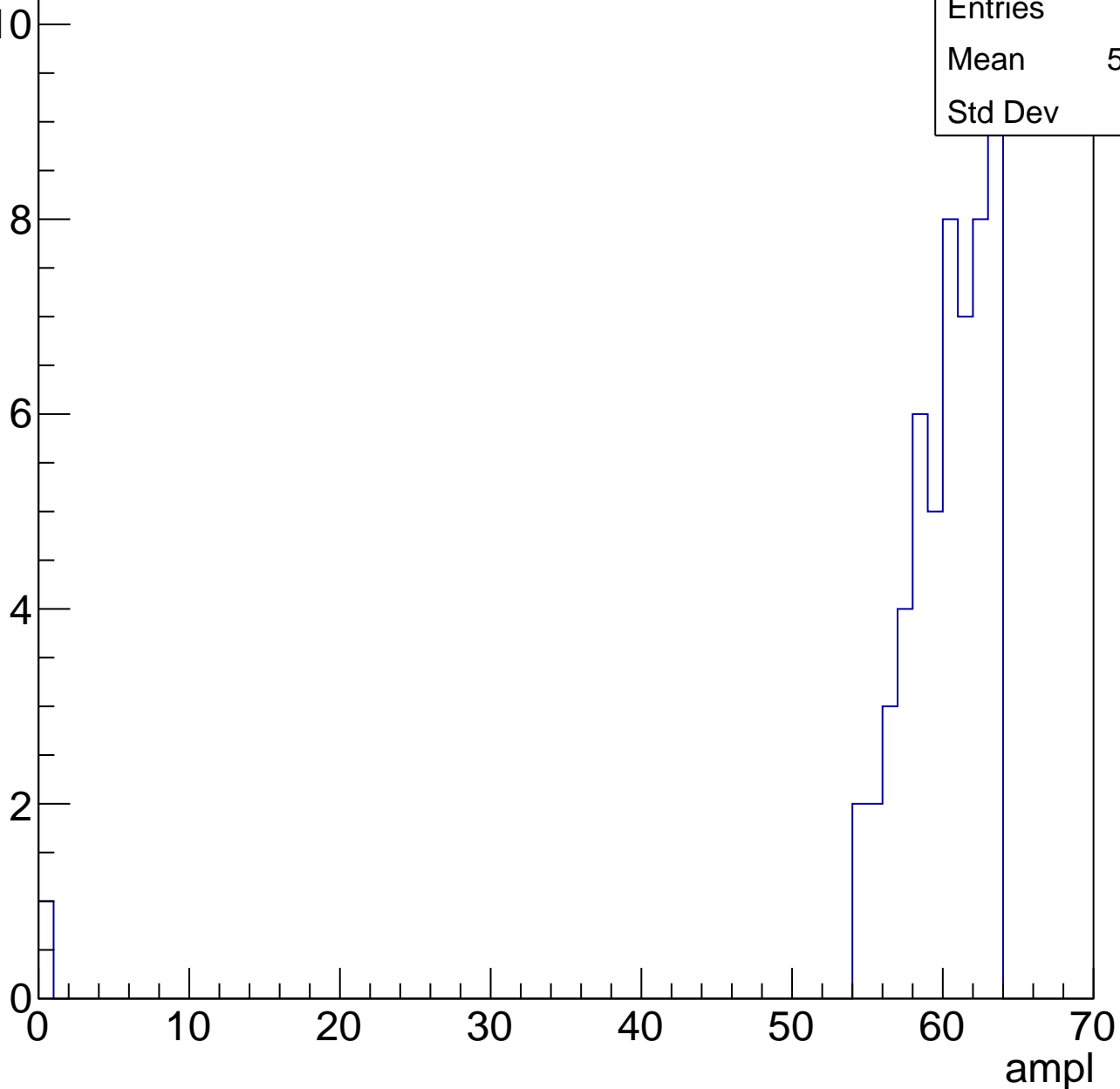


# B1L003S, U22-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	56
Mean	58.75
Std Dev	8.32



# B1L003S, U22-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch116, adc0

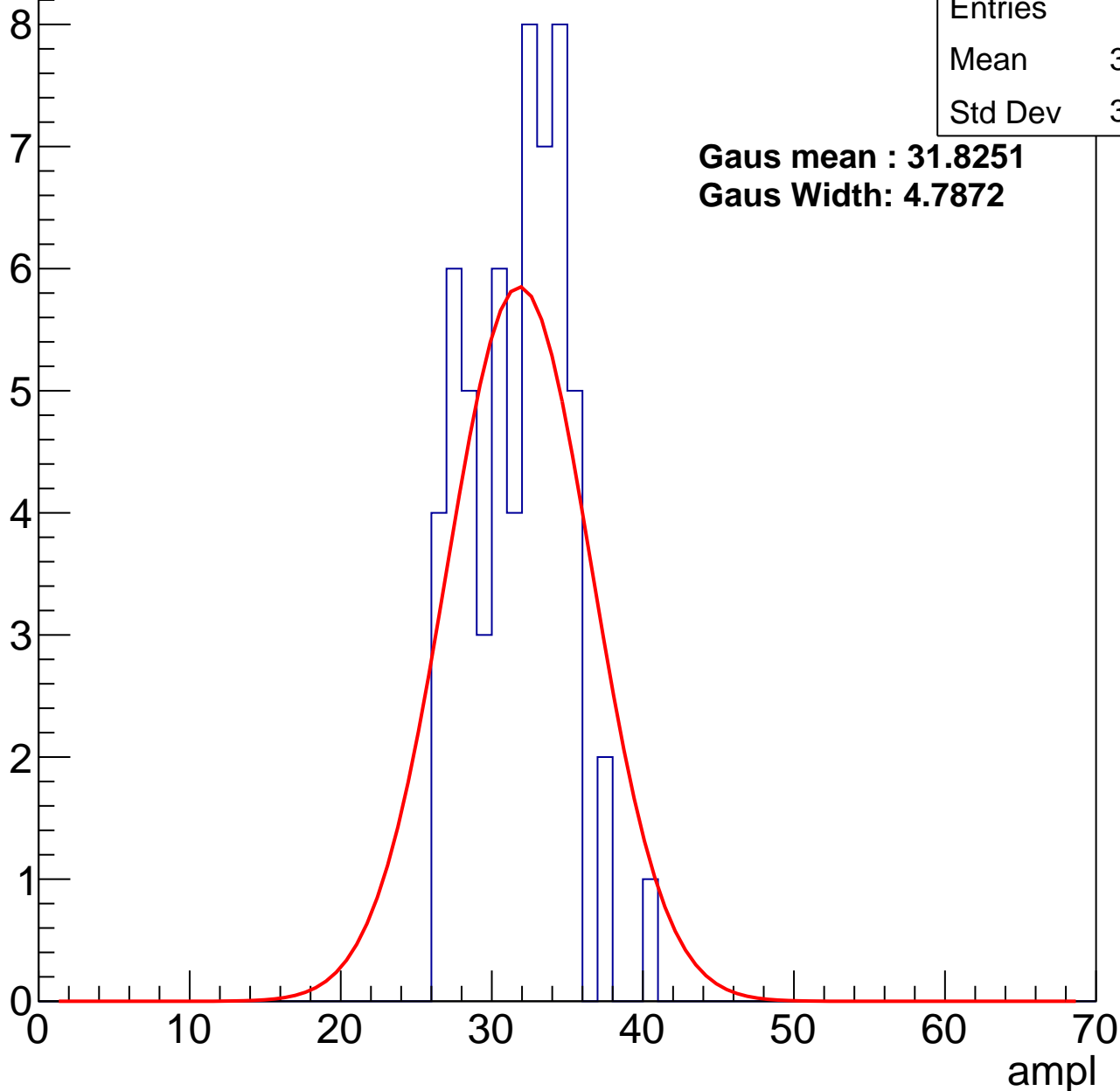
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	31.27
Std Dev	3.177

**Gaus mean : 31.8251**

**Gaus Width: 4.7872**



# B1L003S, U22-ch116, adc1

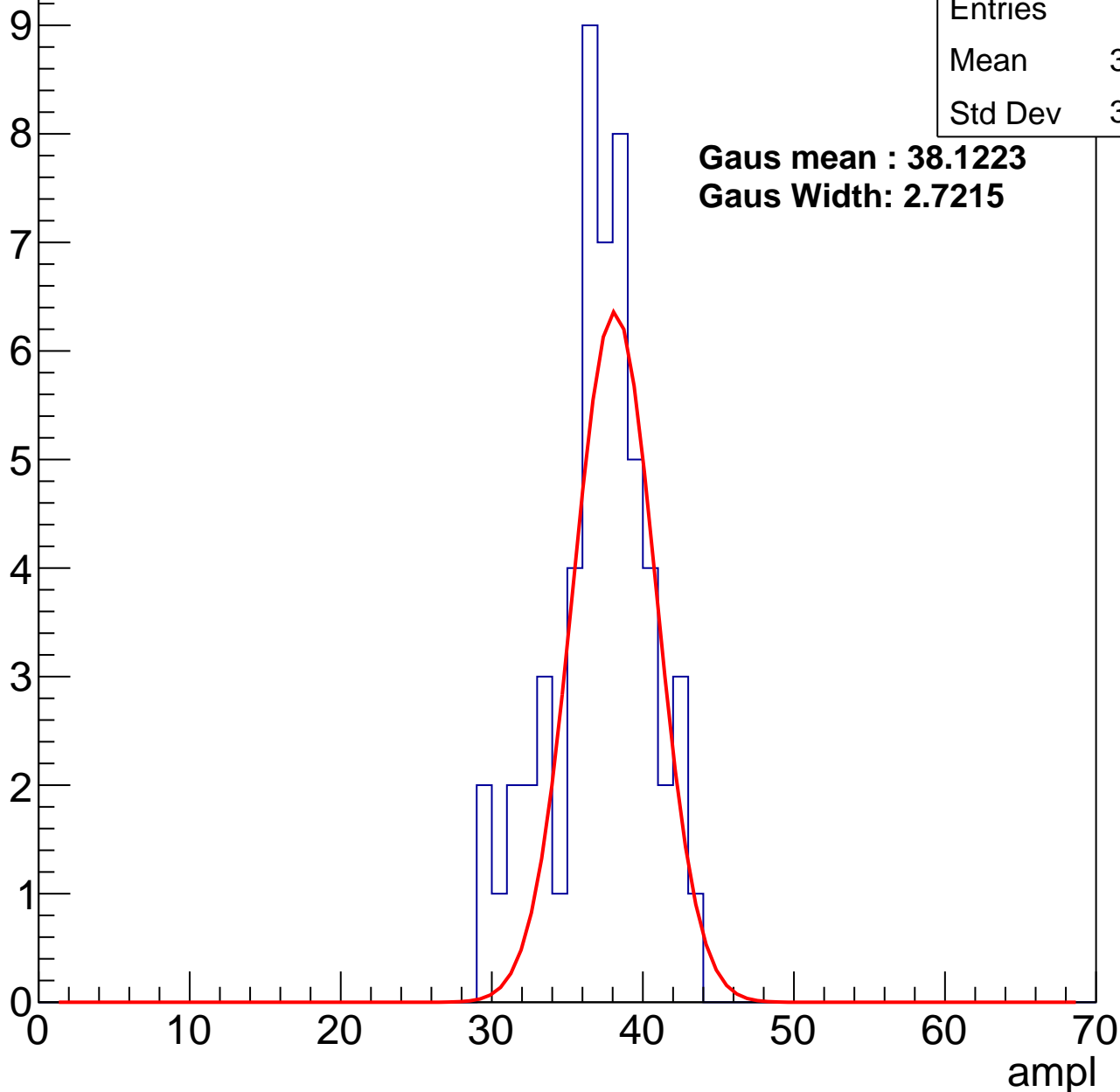
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.67
Std Dev	3.283

**Gaus mean : 38.1223**

**Gaus Width: 2.7215**



# B1L003S, U22-ch116, adc2

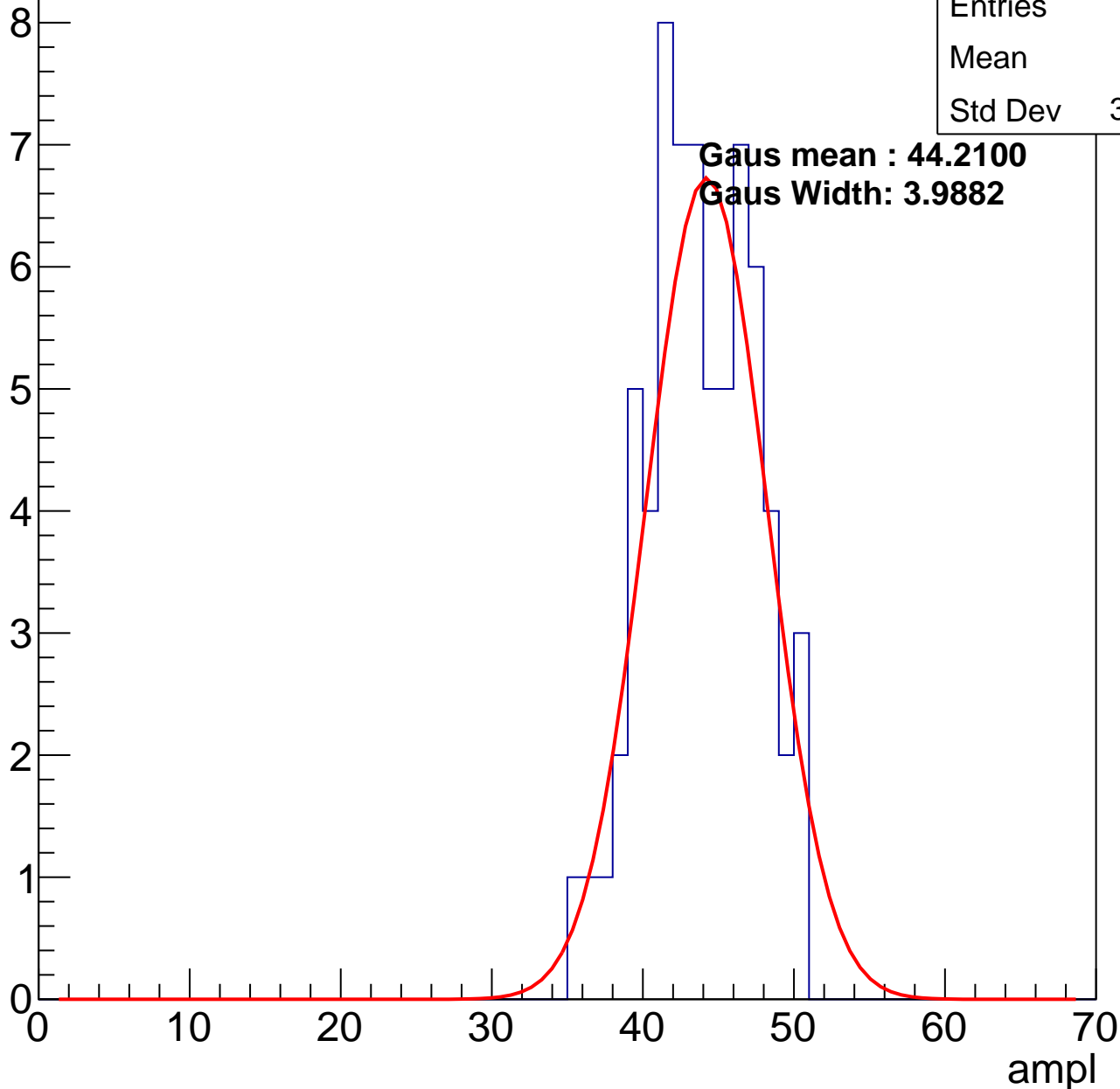
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	43.4
Std Dev	3.528

Gaus mean : 44.2100

Gaus Width: 3.9882

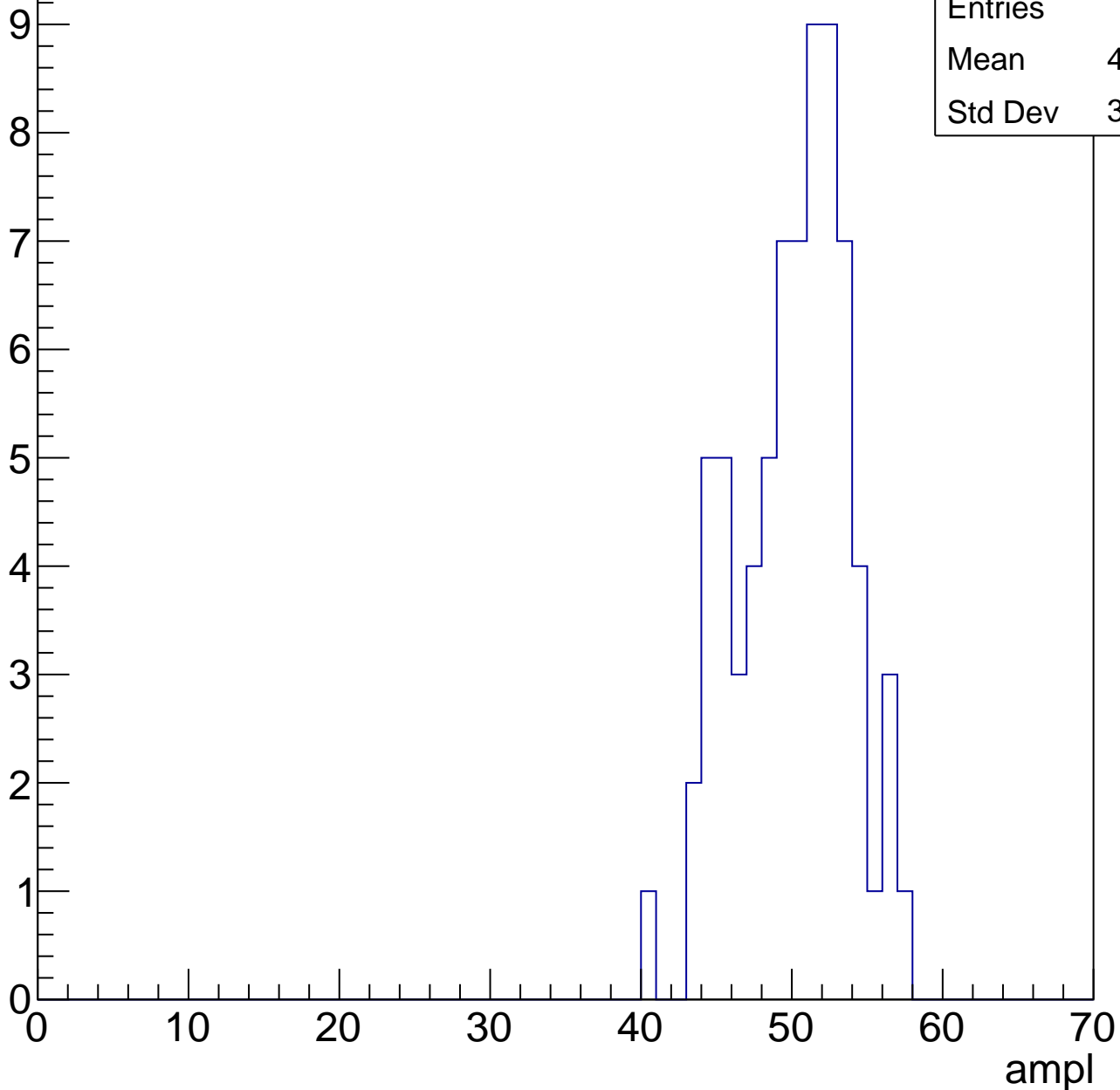


# B1L003S, U22-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	49.64
Std Dev	3.624

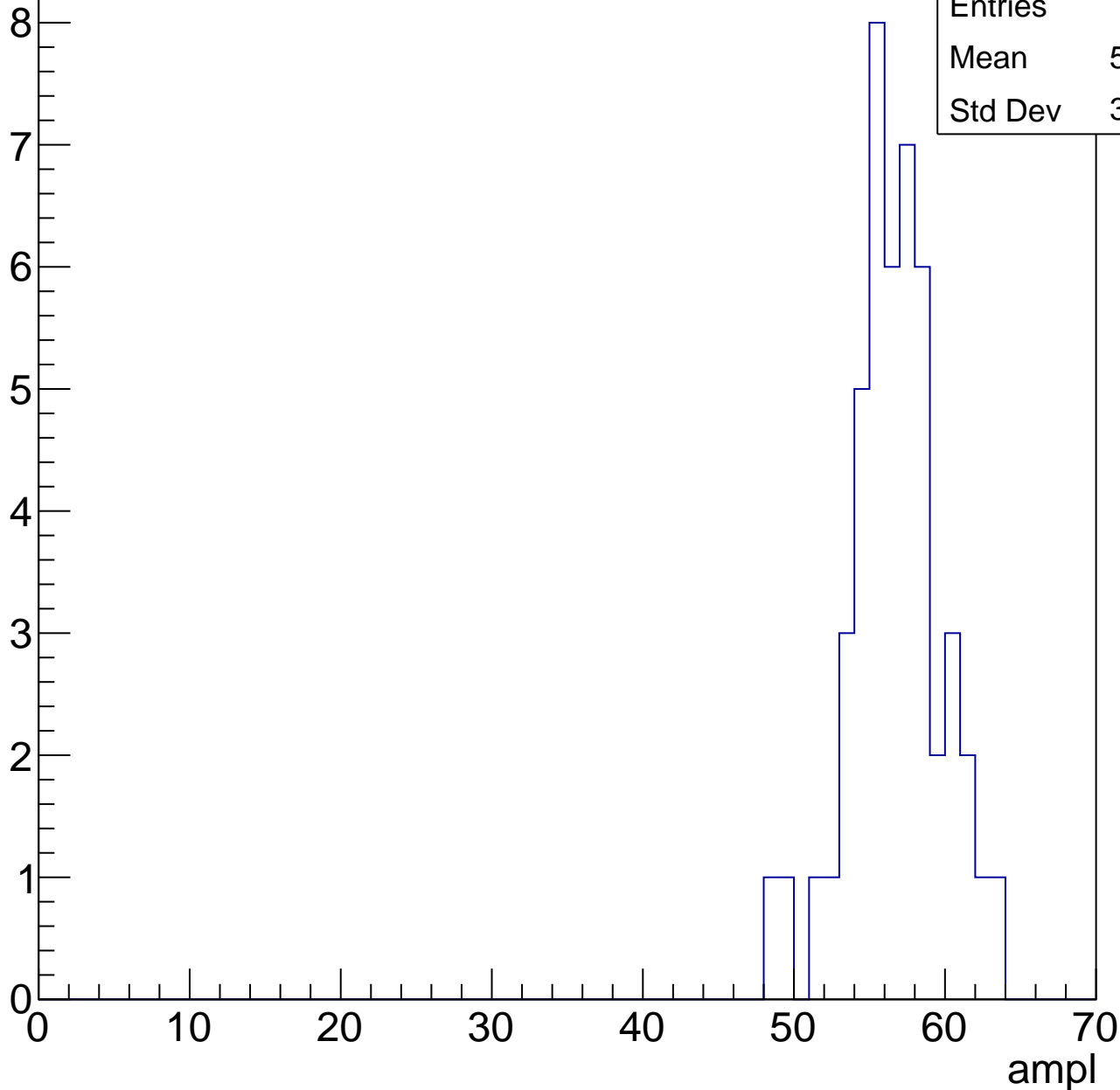


# B1L003S, U22-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

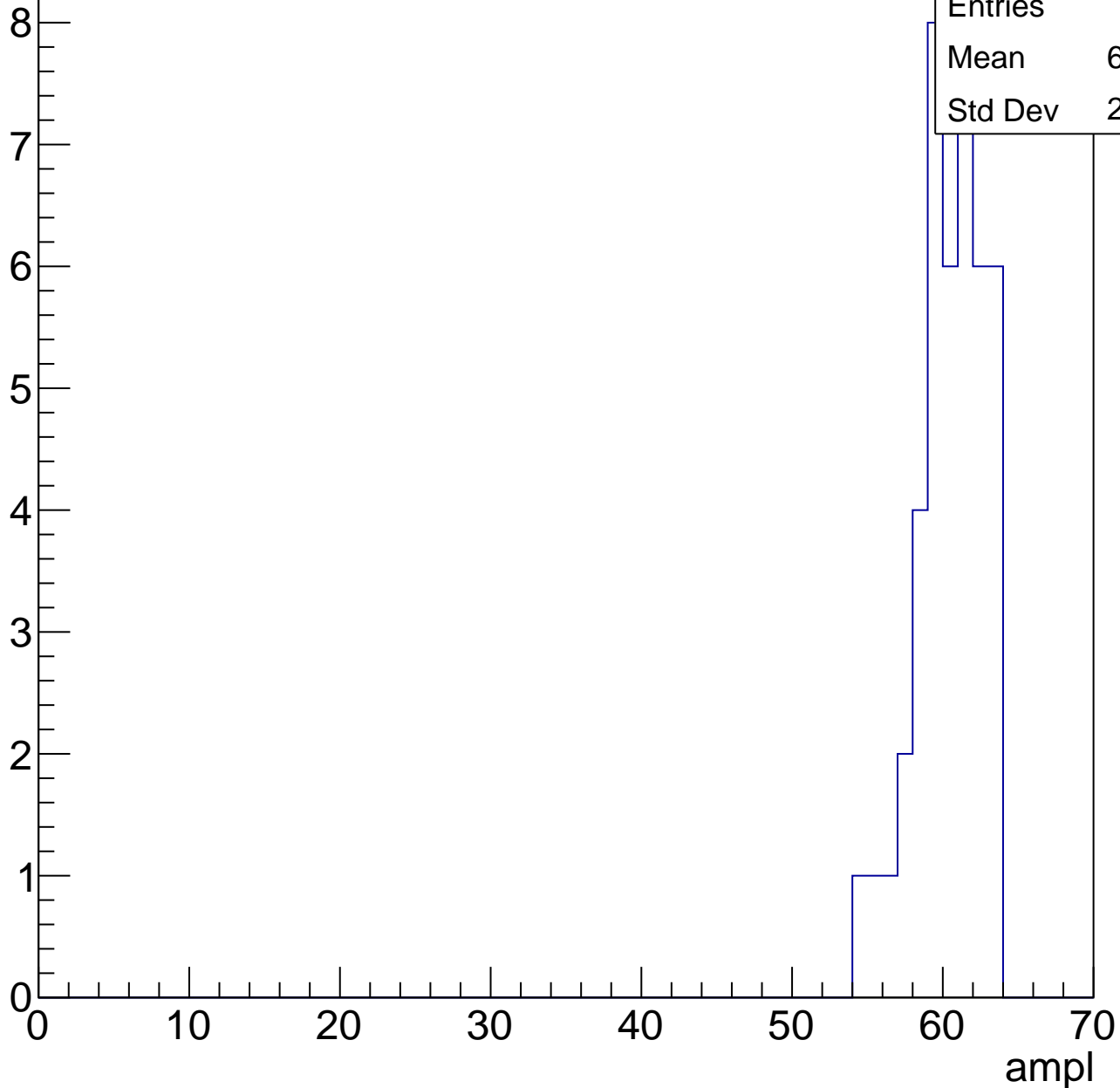
Entries	48
Mean	56.19
Std Dev	3.025



# B1L003S, U22-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

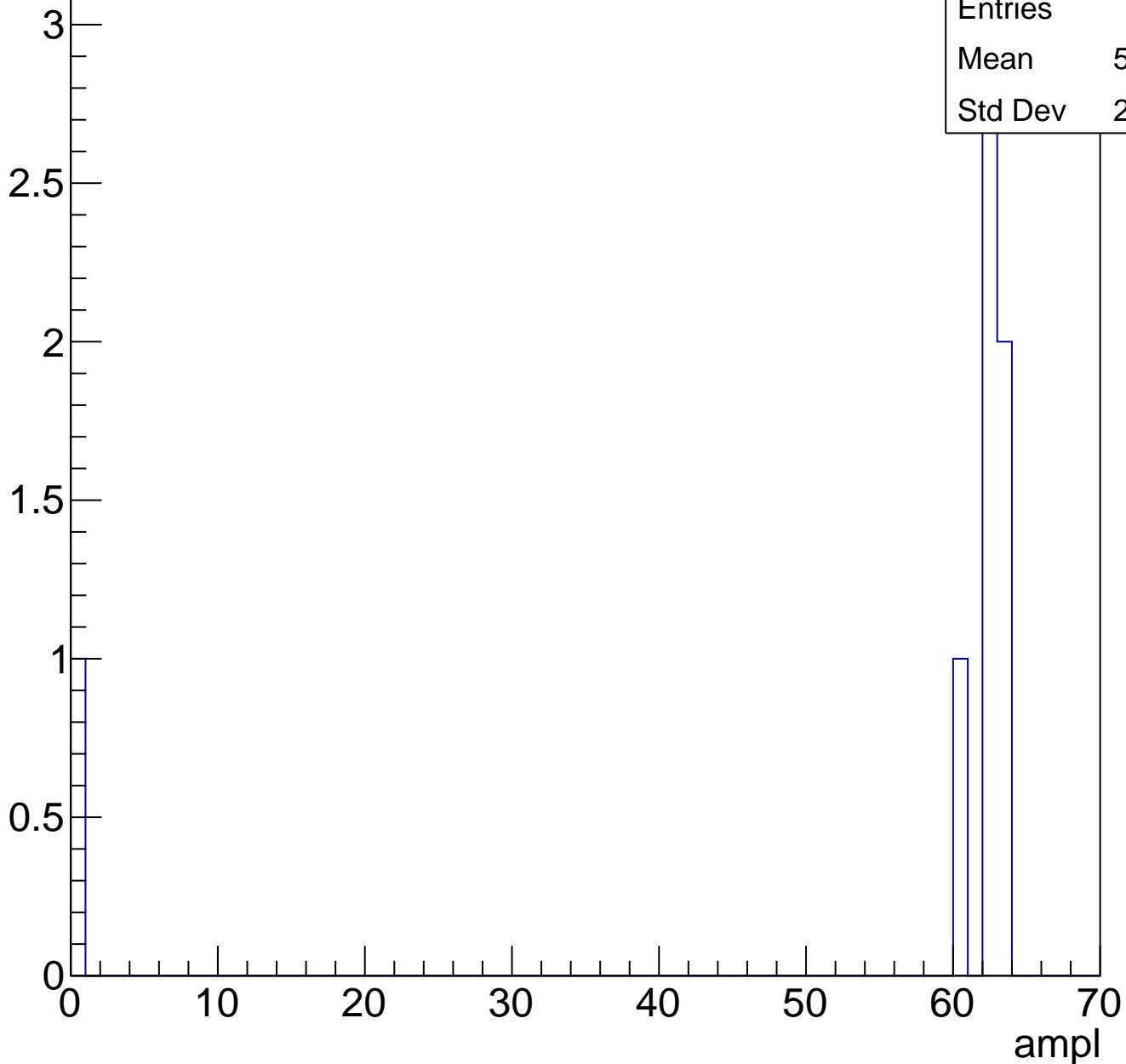
Entry



# B1L003S, U22-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

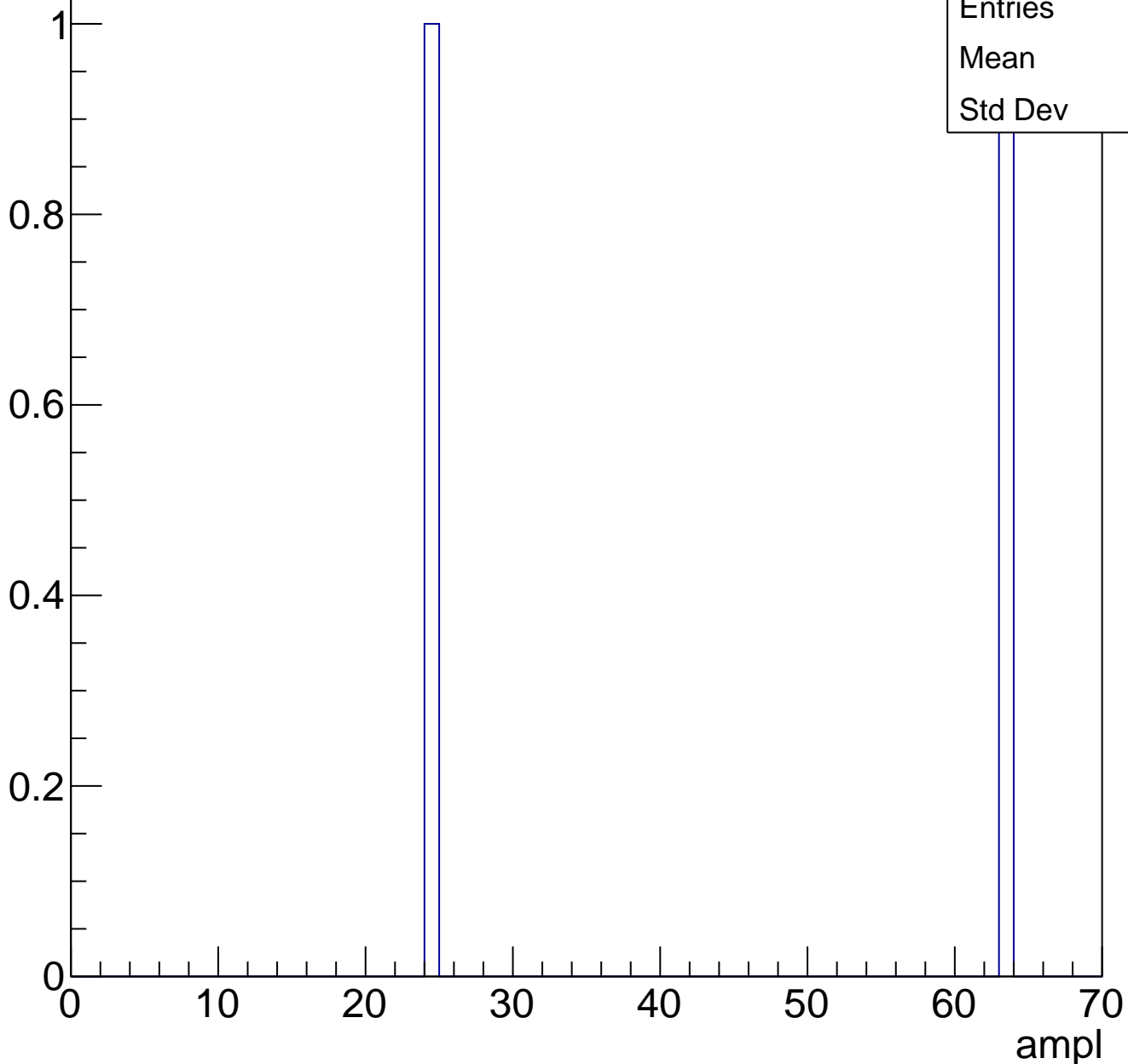




# B1L003S, U22-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	2
Mean	43.5
Std Dev	19.5

# B1L003S, U22-ch117, adc0

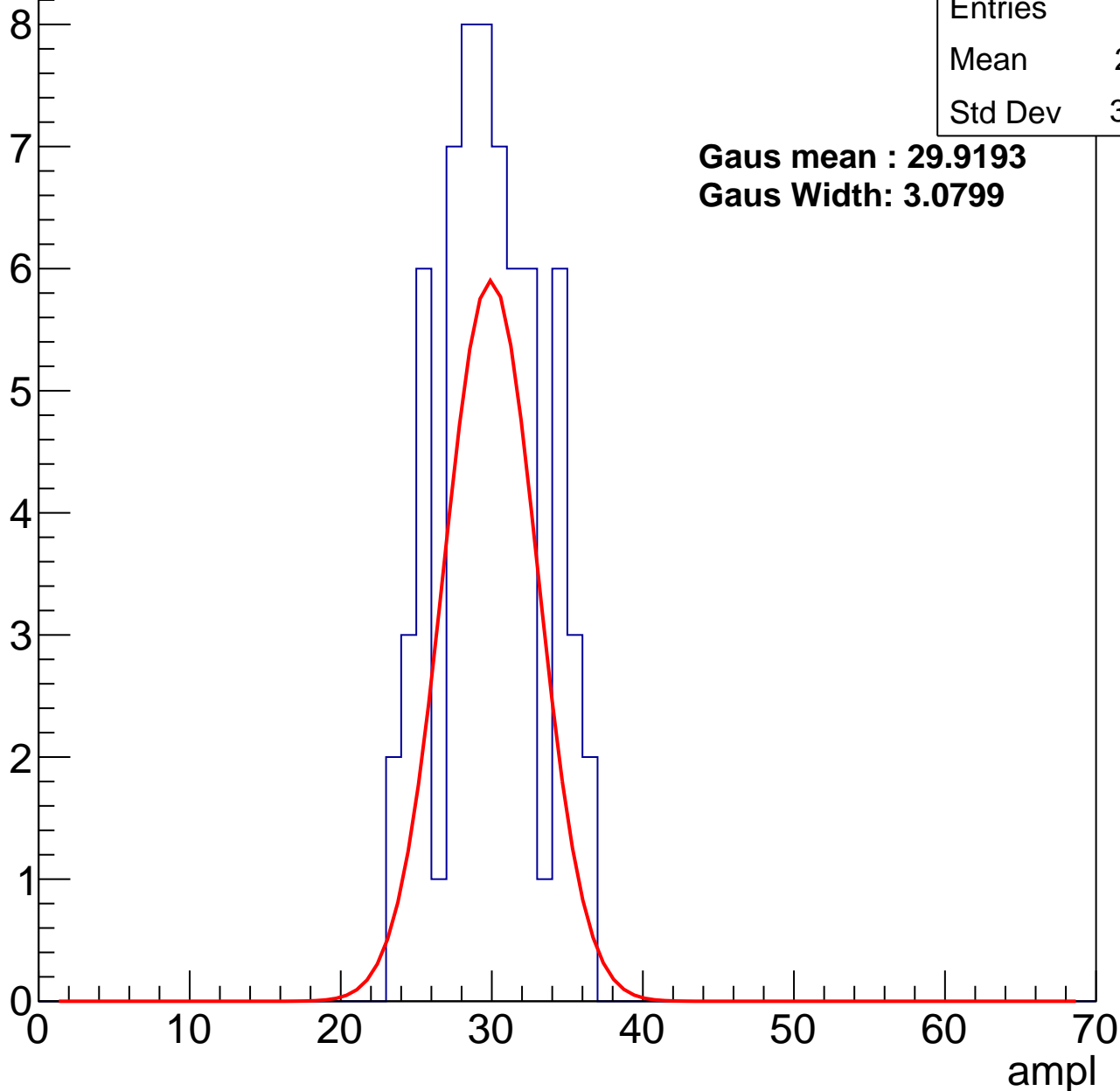
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.41
Std Dev	3.335

**Gaus mean : 29.9193**

**Gaus Width: 3.0799**



# B1L003S, U22-ch117, adc1

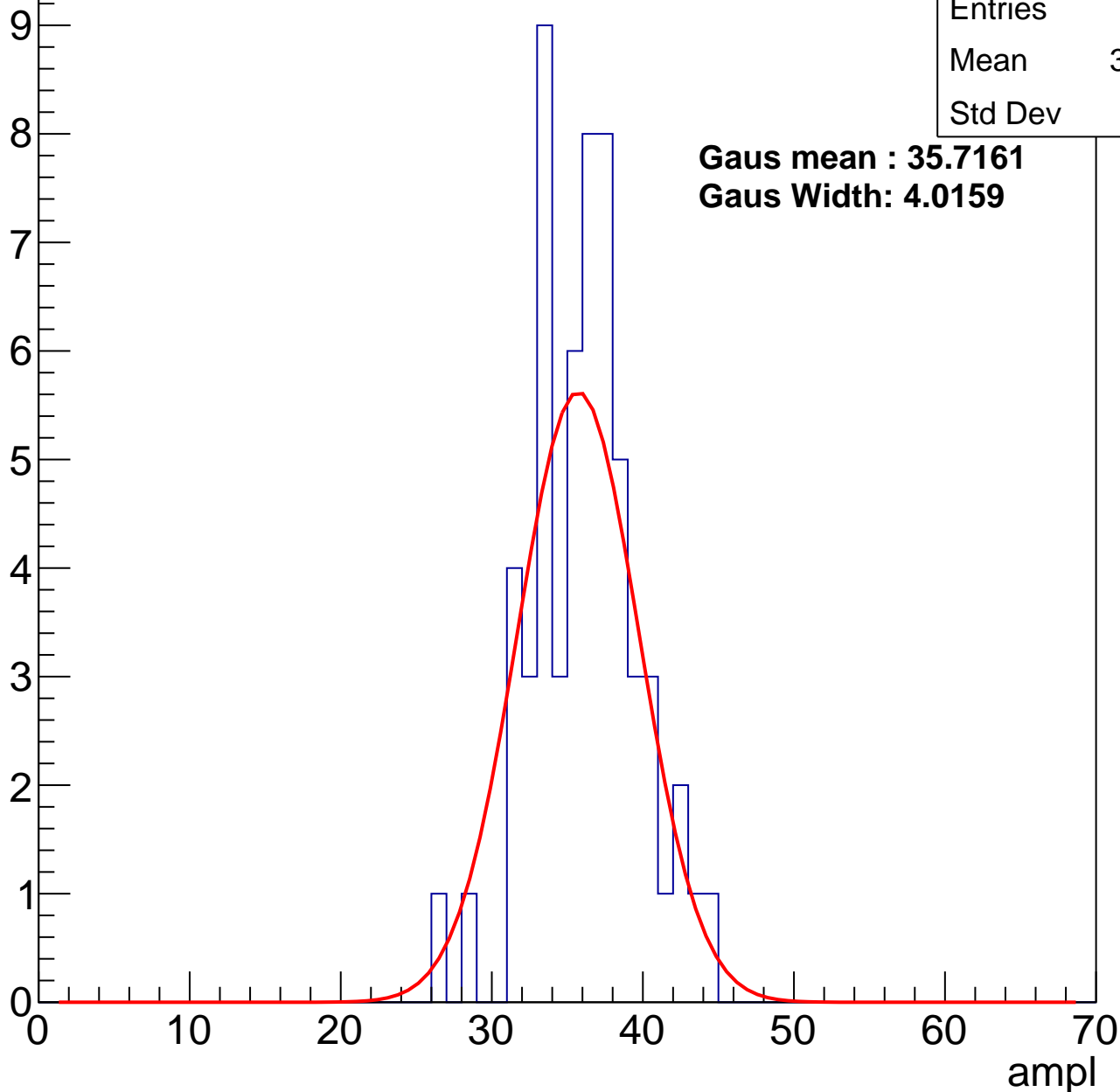
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.69
Std Dev	3.49

**Gaus mean : 35.7161**

**Gaus Width: 4.0159**



# B1L003S, U22-ch117, adc2

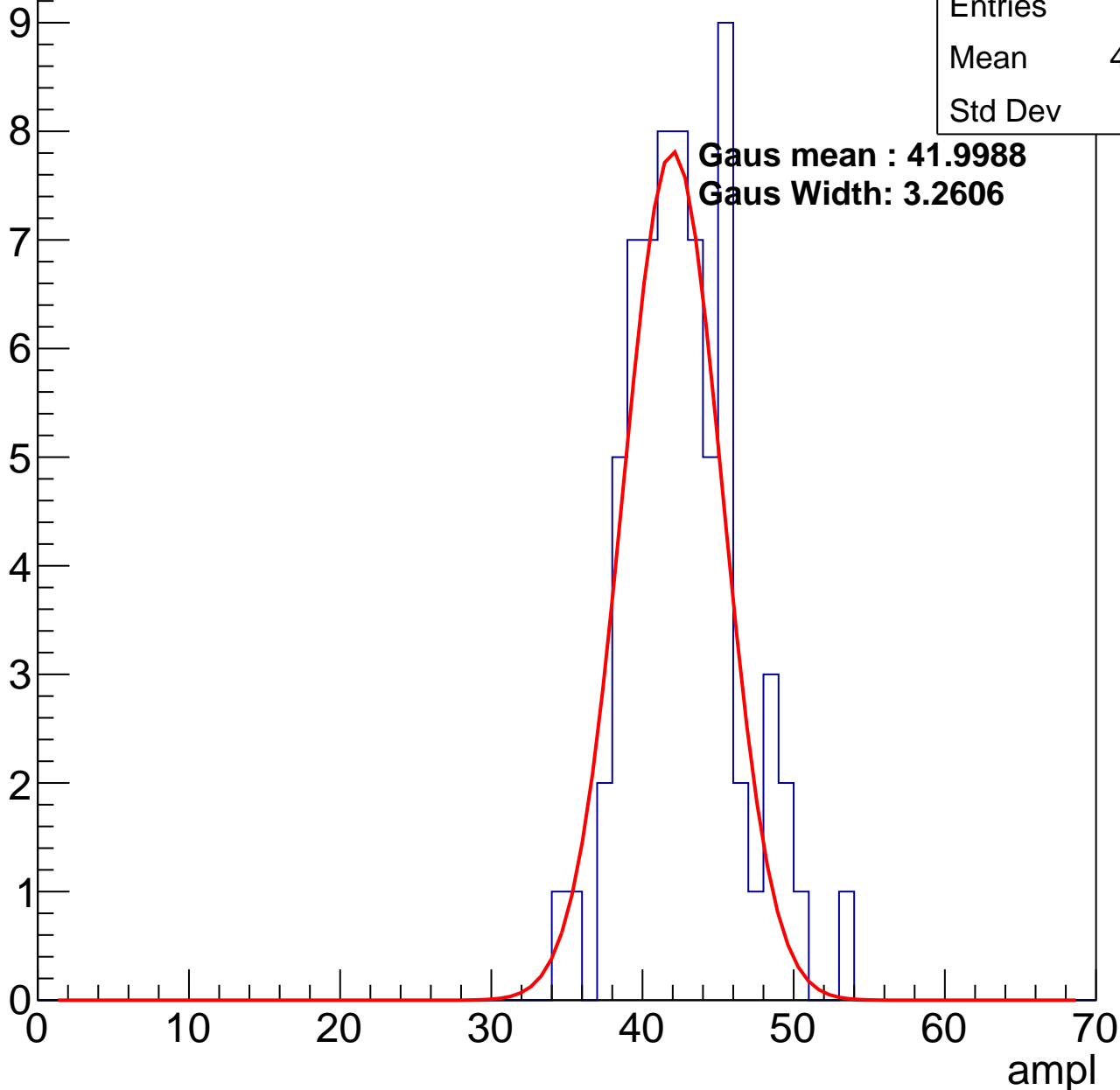
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	42.29
Std Dev	3.57

**Gaus mean : 41.9988**

**Gaus Width: 3.2606**

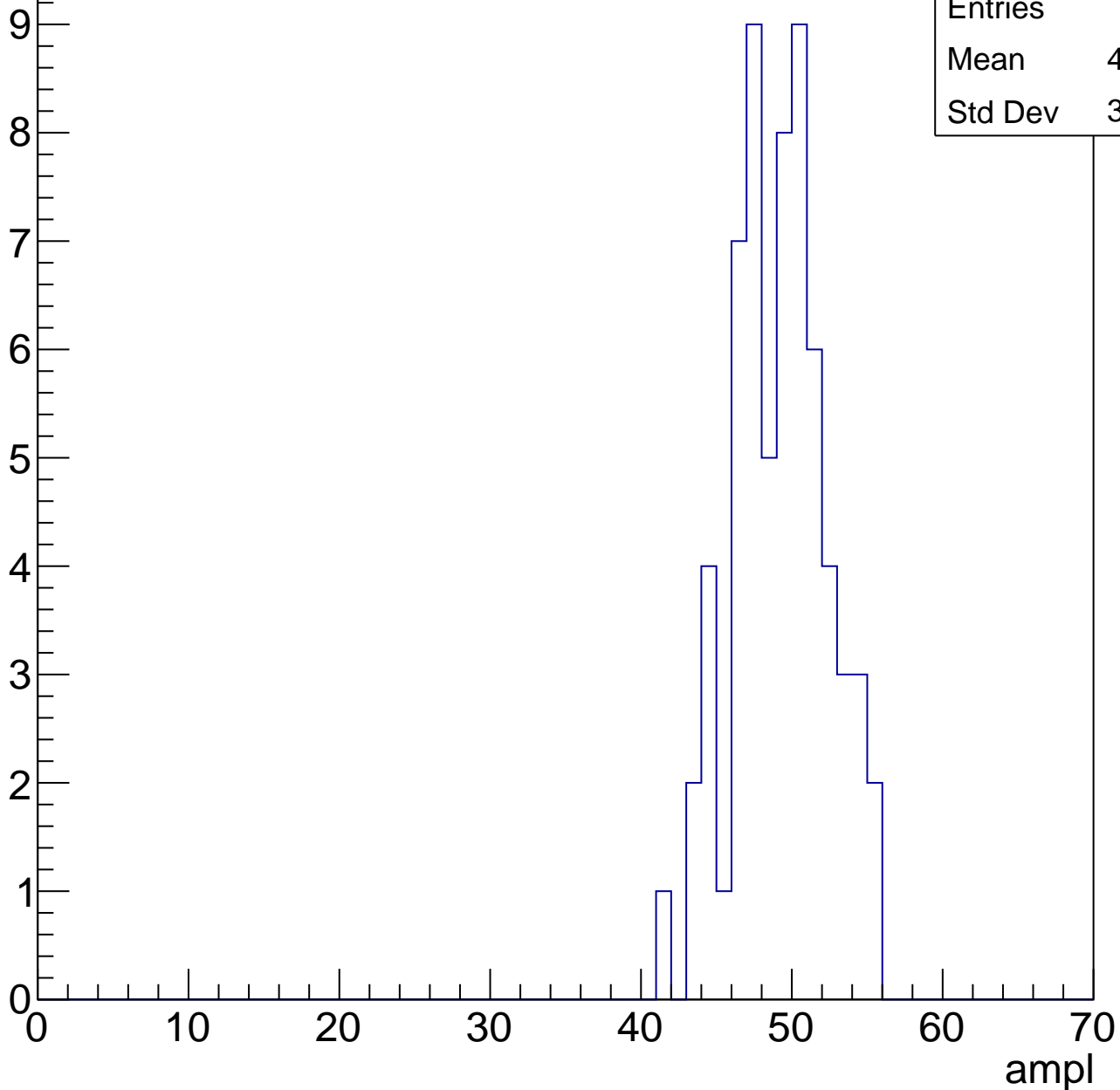


# B1L003S, U22-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.75
Std Dev	3.102

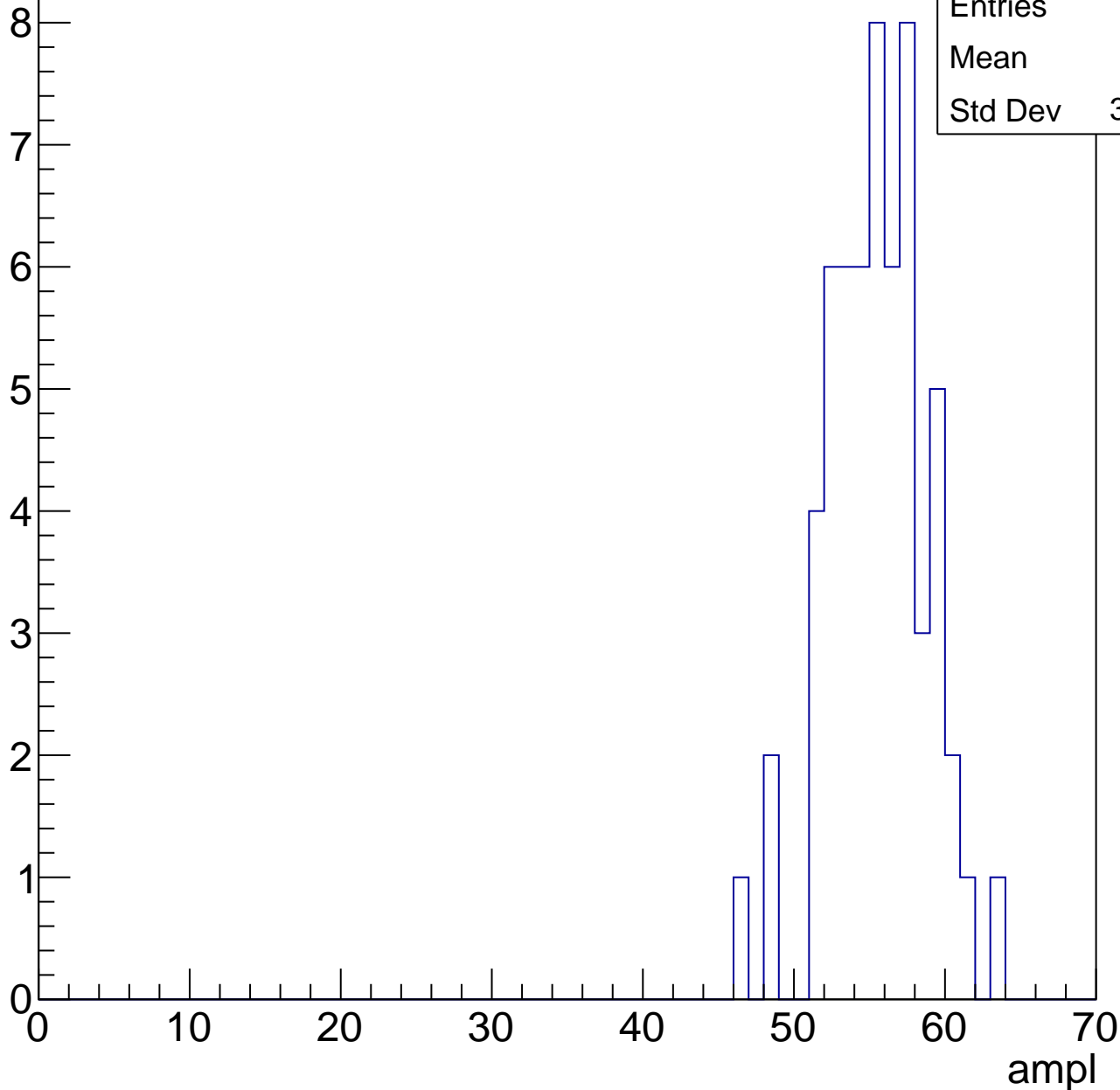


# B1L003S, U22-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55
Std Dev	3.247

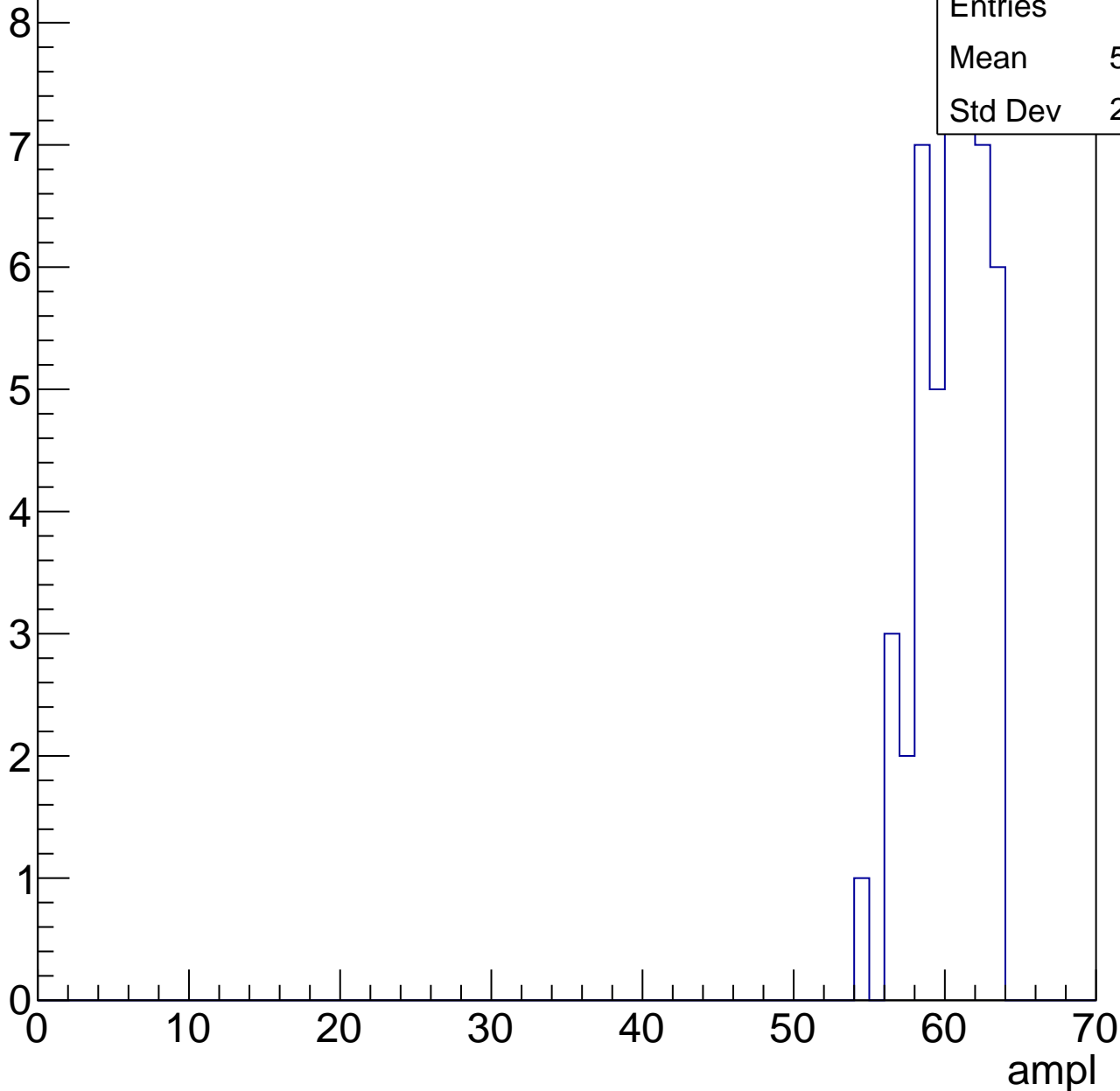


# B1L003S, U22-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	59.94
Std Dev	2.187



# B1L003S, U22-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

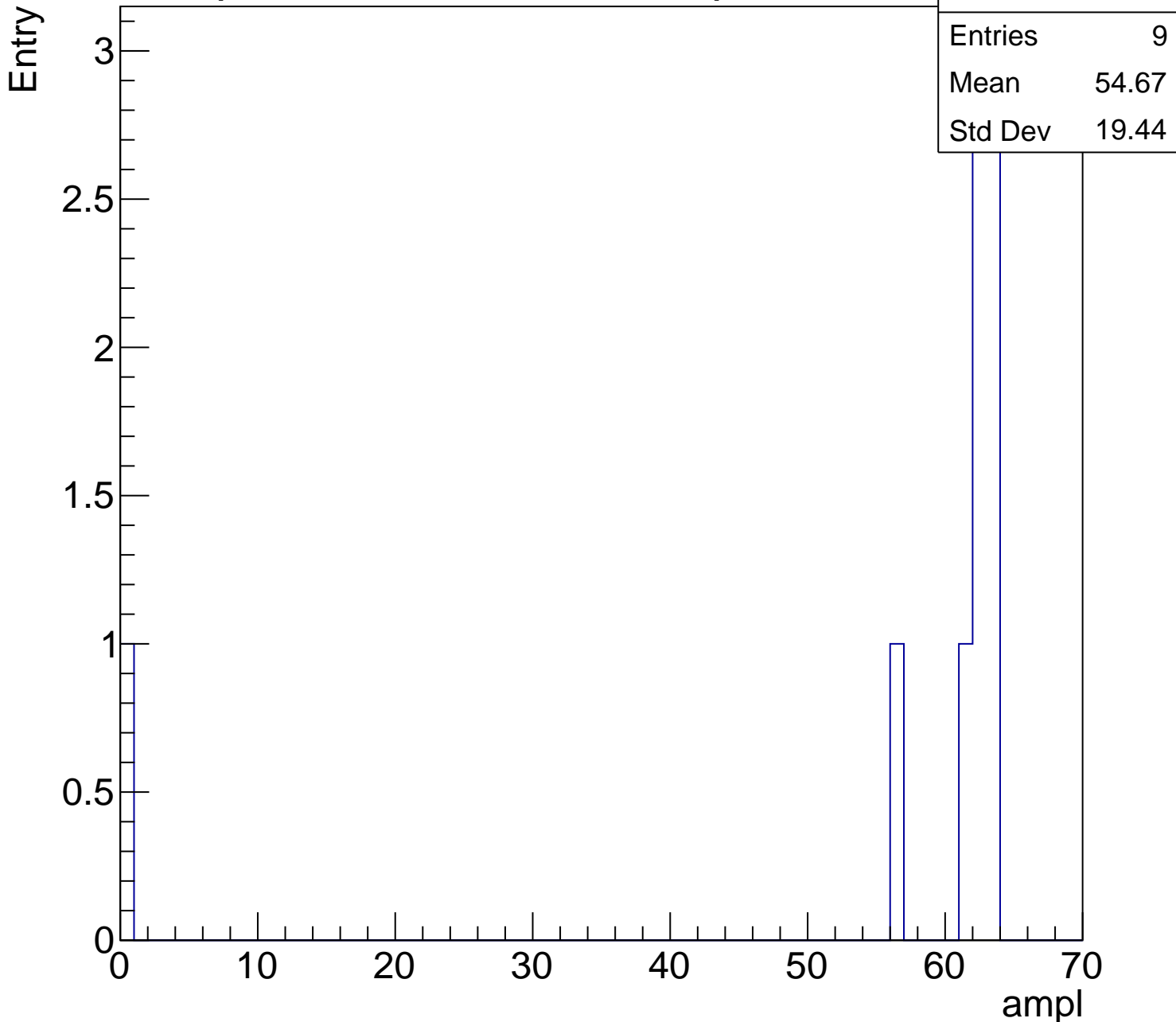
50

60

70

ampl

Entries	9
Mean	54.67
Std Dev	19.44

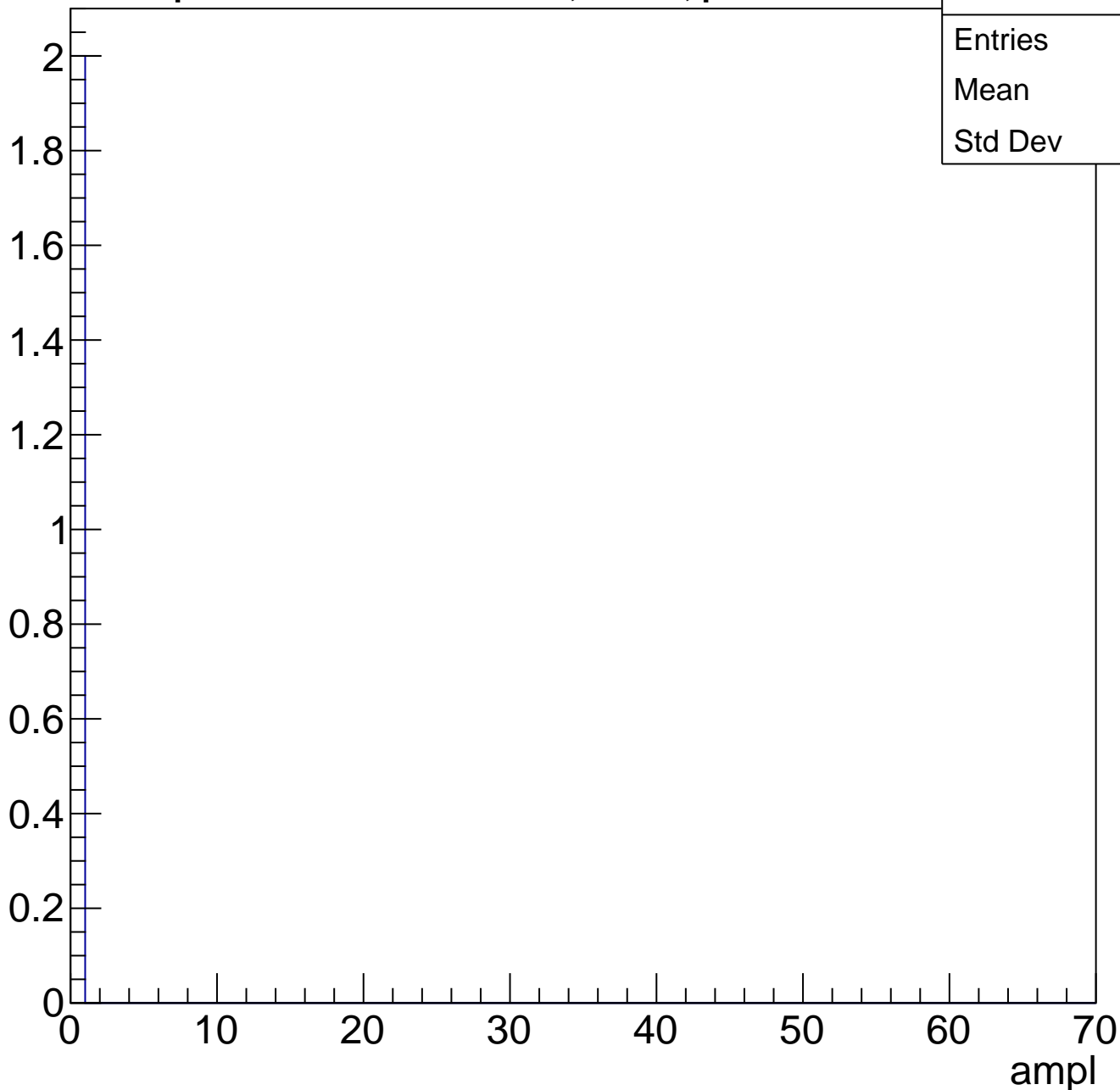




# B1L003S, U22-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch118, adc0

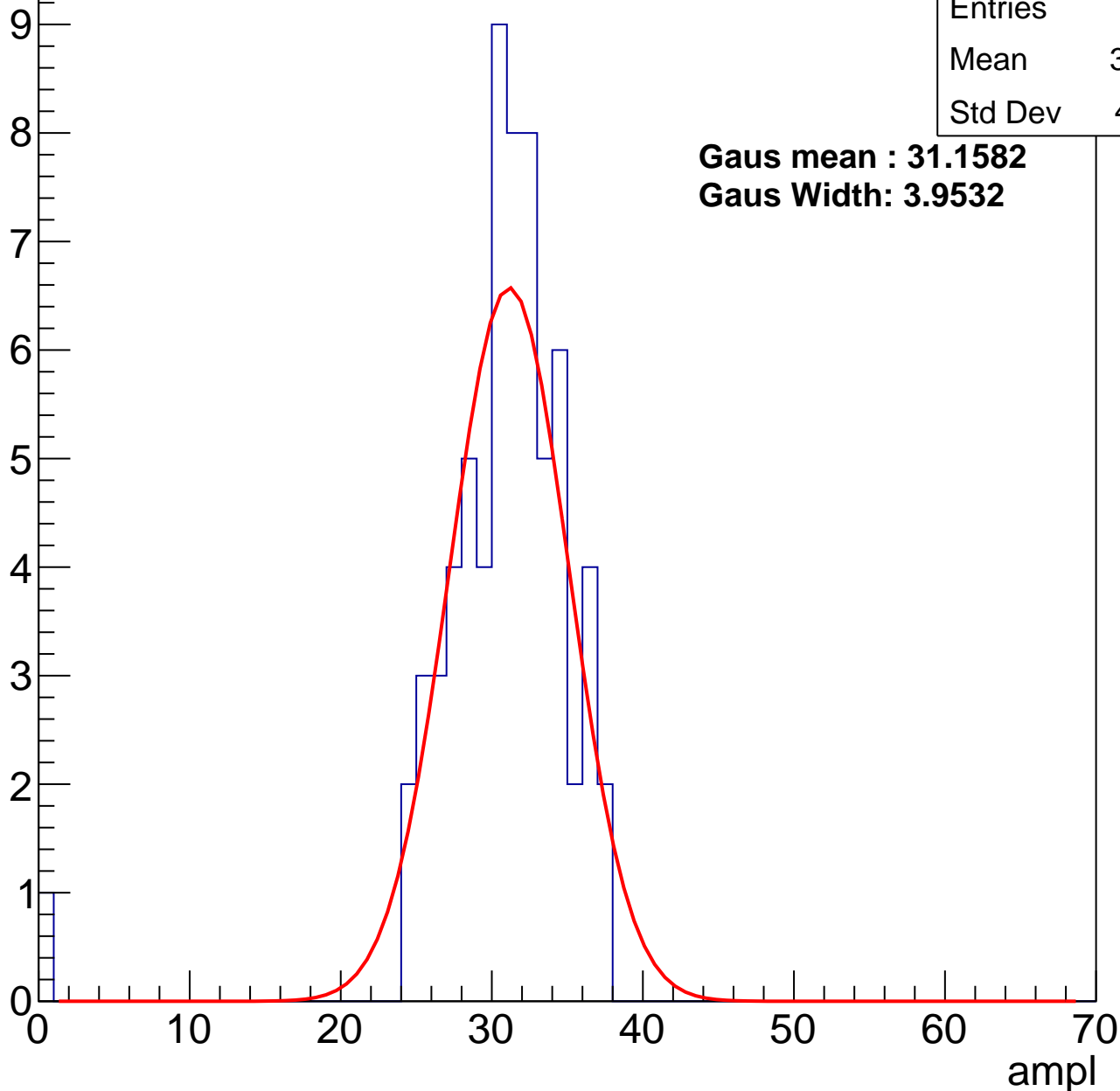
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.24
Std Dev	4.961

**Gaus mean : 31.1582**

**Gaus Width: 3.9532**



# B1L003S, U22-ch118, adc1

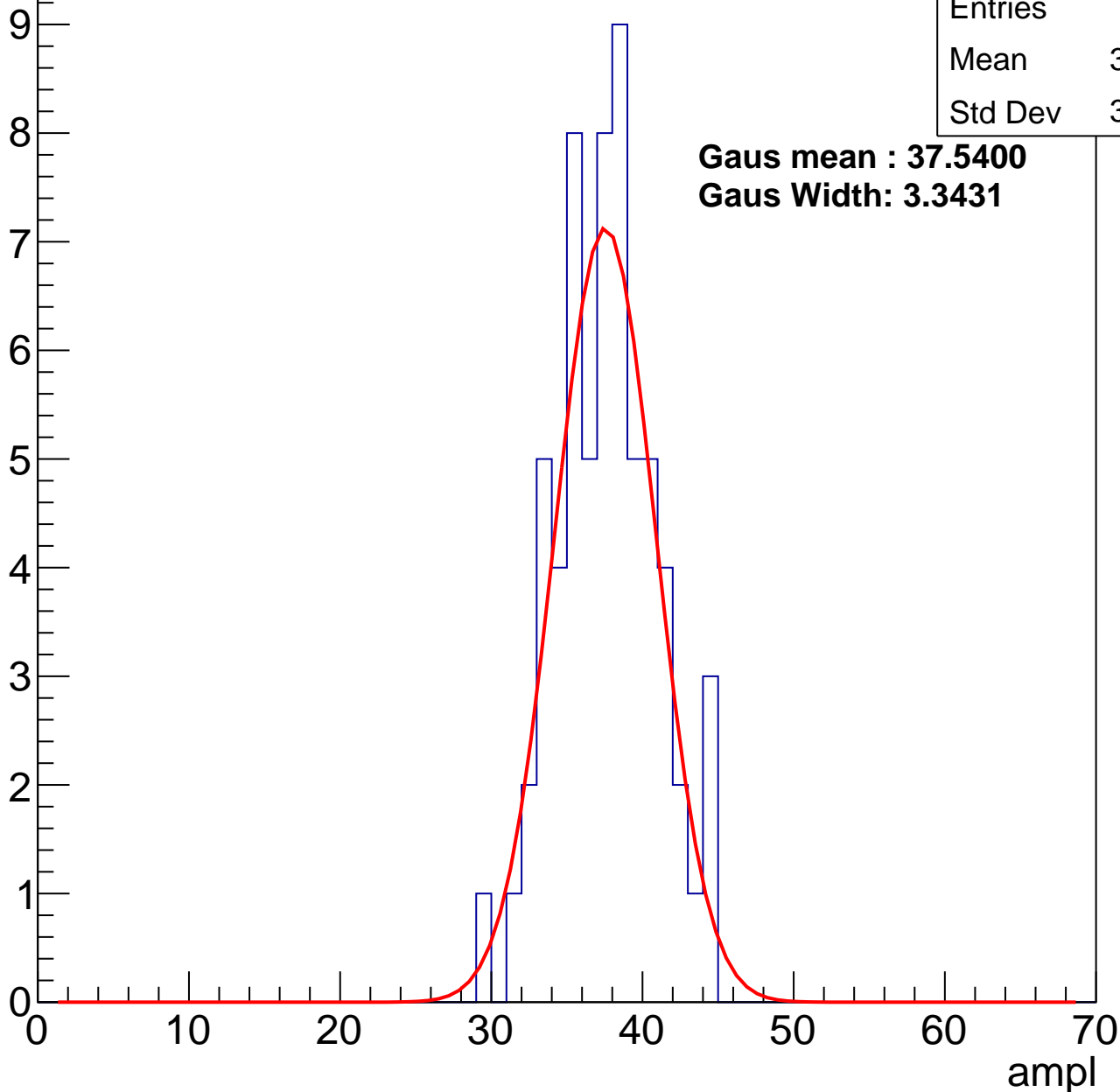
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	63
Mean	37.16
Std Dev	3.267

**Gaus mean : 37.5400**

**Gaus Width: 3.3431**



# B1L003S, U22-ch118, adc2

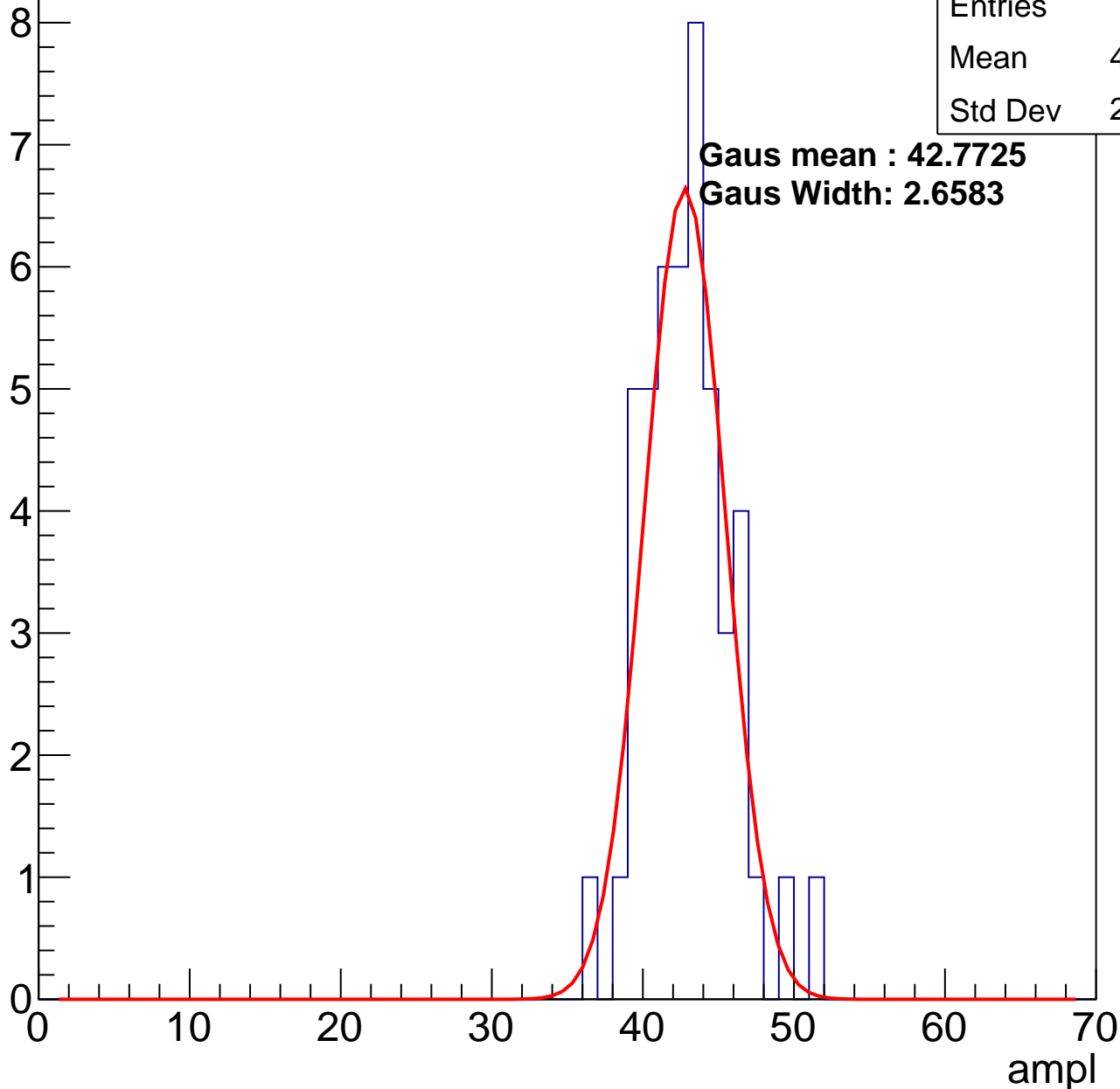
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	42.49
Std Dev	2.865

**Gaus mean : 42.7725**

**Gaus Width: 2.6583**

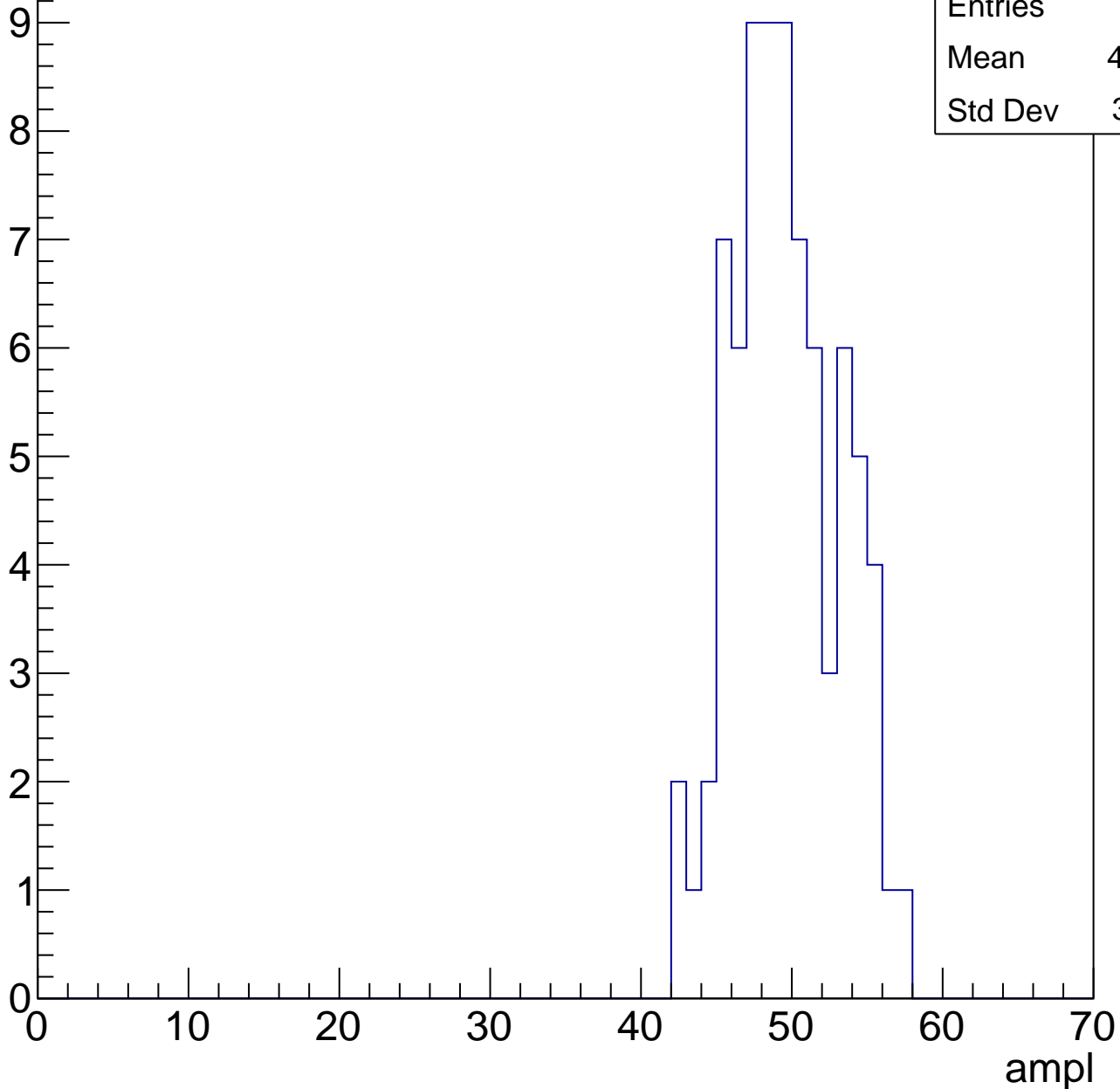


# B1L003S, U22-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	49.17
Std Dev	3.451

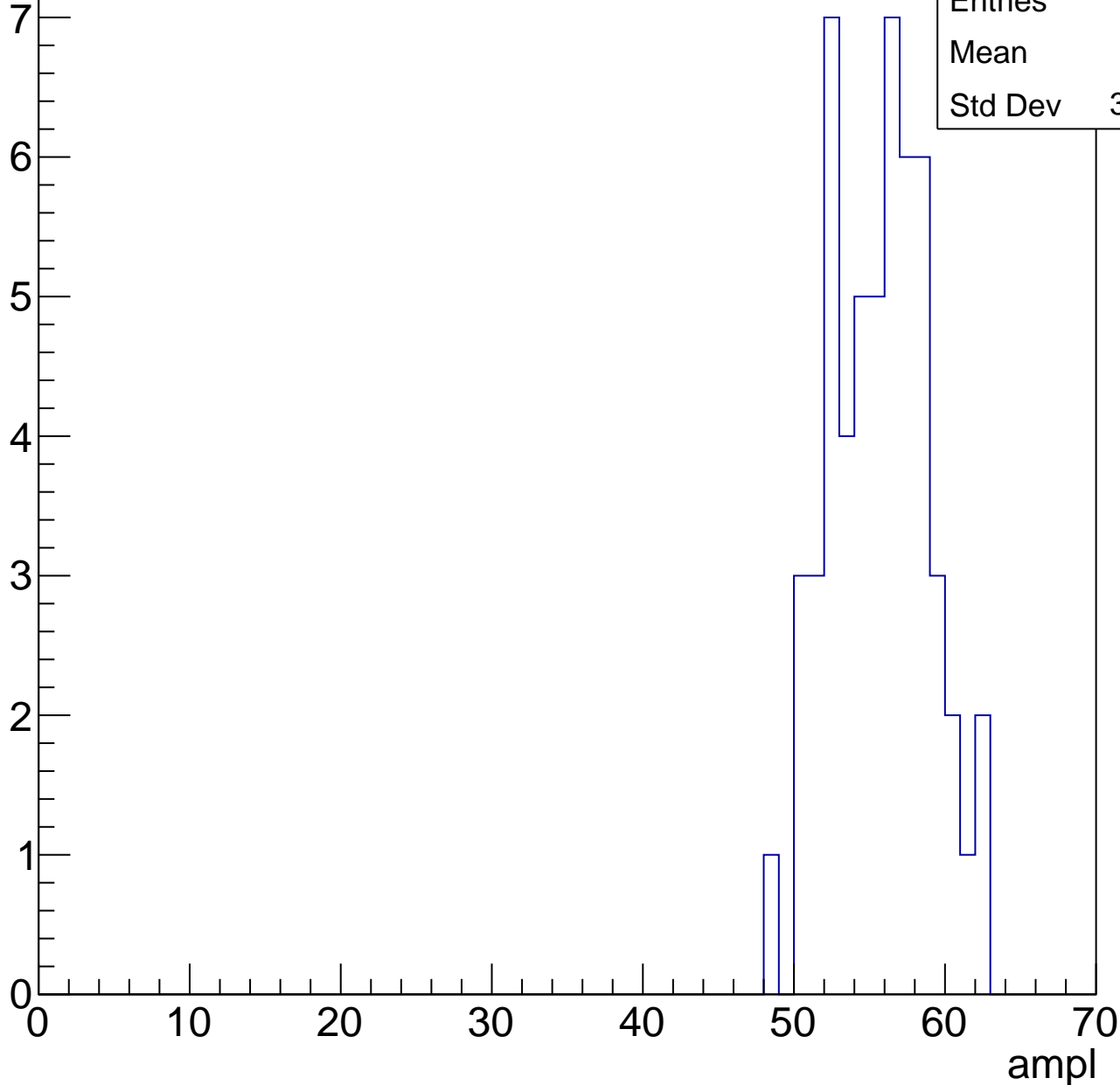


# B1L003S, U22-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.2
Std Dev	3.222



# B1L003S, U22-ch118, adc5

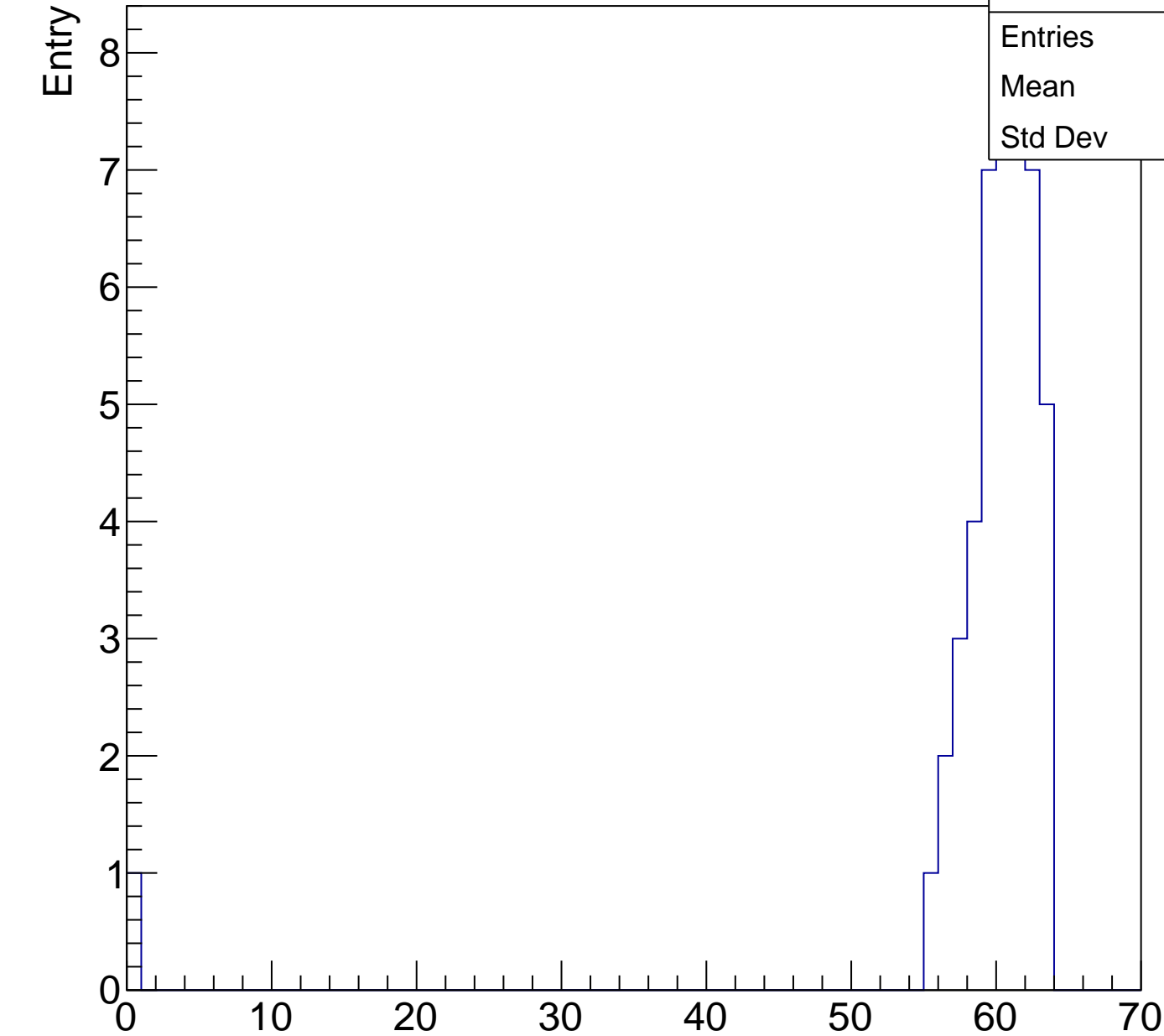
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	58.7
Std Dev	8.98

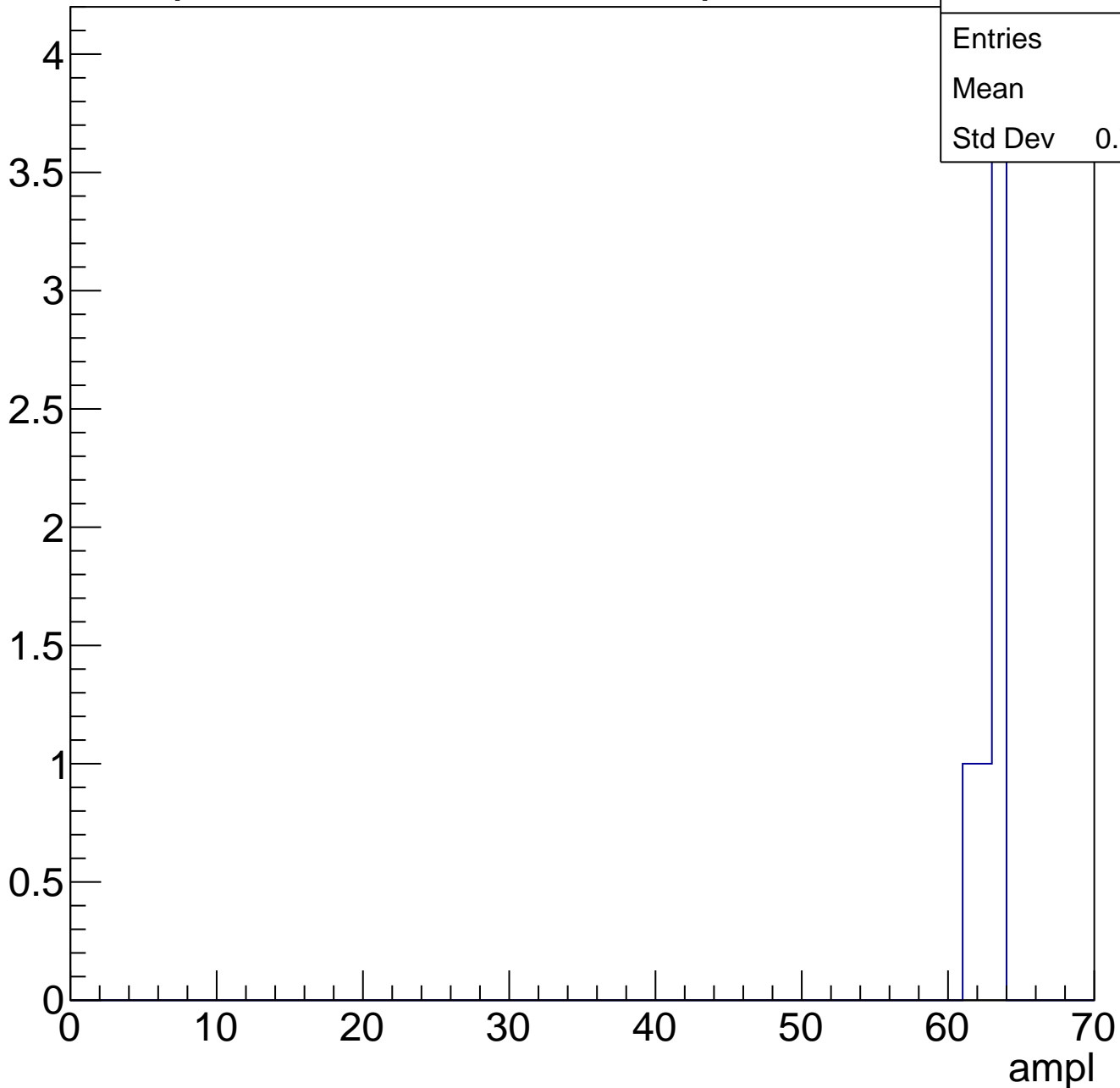
ampl



# B1L003S, U22-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	6
Mean	62.5
Std Dev	0.7638



# B1L003S, U22-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0

# B1L003S, U22-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	82
Mean	29.71
Std Dev	5.927

**Gaus mean : 31.0788**

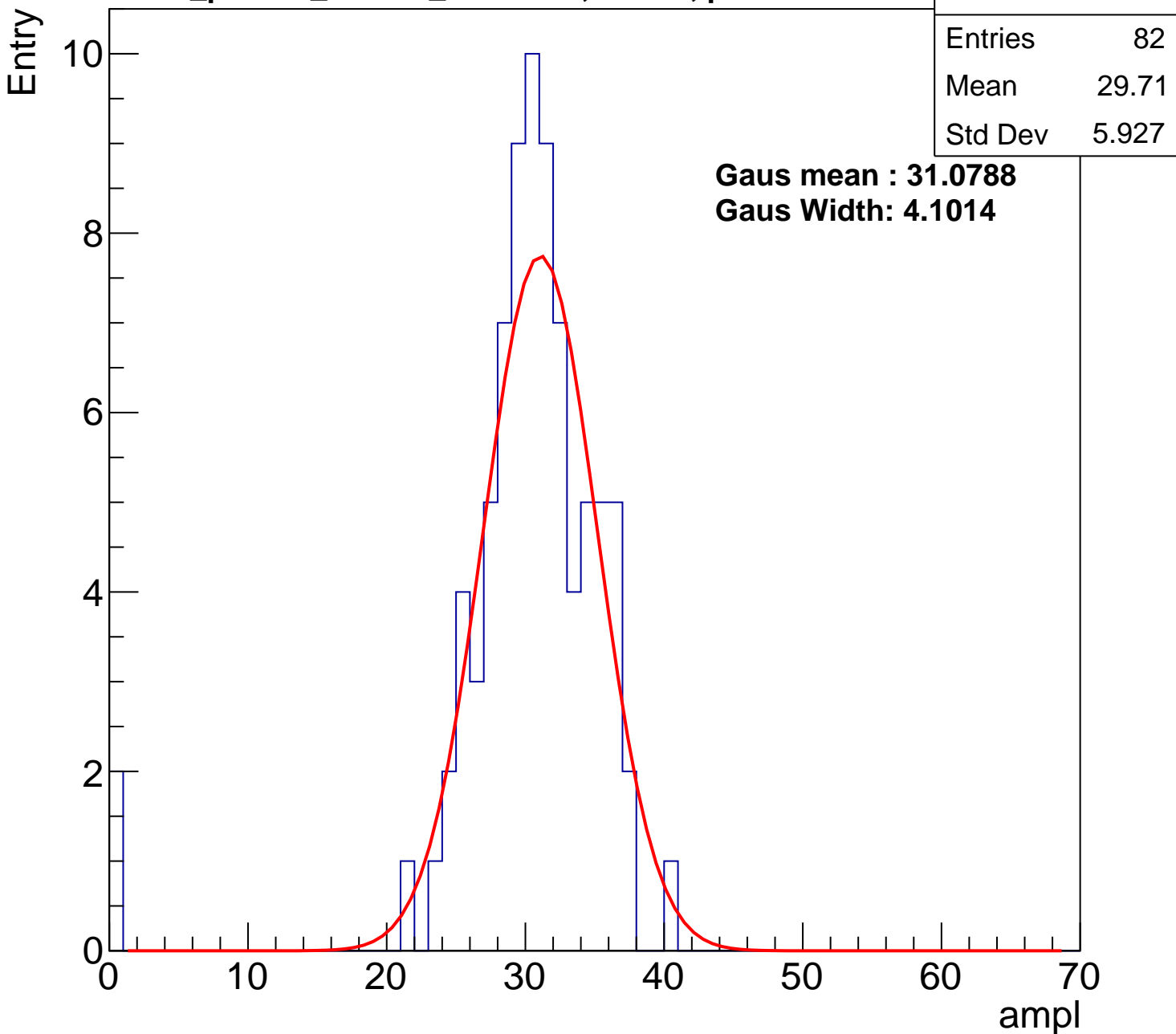
**Gaus Width: 4.1014**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch119, adc1

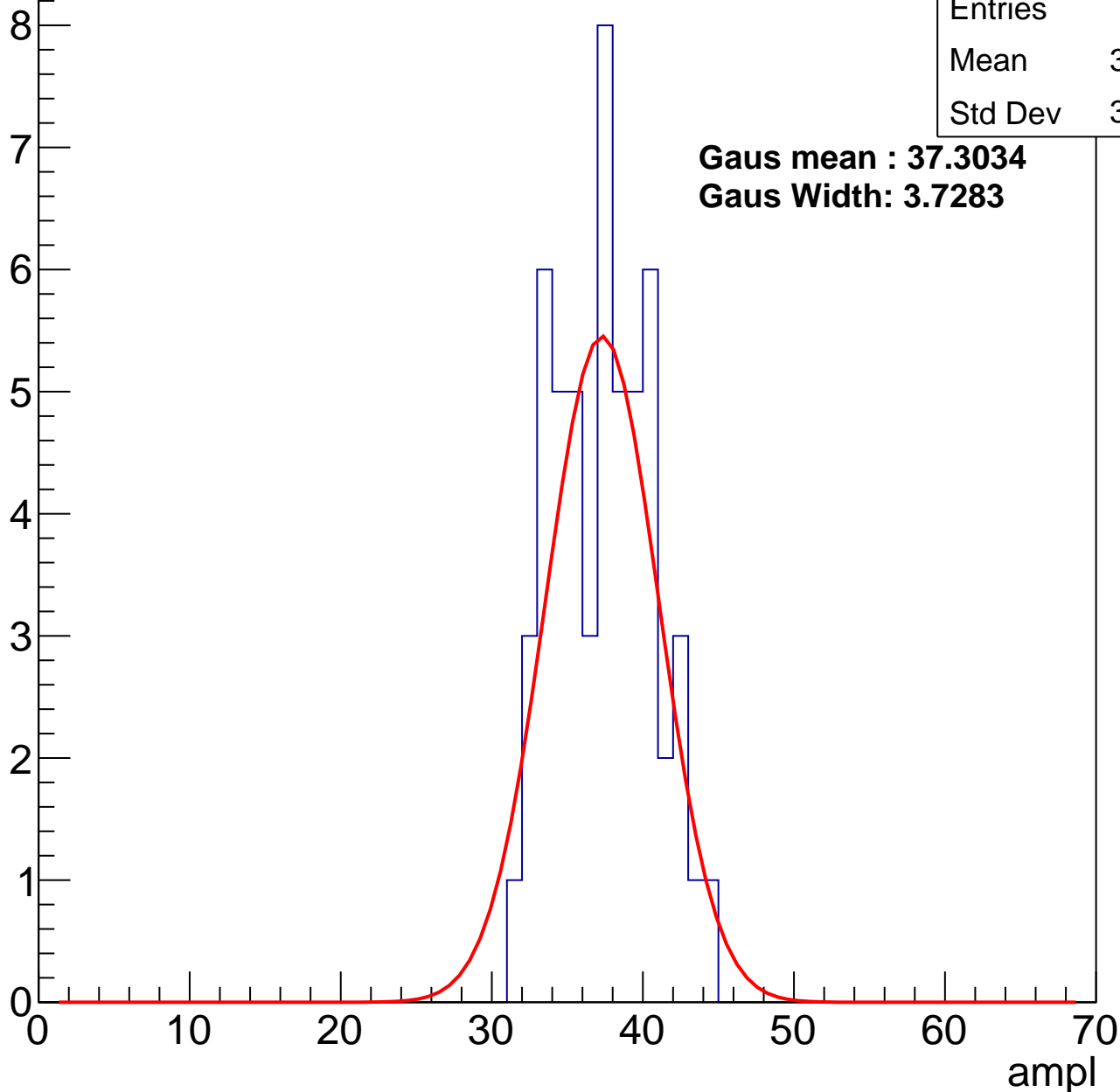
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.93
Std Dev	3.179

**Gaus mean : 37.3034**

**Gaus Width: 3.7283**



# B1L003S, U22-ch119, adc2

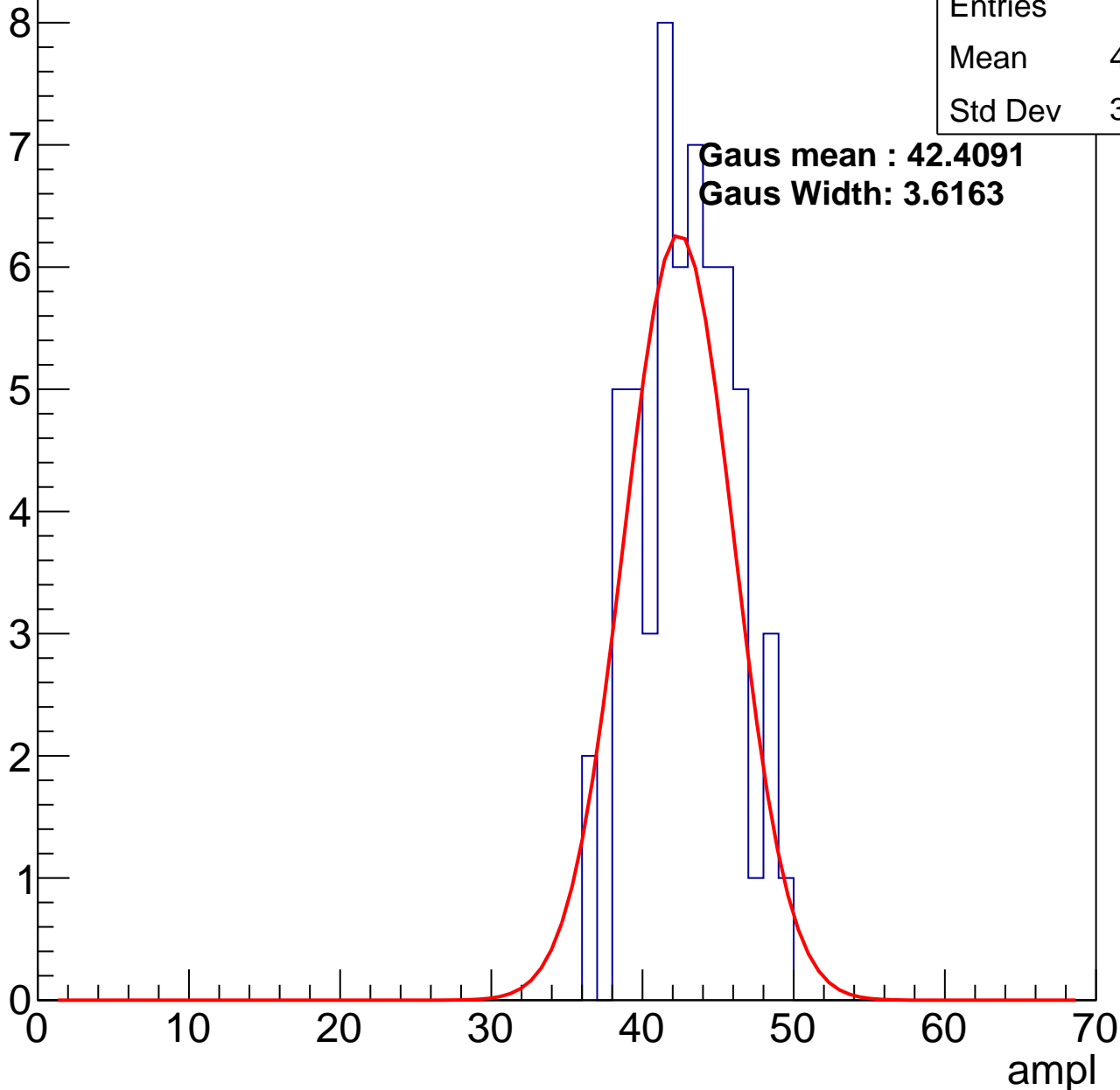
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.45
Std Dev	3.086

**Gaus mean : 42.4091**

**Gaus Width: 3.6163**

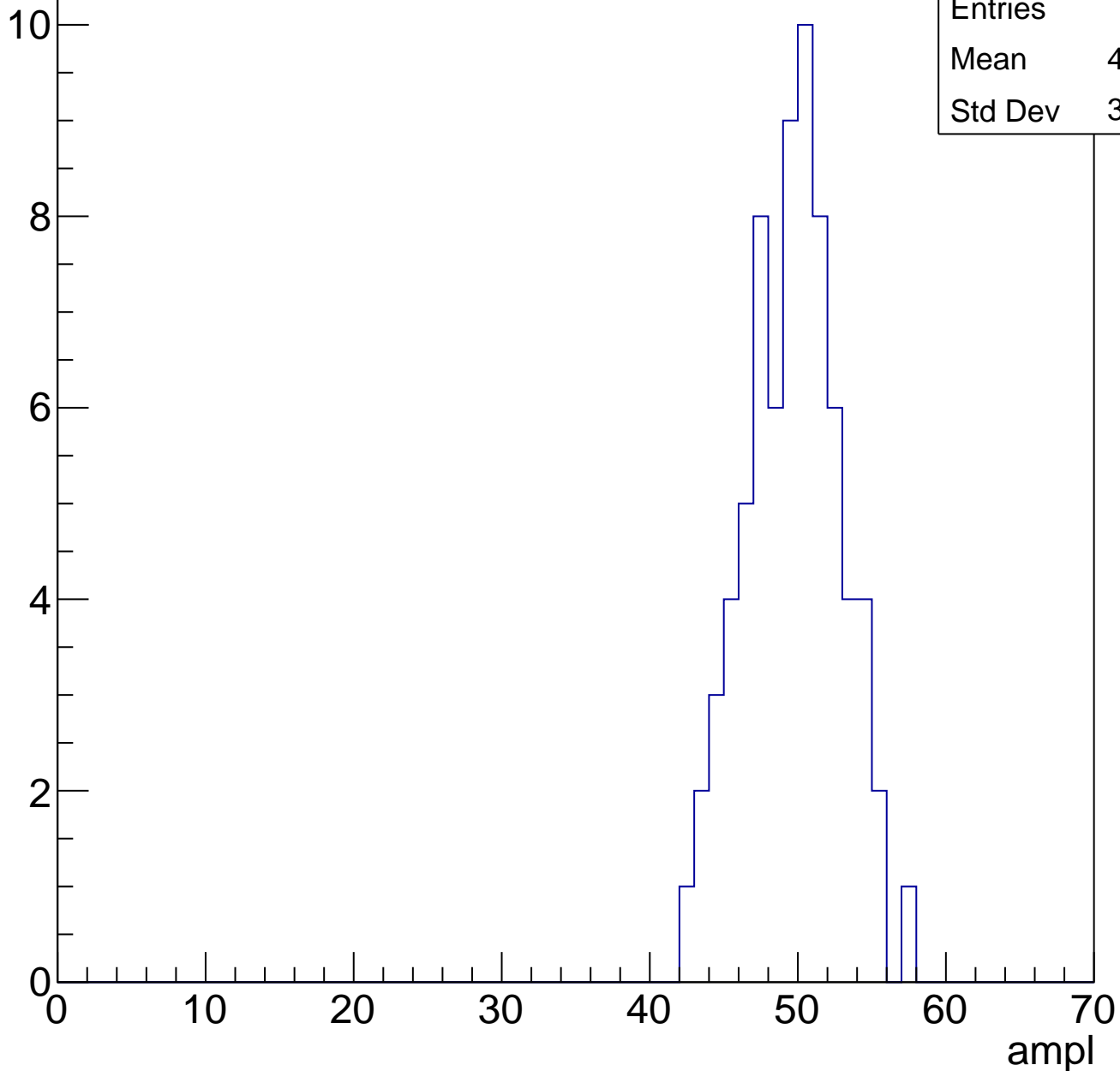


# B1L003S, U22-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	73
Mean	49.18
Std Dev	3.177

Entry

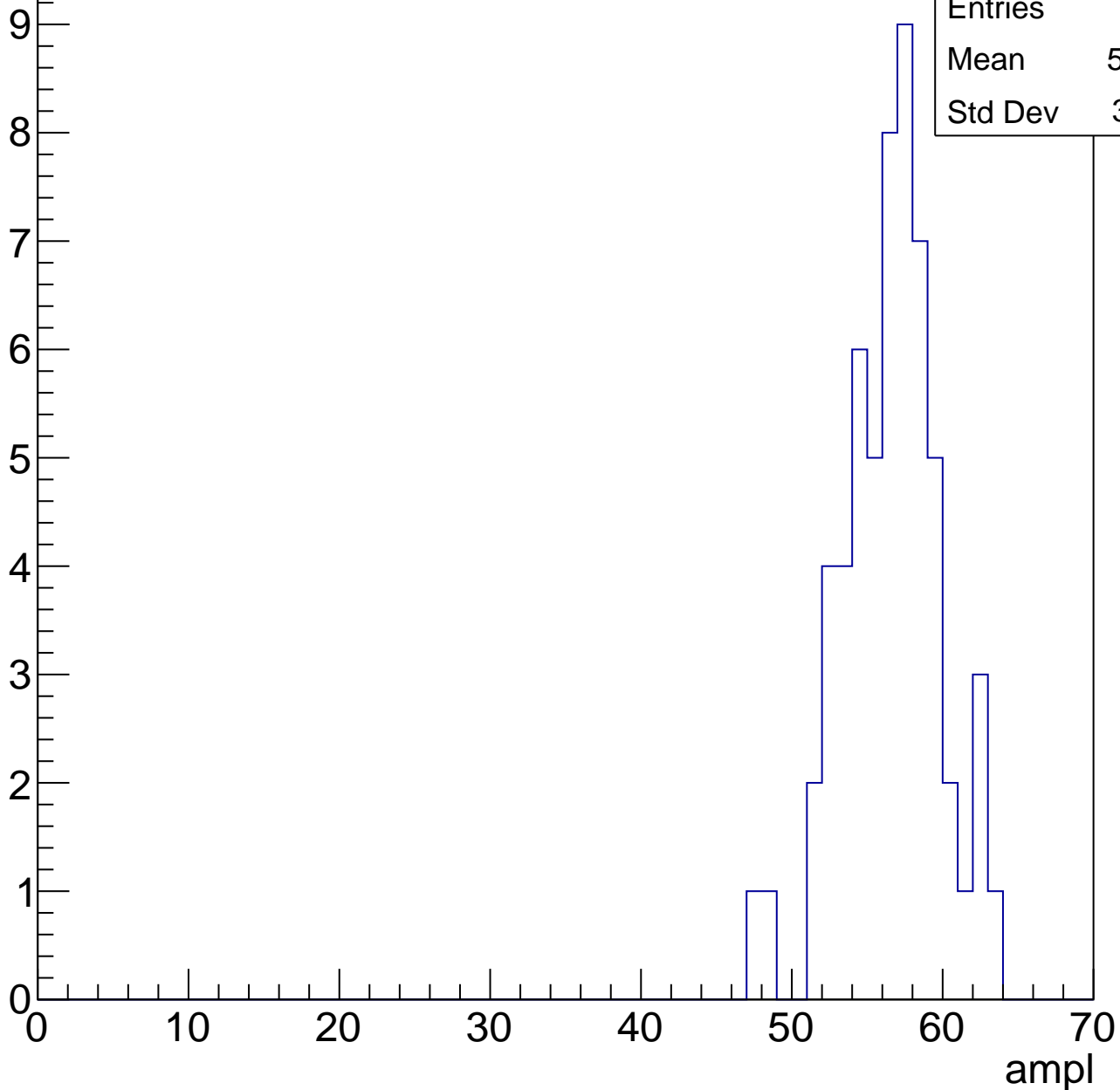


# B1L003S, U22-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.07
Std Dev	3.241

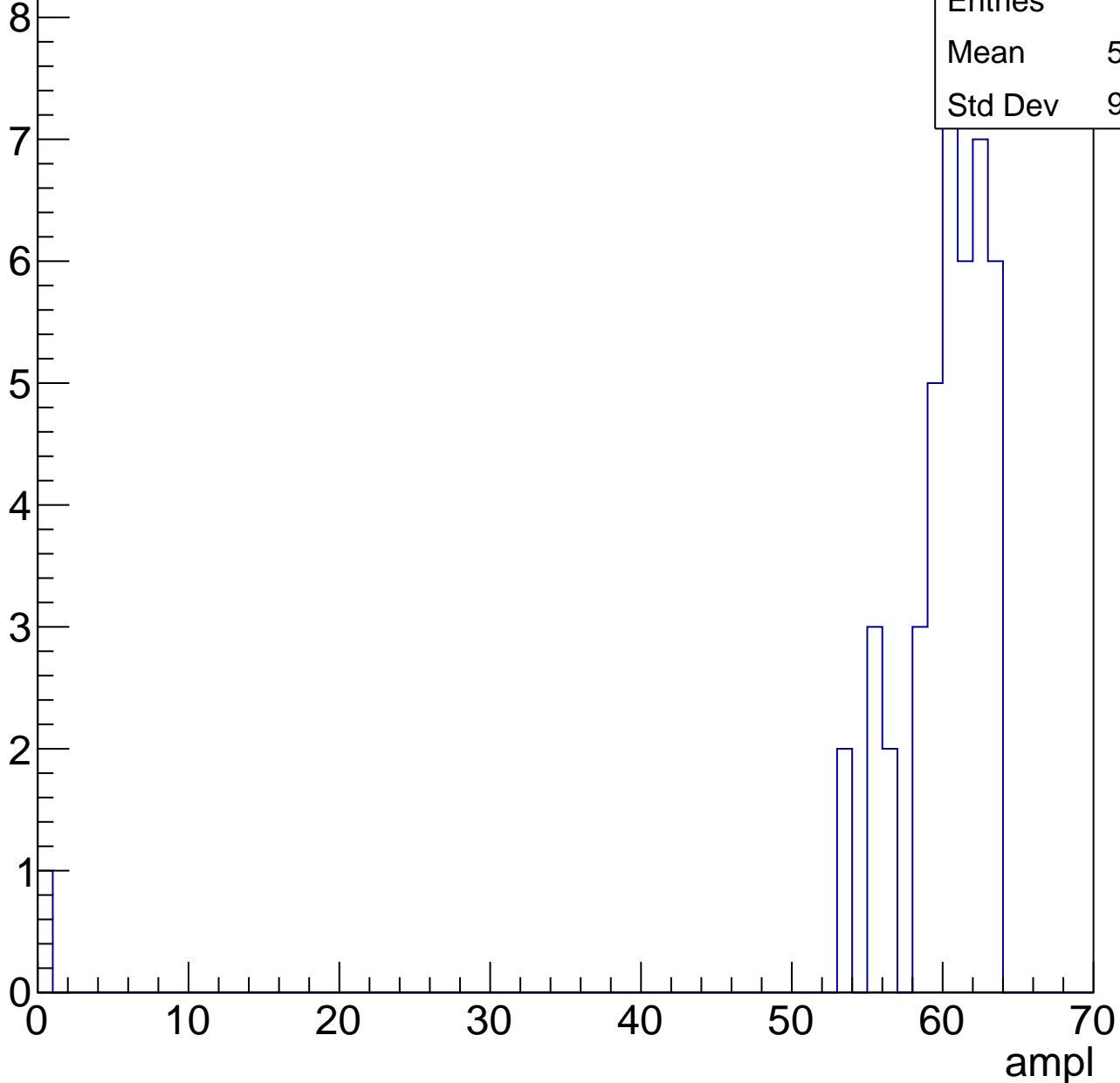


# B1L003S, U22-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

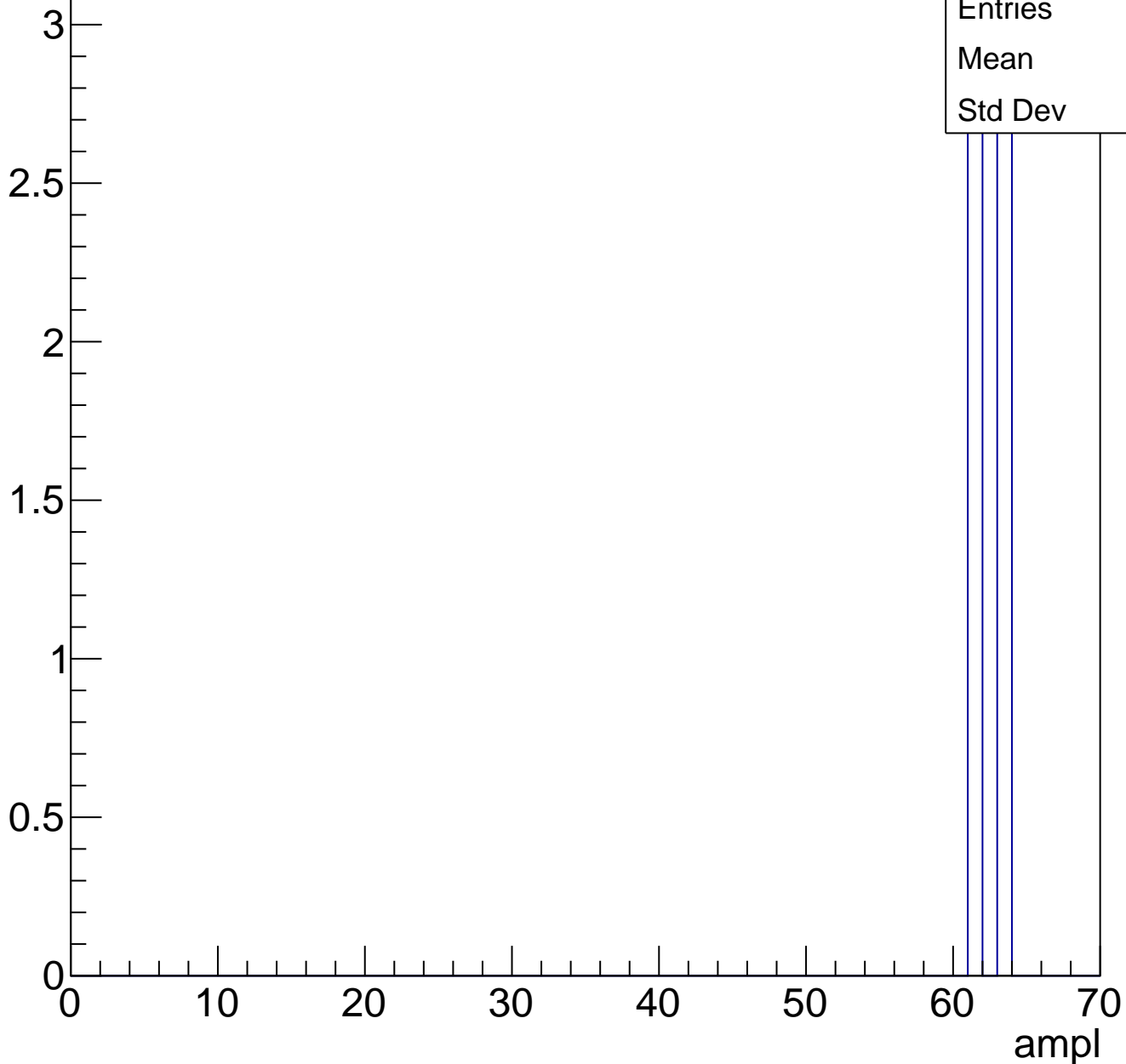
Entries	43
Mean	58.37
Std Dev	9.396



# B1L003S, U22-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

# B1L003S, U22-ch120, adc0

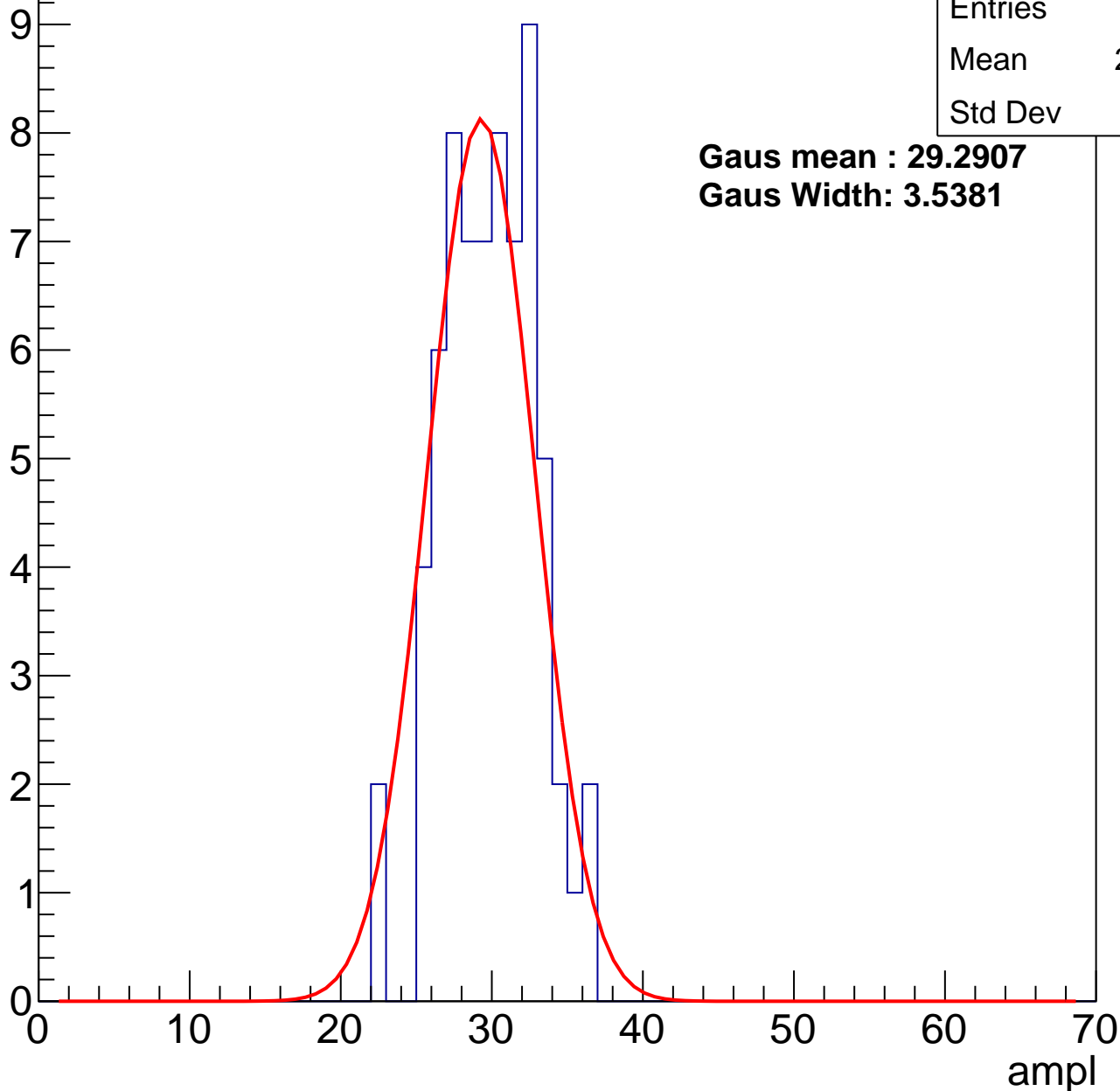
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.41
Std Dev	3.03

**Gaus mean : 29.2907**

**Gaus Width: 3.5381**



# B1L003S, U22-ch120, adc1

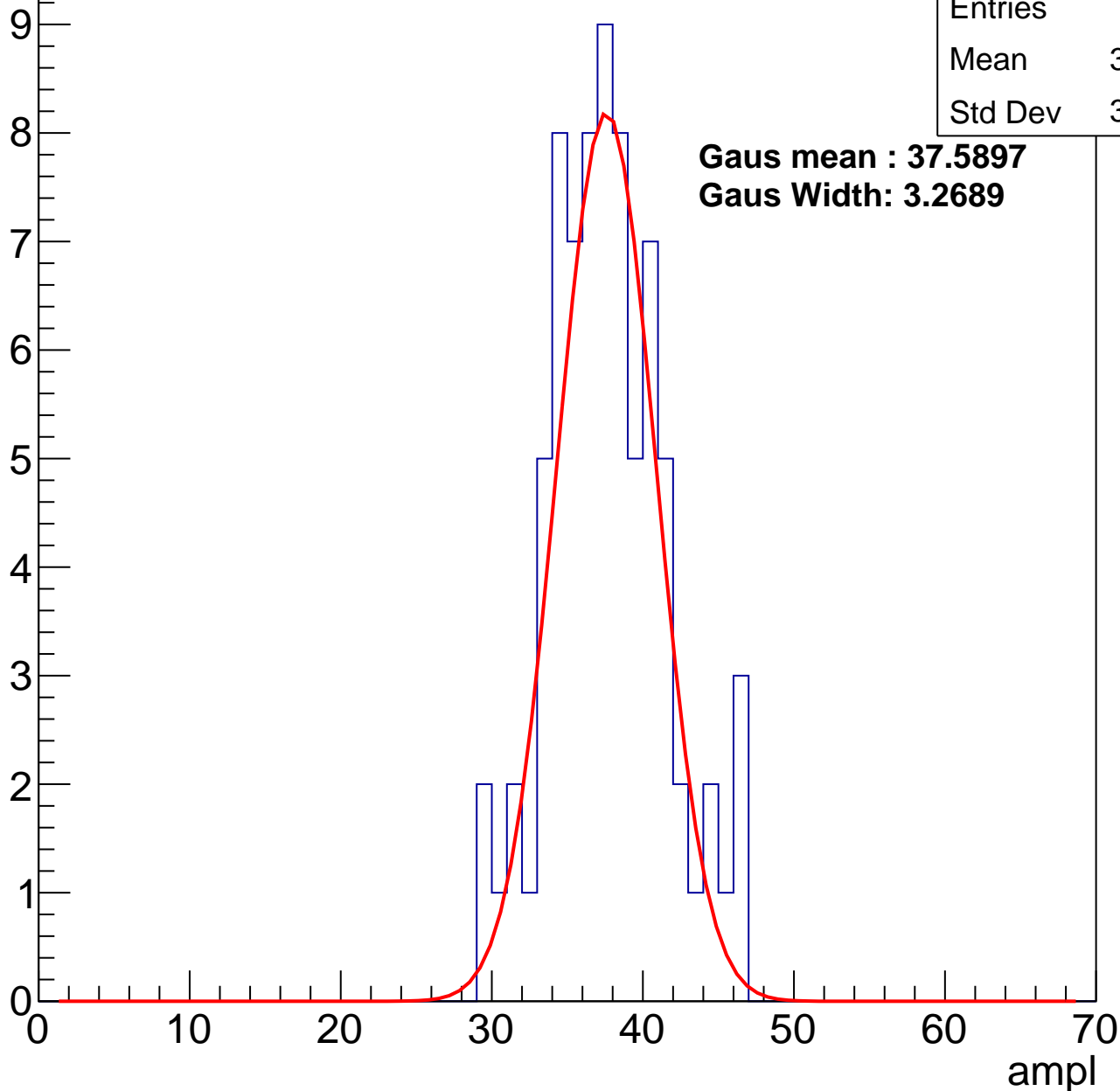
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	37.23
Std Dev	3.813

**Gaus mean : 37.5897**

**Gaus Width: 3.2689**



# B1L003S, U22-ch120, adc2

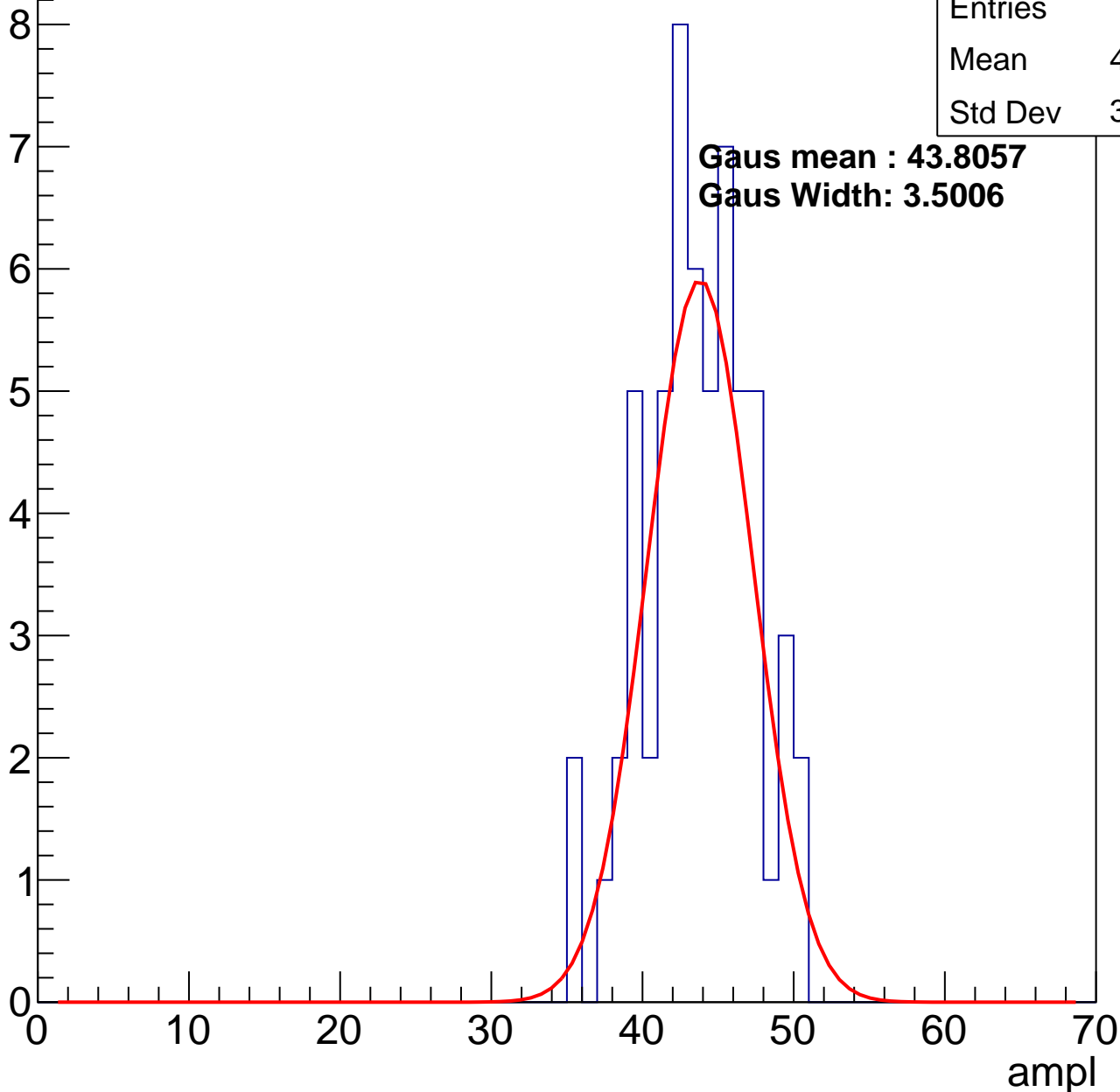
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.25
Std Dev	3.506

**Gaus mean : 43.8057**

**Gaus Width: 3.5006**

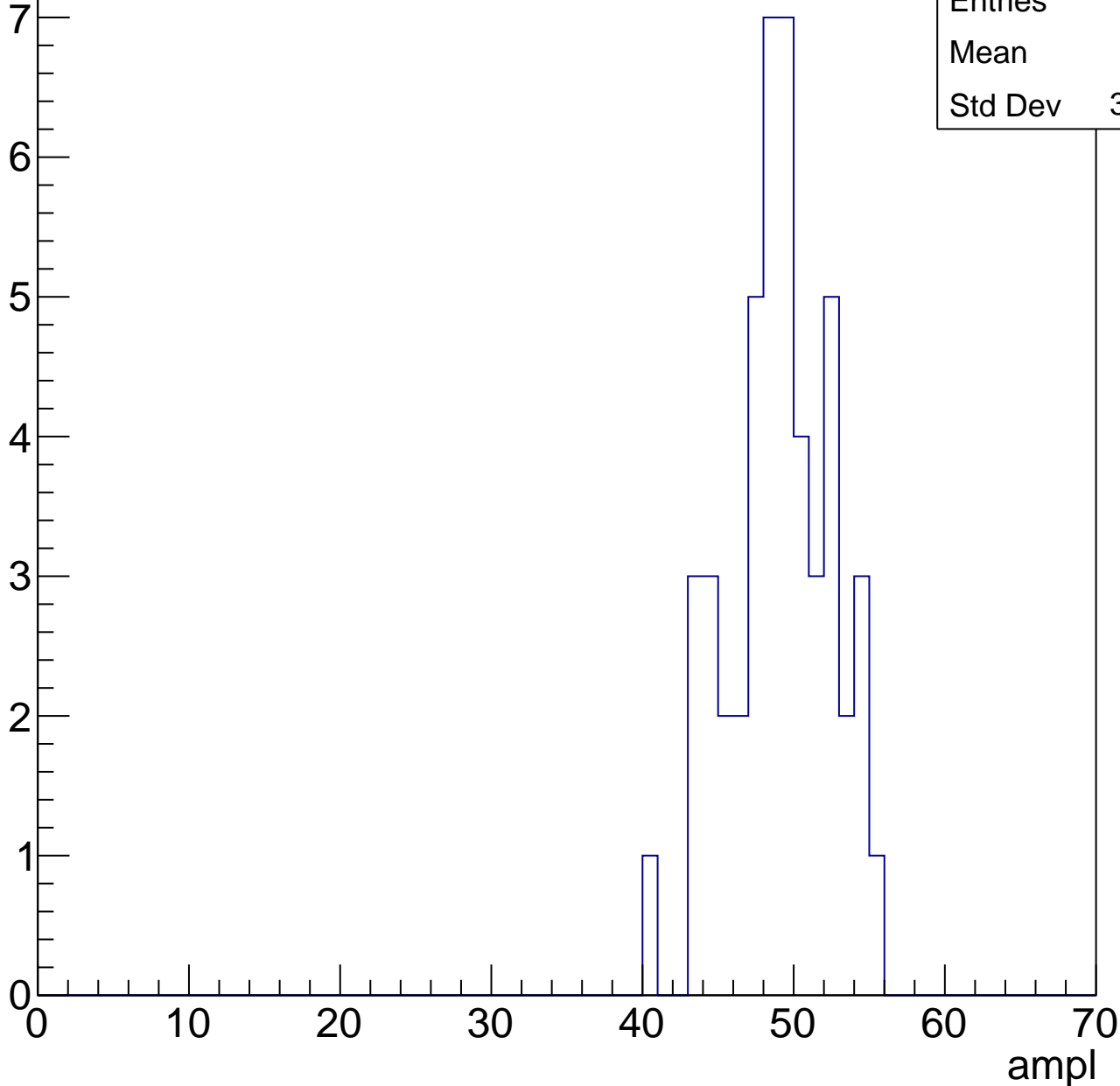


# B1L003S, U22-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	48
Mean	48.6
Std Dev	3.346

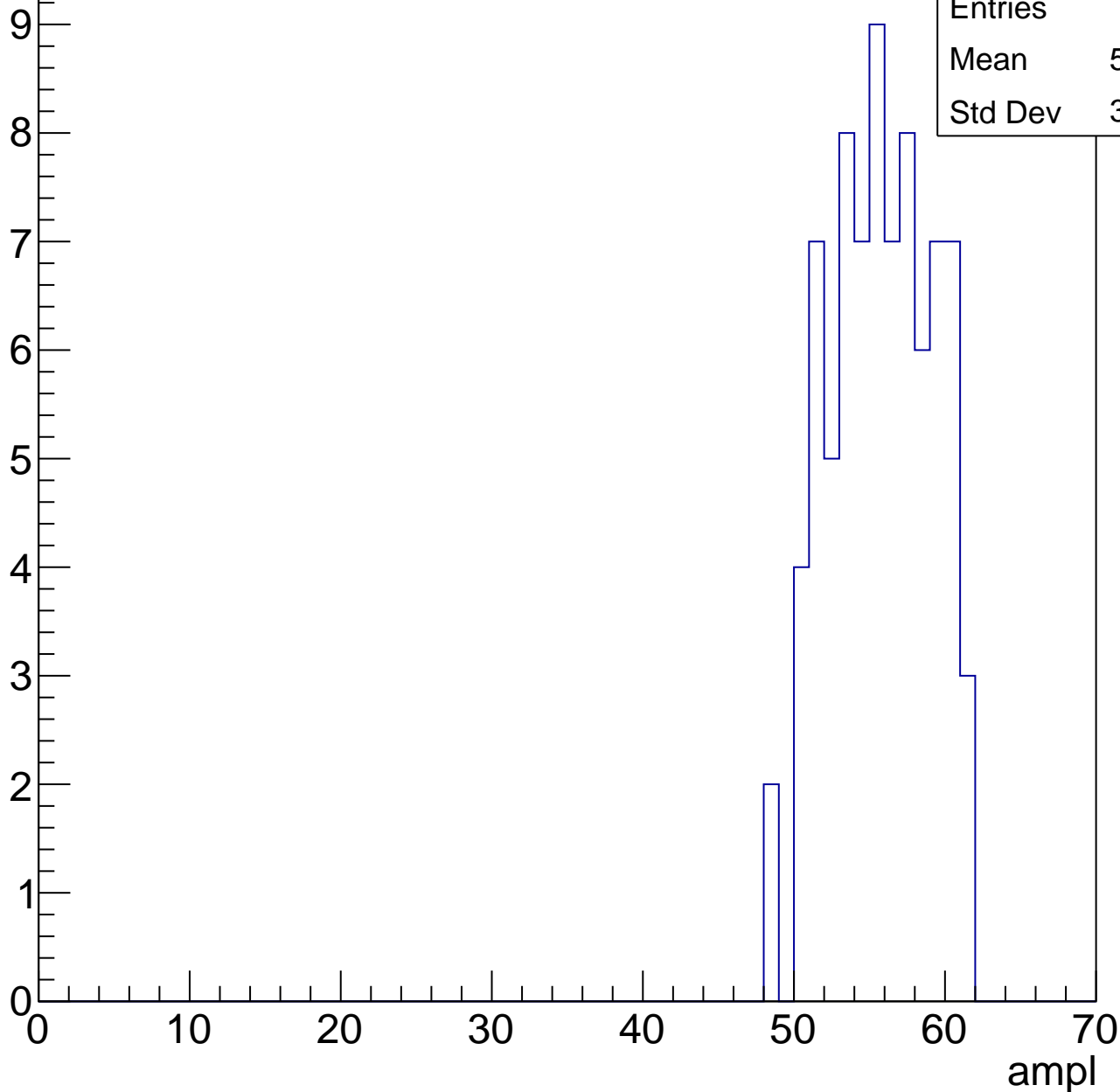


# B1L003S, U22-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	80
Mean	55.27
Std Dev	3.309

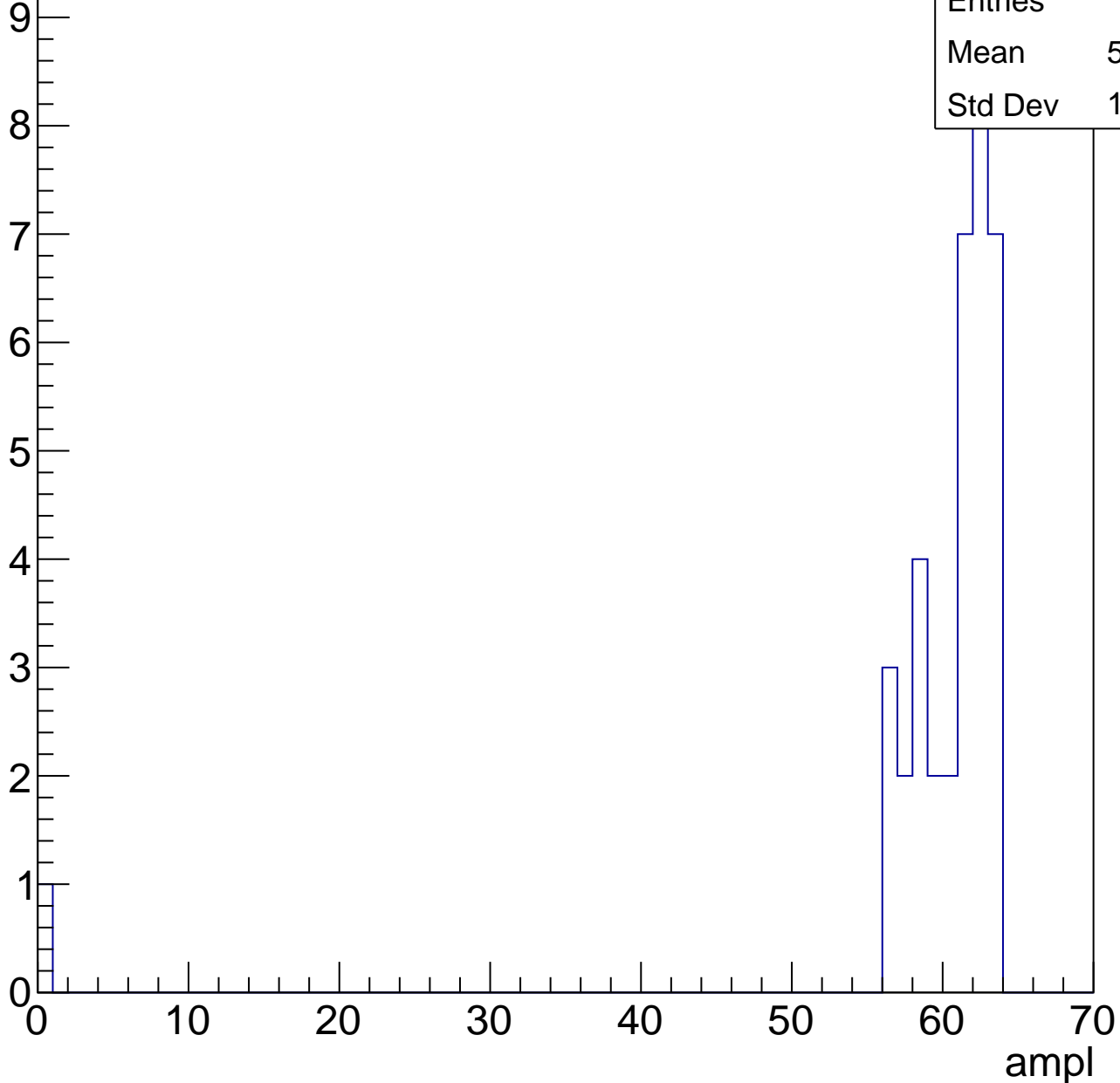


# B1L003S, U22-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

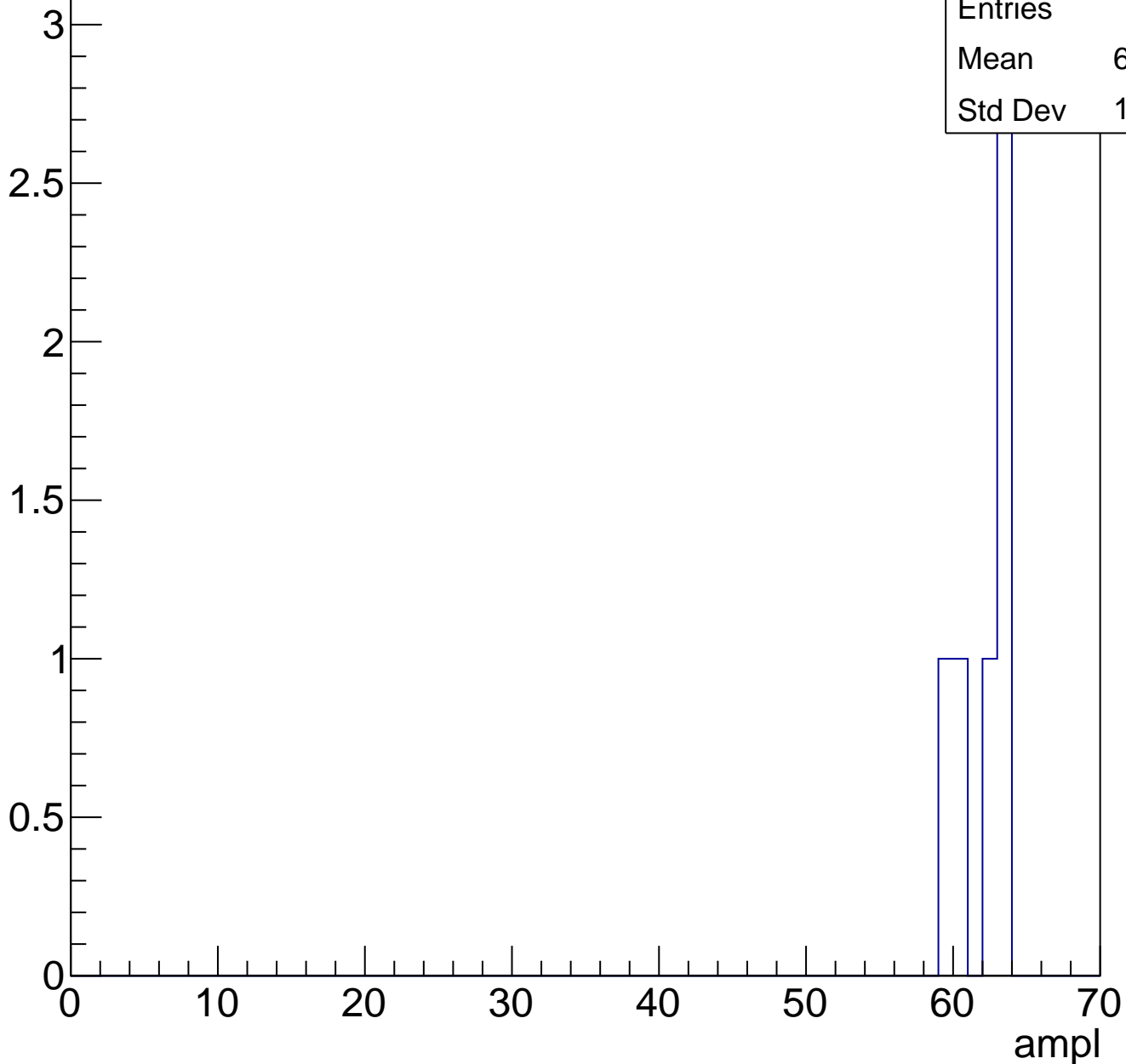
Entries	37
Mean	58.86
Std Dev	10.06



# B1L003S, U22-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	11
Std Dev	11

ampl

# B1L003S, U22-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	83
Mean	28.19
Std Dev	3.309

**Gaus mean : 28.9576**

**Gaus Width: 3.2414**

10

8

6

4

2

0

0

10

20

30

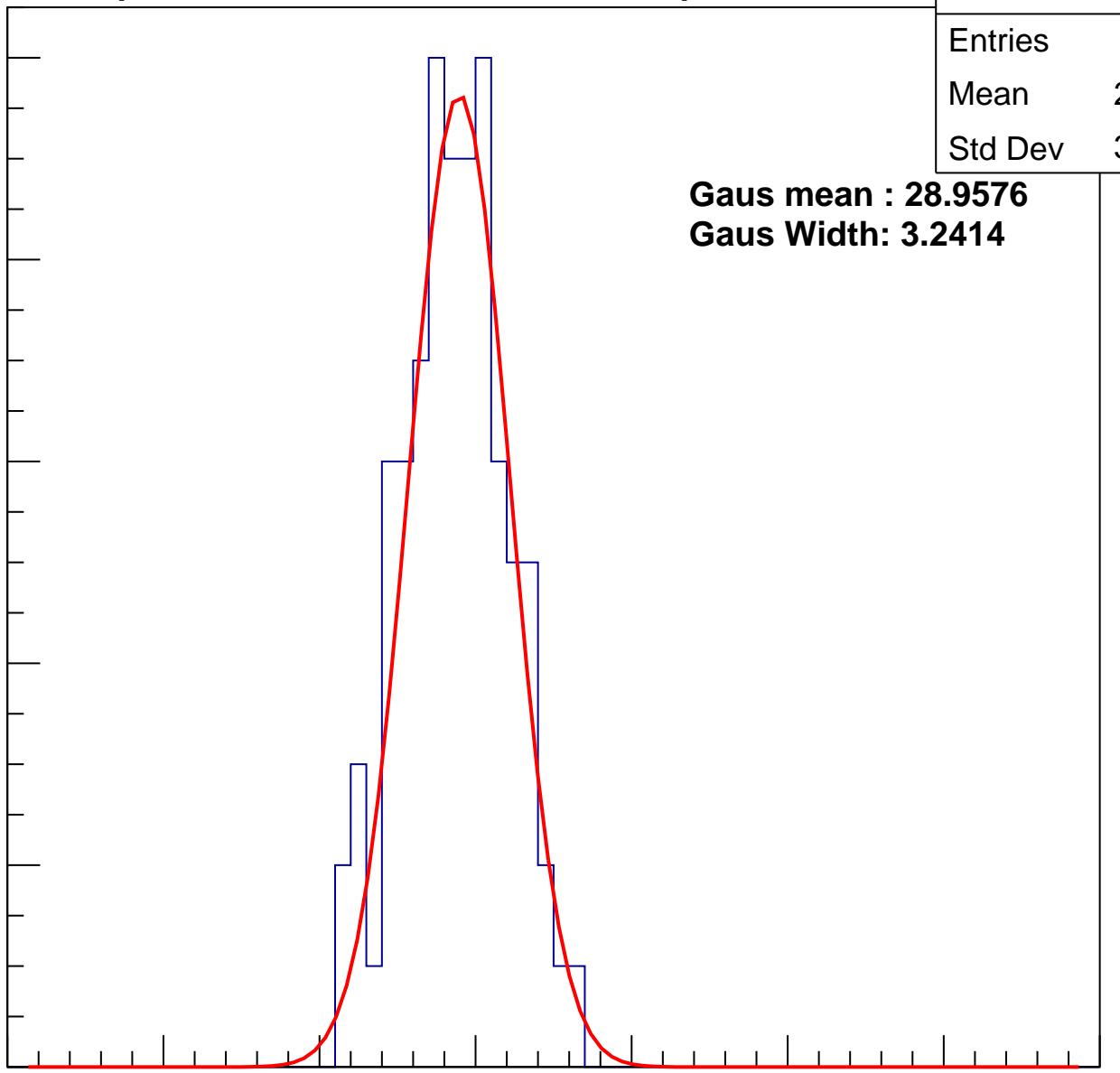
40

50

60

70

ampl



# B1L003S, U22-ch121, adc1

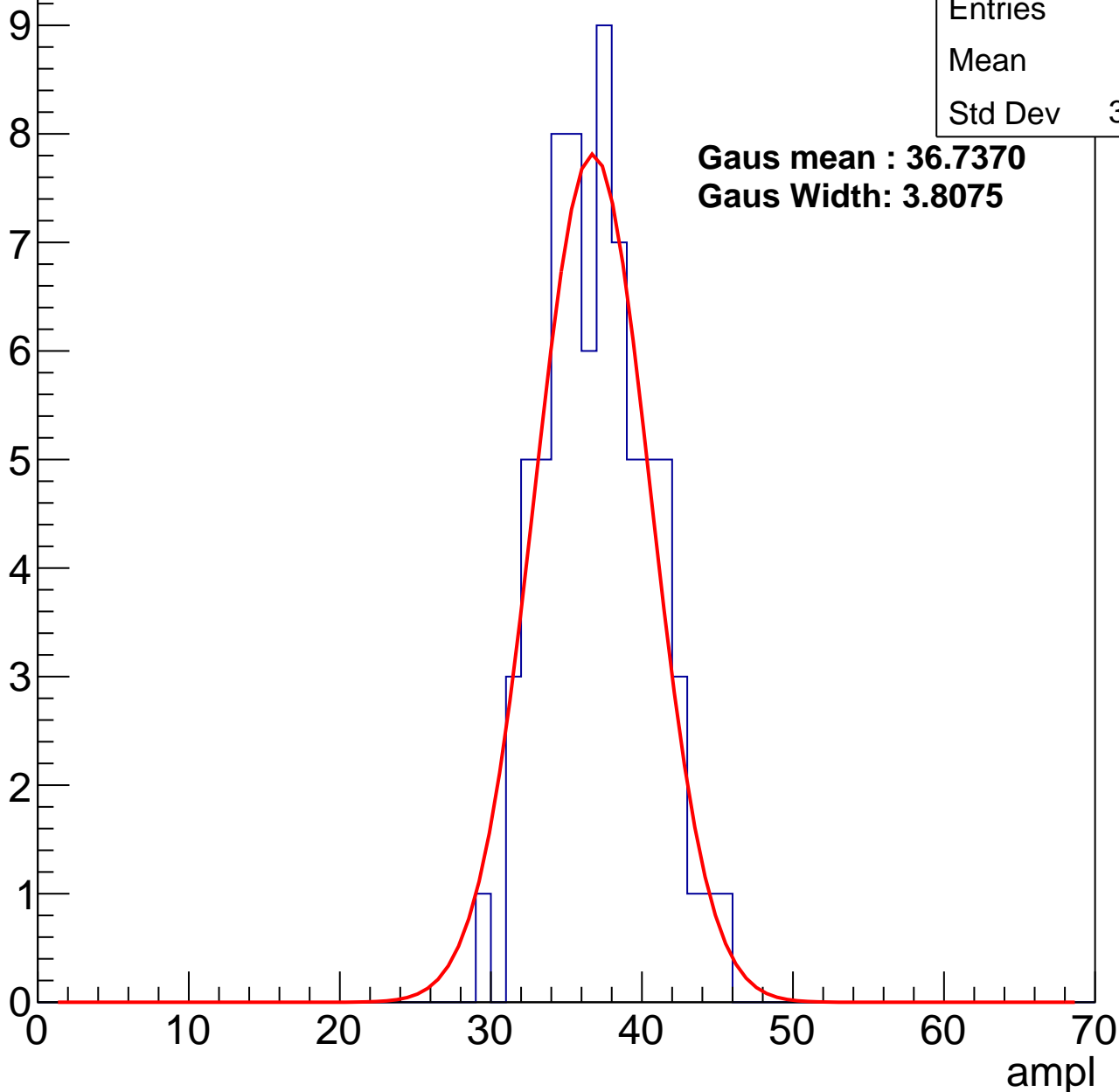
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	36.6
Std Dev	3.415

**Gaus mean : 36.7370**

**Gaus Width: 3.8075**



# B1L003S, U22-ch121, adc2

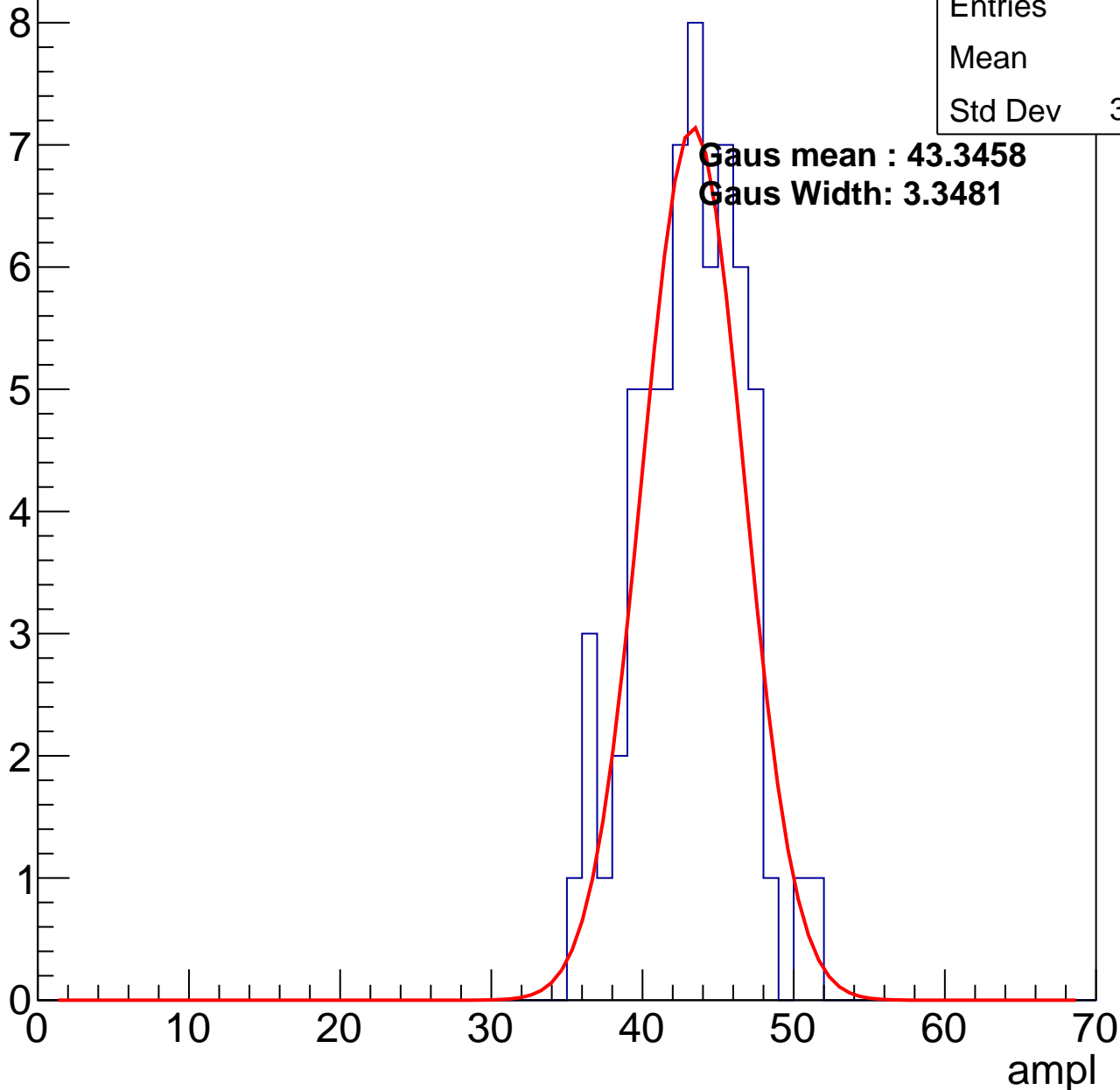
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.7
Std Dev	3.422

**Gaus mean : 43.3458**

**Gaus Width: 3.3481**

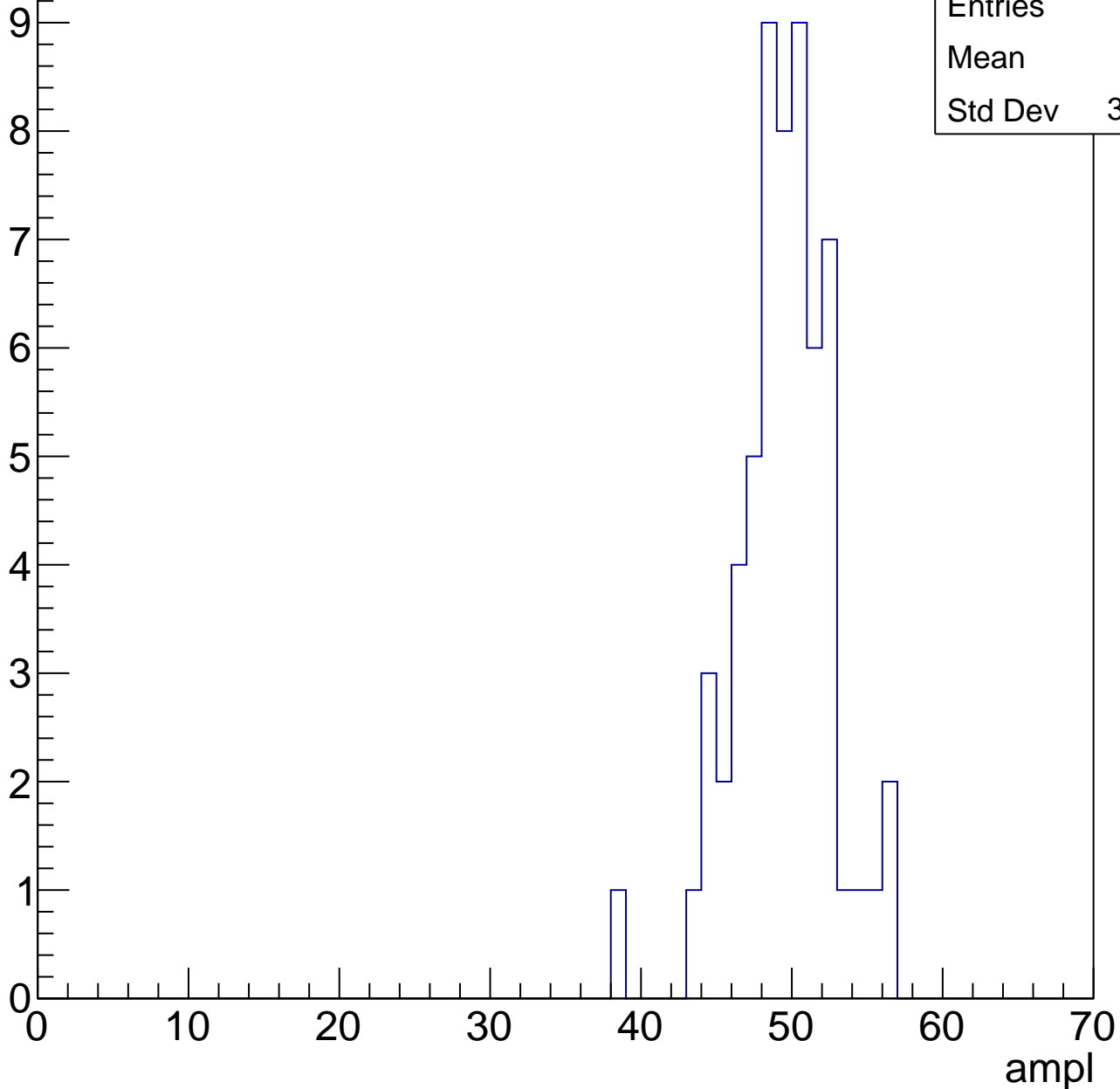


# B1L003S, U22-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49
Std Dev	3.162



# B1L003S, U22-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	55.59
Std Dev	2.715

Entry

10

8

6

4

2

0

0

10

20

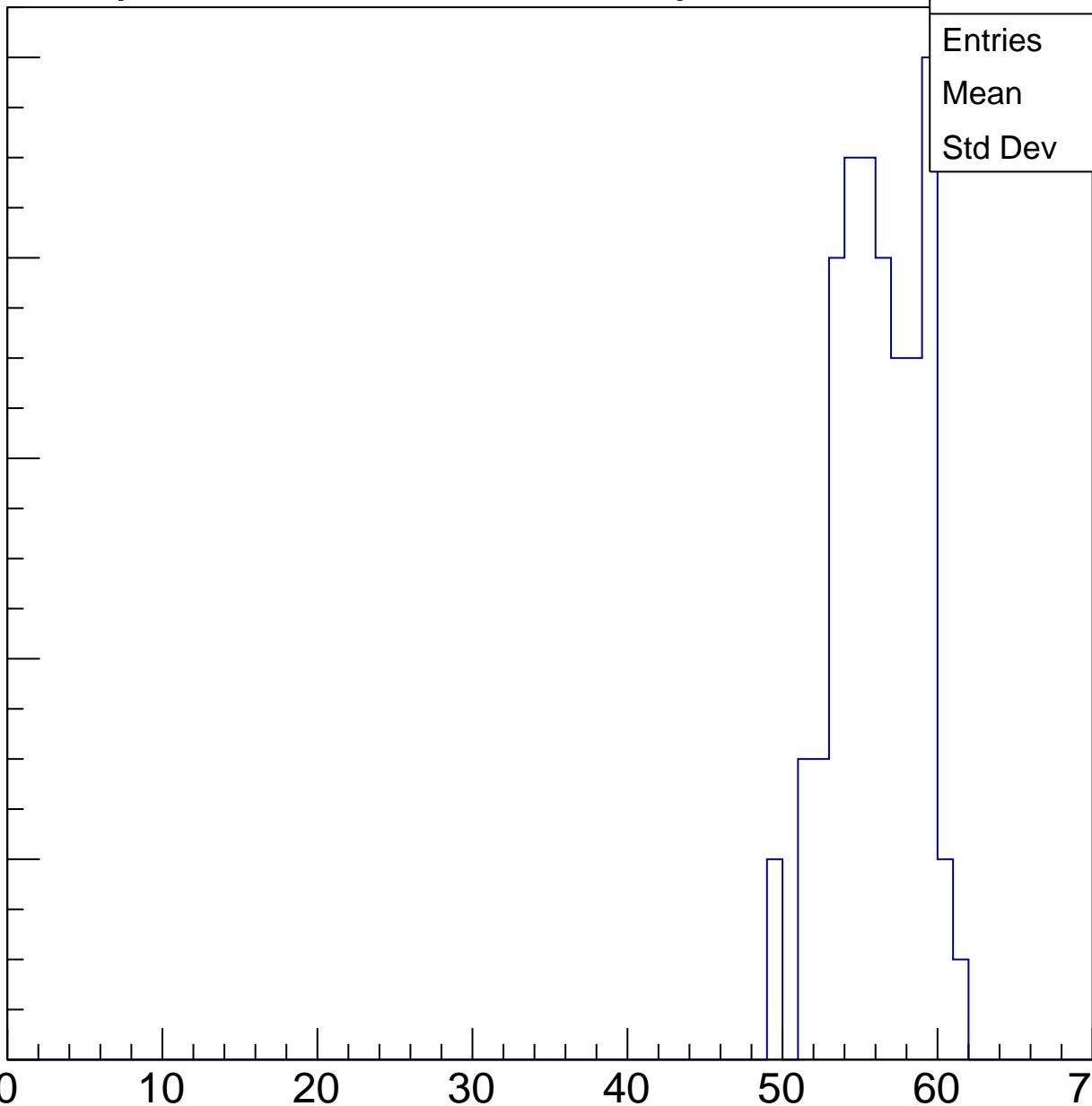
30

40

50

60

ampl



# B1L003S, U22-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

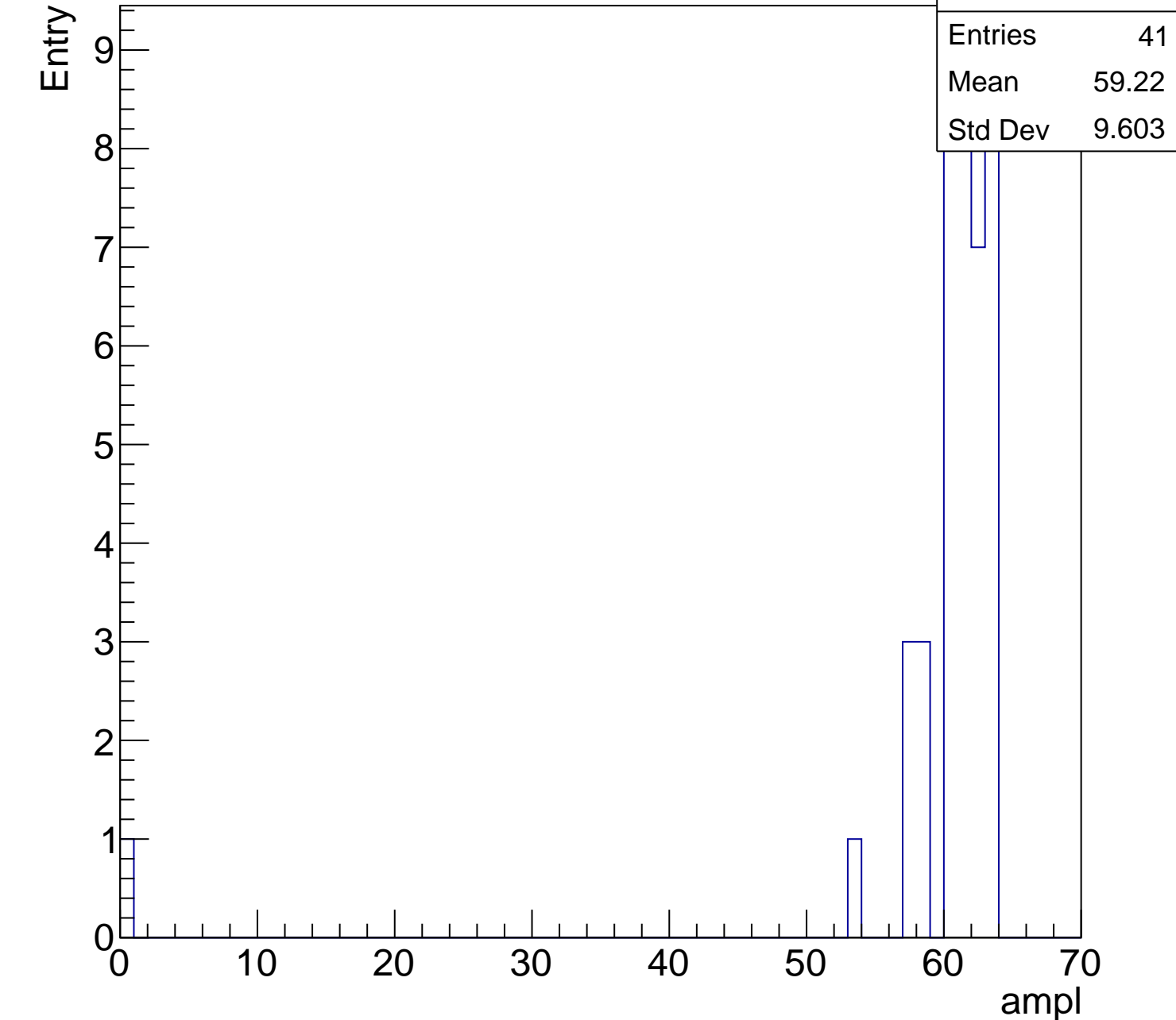
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	41
Mean	59.22
Std Dev	9.603

ampl

0 10 20 30 40 50 60 70



# B1L003S, U22-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	4
Mean	62.25
Std Dev	0.433



# B1L003S, U22-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch122, adc0

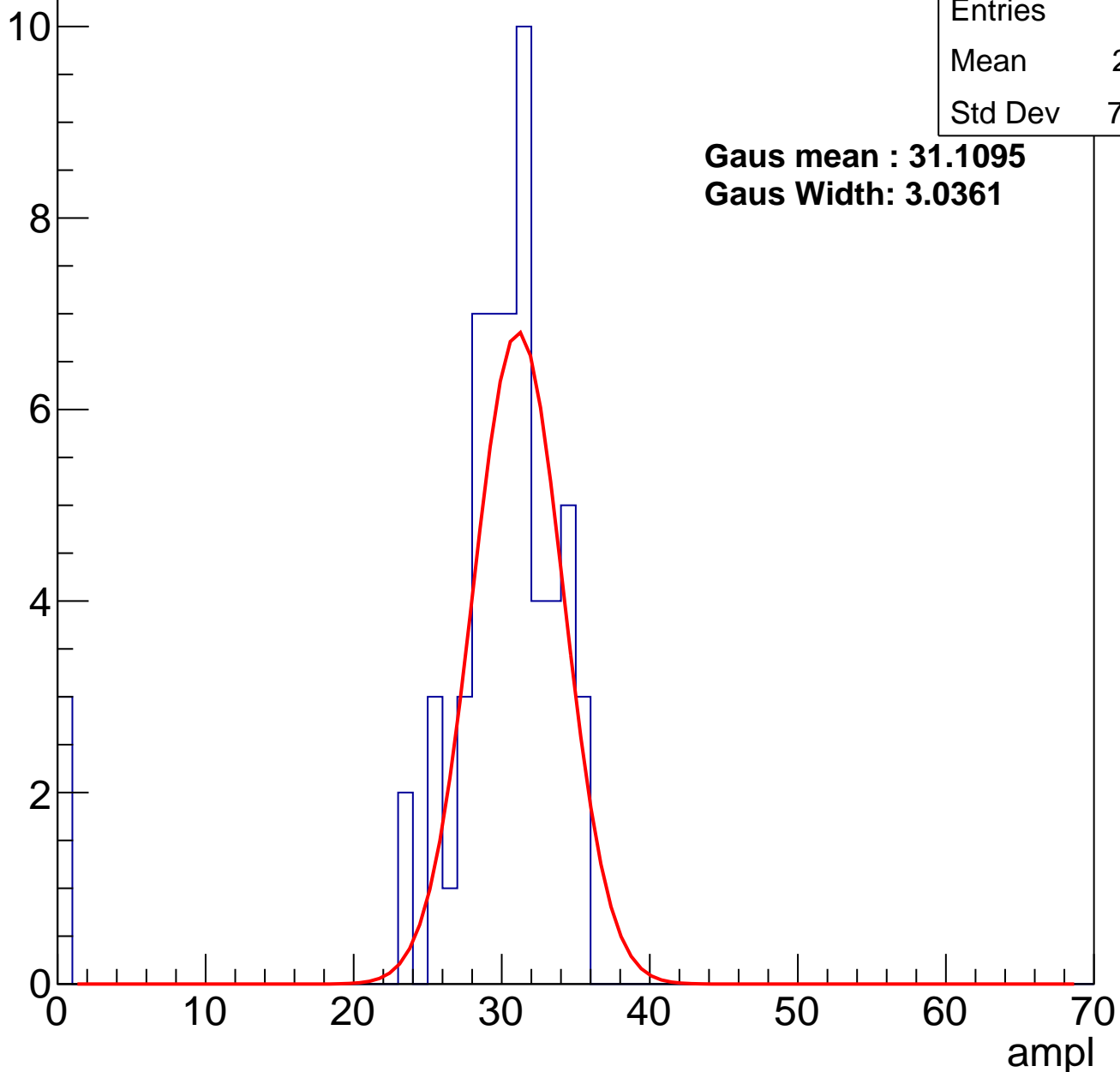
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	59
Mean	28.51
Std Dev	7.174

**Gaus mean : 31.1095**

**Gaus Width: 3.0361**

Entry



# B1L003S, U22-ch122, adc1

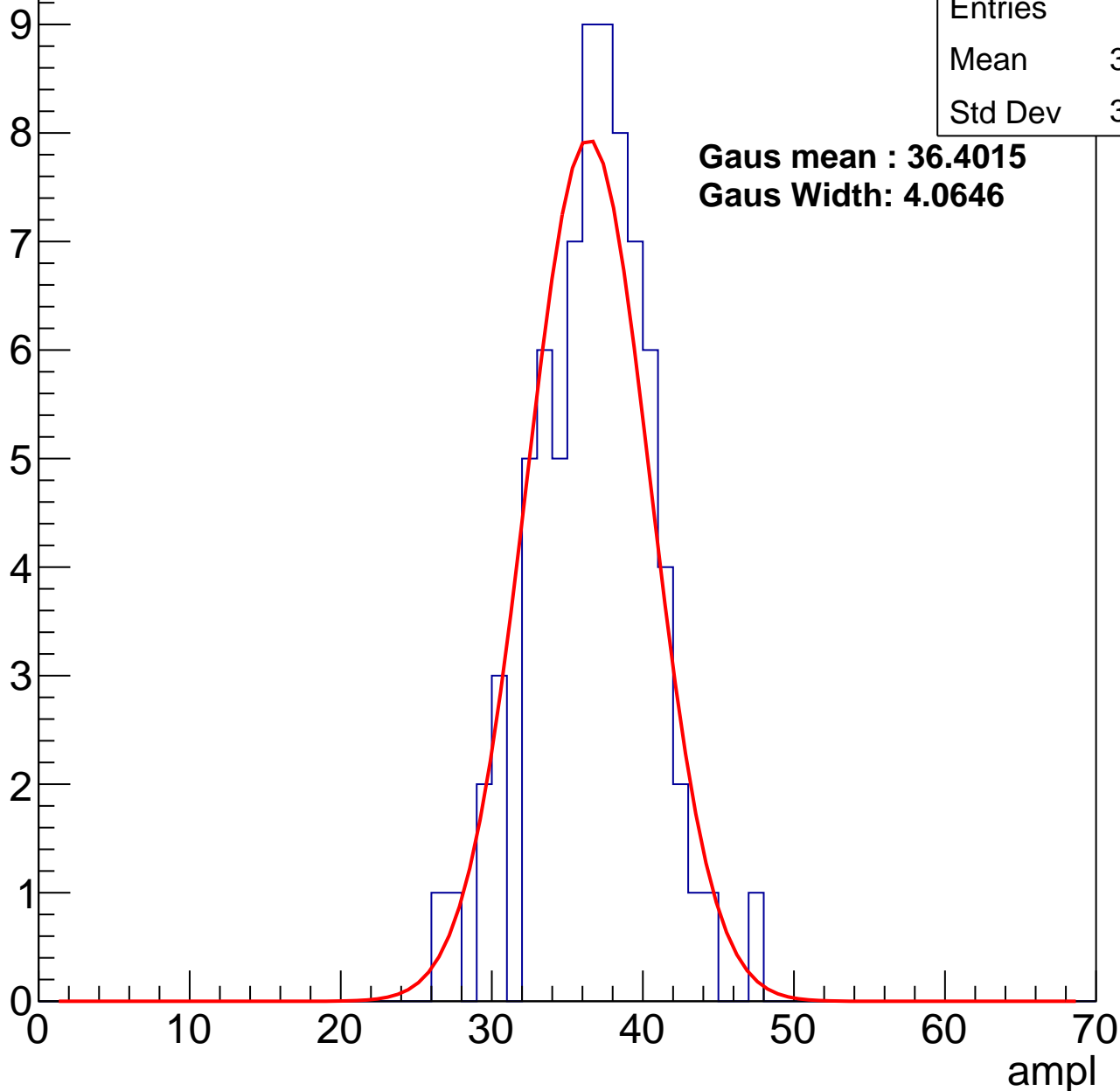
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	78
Mean	36.28
Std Dev	3.836

**Gaus mean : 36.4015**

**Gaus Width: 4.0646**



# B1L003S, U22-ch122, adc2

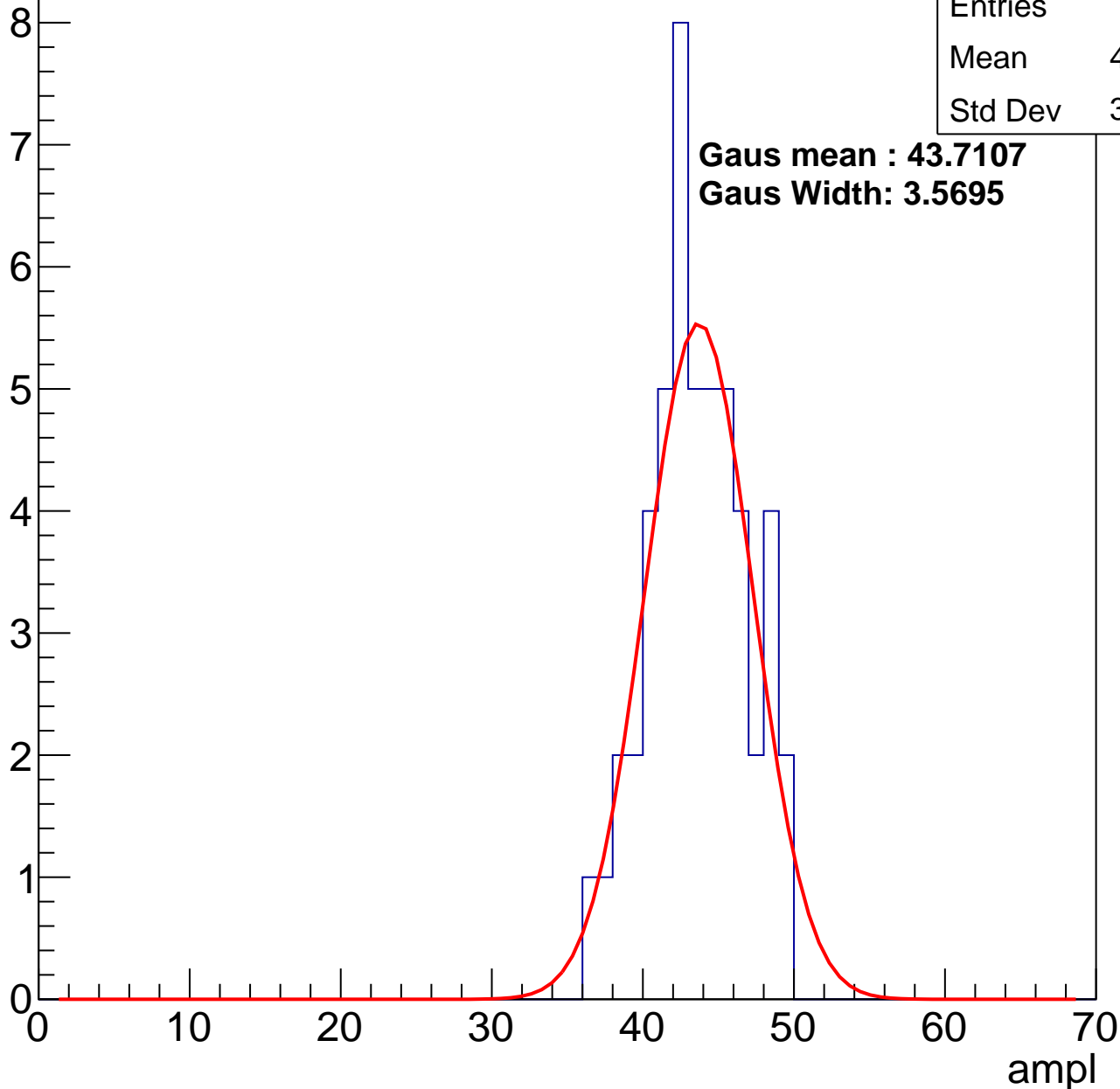
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.12
Std Dev	3.147

**Gaus mean : 43.7107**

**Gaus Width: 3.5695**

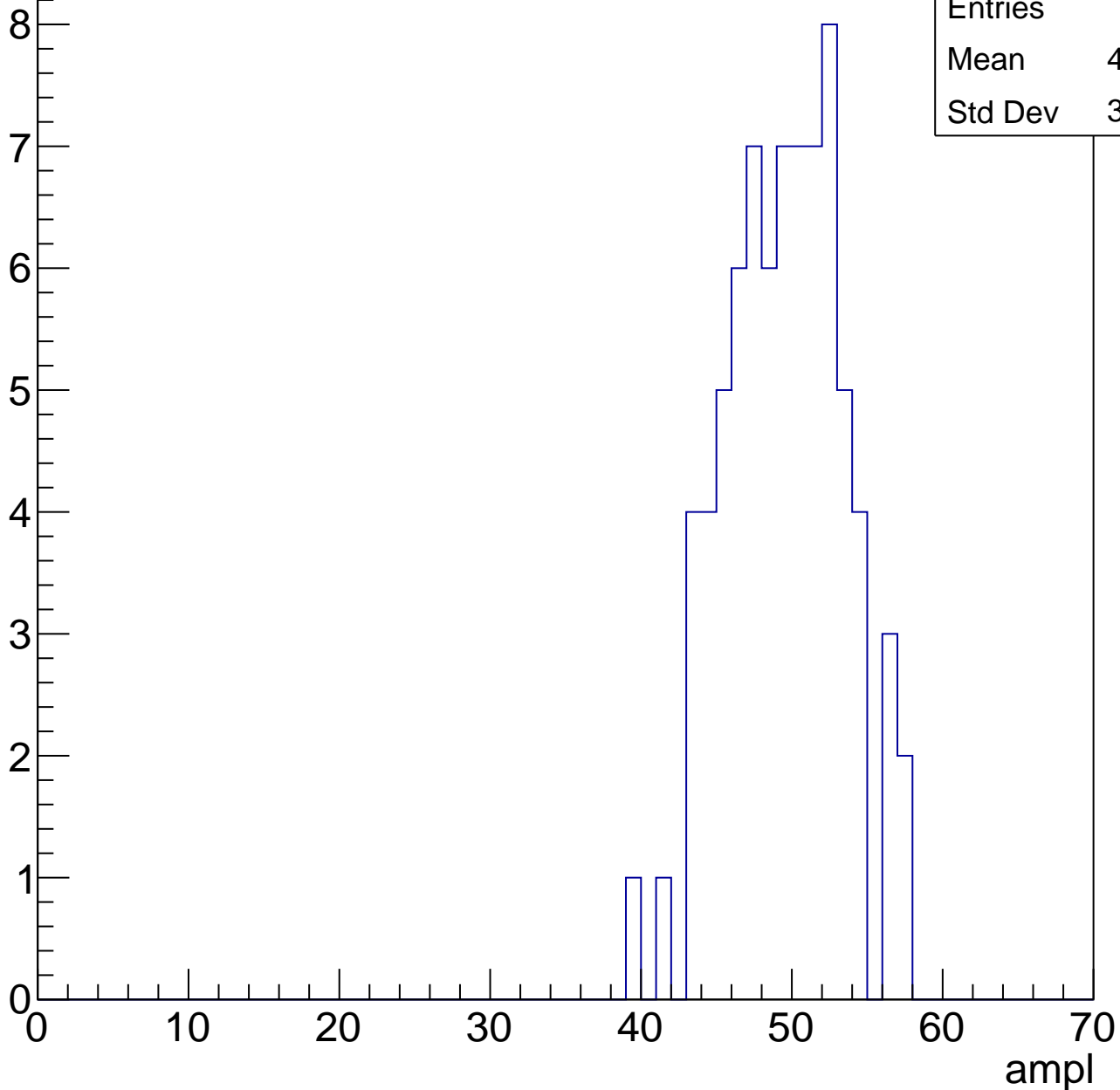


# B1L003S, U22-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	49.03
Std Dev	3.844

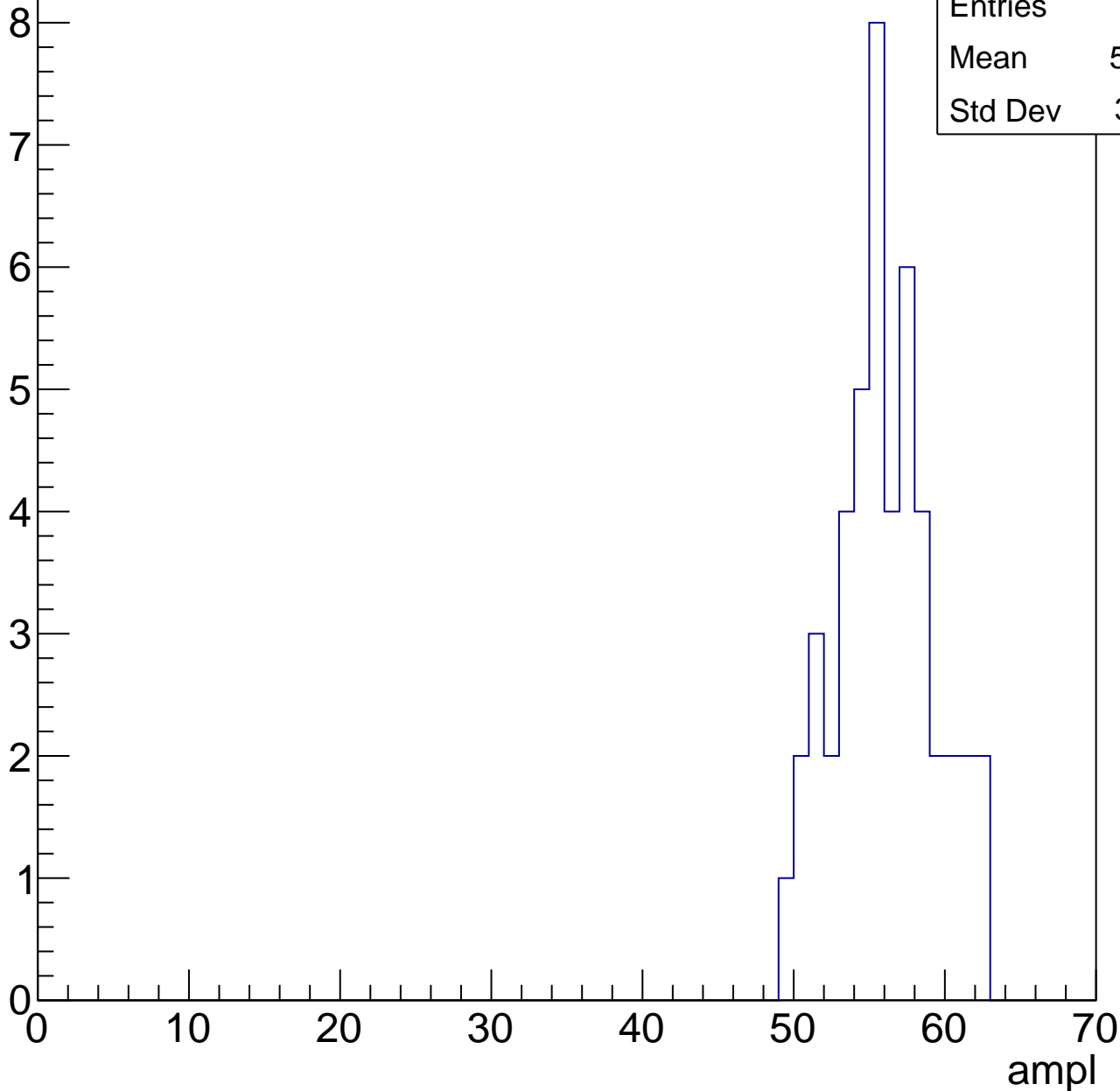


# B1L003S, U22-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	47
Mean	55.53
Std Dev	3.181

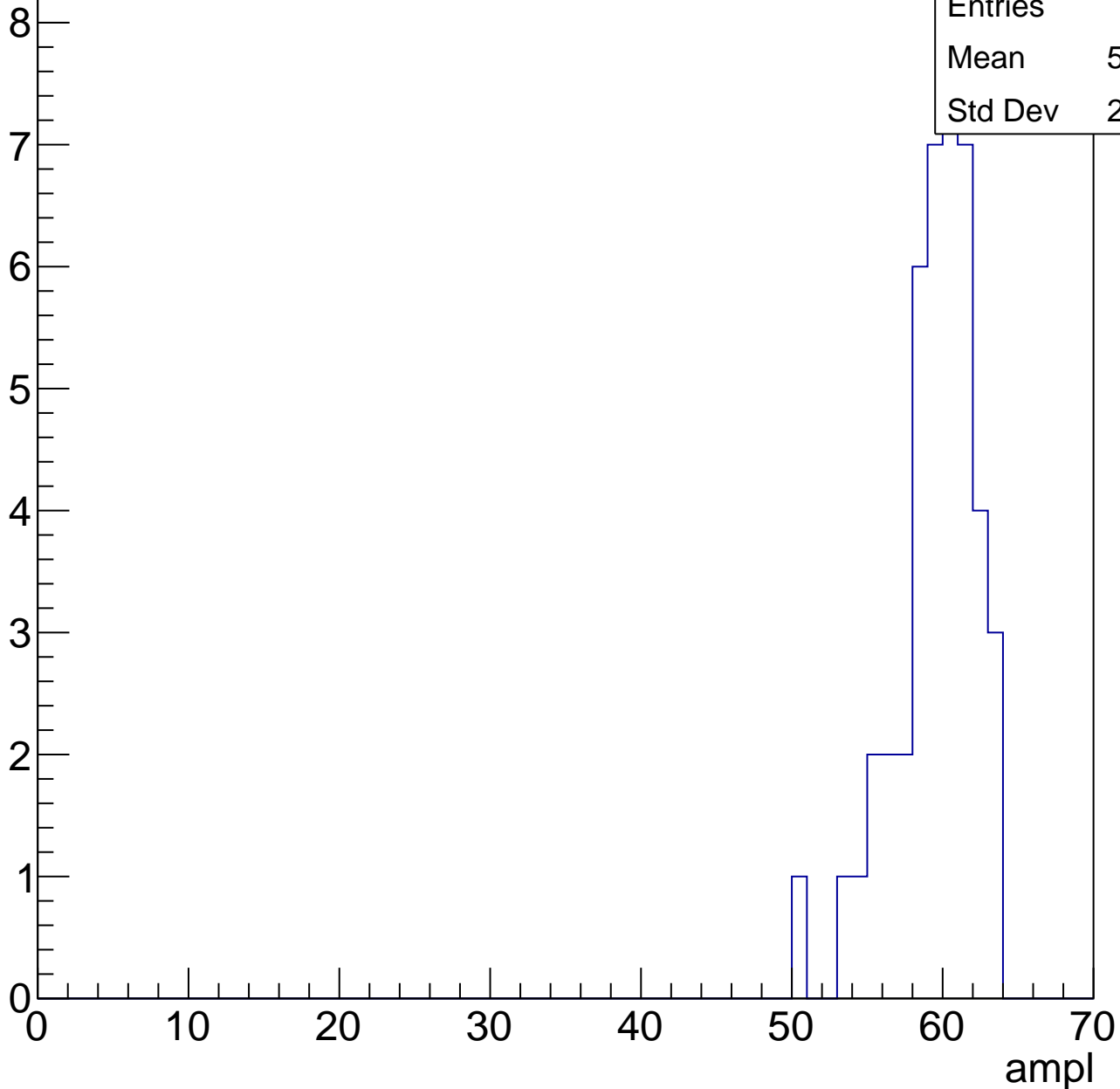


# B1L003S, U22-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.05
Std Dev	2.722

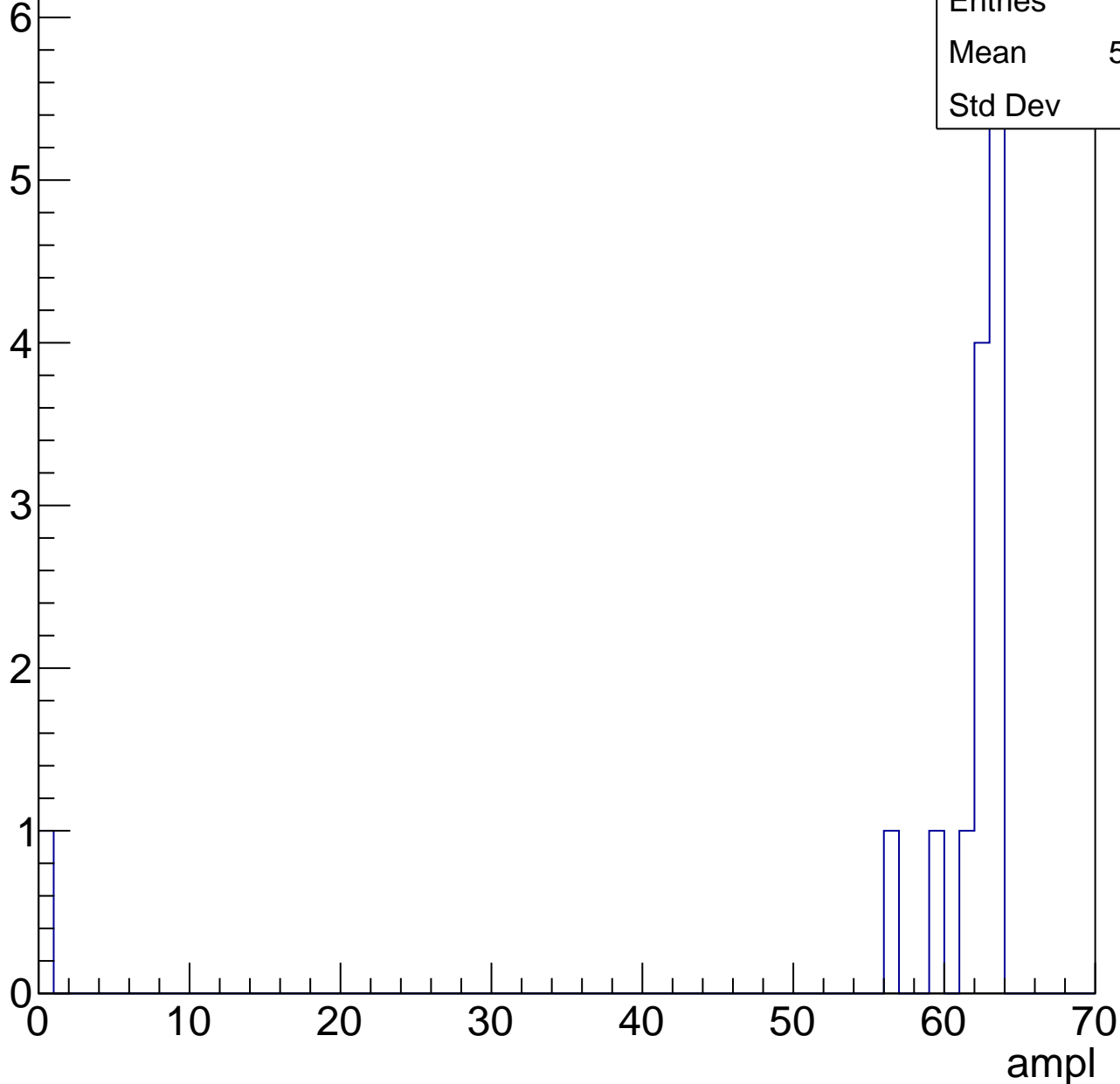


# B1L003S, U22-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	14
Mean	57.29
Std Dev	16





# B1L003S, U22-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch123, adc0

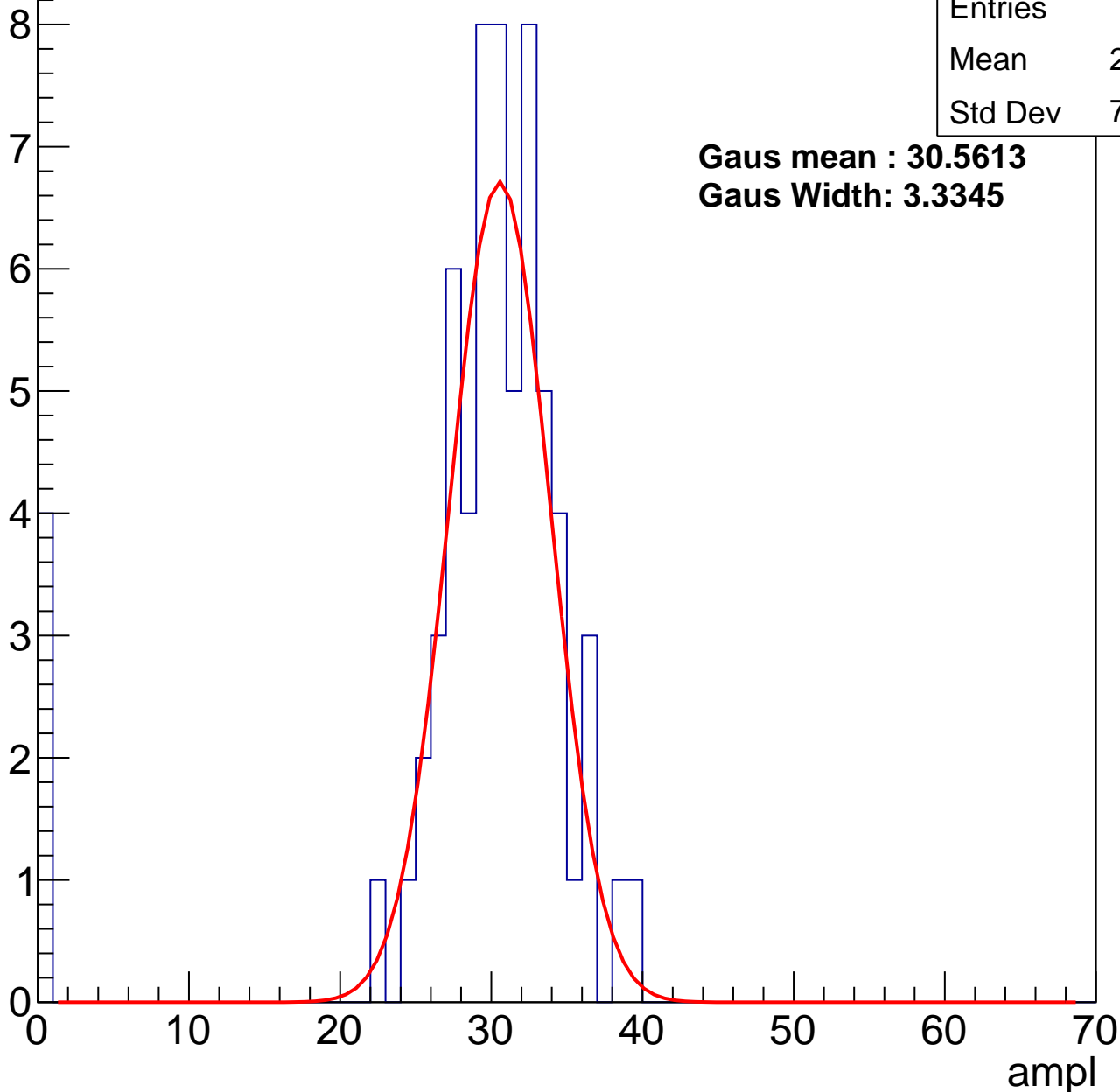
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.49
Std Dev	7.993

**Gaus mean : 30.5613**

**Gaus Width: 3.3345**



# B1L003S, U22-ch123, adc1

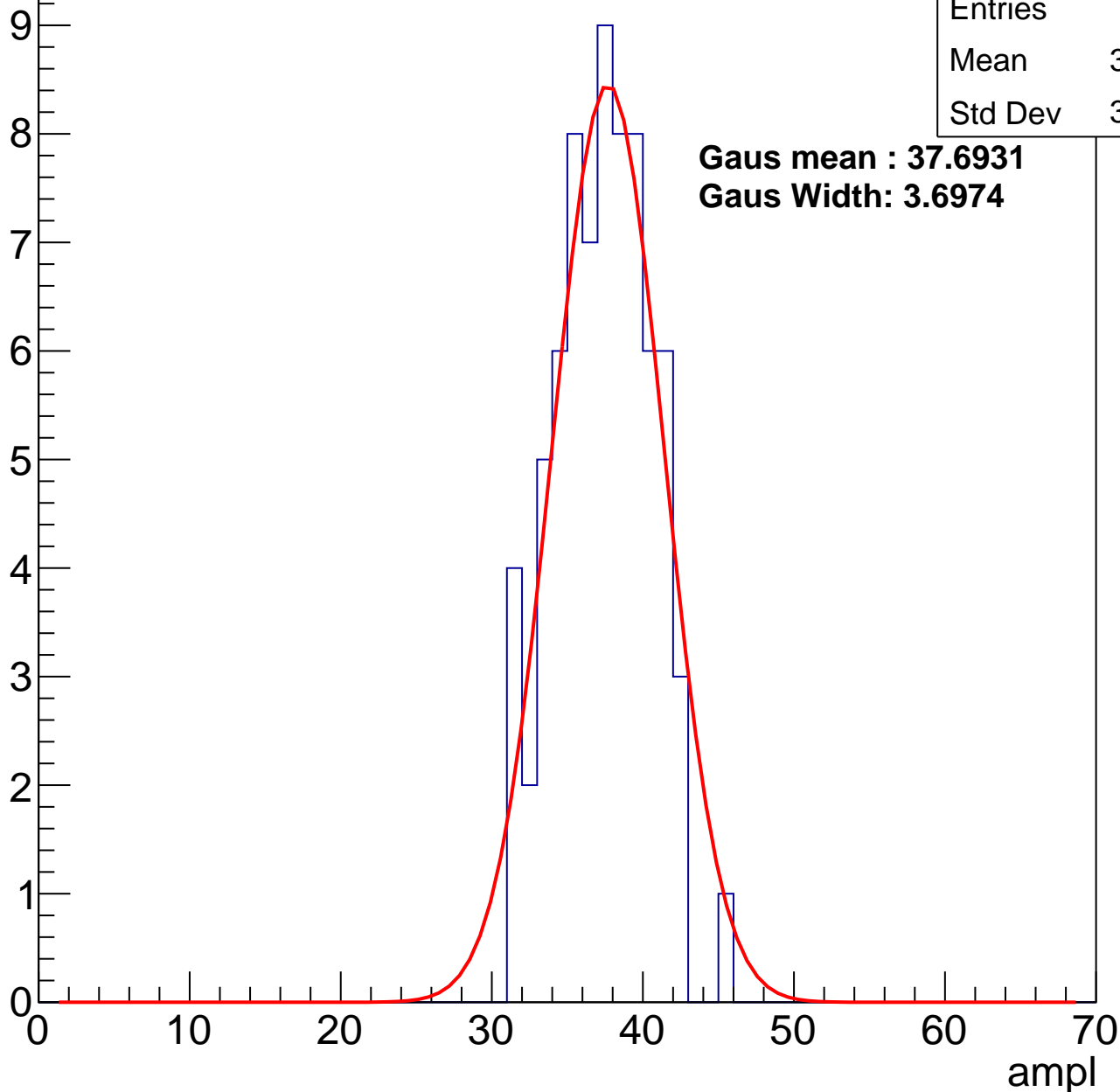
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	73
Mean	36.92
Std Dev	3.087

**Gaus mean : 37.6931**

**Gaus Width: 3.6974**



# B1L003S, U22-ch123, adc2

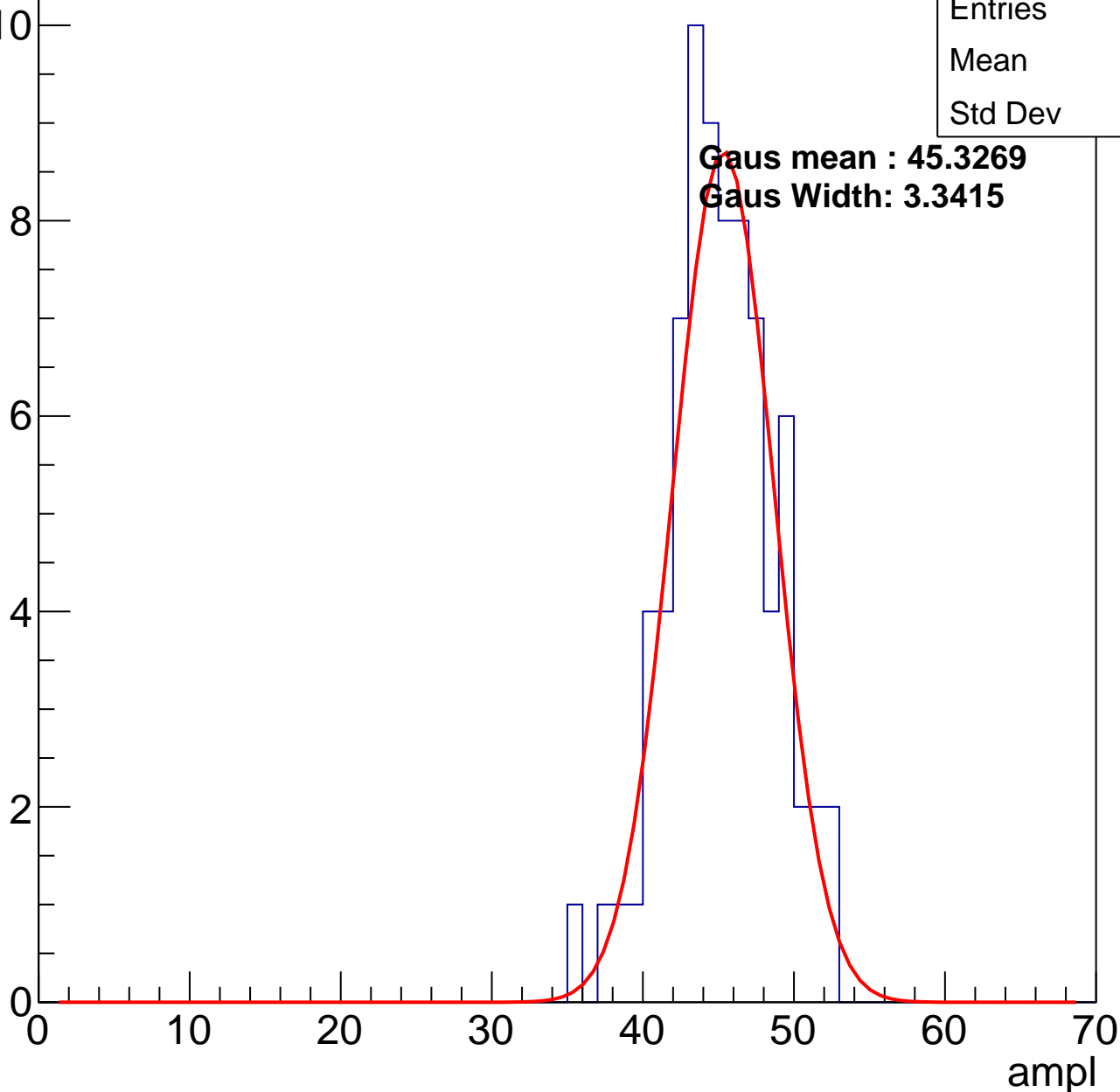
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	77
Mean	44.7
Std Dev	3.43

**Gaus mean : 45.3269**

**Gaus Width: 3.3415**

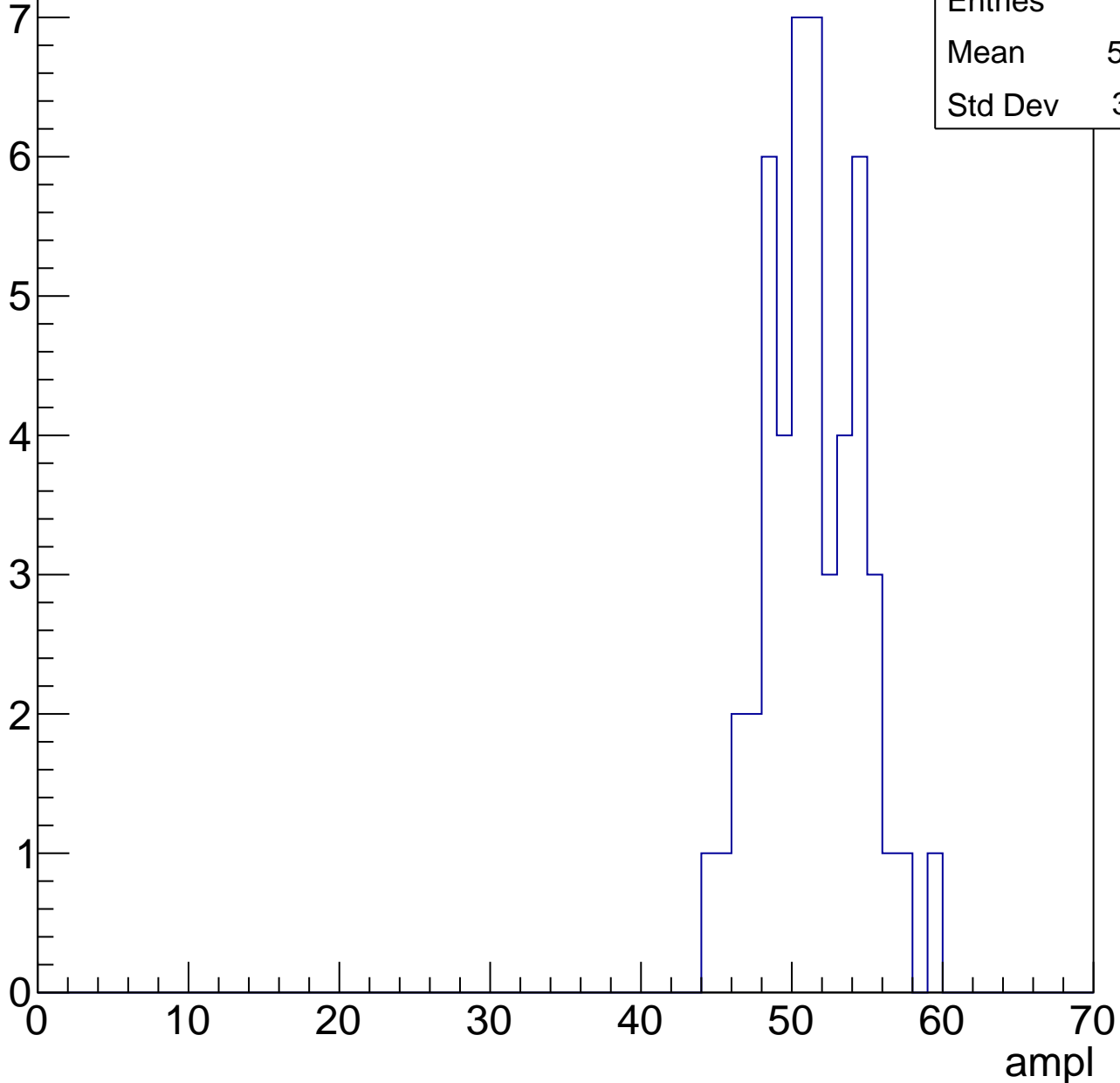


# B1L003S, U22-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	49
Mean	50.92
Std Dev	3.161

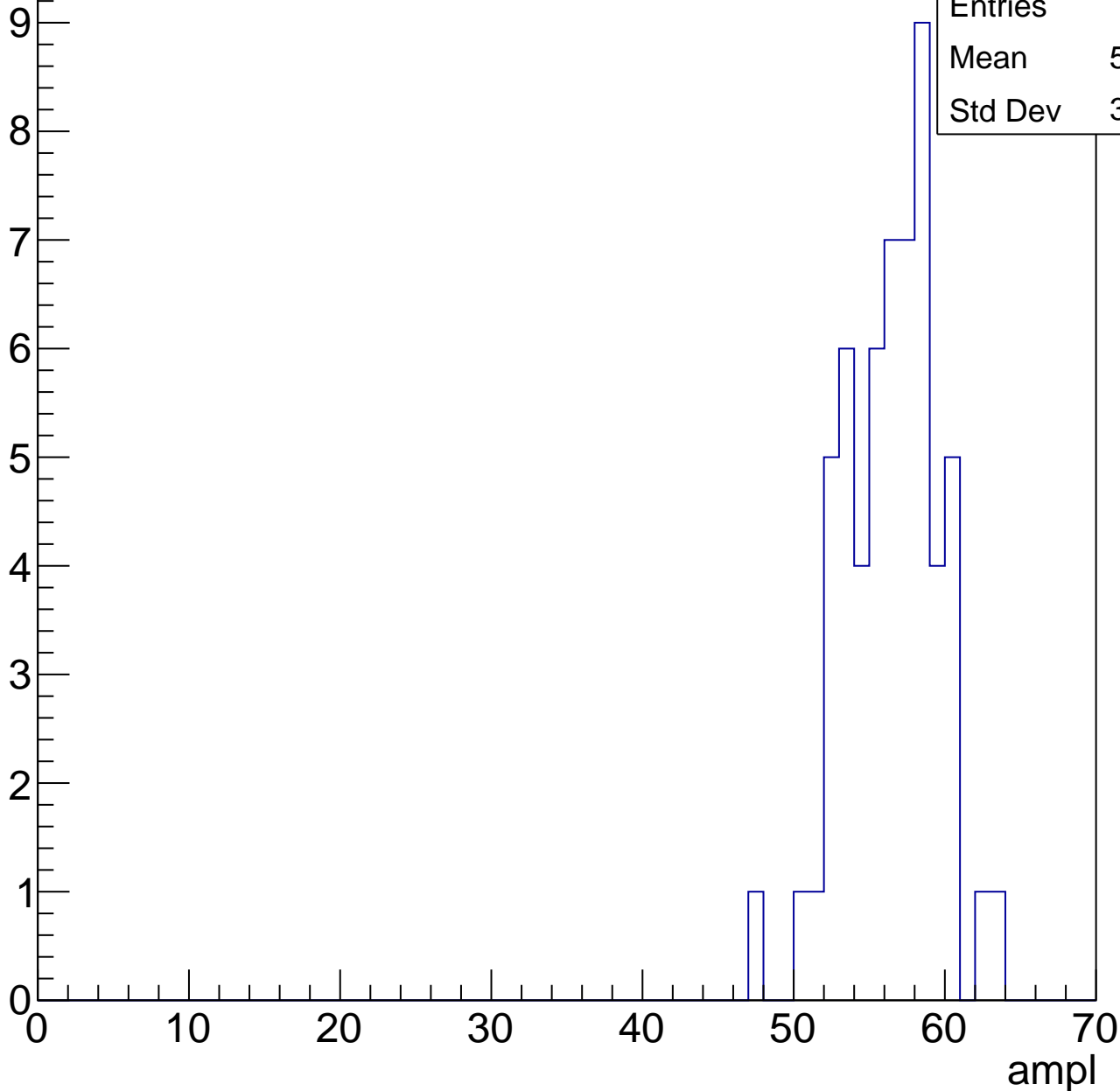


# B1L003S, U22-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.97
Std Dev	3.057



# B1L003S, U22-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	44
Mean	59.23
Std Dev	9.263

Entry

10

8

6

4

2

0

0

10

20

30

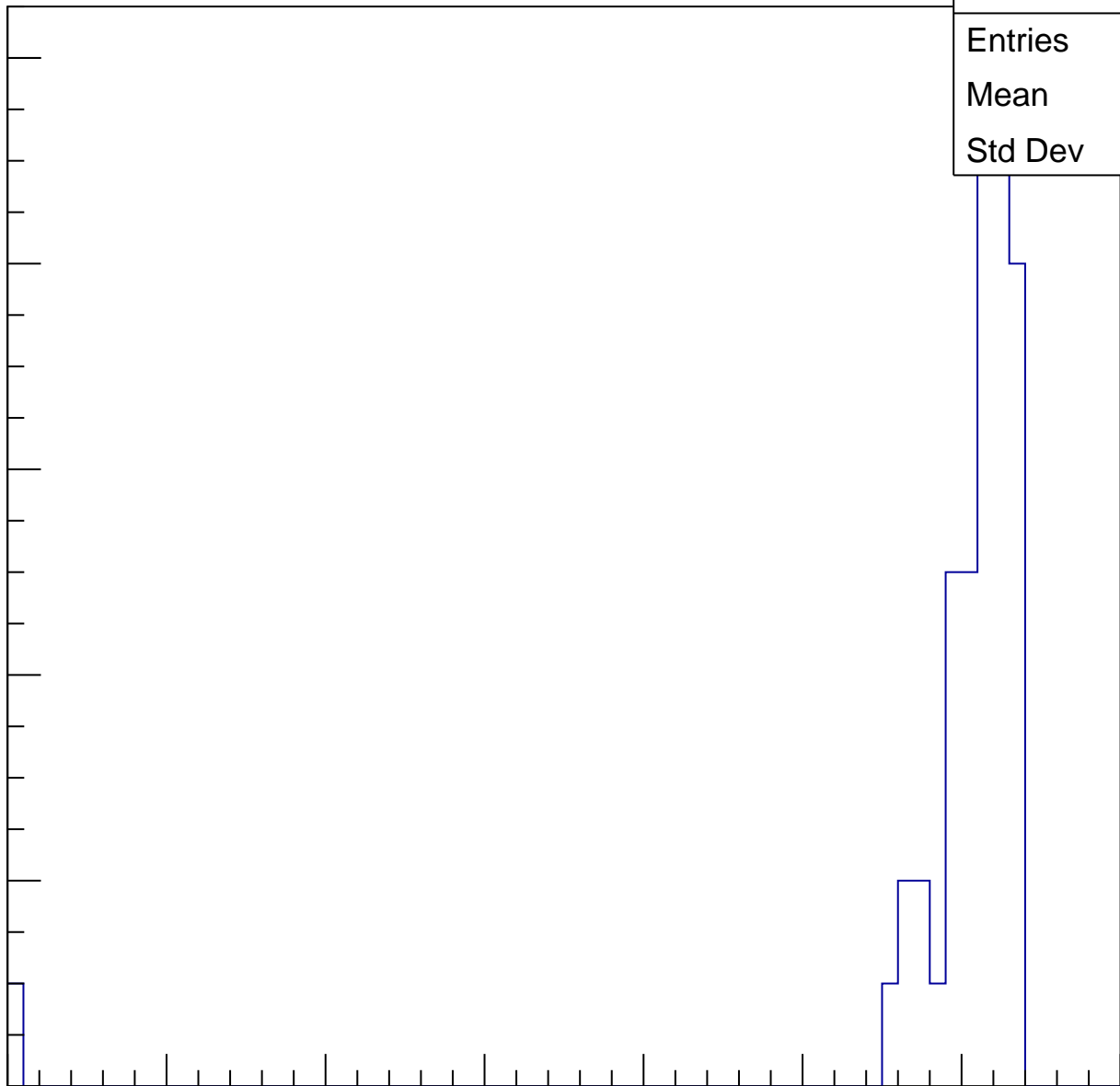
40

50

60

70

ampl



# B1L003S, U22-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	63
Std Dev	0



# B1L003S, U22-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch124, adc0

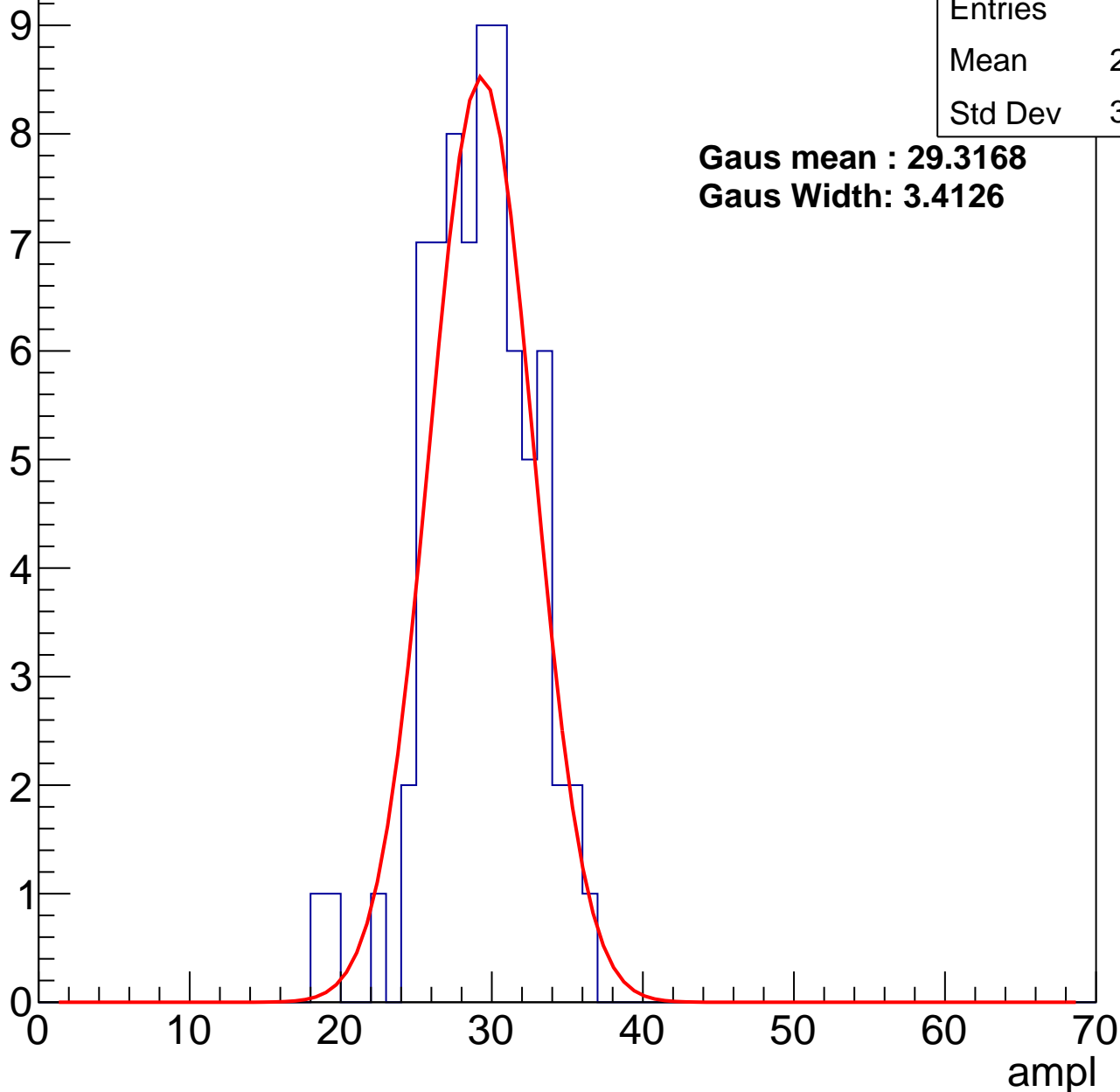
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	74
Mean	28.72
Std Dev	3.435

**Gaus mean : 29.3168**

**Gaus Width: 3.4126**



# B1L003S, U22-ch124, adc1

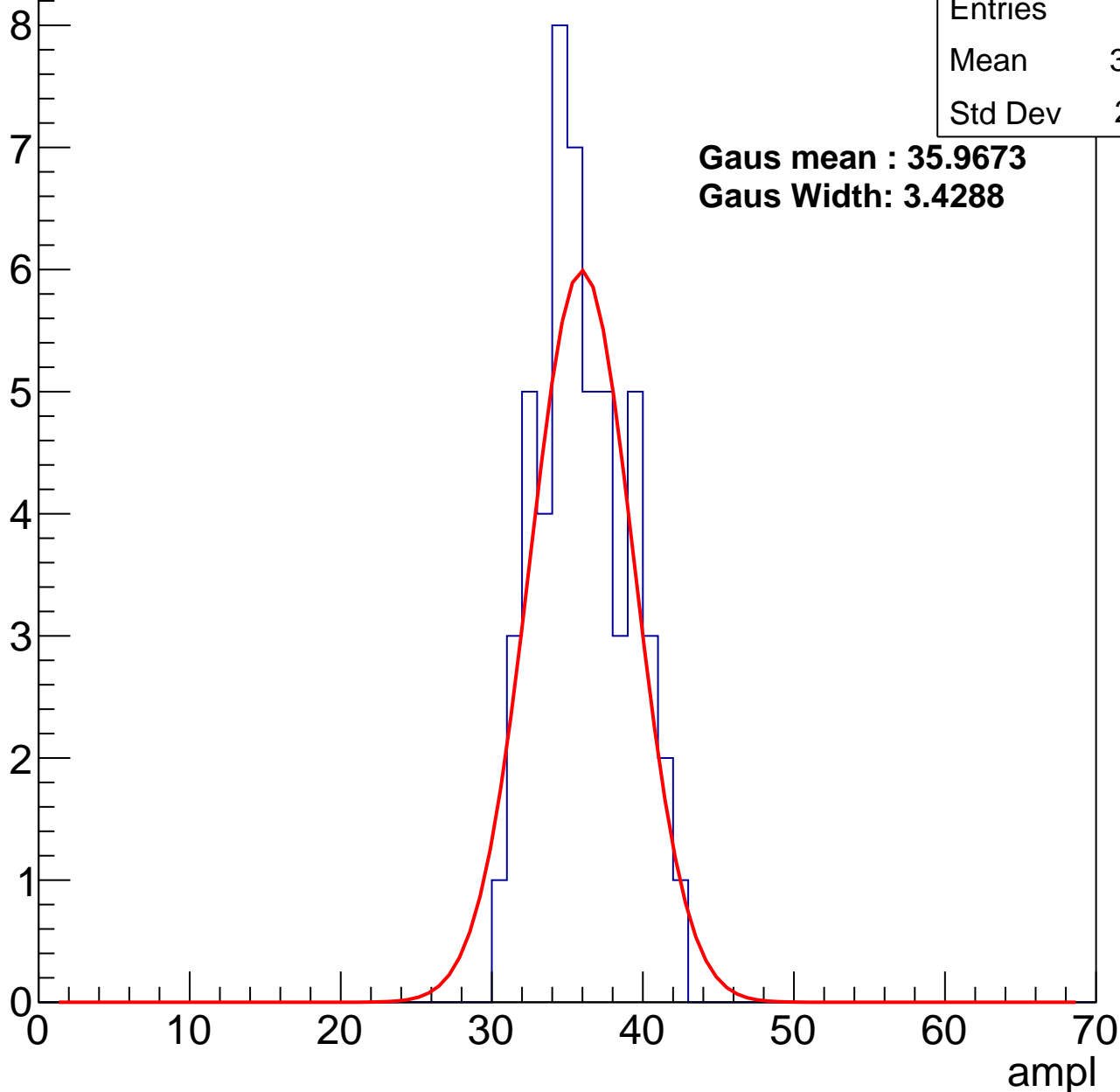
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	35.58
Std Dev	2.951

**Gaus mean : 35.9673**

**Gaus Width: 3.4288**



# B1L003S, U22-ch124, adc2

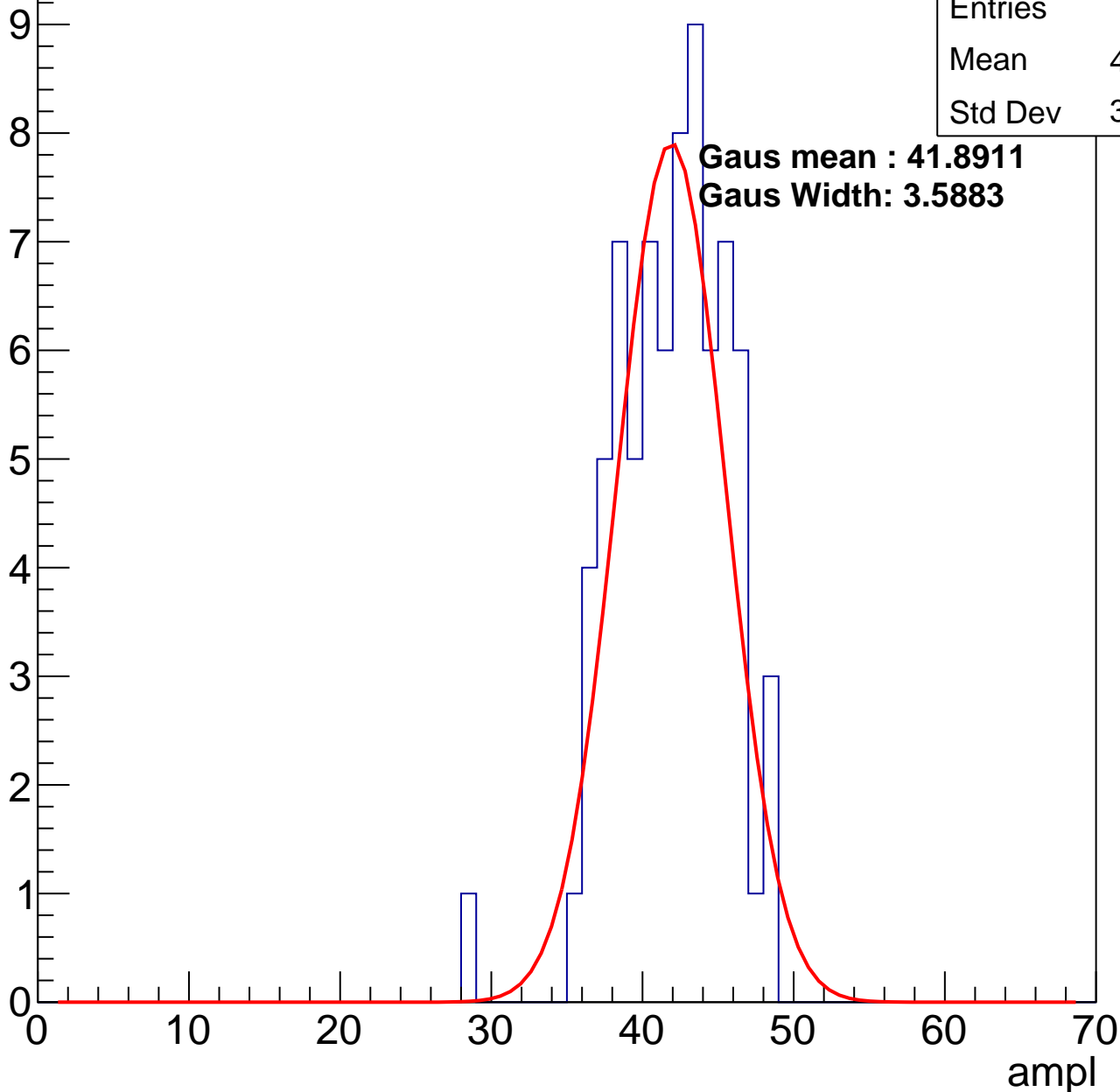
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	41.42
Std Dev	3.636

**Gaus mean : 41.8911**

**Gaus Width: 3.5883**

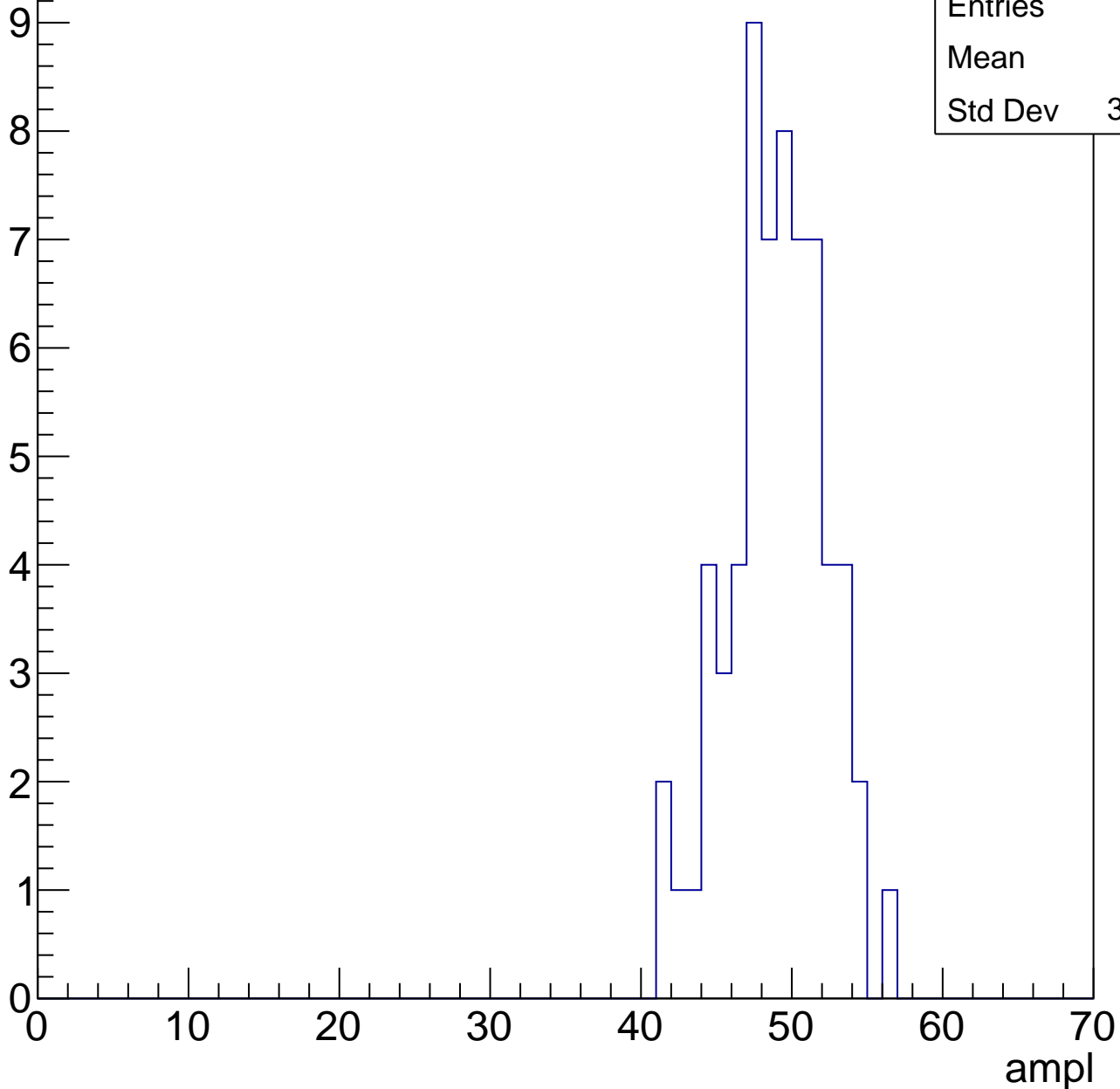


# B1L003S, U22-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.5
Std Dev	3.206

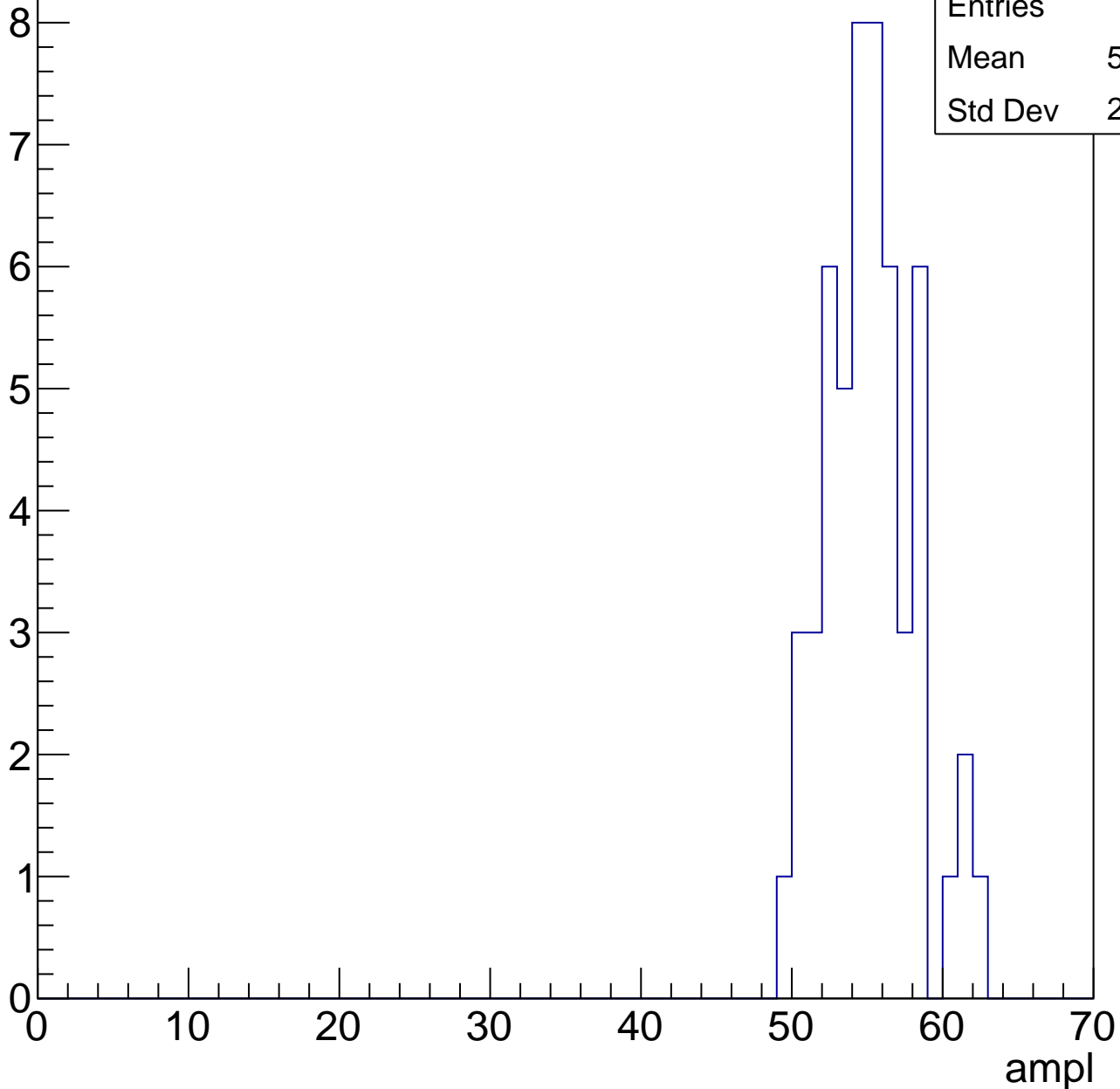


# B1L003S, U22-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	53
Mean	54.72
Std Dev	2.929

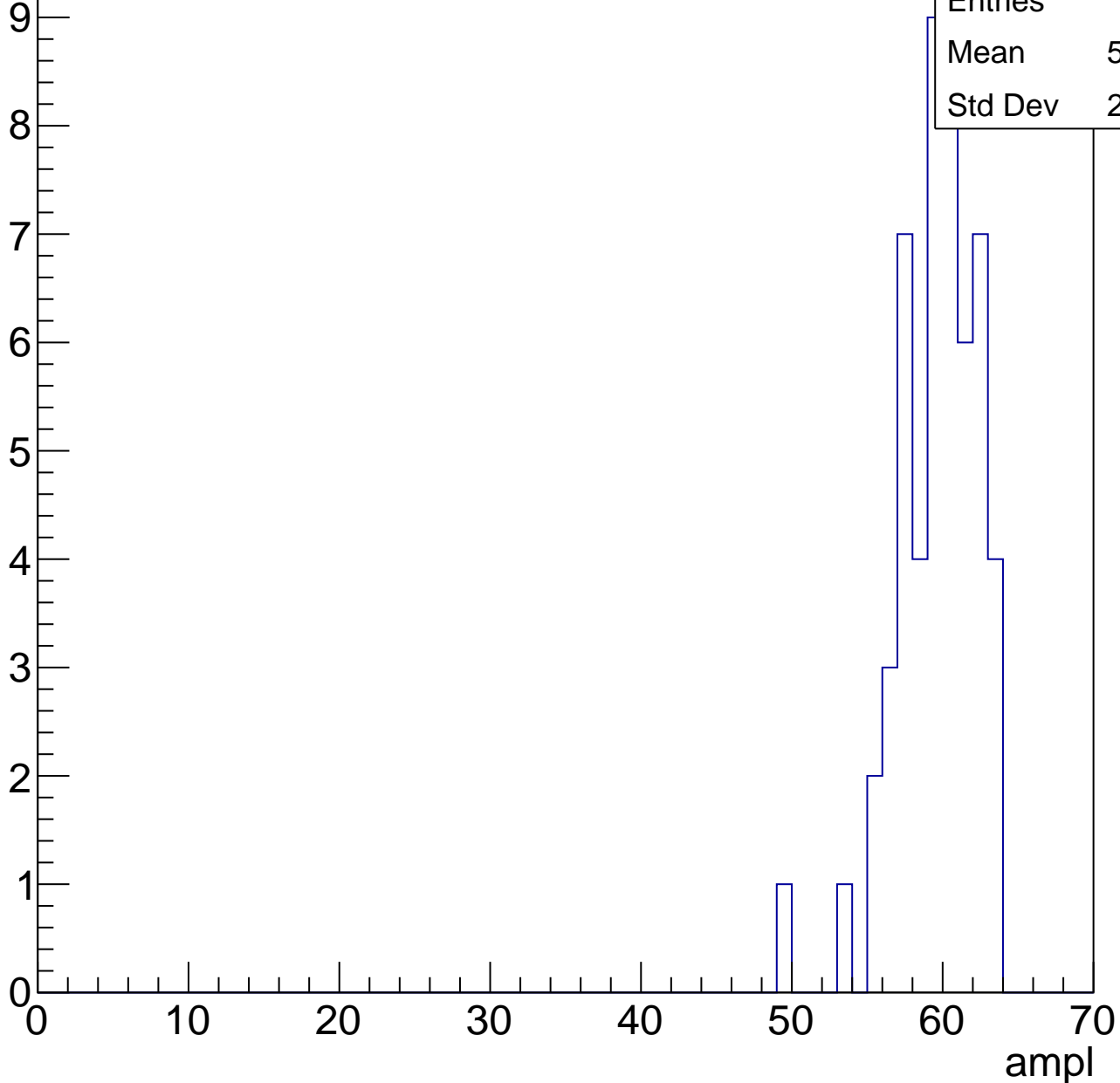


# B1L003S, U22-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	52
Mean	59.12
Std Dev	2.722

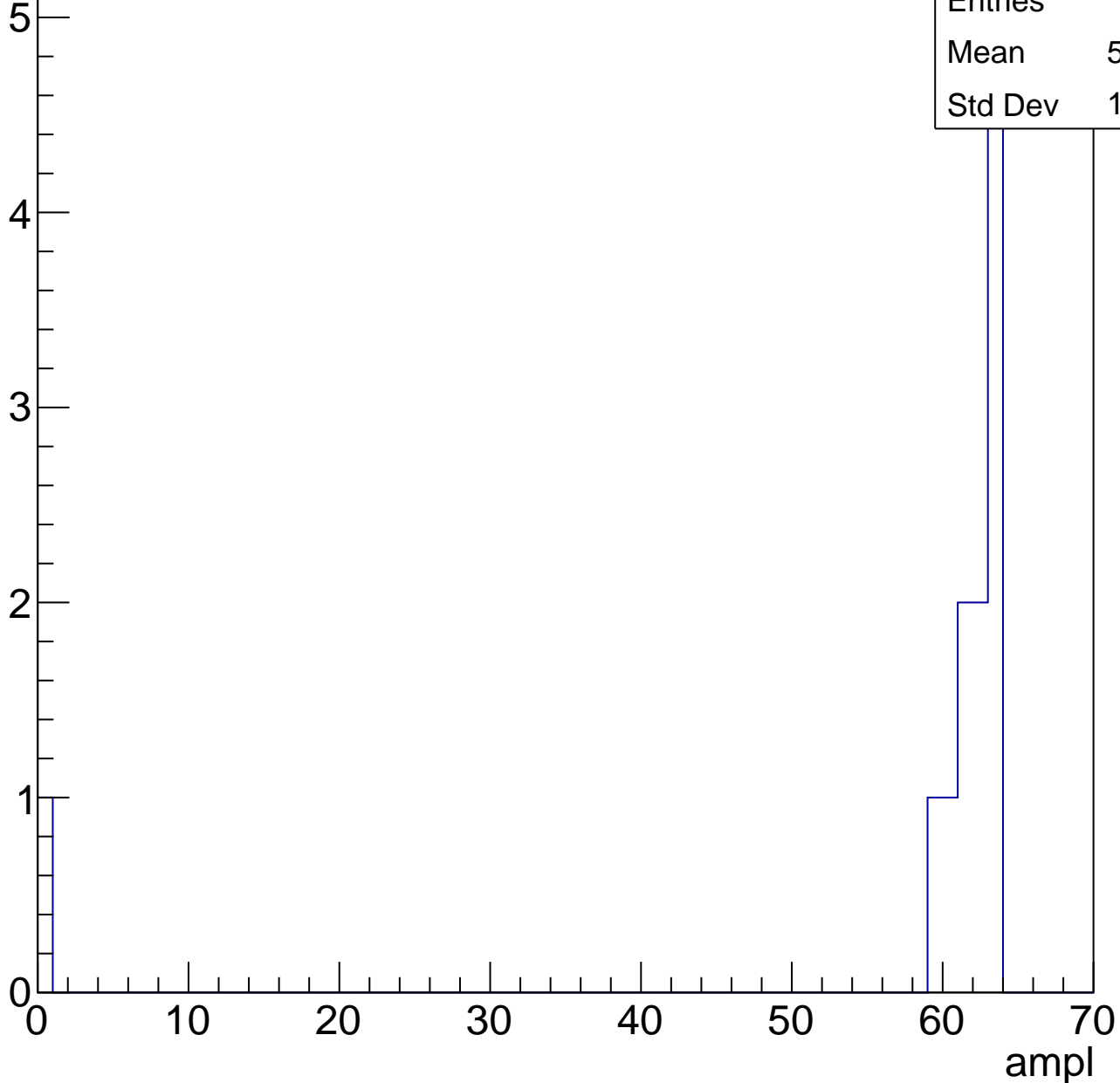


# B1L003S, U22-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	12
Mean	56.67
Std Dev	17.13





# B1L003S, U22-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch125, adc0

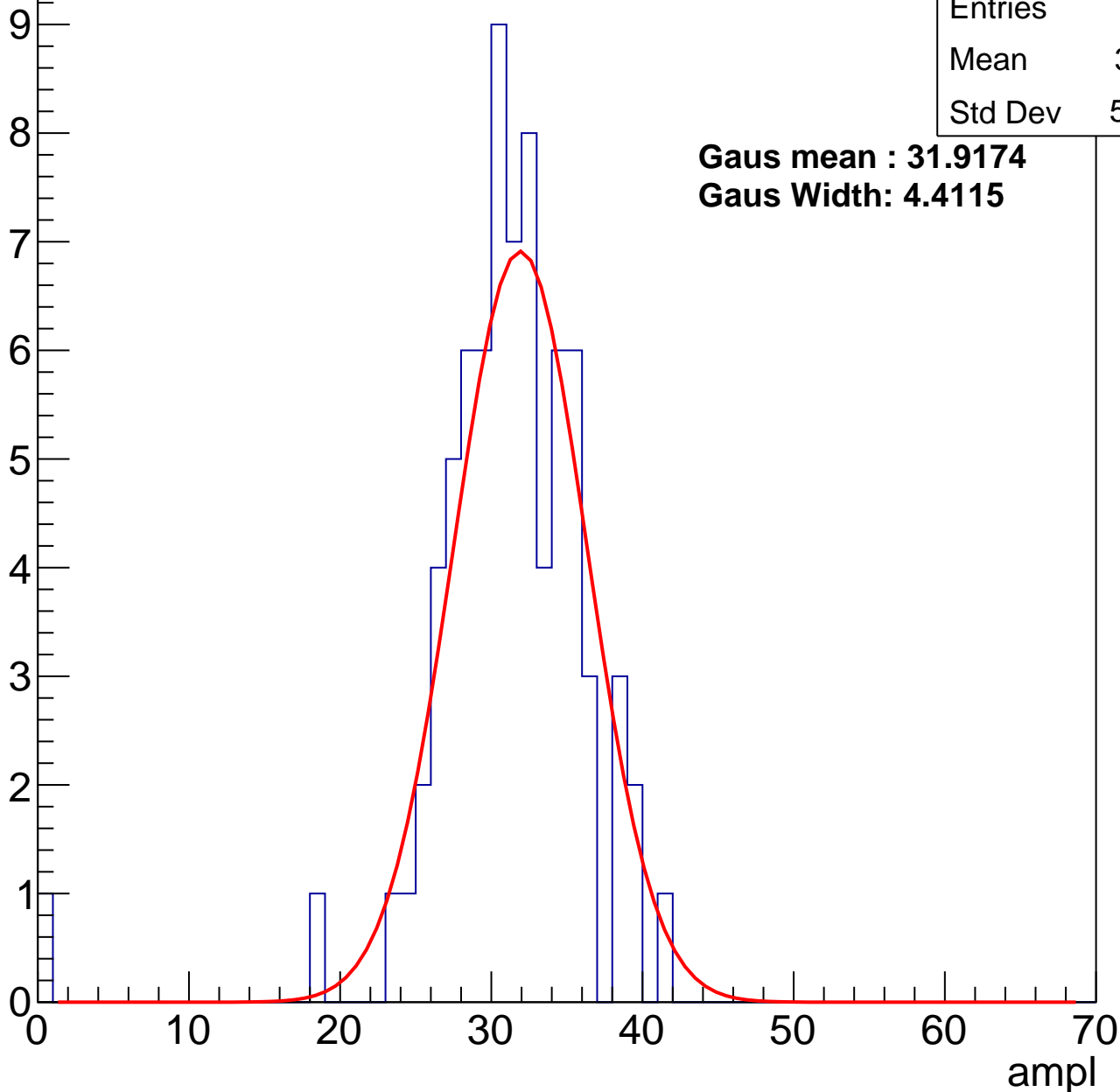
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	76
Mean	30.61
Std Dev	5.373

**Gaus mean : 31.9174**

**Gaus Width: 4.4115**



# B1L003S, U22-ch125, adc1

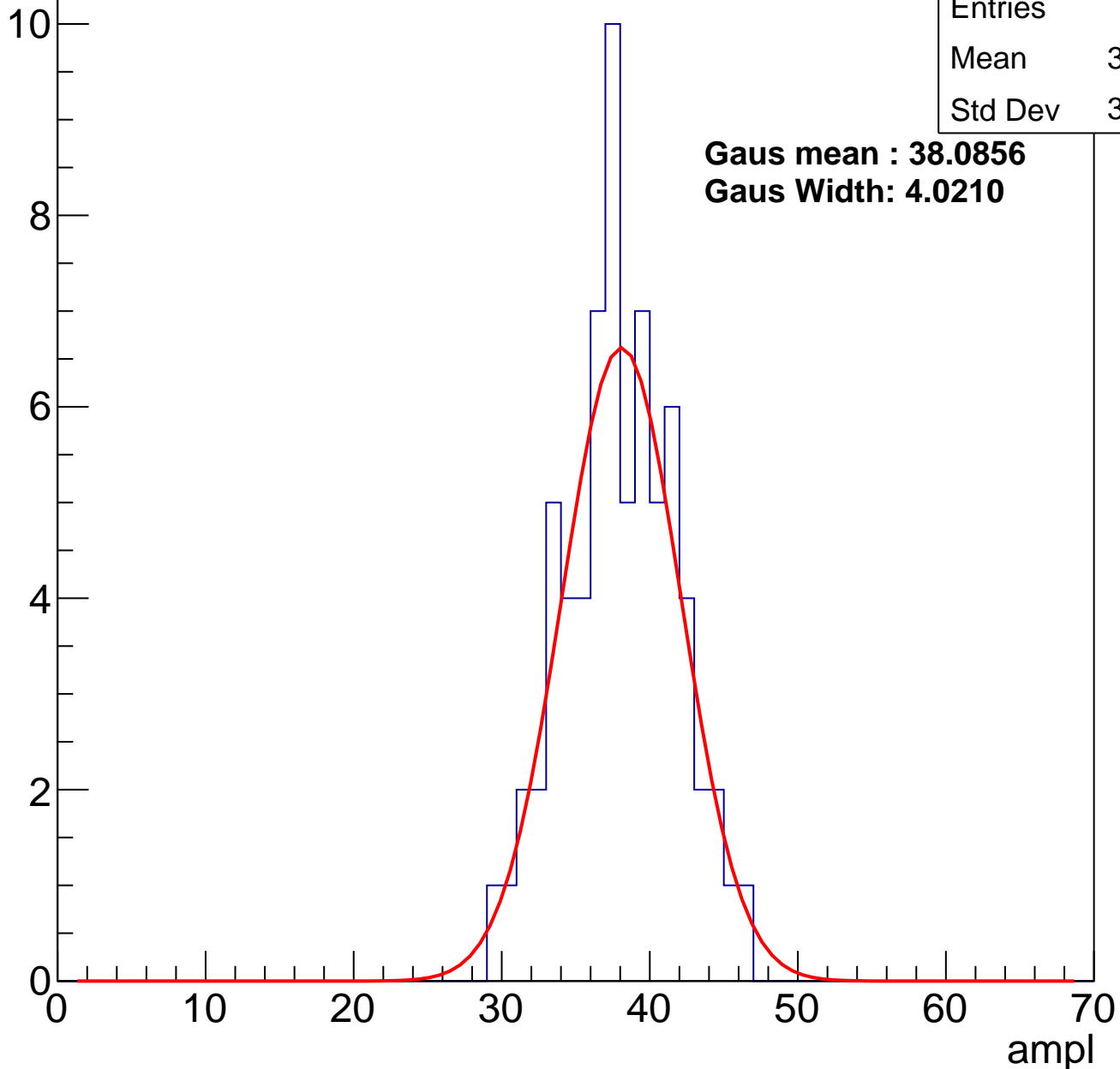
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	69
Mean	37.54
Std Dev	3.697

**Gaus mean : 38.0856**

**Gaus Width: 4.0210**

Entry



# B1L003S, U22-ch125, adc2

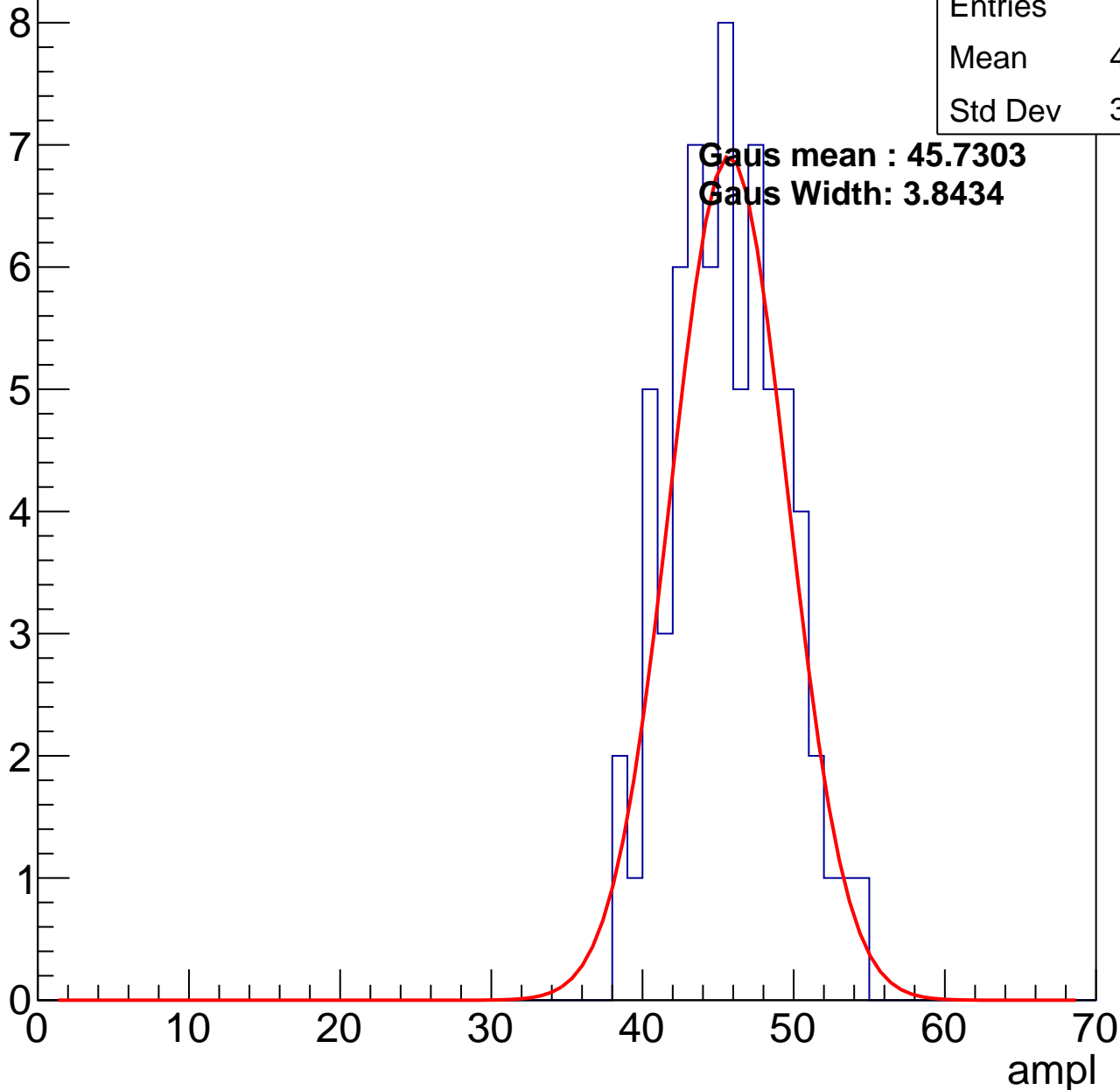
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	45.22
Std Dev	3.643

**Gaus mean : 45.7303**

**Gaus Width: 3.8434**

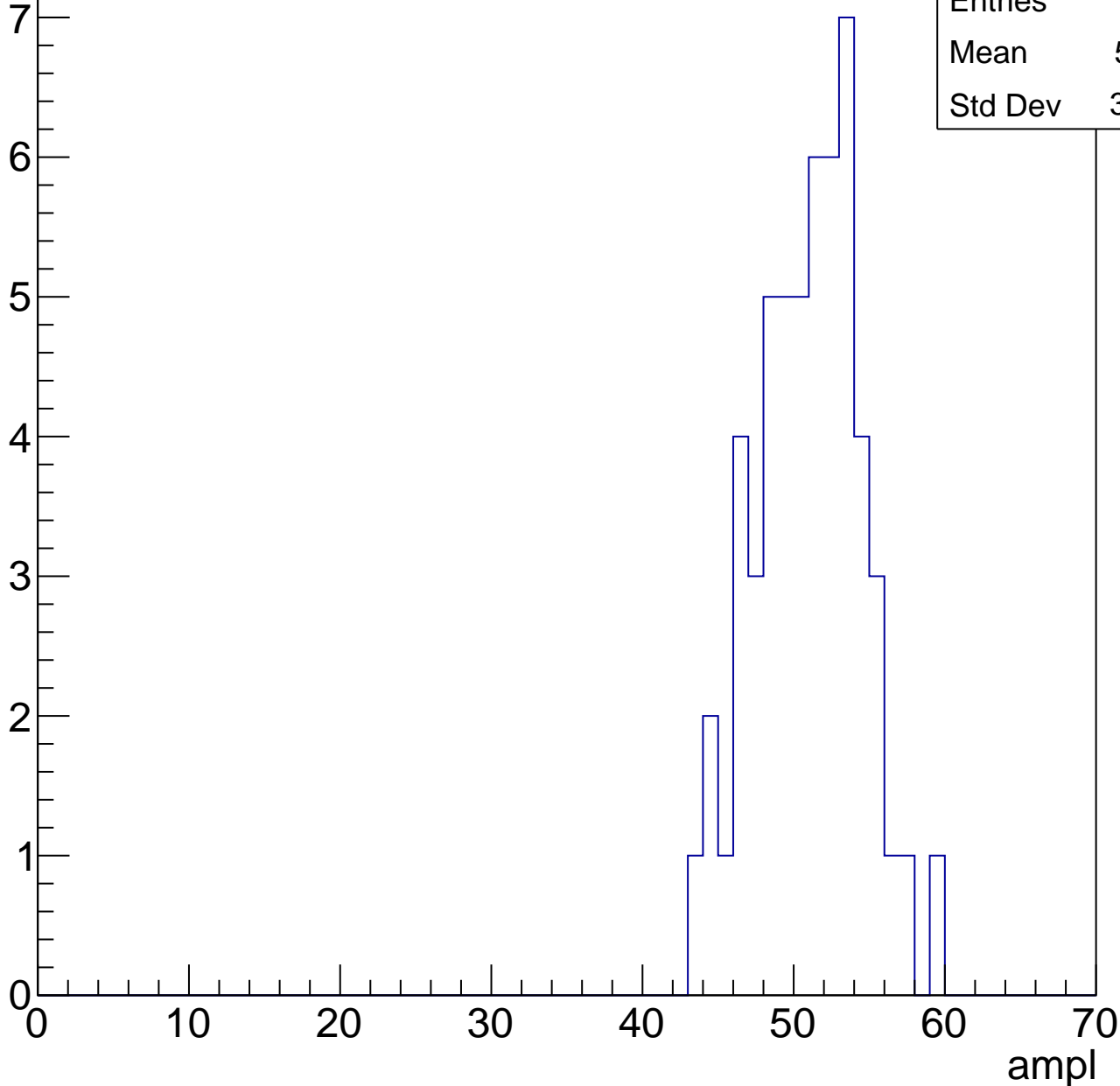


# B1L003S, U22-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	50.51
Std Dev	3.426

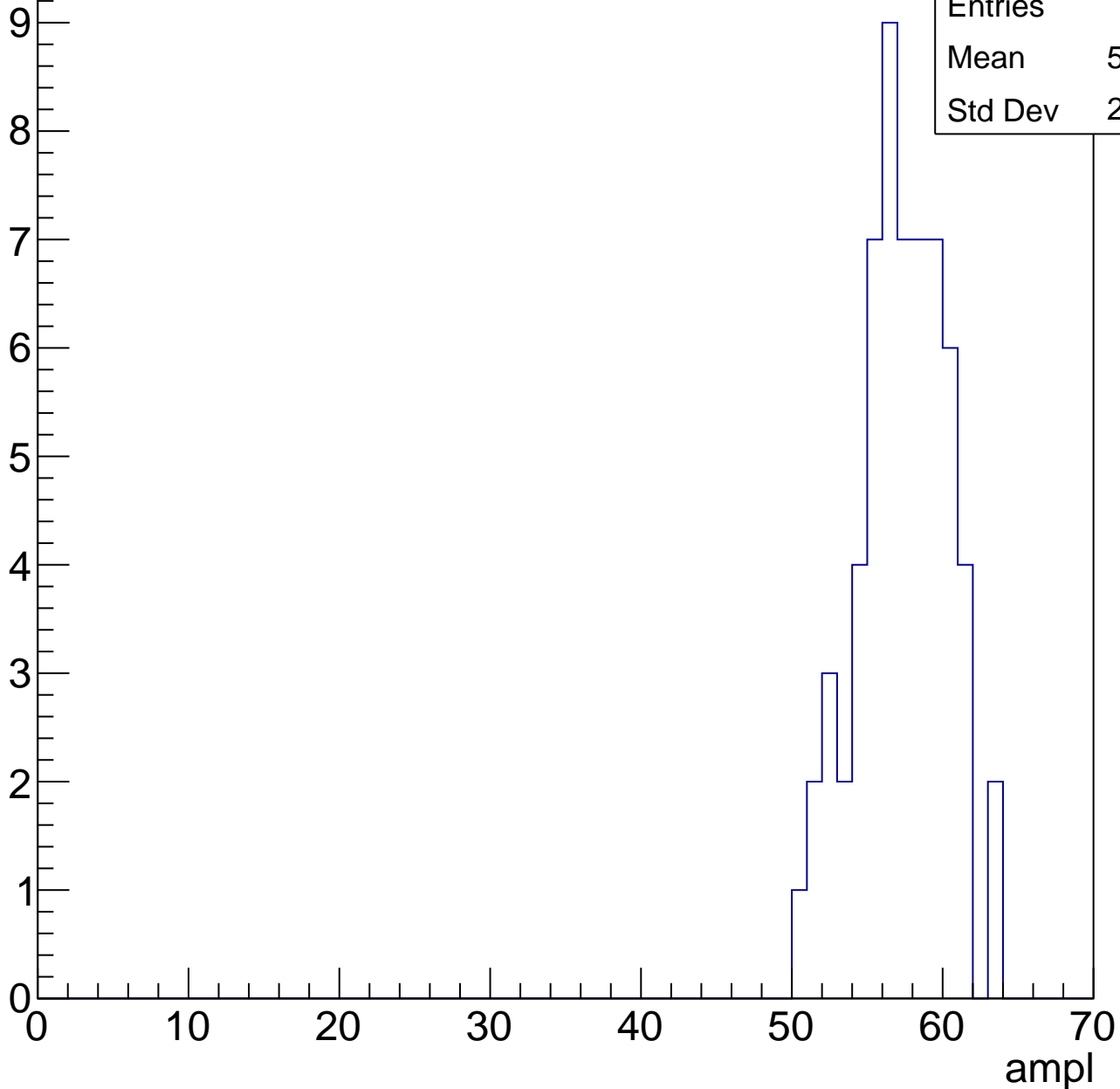


# B1L003S, U22-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	61
Mean	56.84
Std Dev	2.932

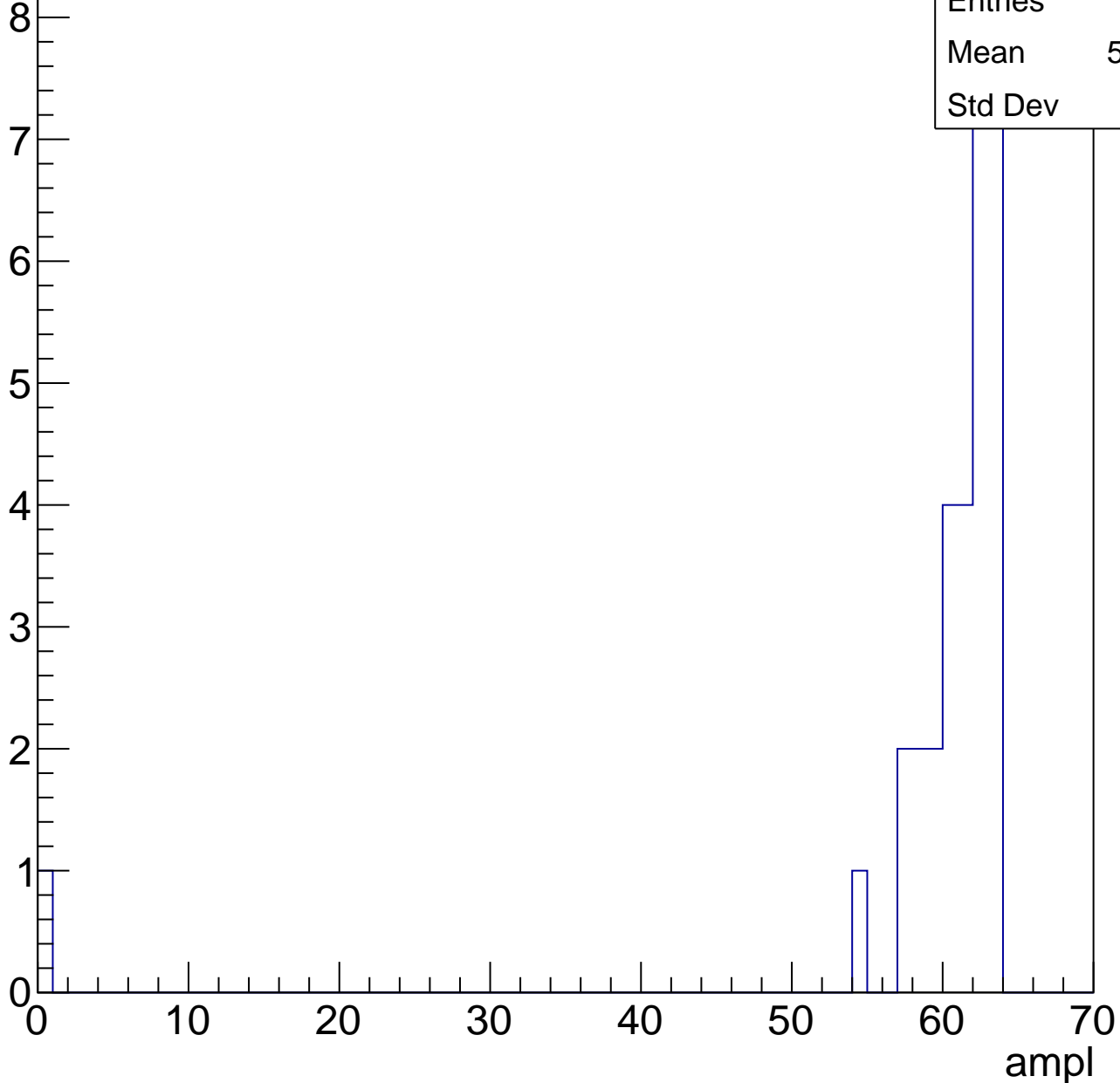


# B1L003S, U22-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	32
Mean	58.94
Std Dev	10.8



# B1L003S, U22-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	60.8
Std Dev	1.47

ampl

0 10 20 30 40 50 60 70

0

0.2

0.4

0.6

0.8

1

1.2

1.4

1.6

1.8

2



# B1L003S, U22-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	4
Mean	0
Std Dev	0

# B1L003S, U22-ch126, adc0

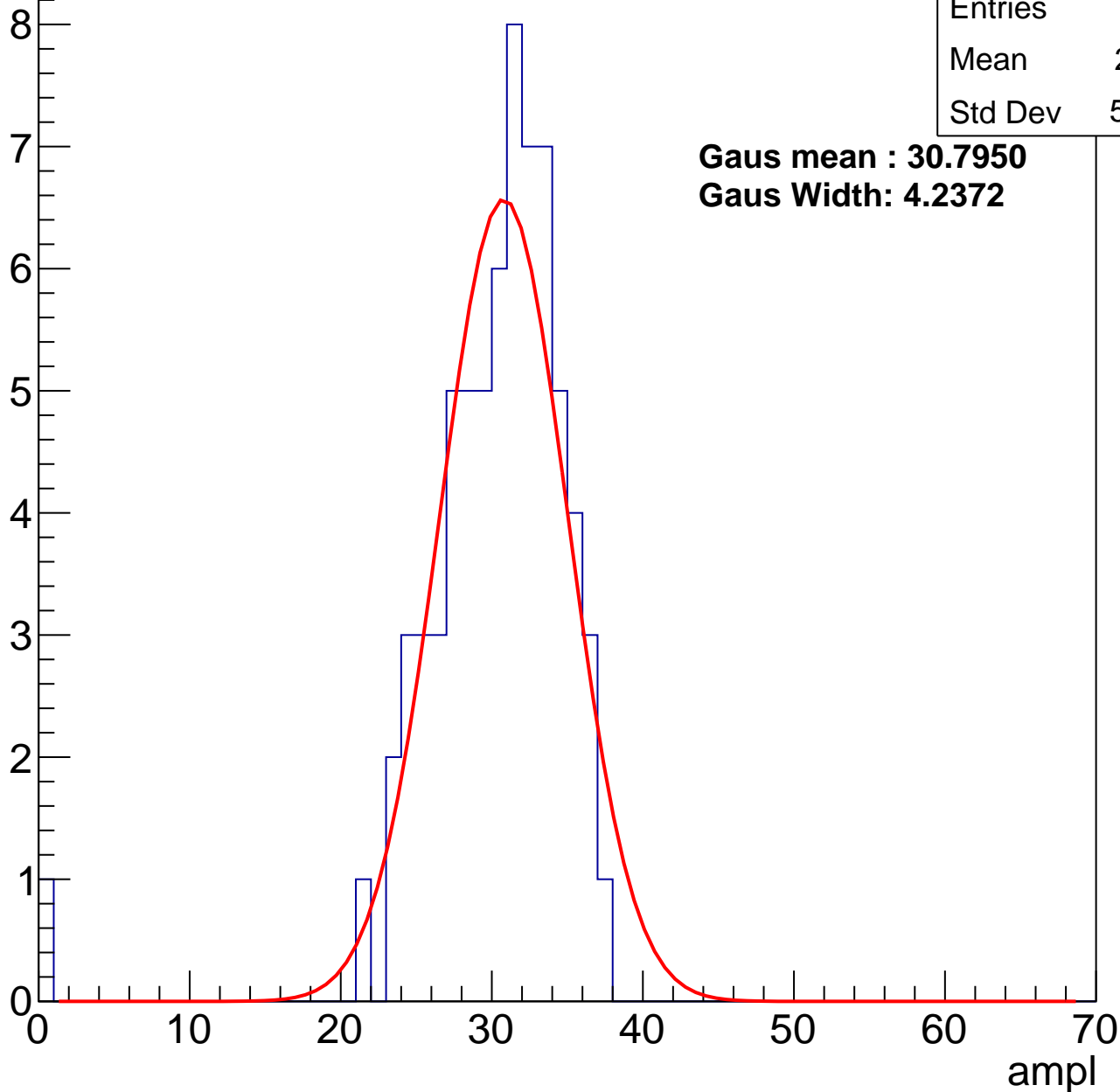
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.71
Std Dev	5.122

**Gaus mean : 30.7950**

**Gaus Width: 4.2372**



# B1L003S, U22-ch126, adc1

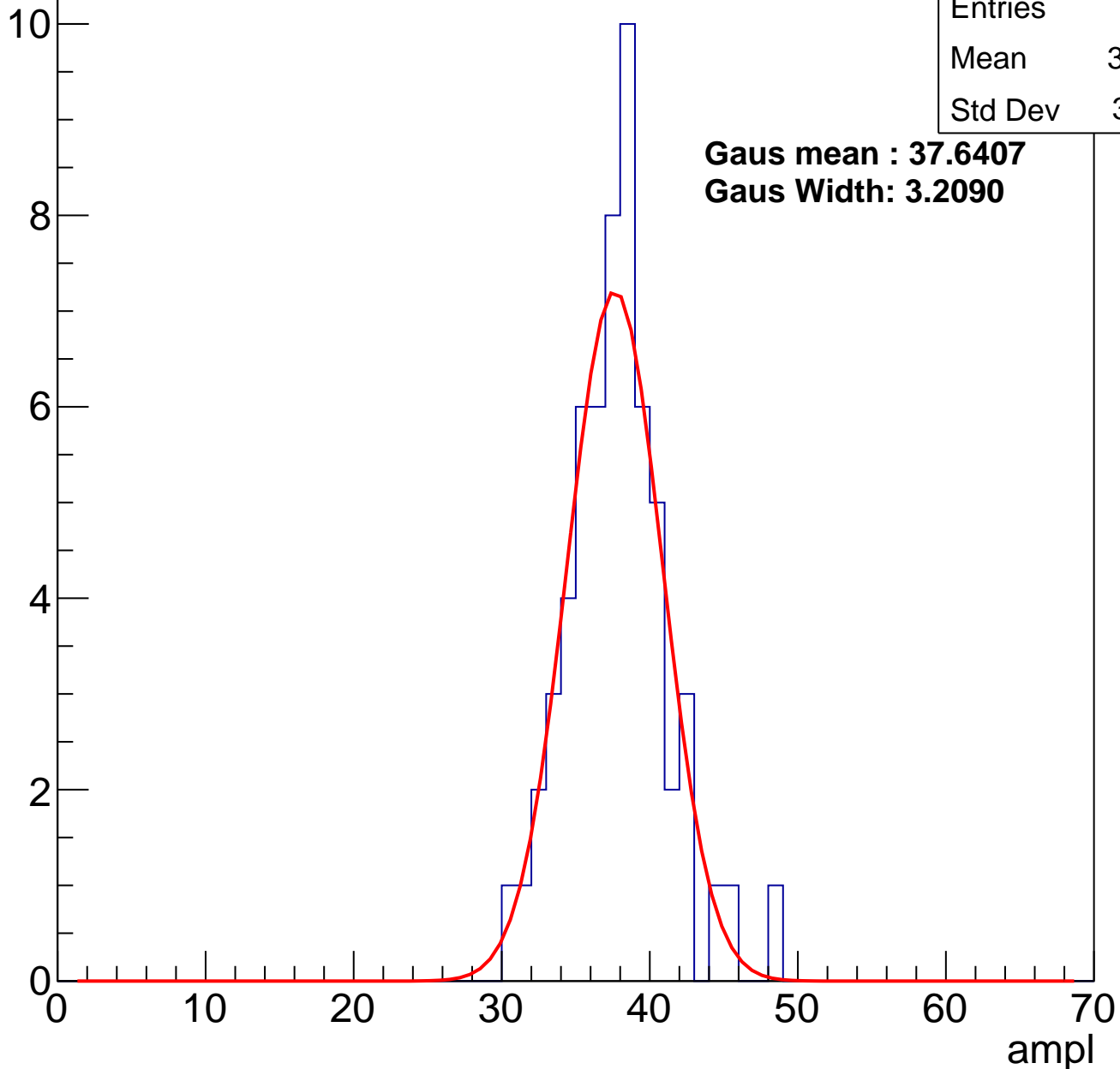
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	60
Mean	37.35
Std Dev	3.321

**Gaus mean : 37.6407**

**Gaus Width: 3.2090**

Entry



# B1L003S, U22-ch126, adc2

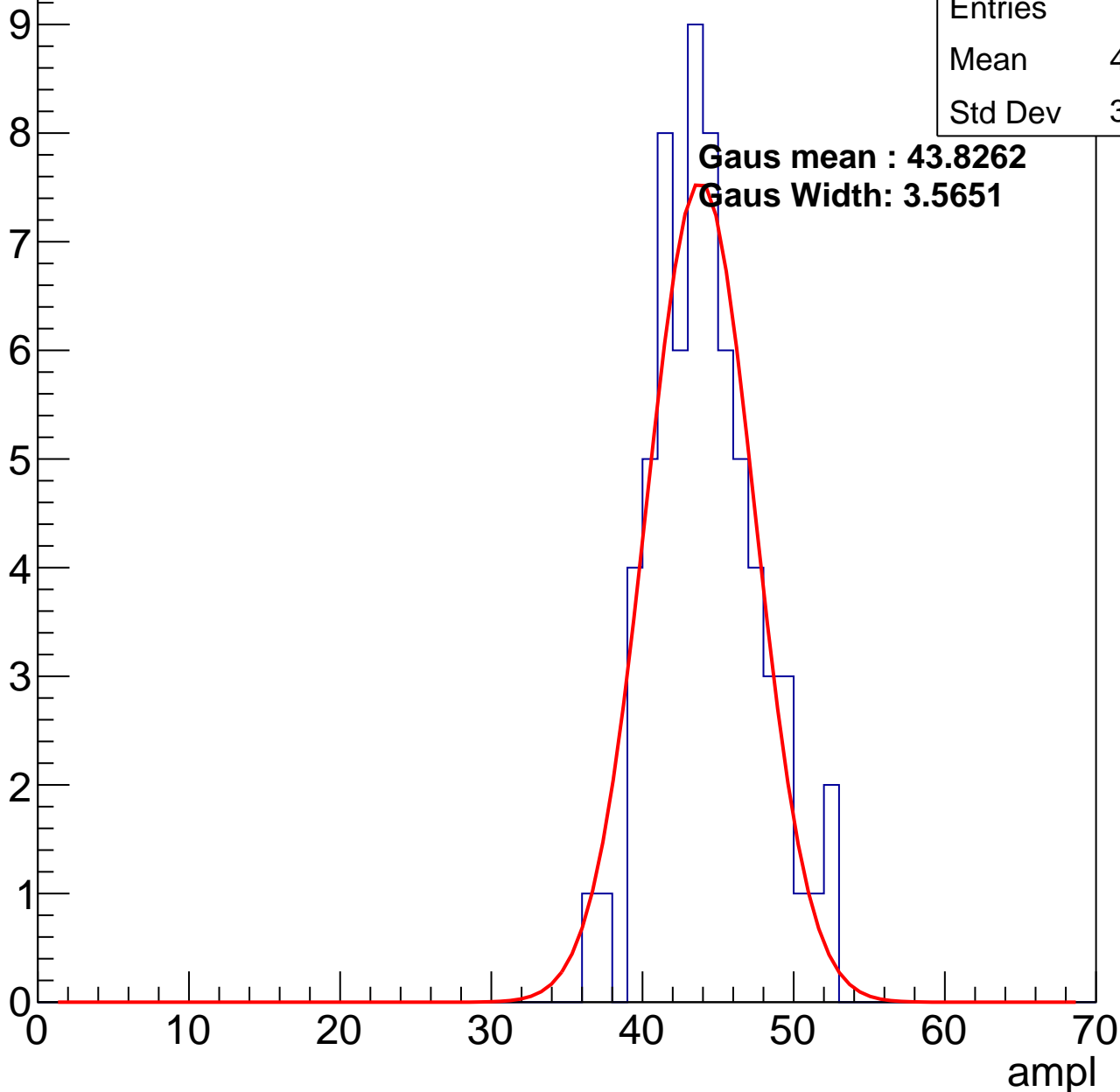
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	67
Mean	43.76
Std Dev	3.439

**Gaus mean : 43.8262**

**Gaus Width: 3.5651**

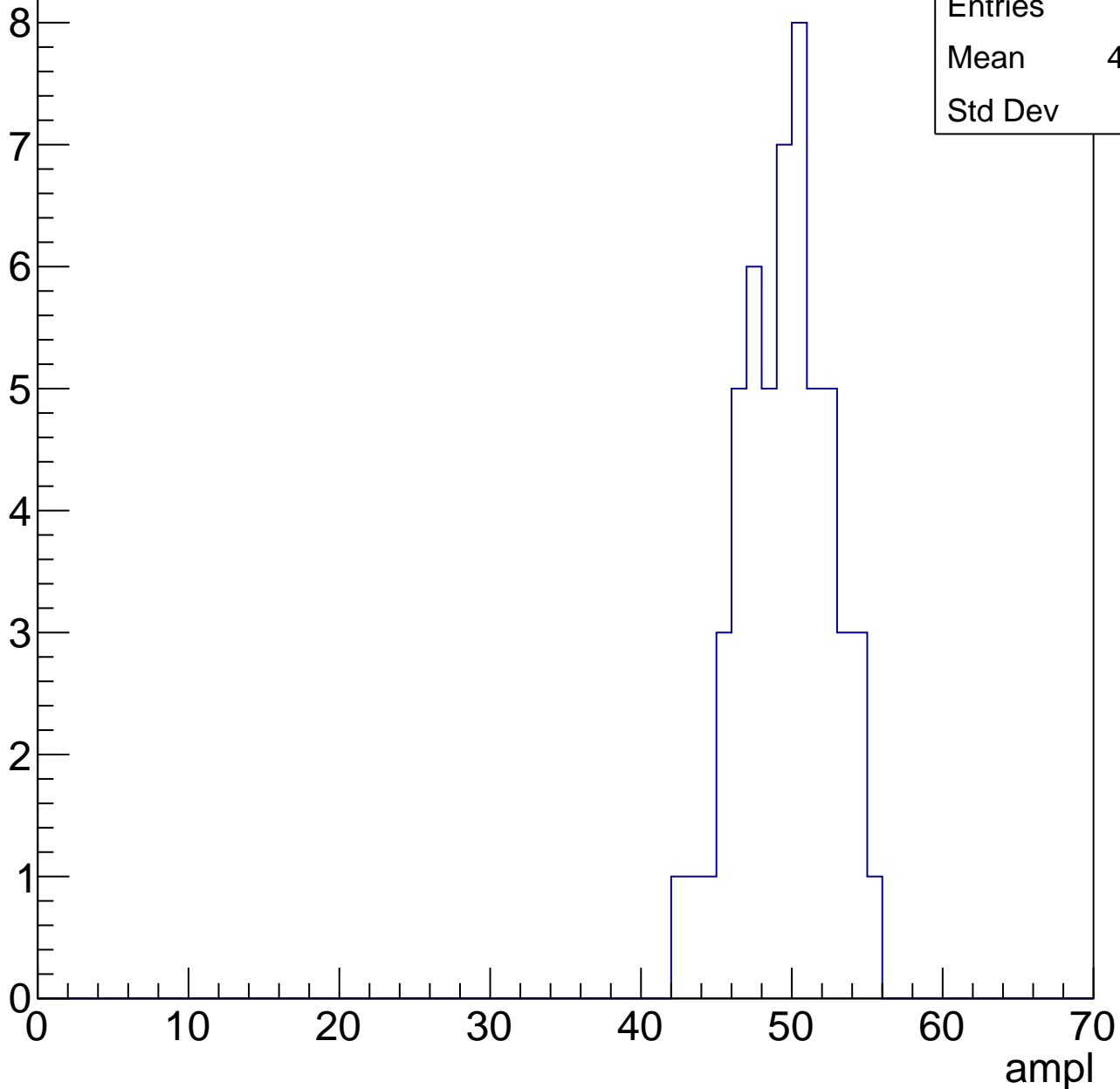


# B1L003S, U22-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	54
Mean	49.07
Std Dev	2.93

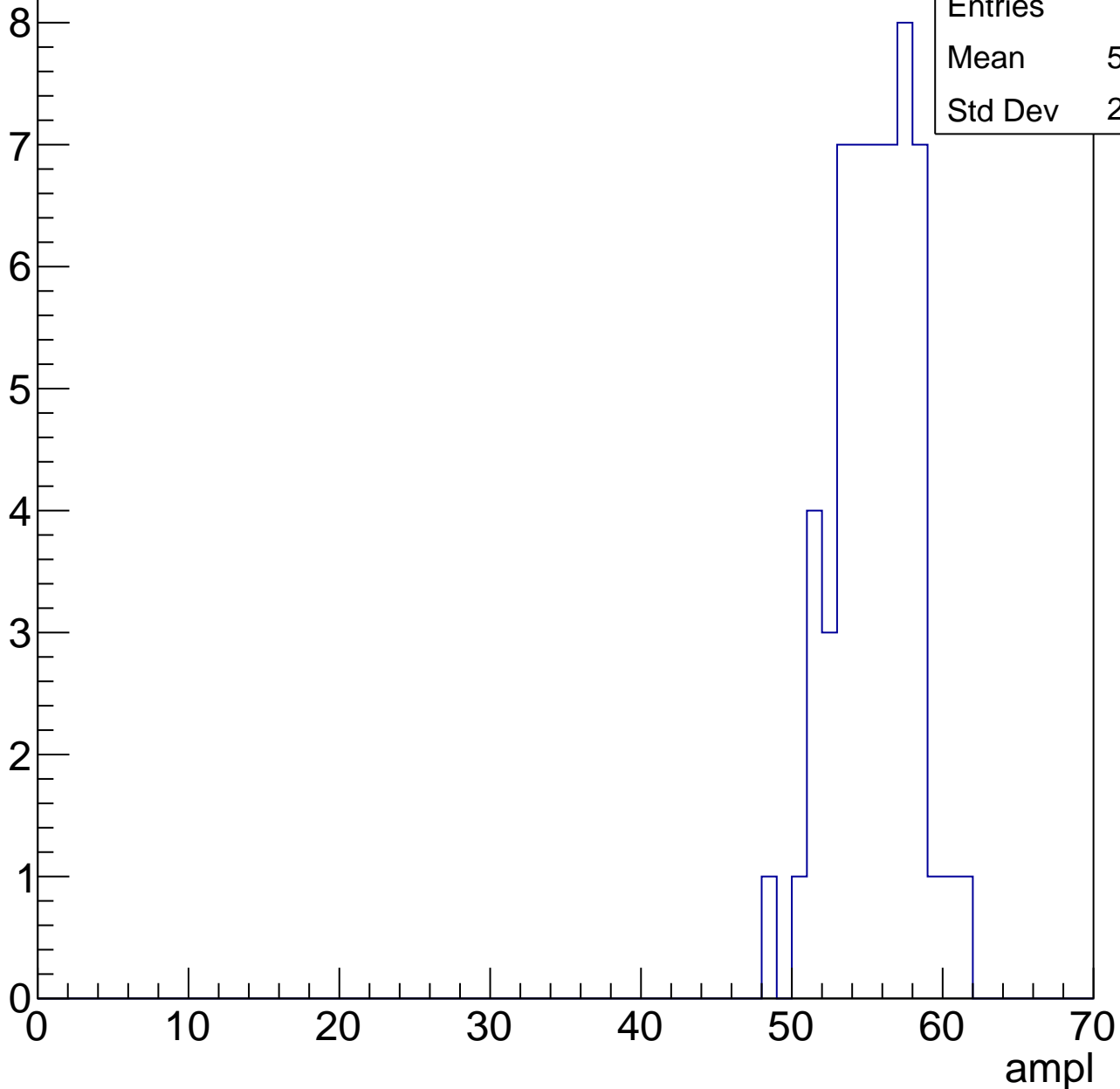


# B1L003S, U22-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.02
Std Dev	2.625

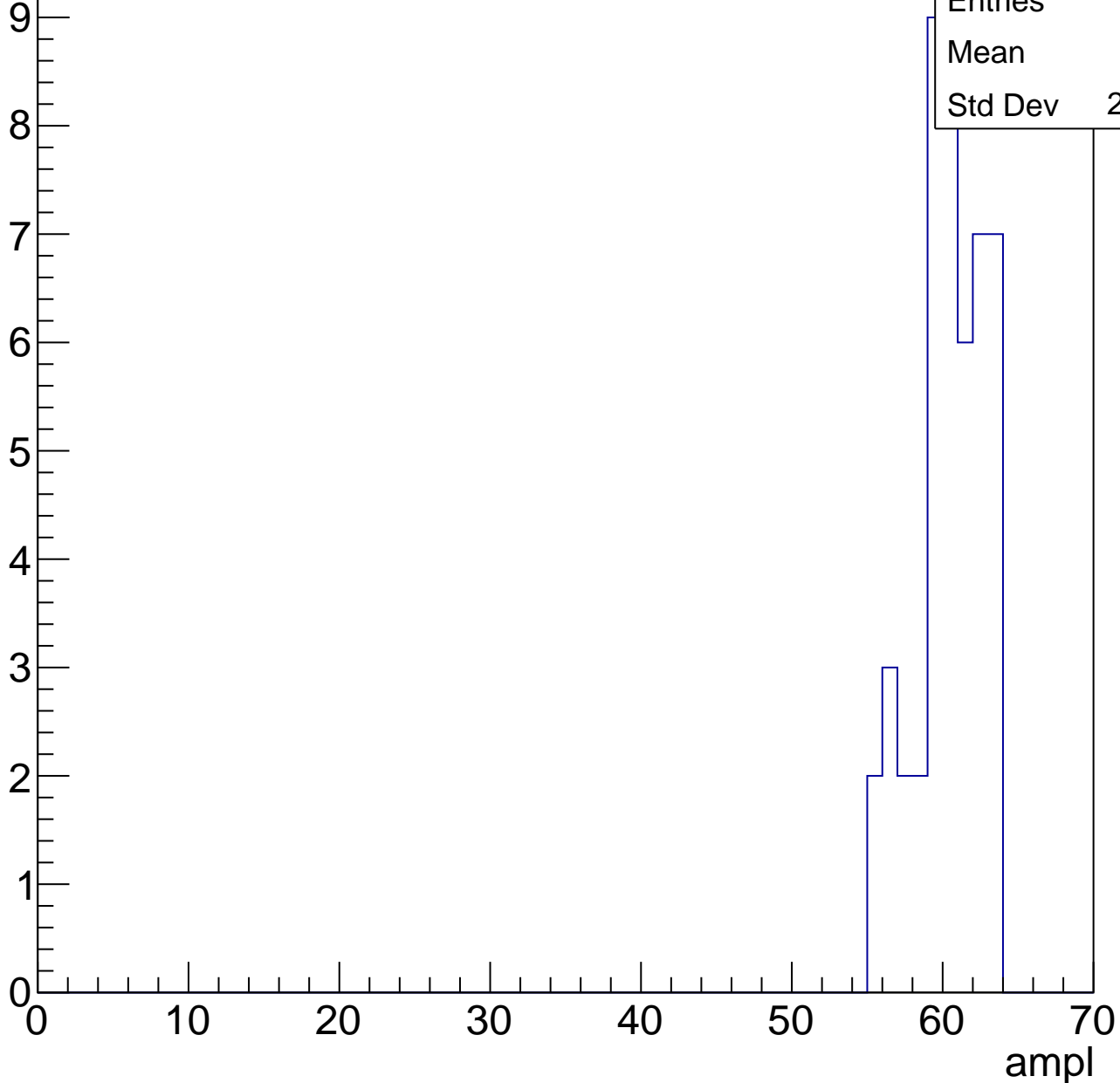


# B1L003S, U22-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

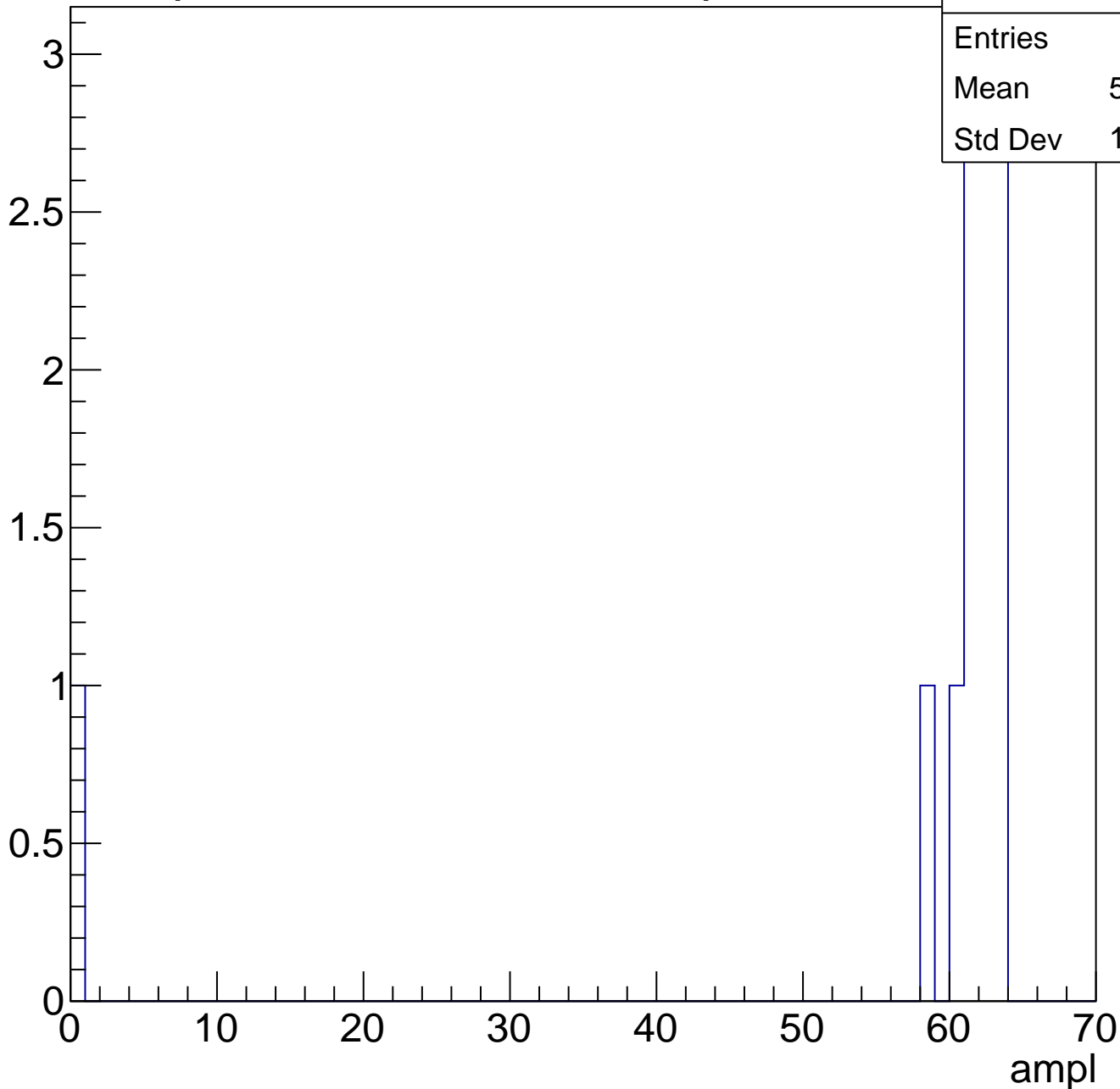
Entries	46
Mean	60
Std Dev	2.236



# B1L003S, U22-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch127, adc0

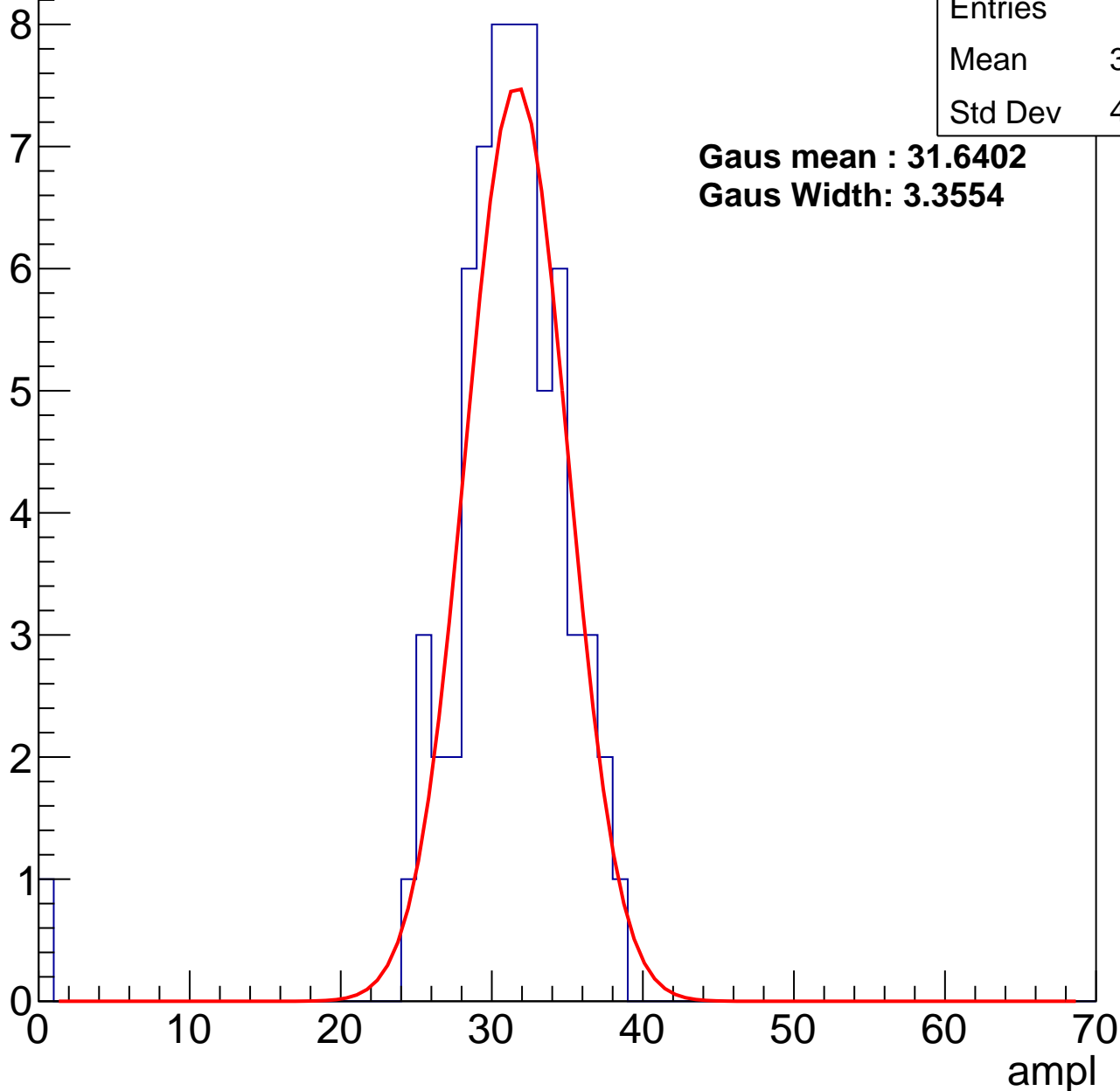
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.52
Std Dev	4.924

**Gaus mean : 31.6402**

**Gaus Width: 3.3554**



# B1L003S, U22-ch127, adc1

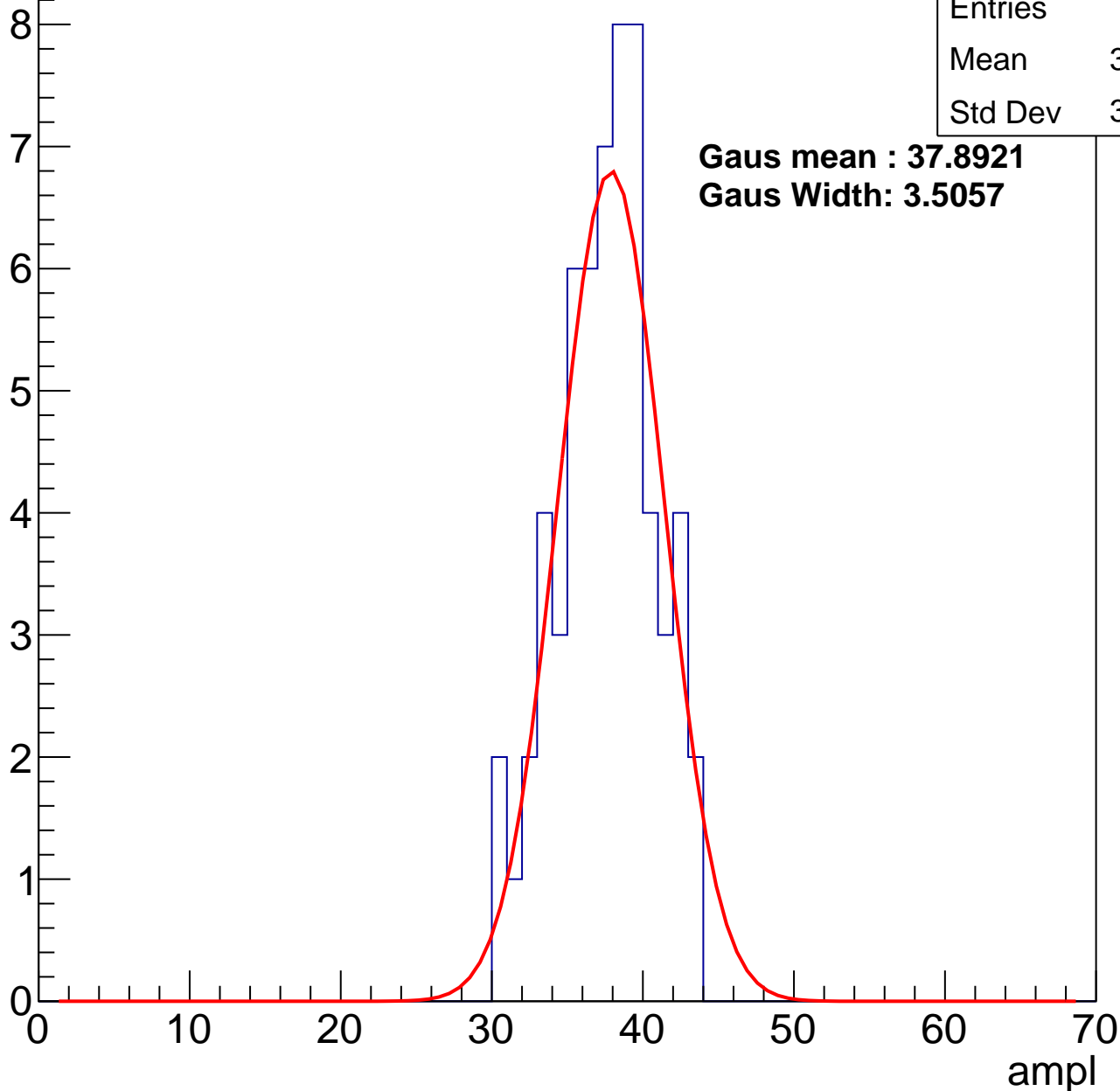
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	60
Mean	37.12
Std Dev	3.163

**Gaus mean : 37.8921**

**Gaus Width: 3.5057**



# B1L003S, U22-ch127, adc2

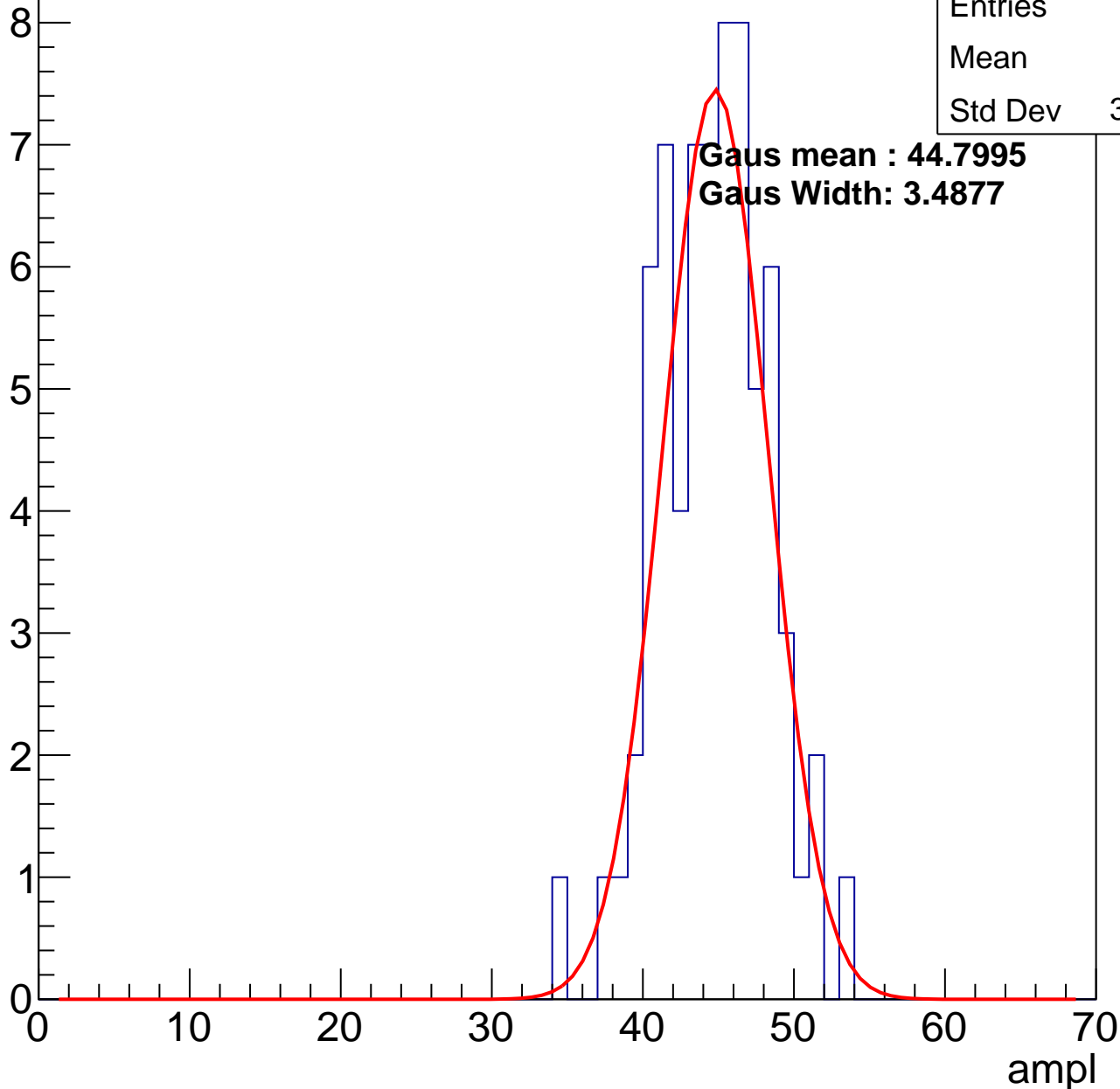
calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	70
Mean	44.2
Std Dev	3.556

**Gaus mean : 44.7995**

**Gaus Width: 3.4877**

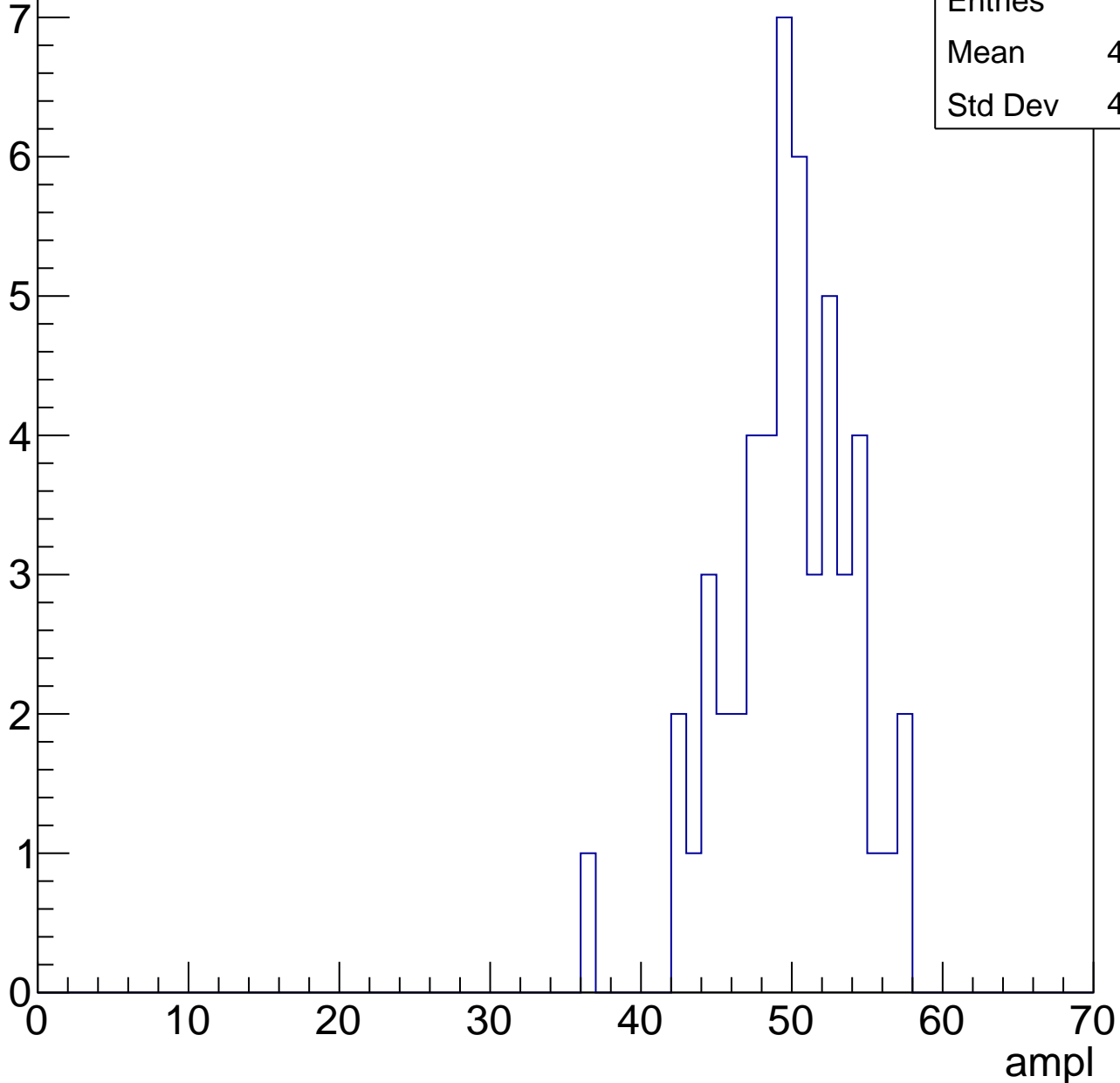


# B1L003S, U22-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	51
Mean	49.27
Std Dev	4.126

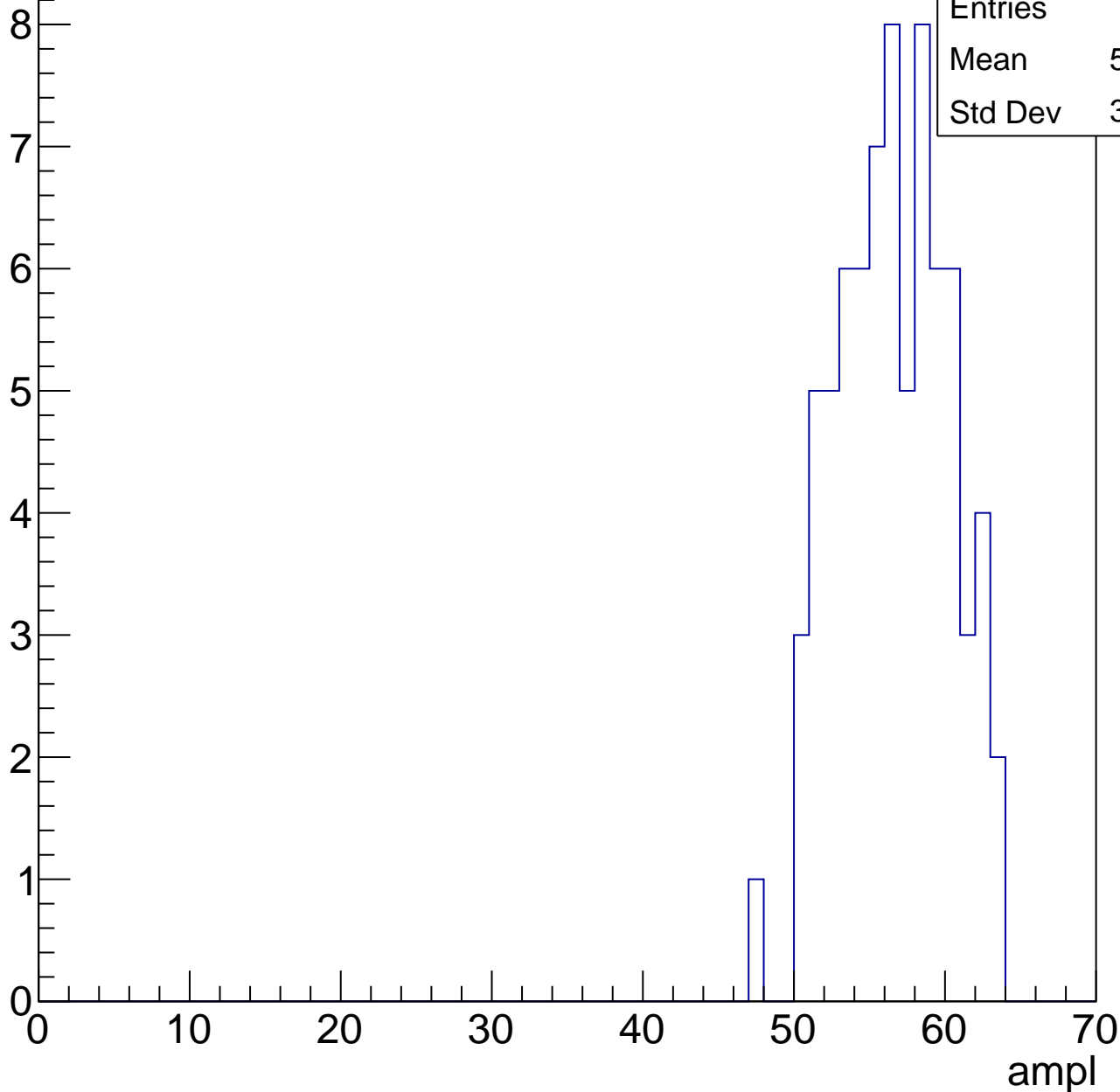


# B1L003S, U22-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

Entries	75
Mean	56.09
Std Dev	3.619

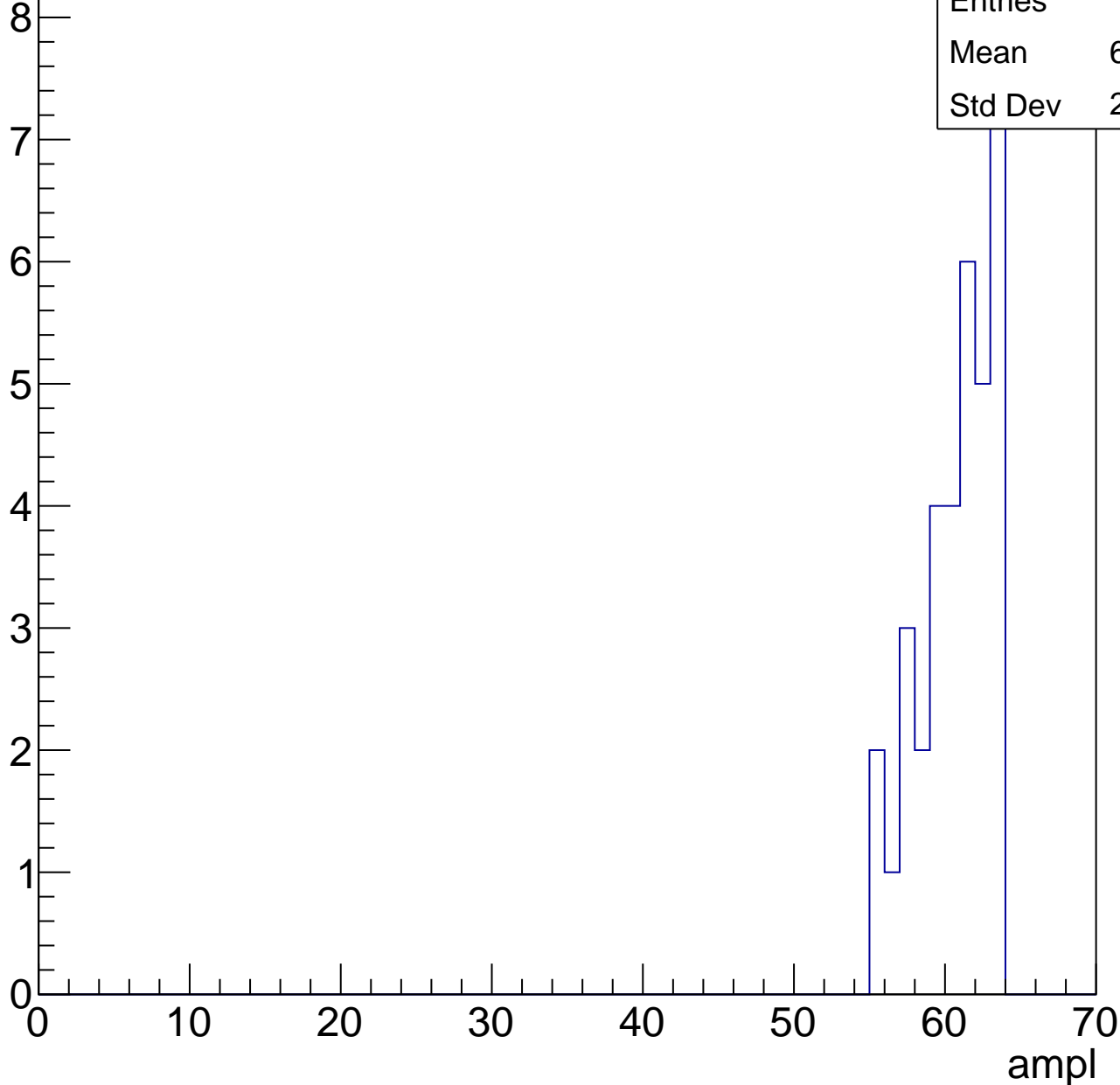


# B1L003S, U22-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry

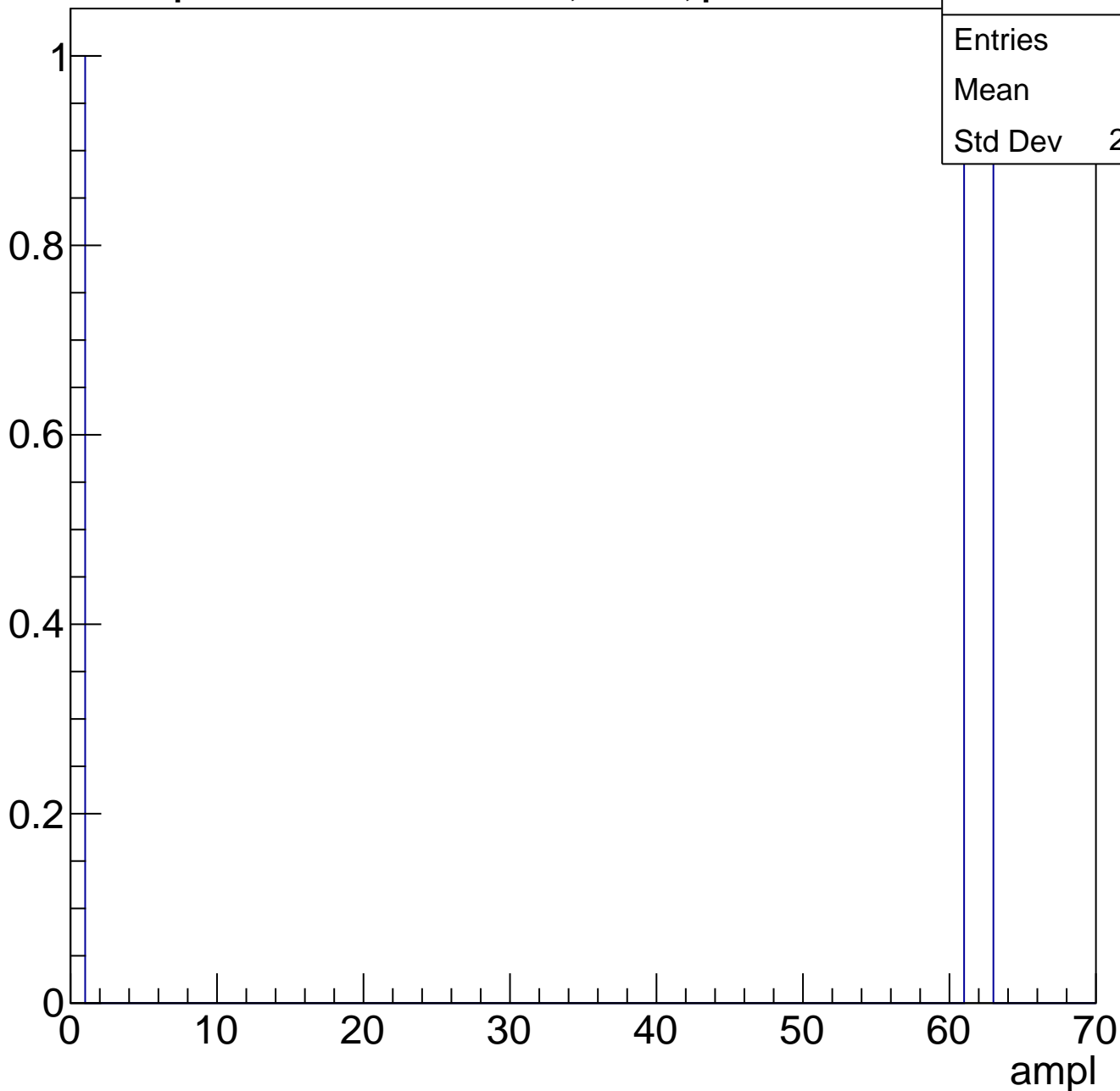
Entries	35
Mean	60.26
Std Dev	2.395



# B1L003S, U22-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry





# B1L003S, U22-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



# B1L003S, U22-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0