

B1L002S, U4-ch0

calib_packv5_042523_0143.root, FC#10, port B3

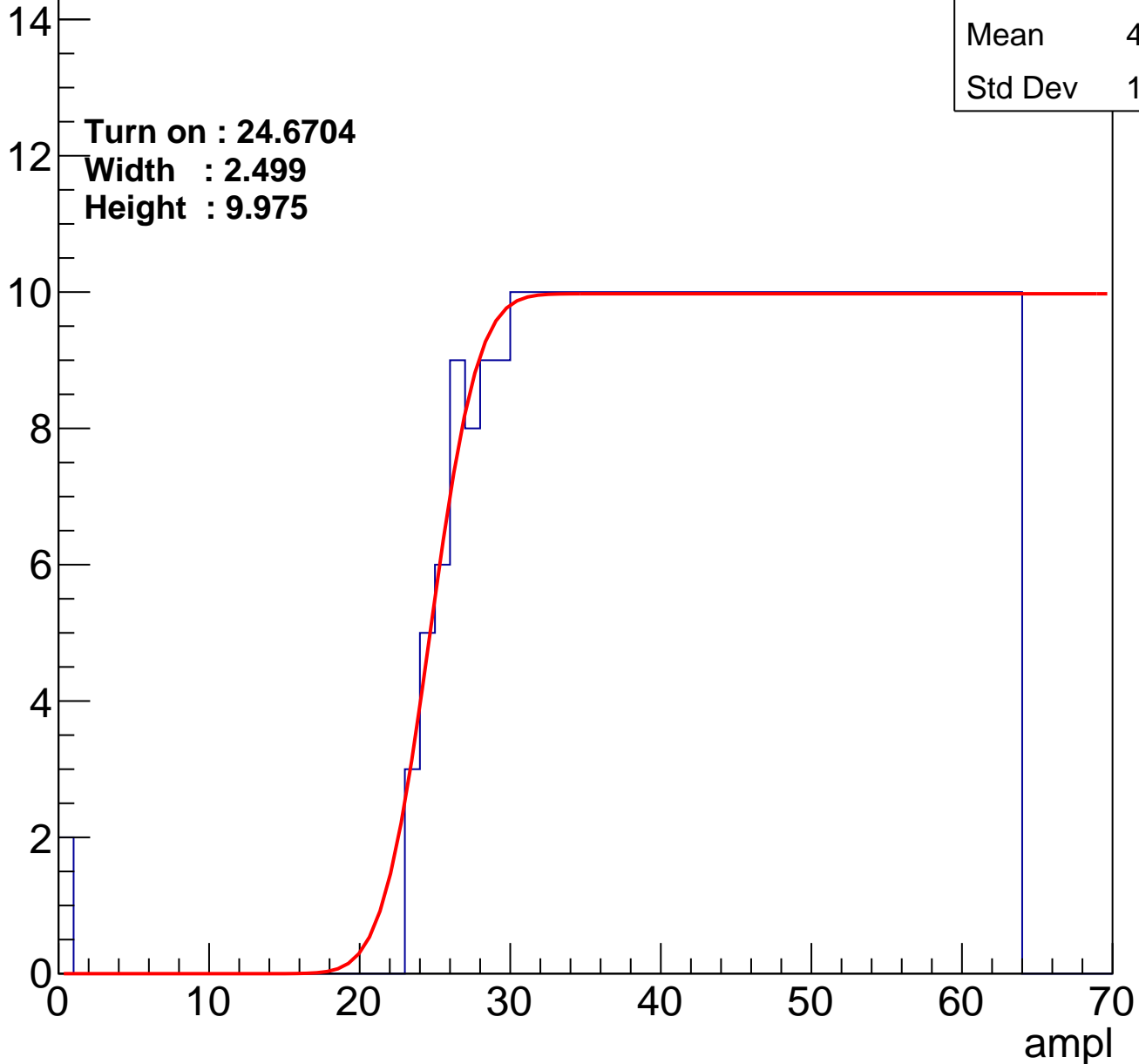
Entries	391
Mean	43.76
Std Dev	11.72

Turn on : 24.6704

Width : 2.499

Height : 9.975

Entry



B1L002S, U4-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.38
Std Dev	11.81

Turn on : 23.9005

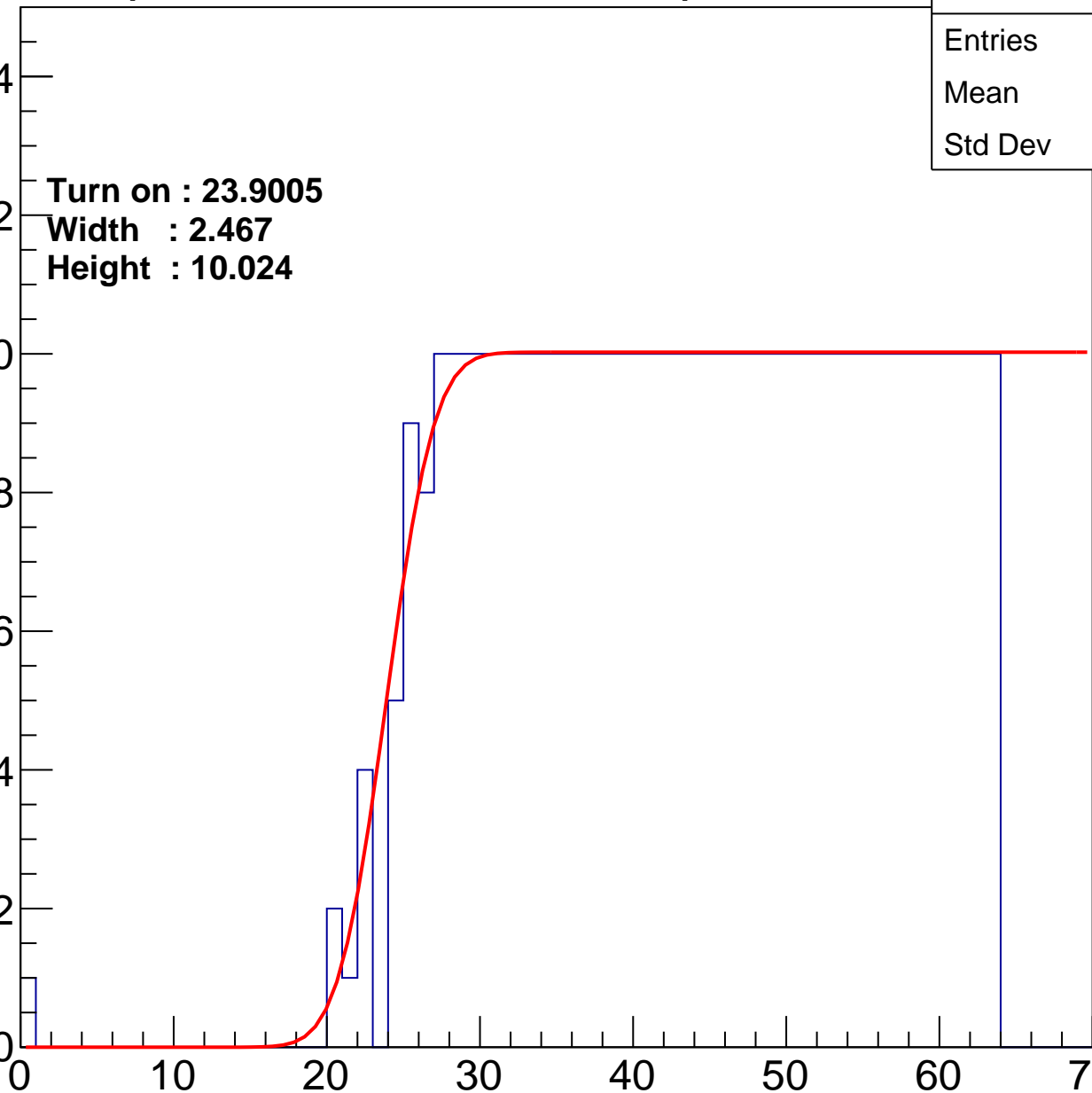
Width : 2.467

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.9524

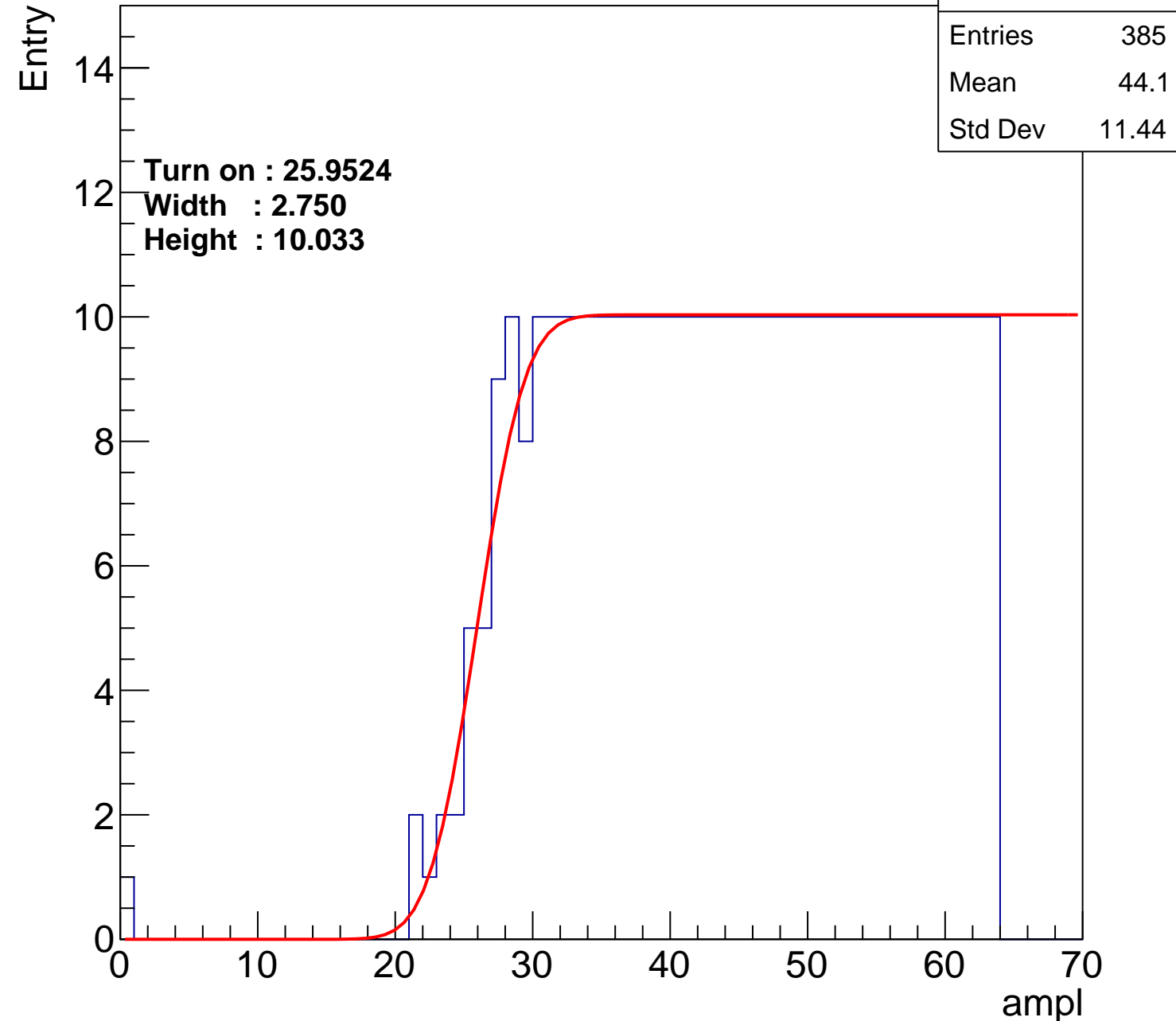
Width : 2.750

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 25.4070

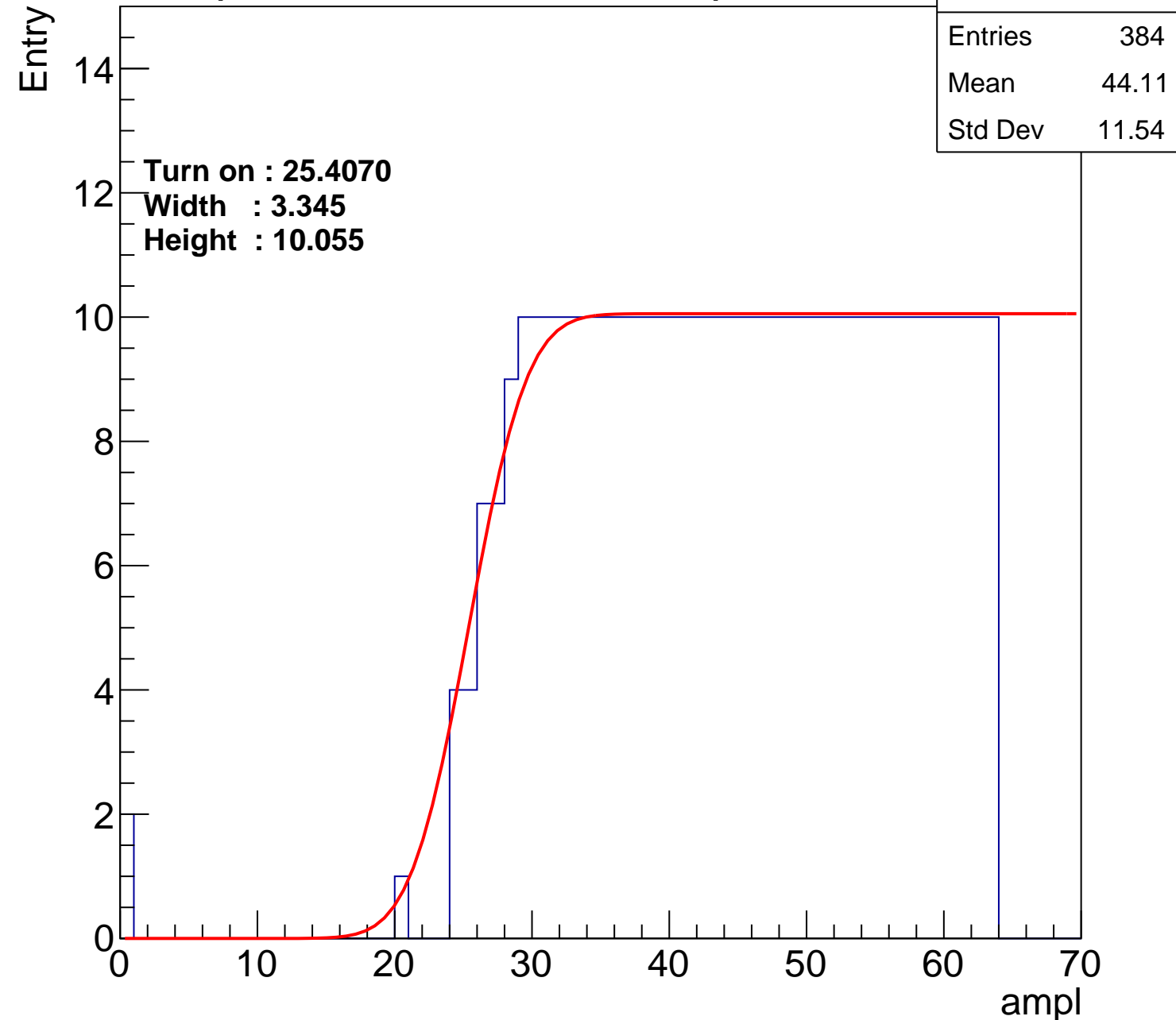
Width : 3.345

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch4

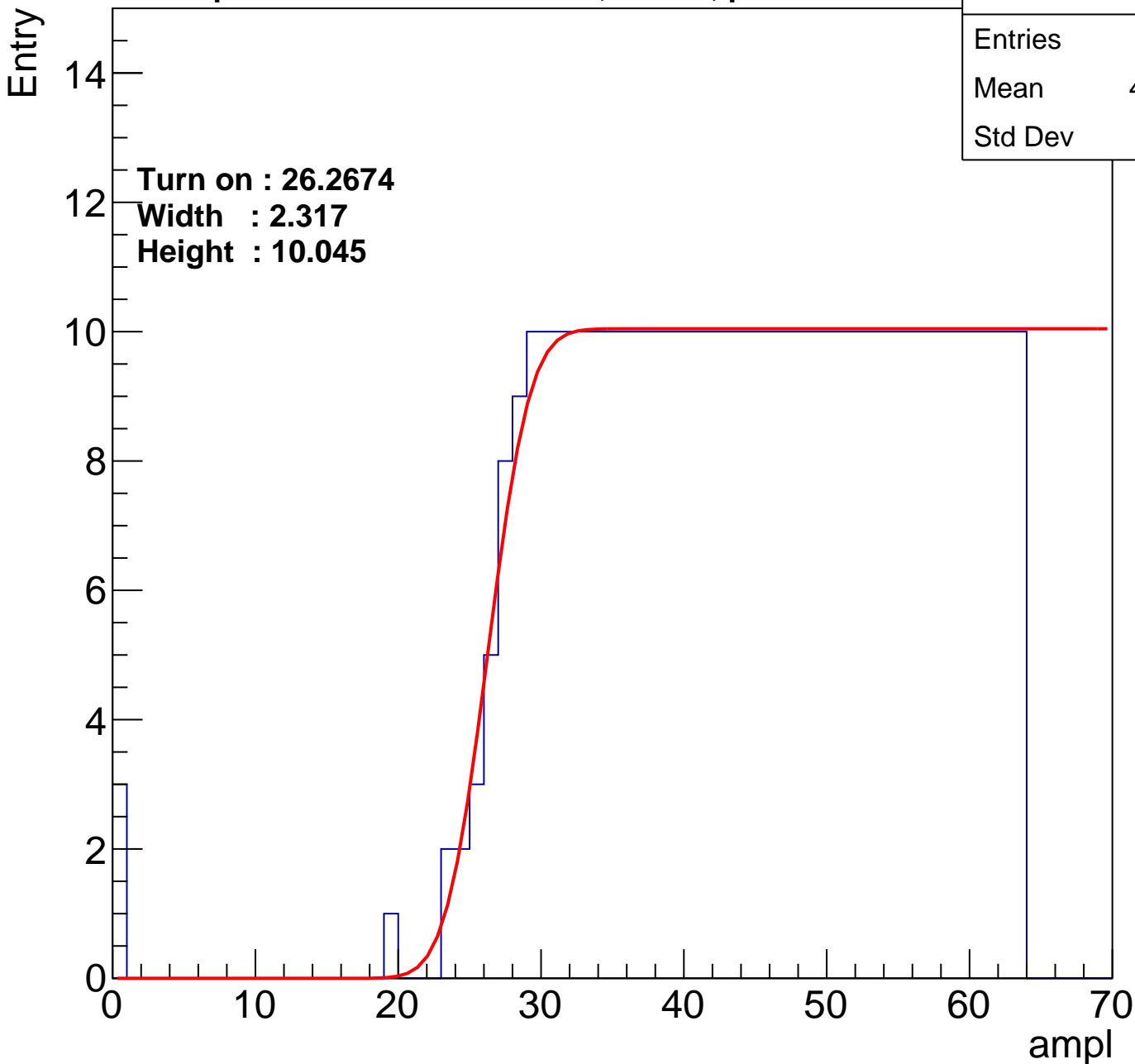
calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.2674

Width : 2.317

Height : 10.045



B1L002S, U4-ch5

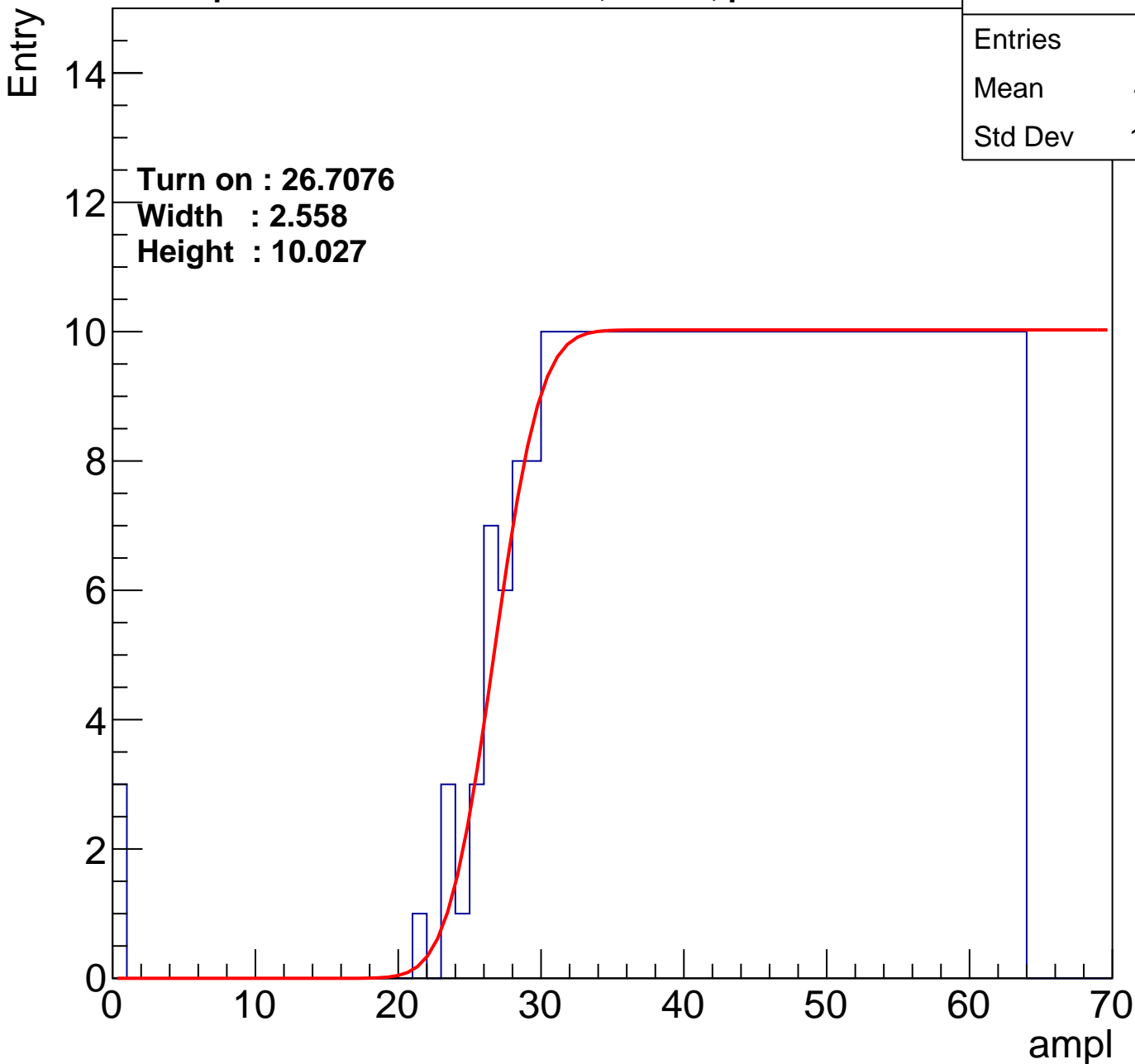
calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.7076

Width : 2.558

Height : 10.027



B1L002S, U4-ch6

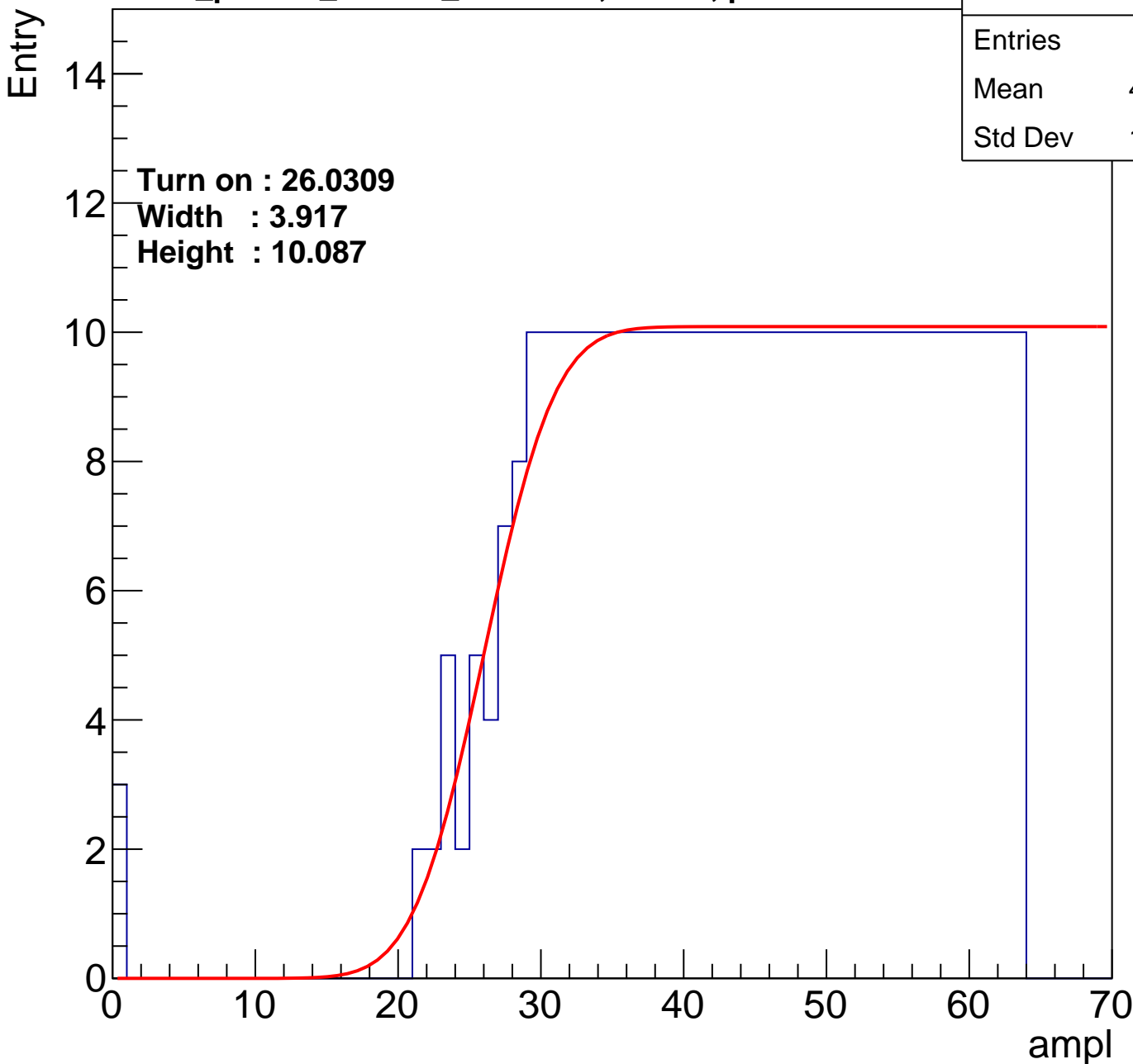
calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.79
Std Dev	11.92

Turn on : 26.0309

Width : 3.917

Height : 10.087



B1L002S, U4-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.12
Std Dev	12.14

Turn on : 27.3789

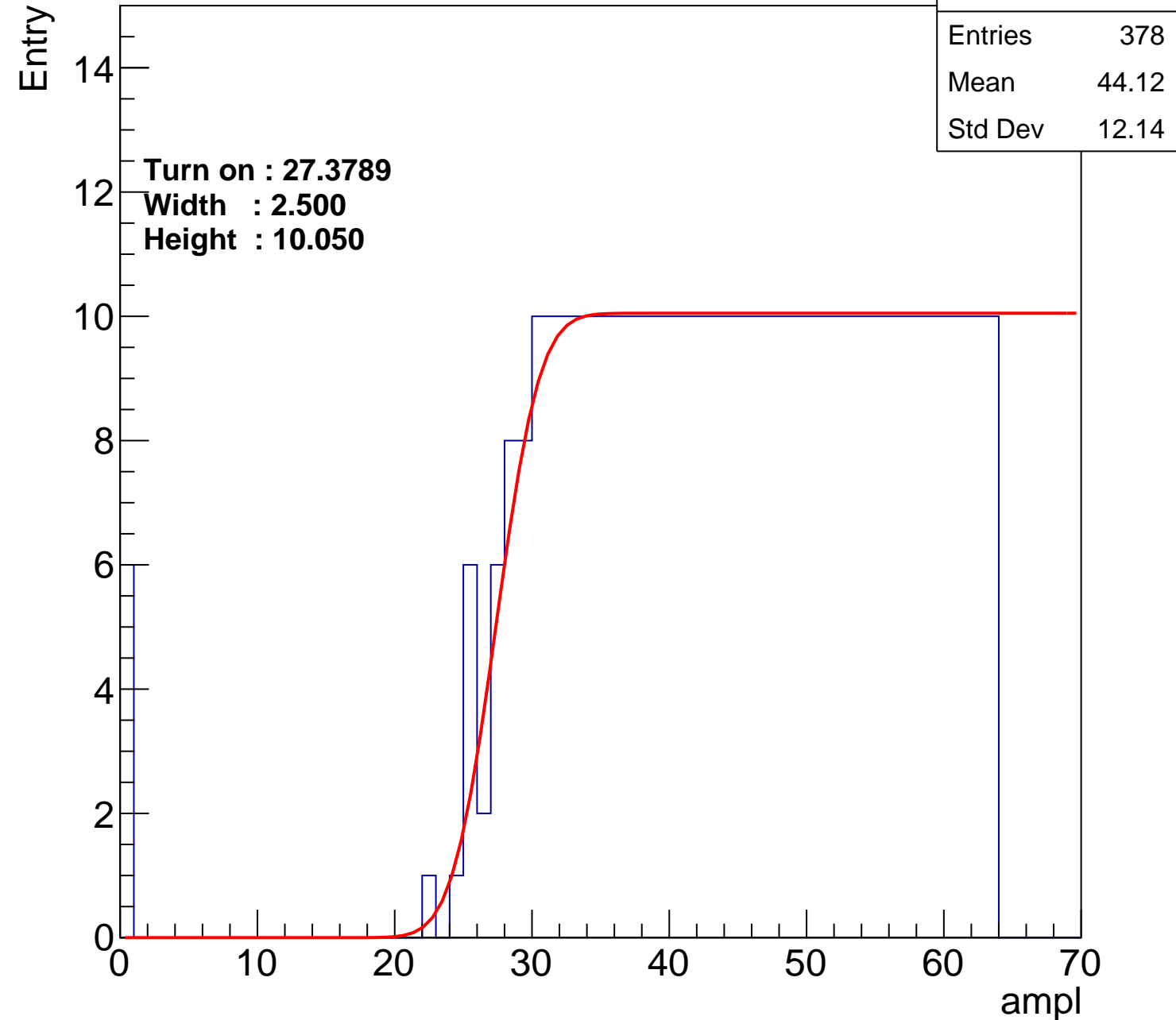
Width : 2.500

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.0871

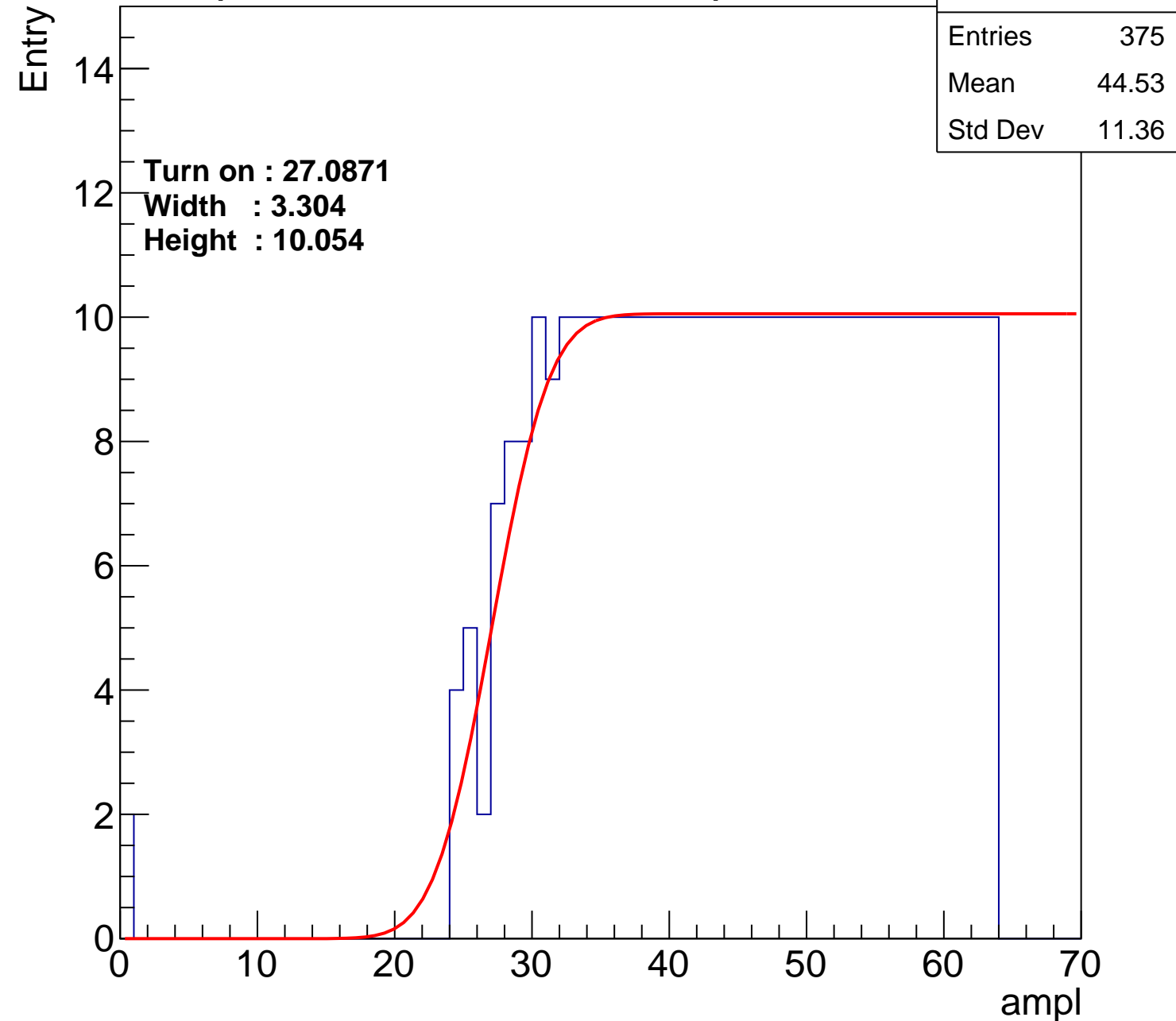
Width : 3.304

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.29
Std Dev	11.96

Turn on : 27.0971

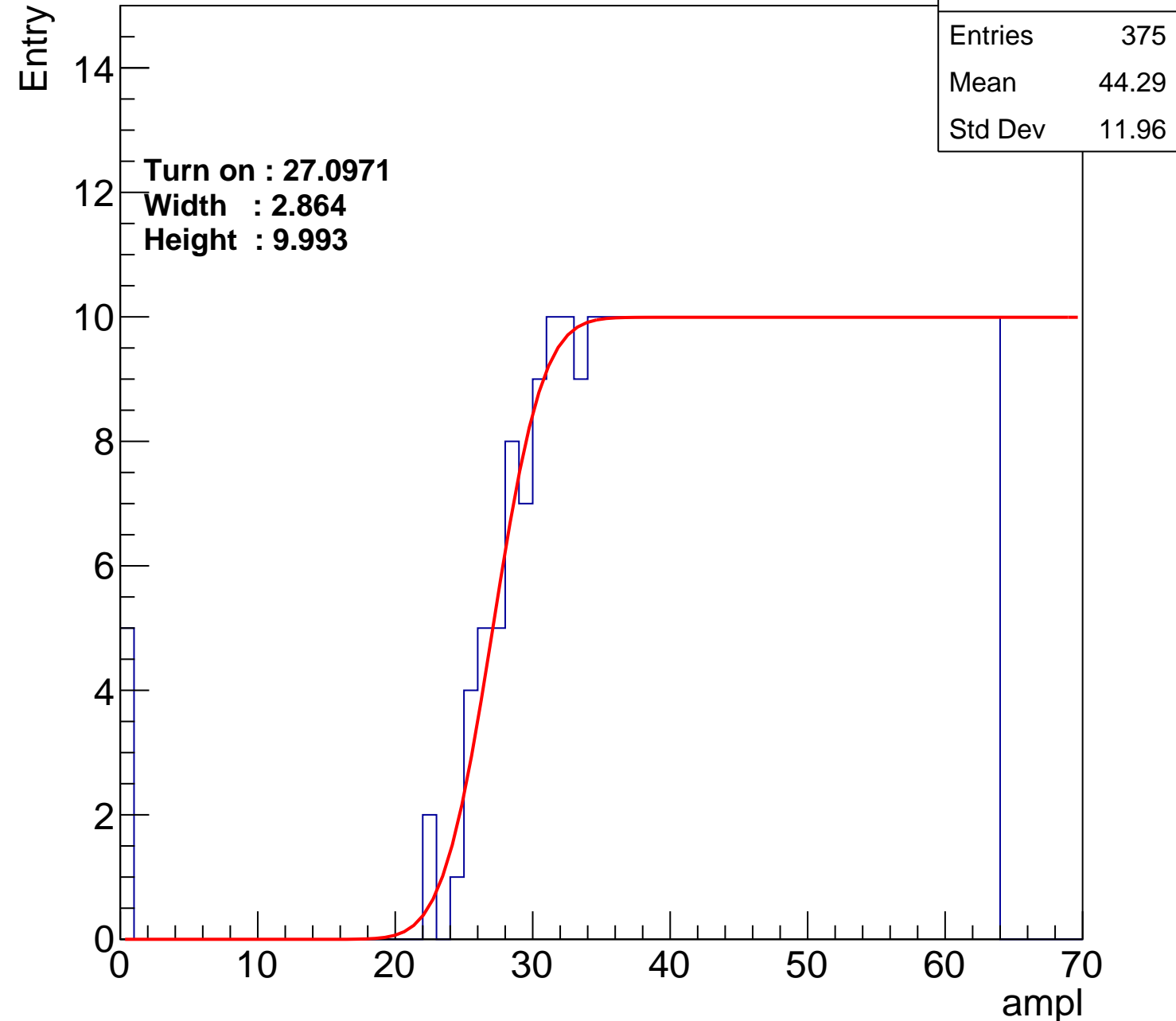
Width : 2.864

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.17
Std Dev	12.04

Turn on : 23.8513

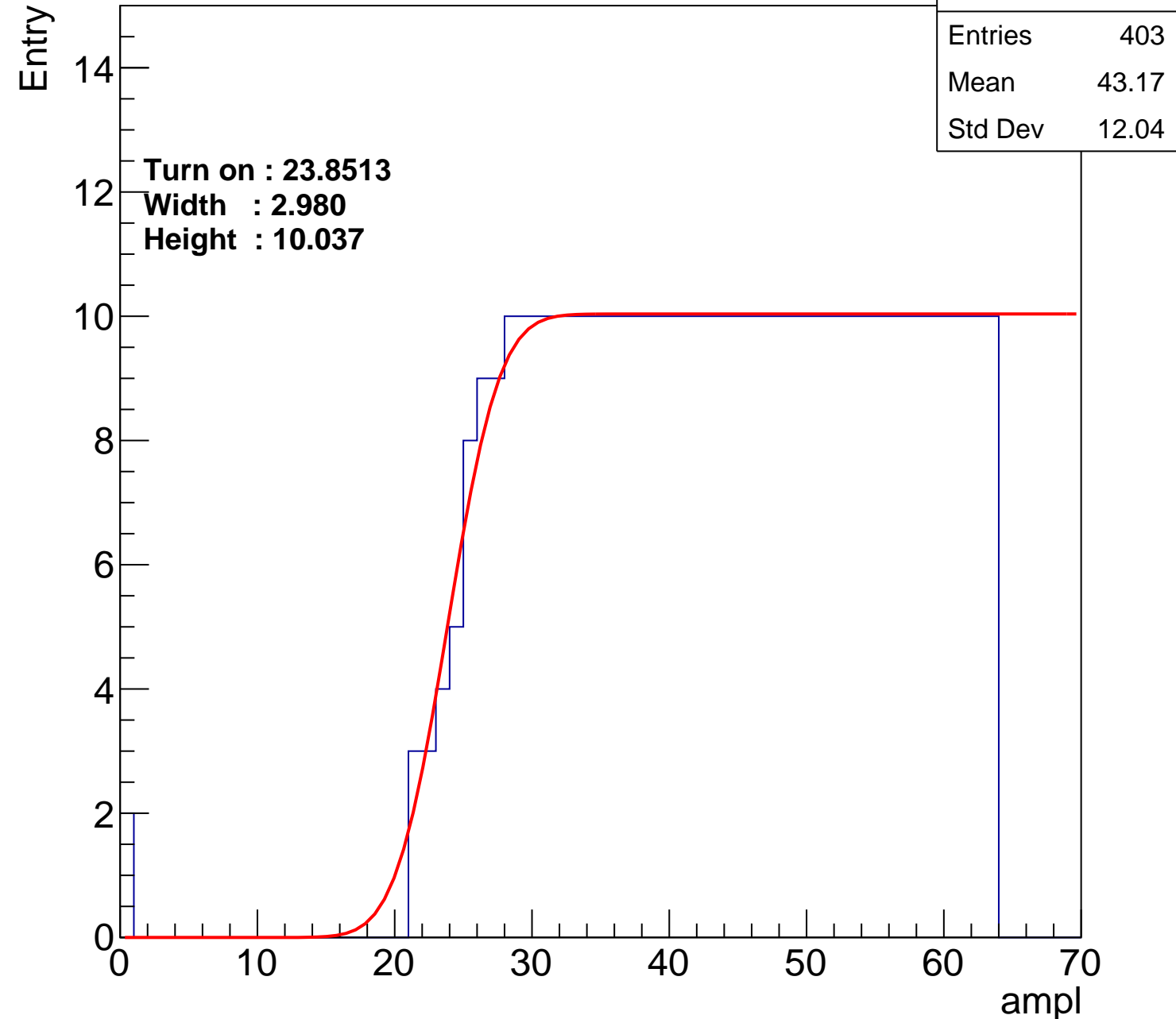
Width : 2.980

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.01
Std Dev	11.72

Turn on : 25.5143

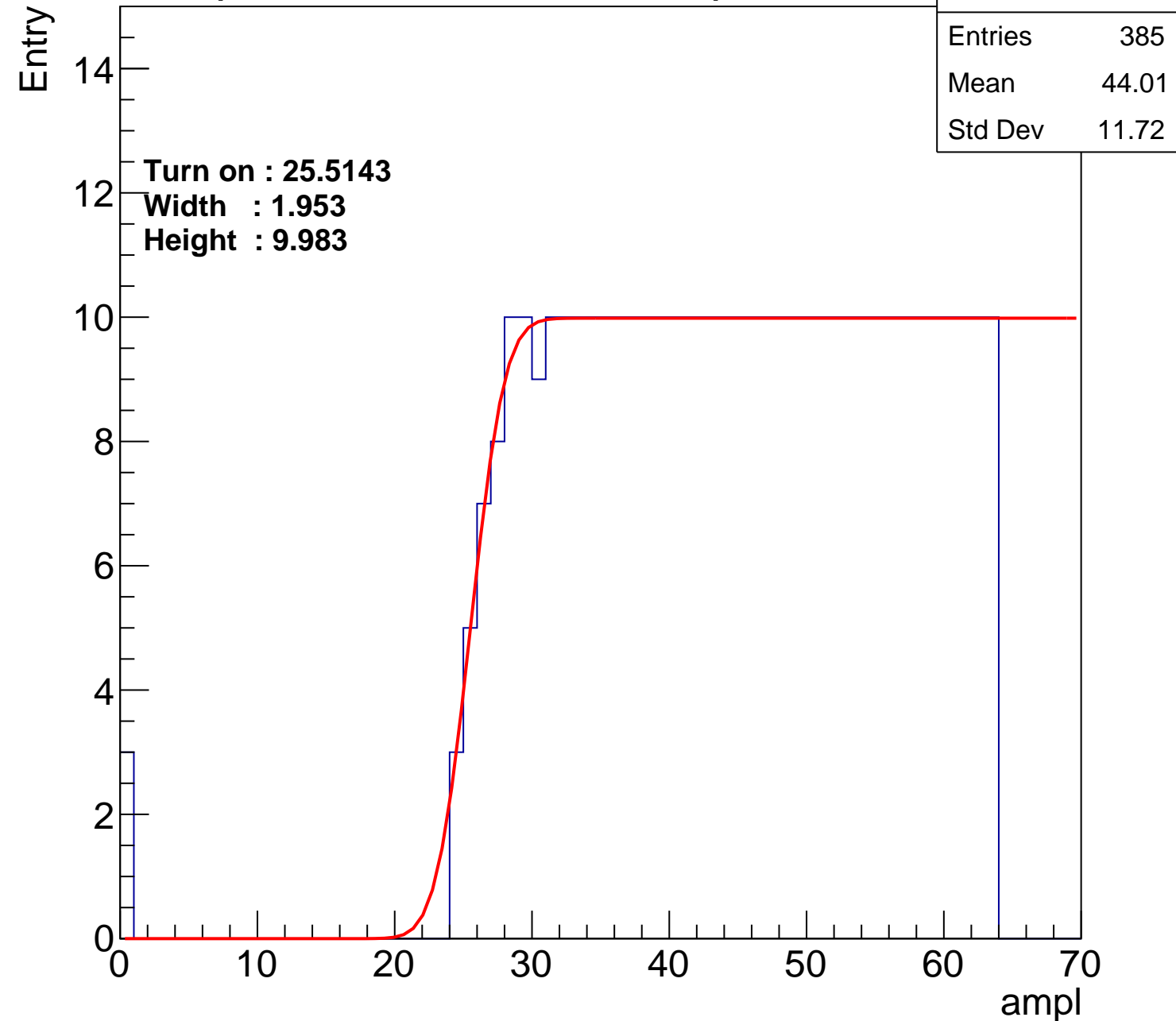
Width : 1.953

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch12

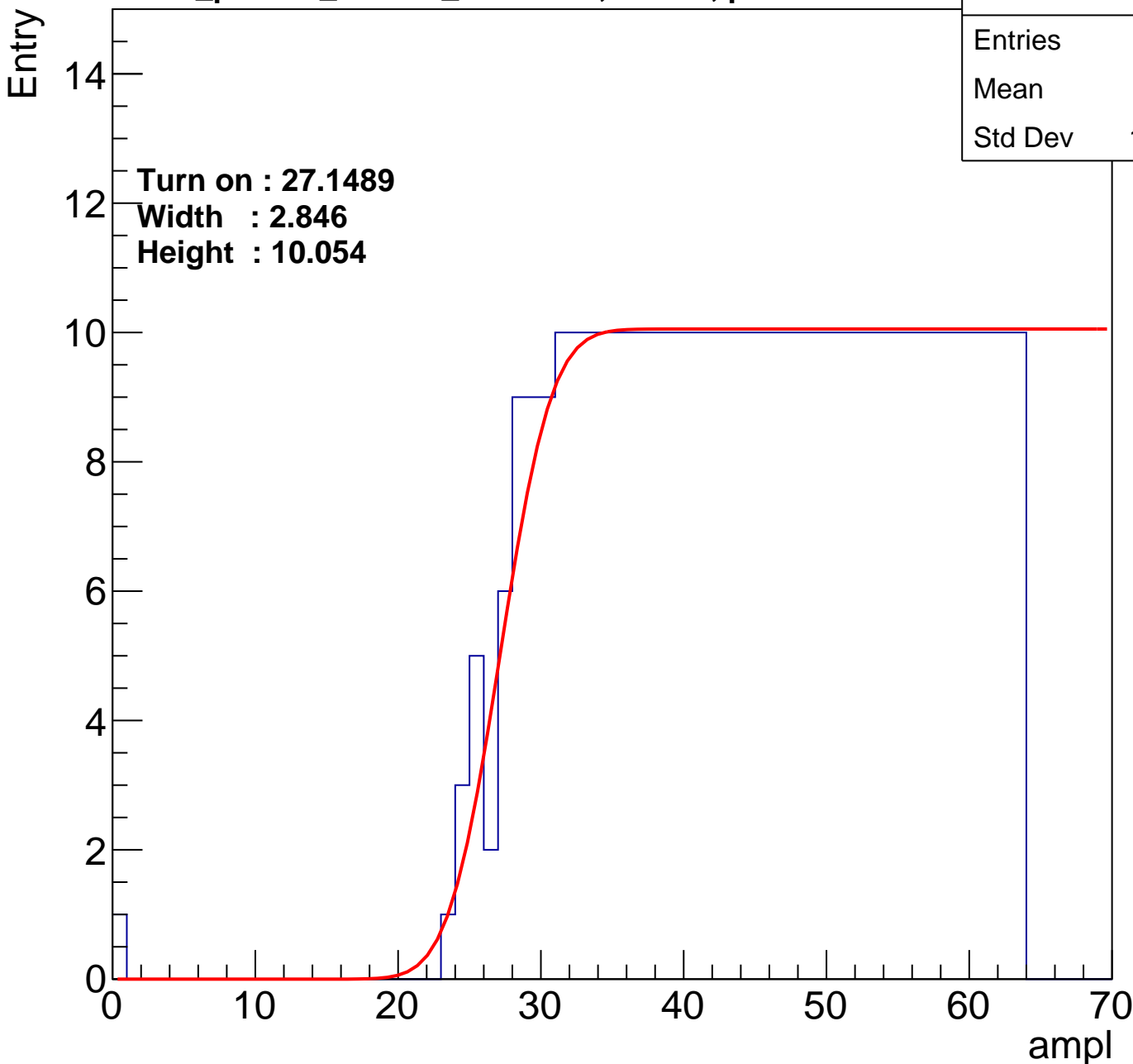
calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.61
Std Dev	11.15

Turn on : 27.1489

Width : 2.846

Height : 10.054



B1L002S, U4-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 25.6710

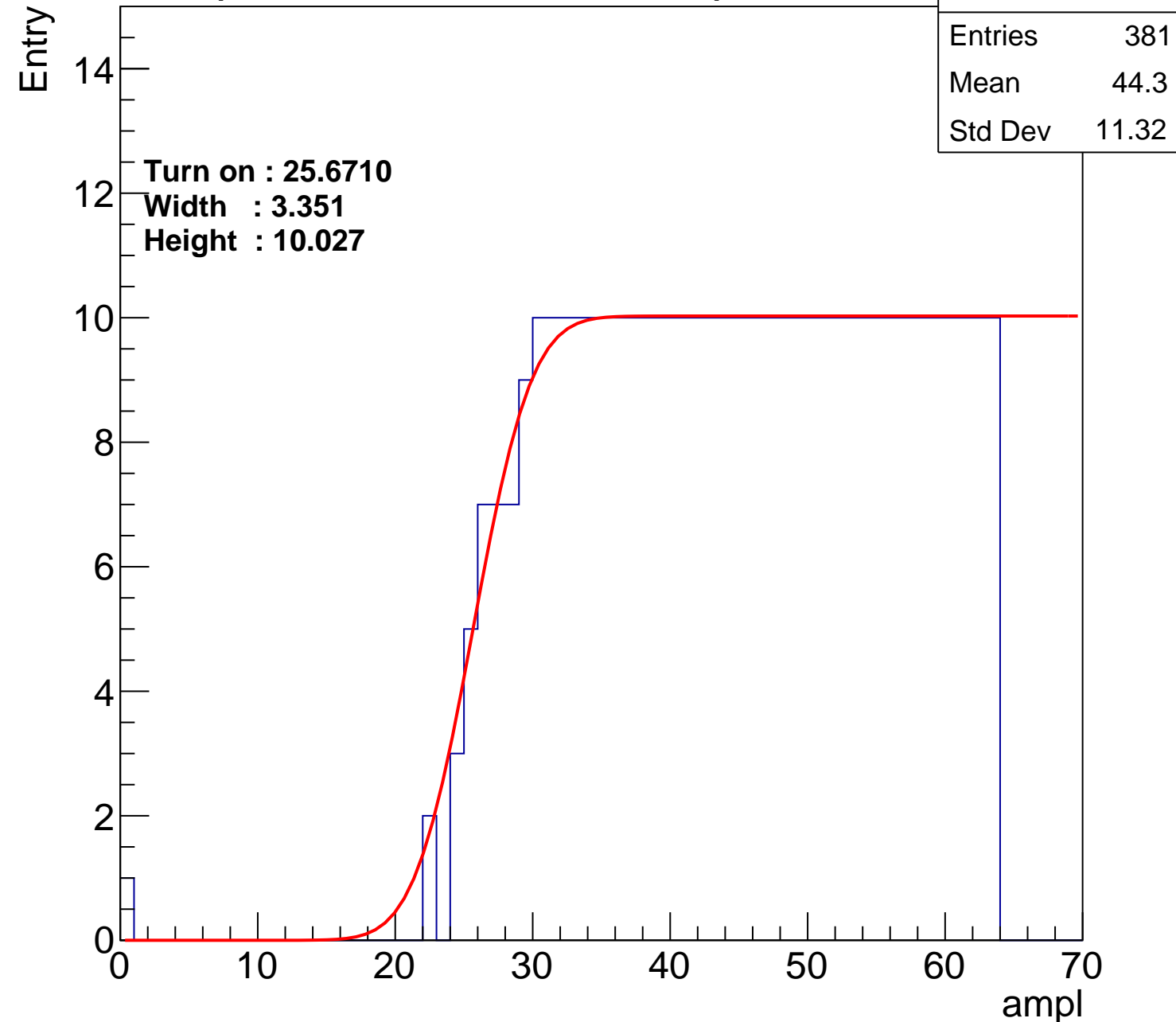
Width : 3.351

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.74
Std Dev	11.28

Turn on : 26.9880

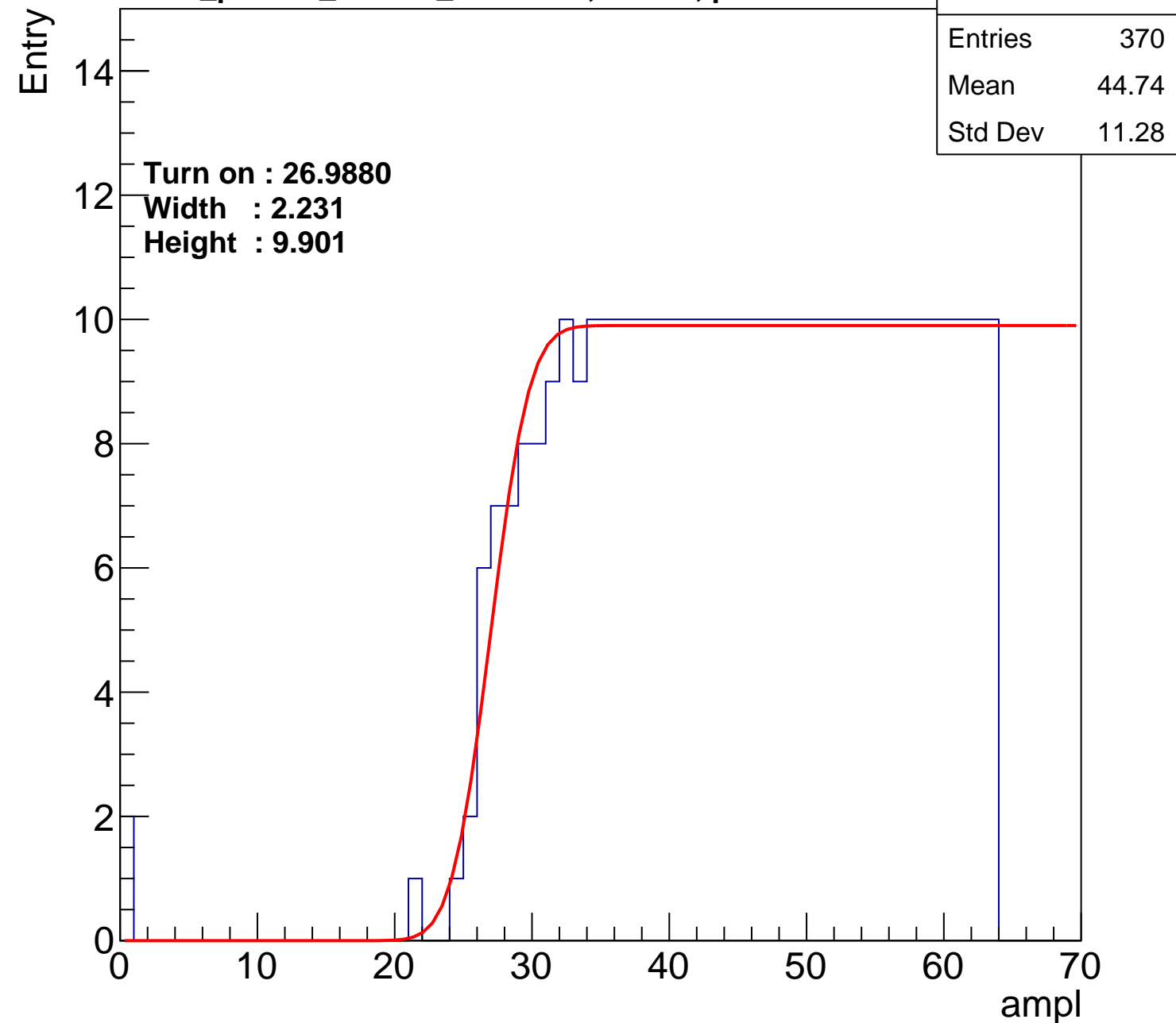
Width : 2.231

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.64
Std Dev	11.48

Turn on : 27.4079

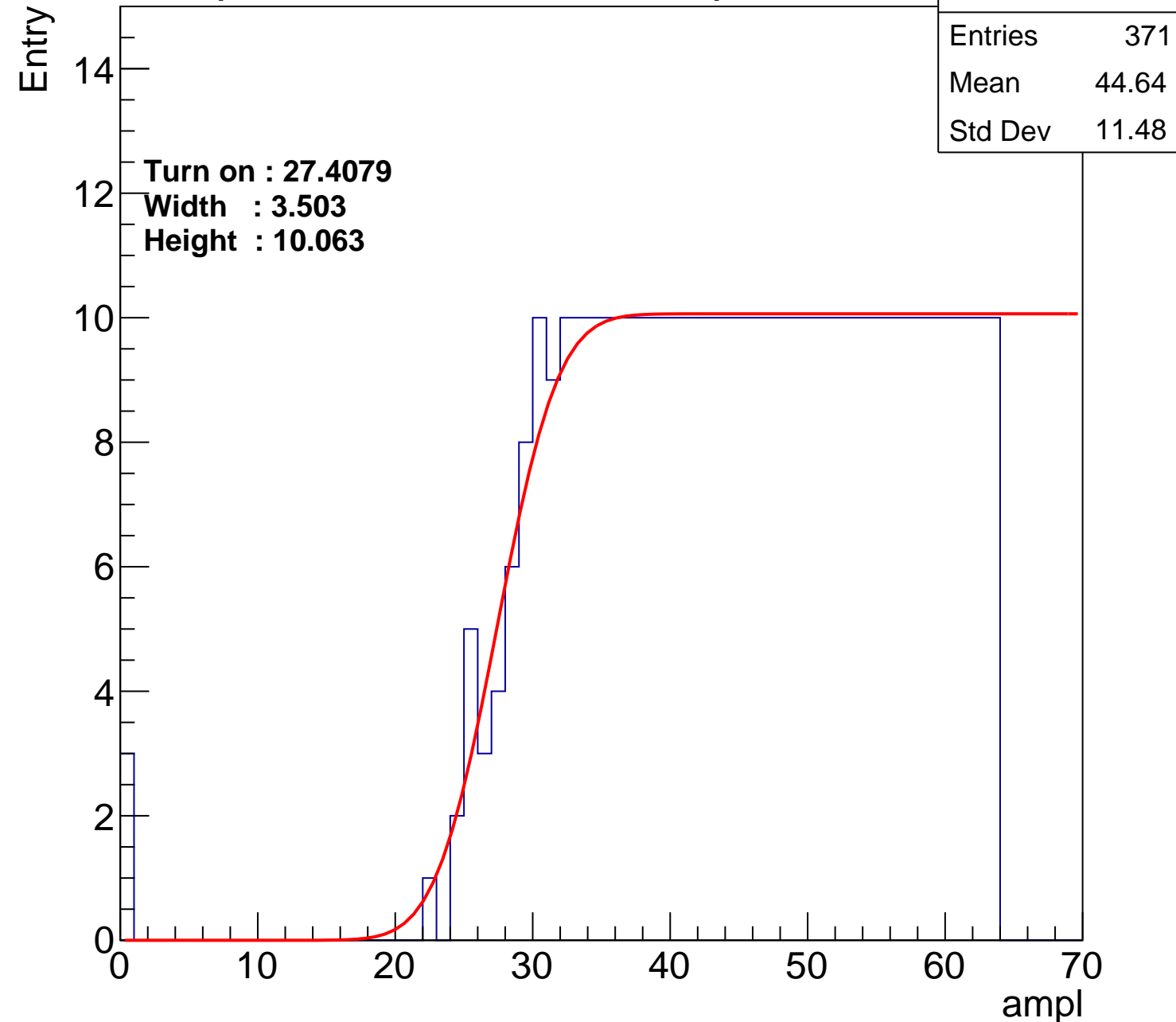
Width : 3.503

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.79
Std Dev	11.57

Turn on : 25.4342

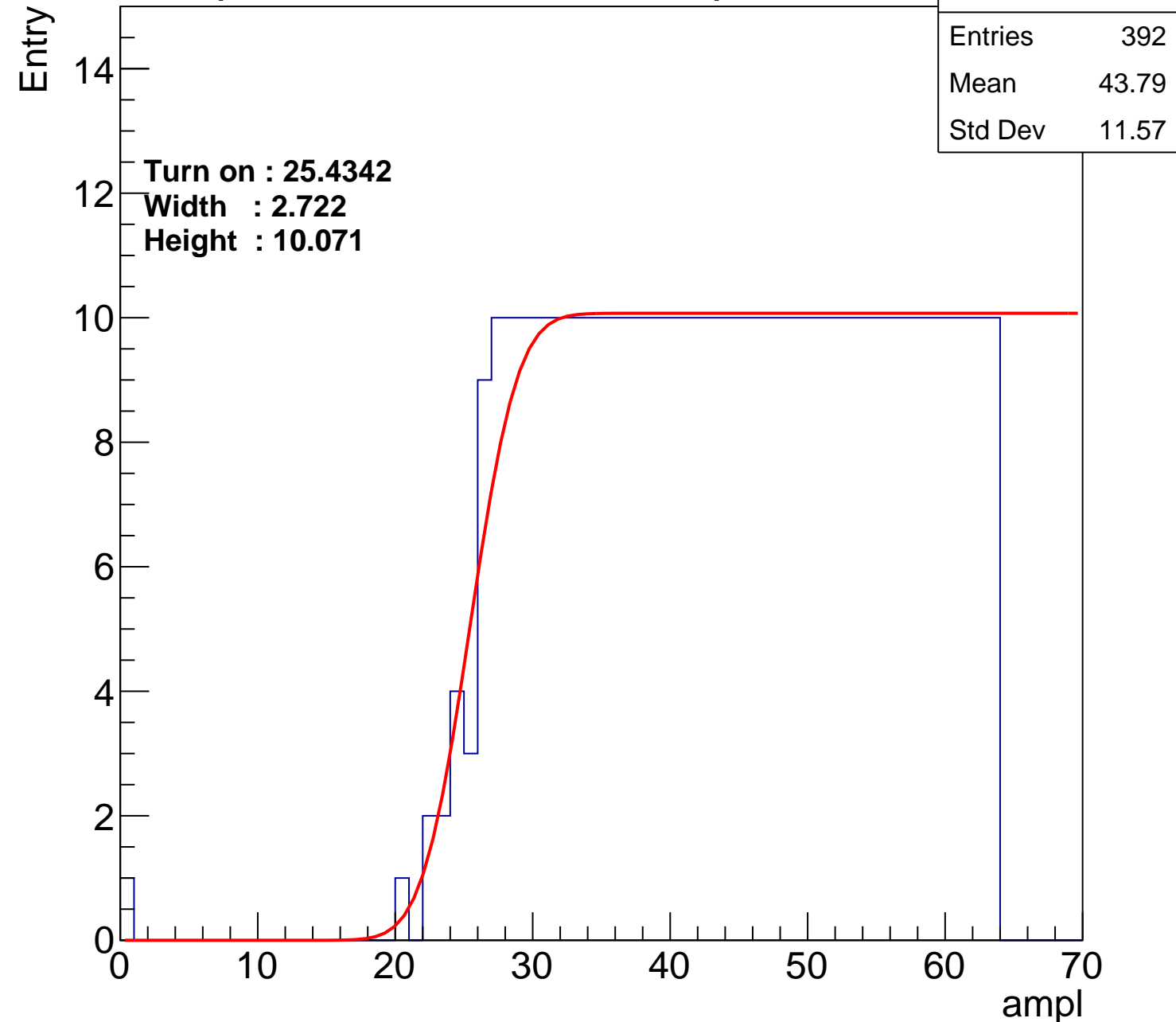
Width : 2.722

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch17

calib_packv5_042523_0143.root, FC#10, port B3

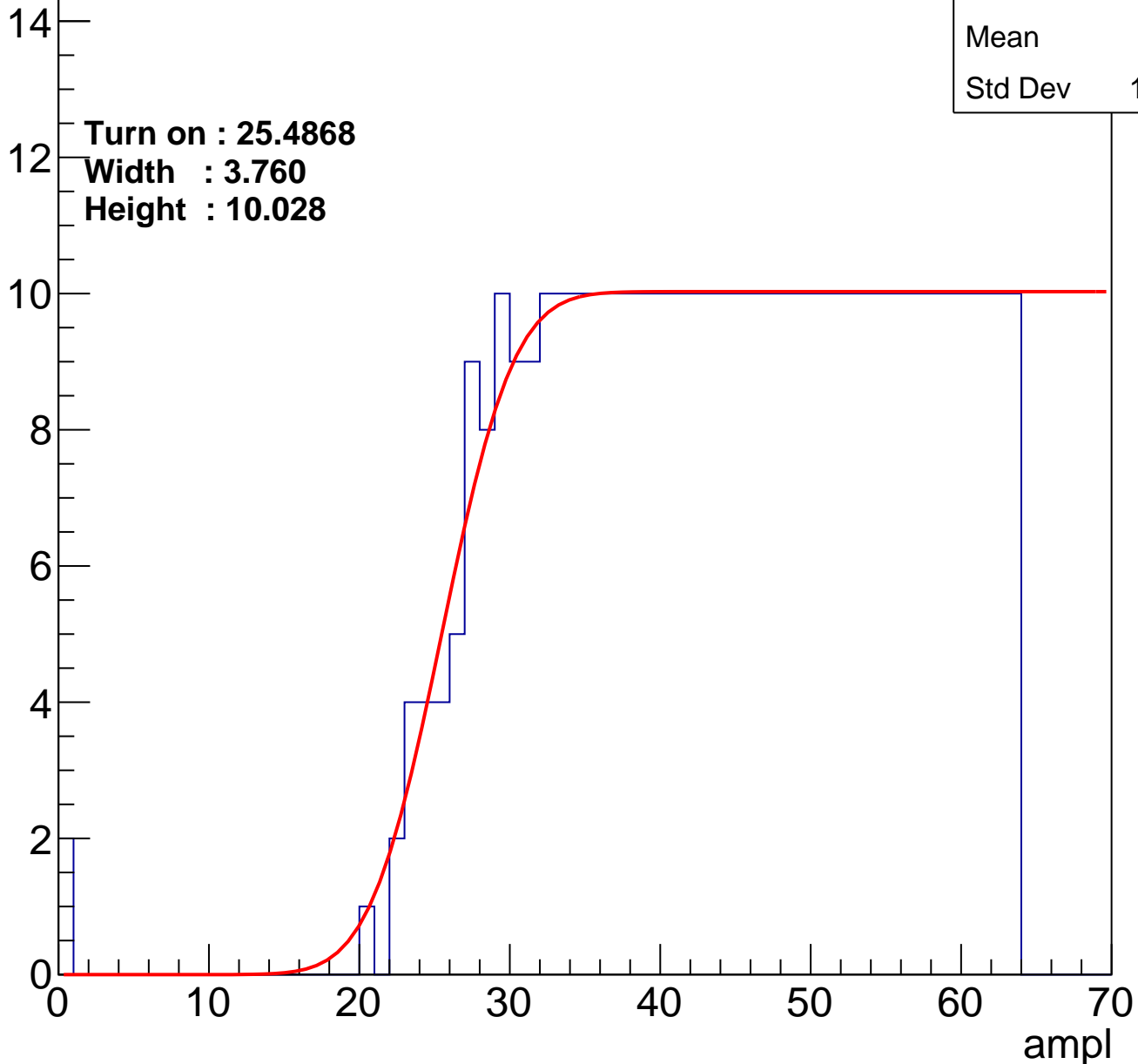
Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 25.4868

Width : 3.760

Height : 10.028

Entry



B1L002S, U4-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.2648

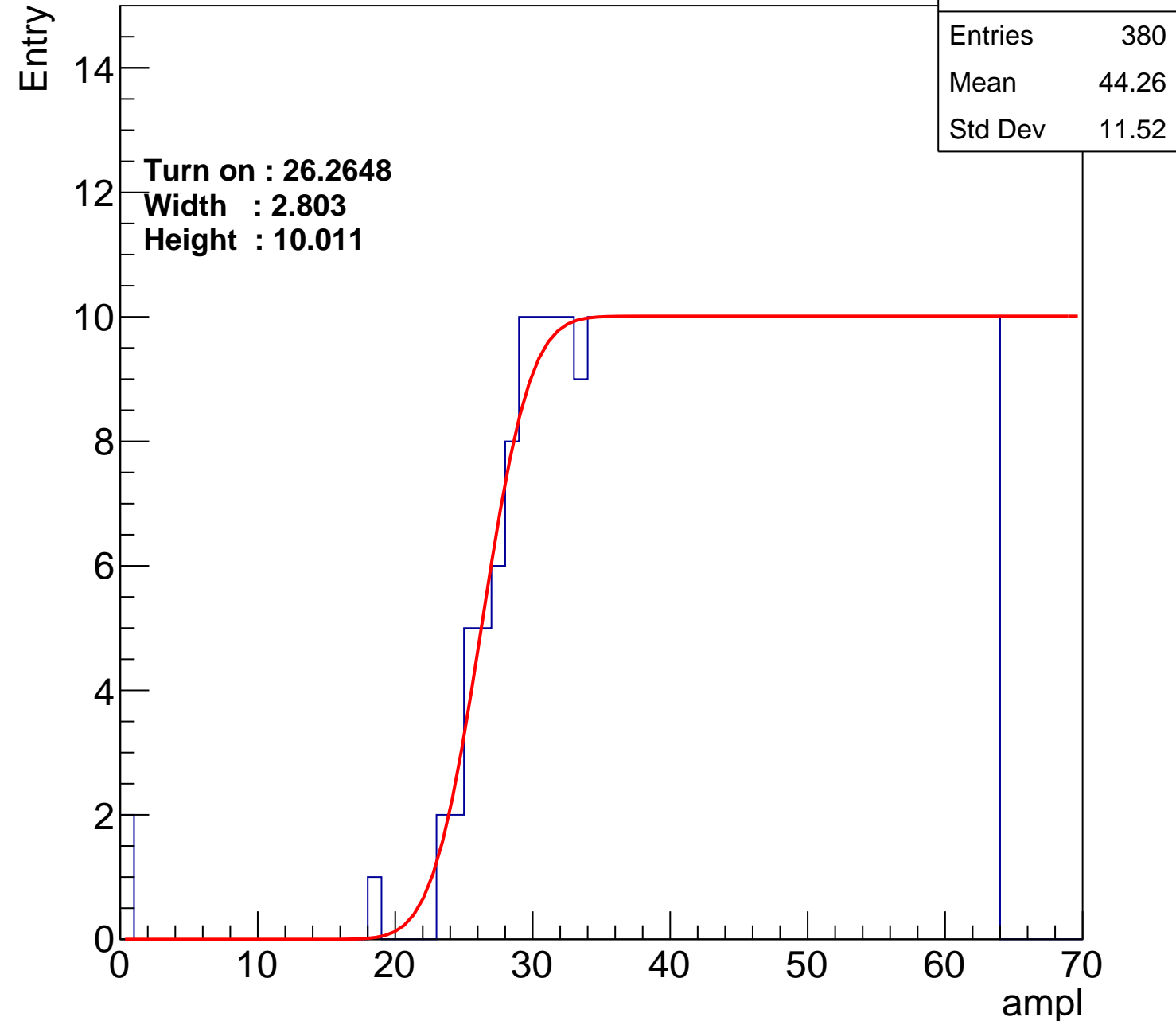
Width : 2.803

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.51
Std Dev	11.76

Turn on : 24.6435

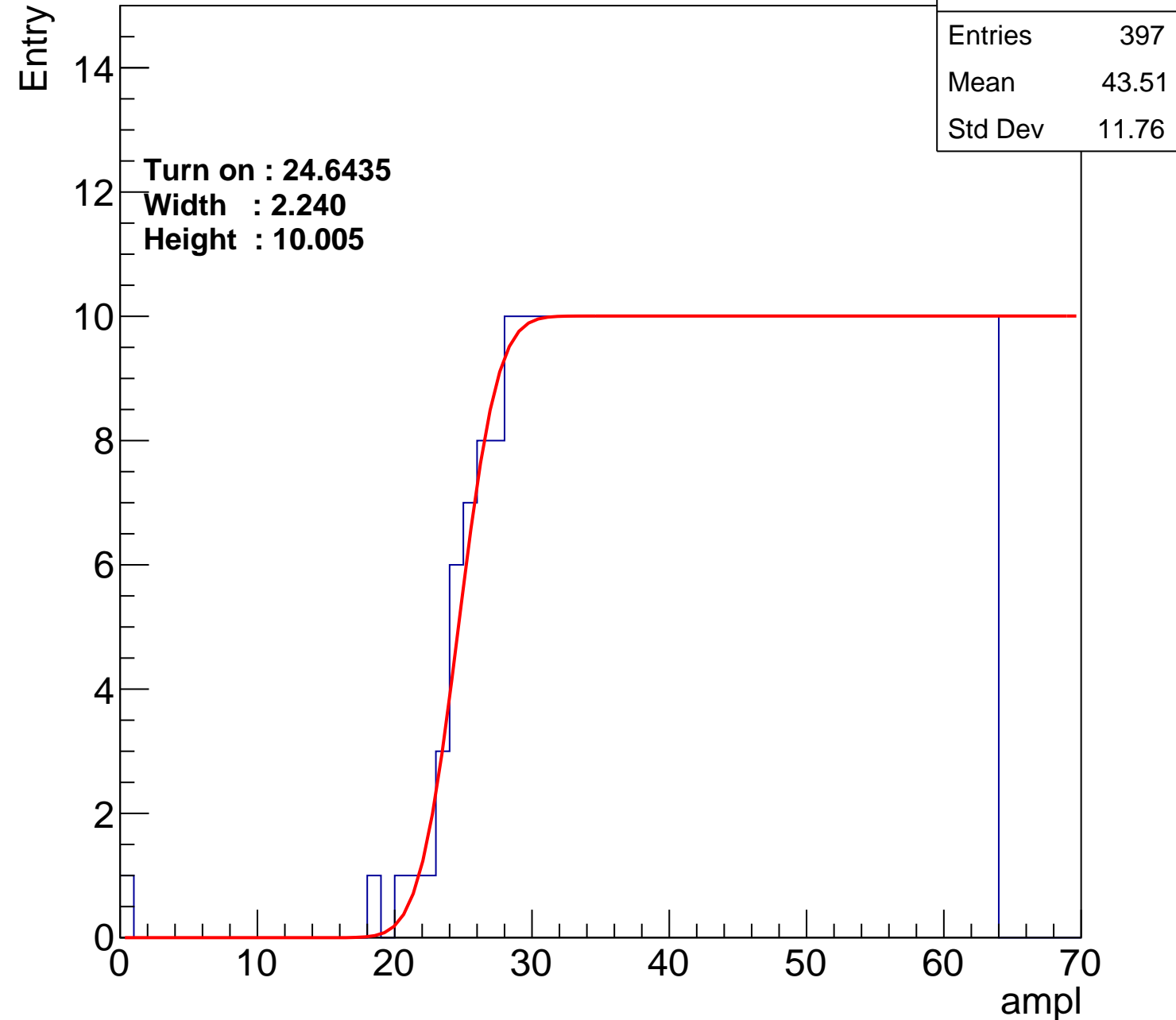
Width : 2.240

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch20

calib_packv5_042523_0143.root, FC#10, port B3

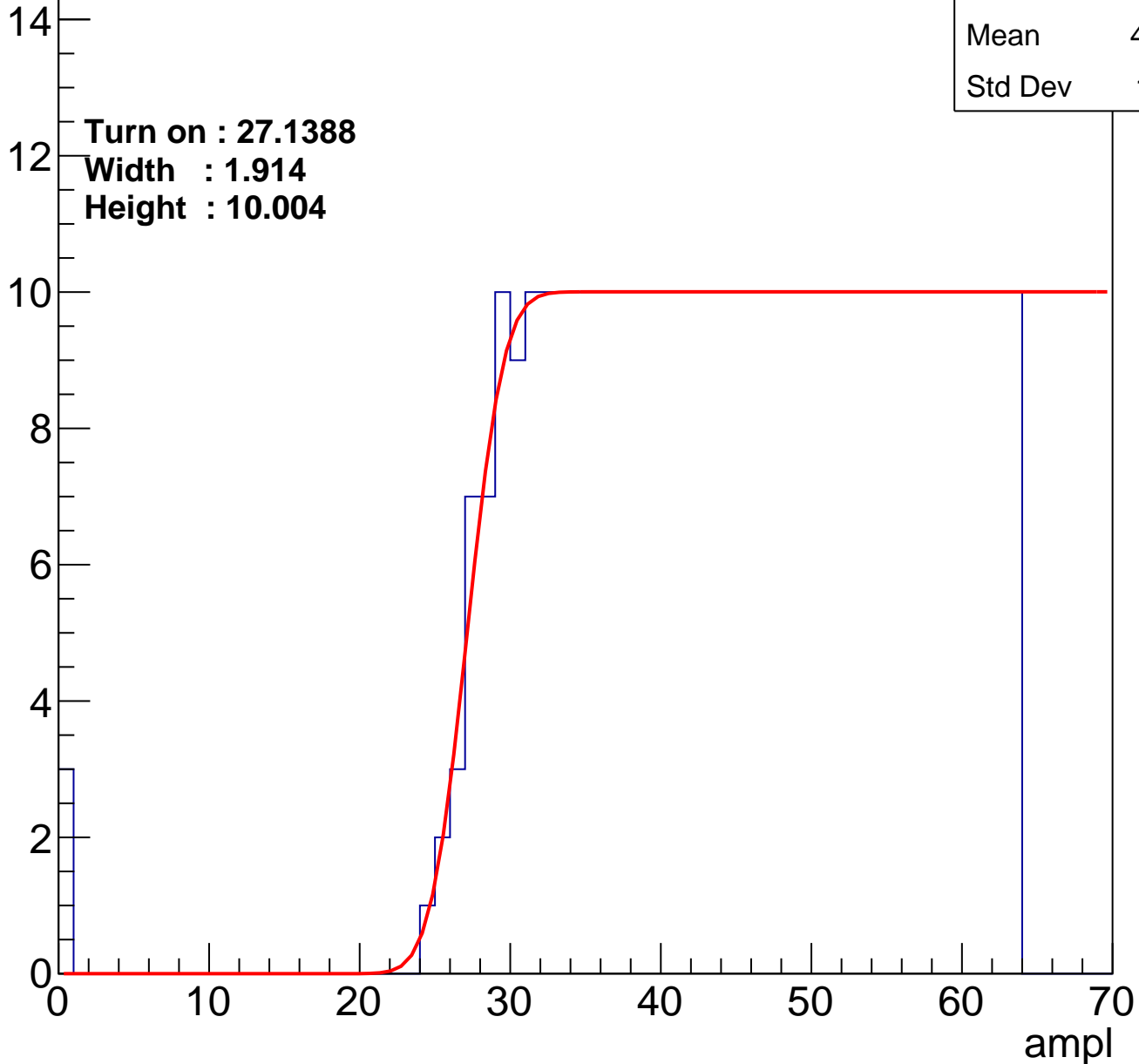
Entries	372
Mean	44.64
Std Dev	11.41

Turn on : 27.1388

Width : 1.914

Height : 10.004

Entry



B1L002S, U4-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.69
Std Dev	11.25

Turn on : 27.0923

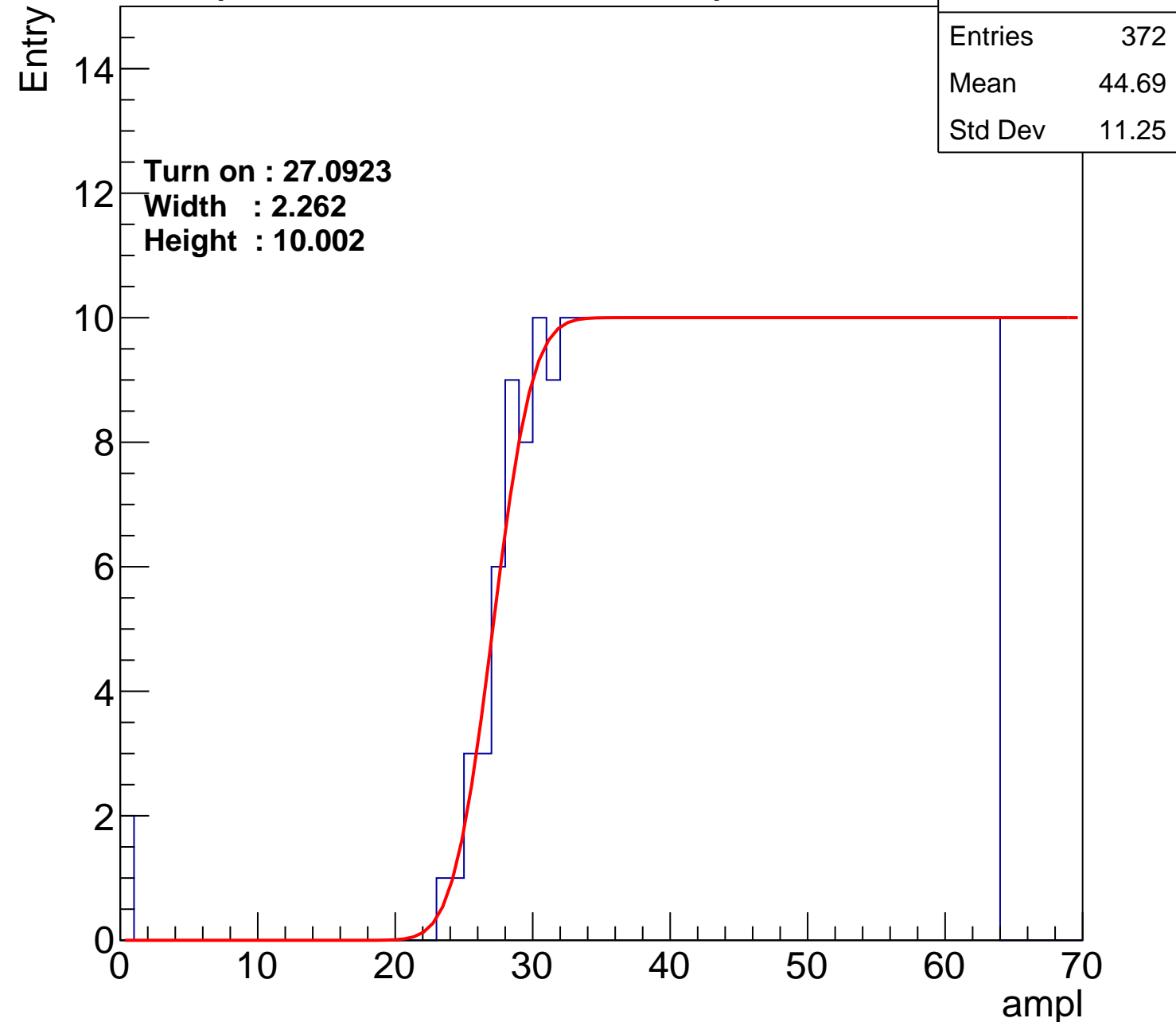
Width : 2.262

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch22

calib_packv5_042523_0143.root, FC#10, port B3

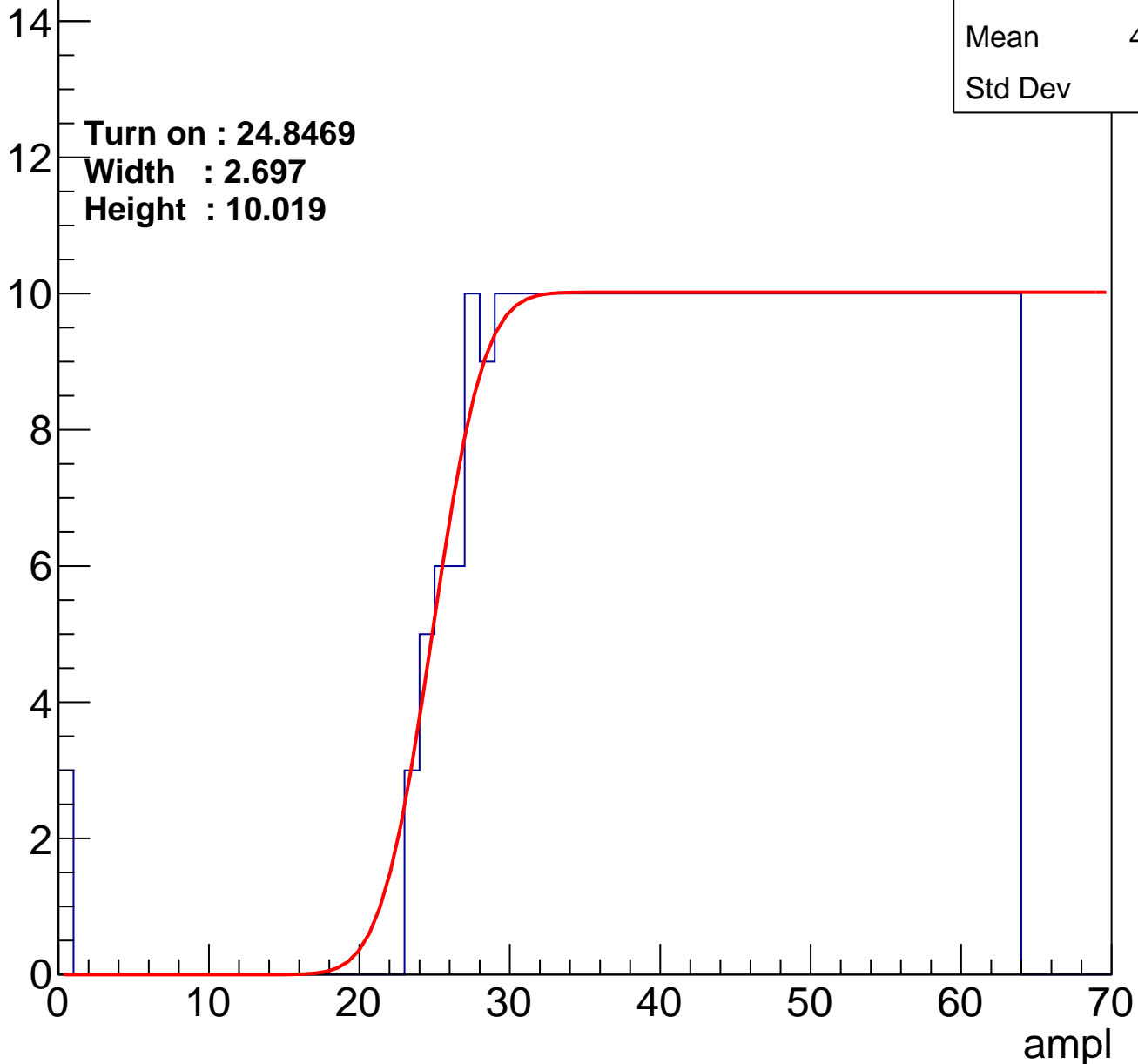
Entries	392
Mean	43.67
Std Dev	11.9

Turn on : 24.8469

Width : 2.697

Height : 10.019

Entry



B1L002S, U4-ch23

calib_packv5_042523_0143.root, FC#10, port B3

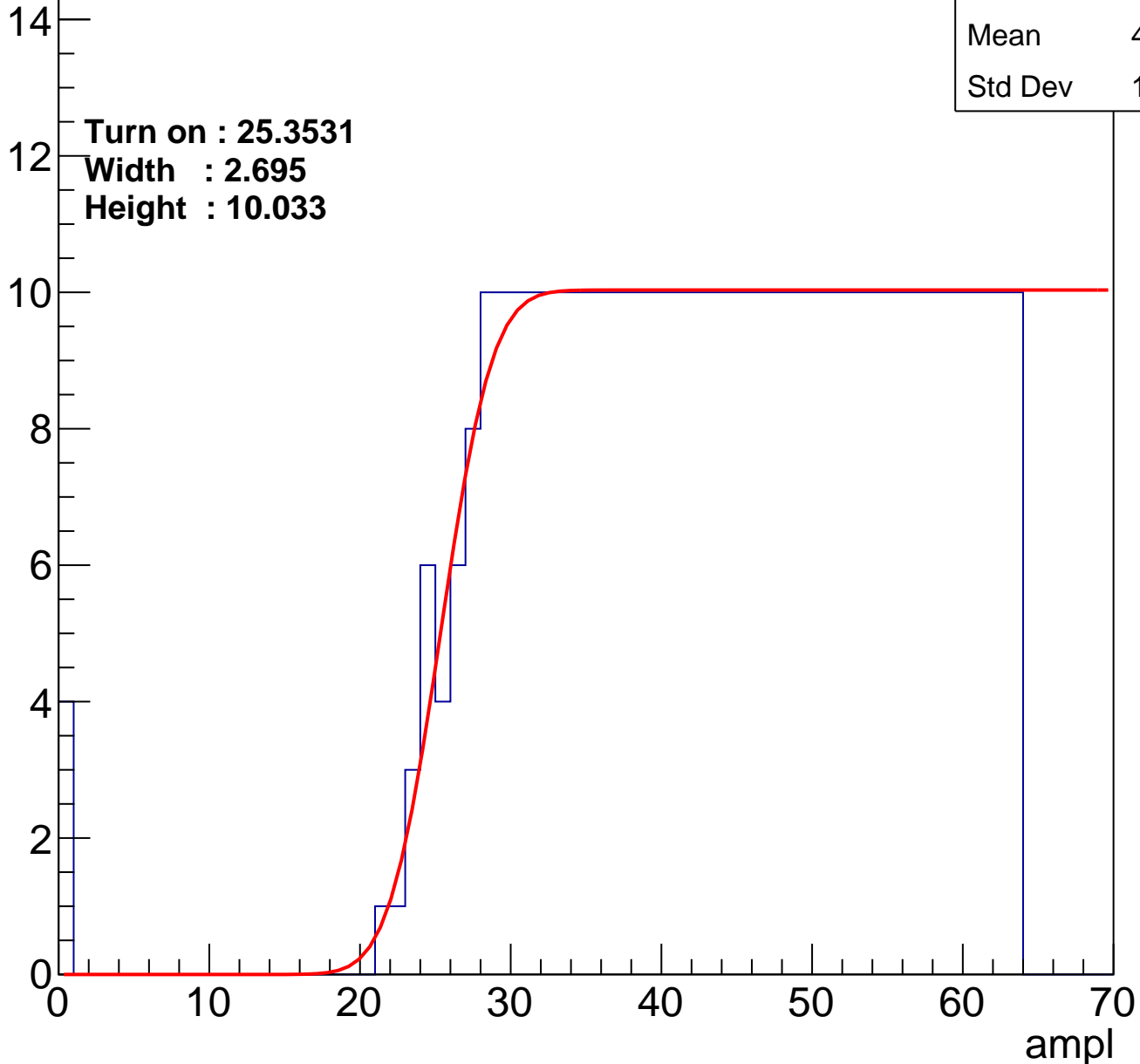
Entries	393
Mean	43.53
Std Dev	12.12

Turn on : 25.3531

Width : 2.695

Height : 10.033

Entry



B1L002S, U4-ch24

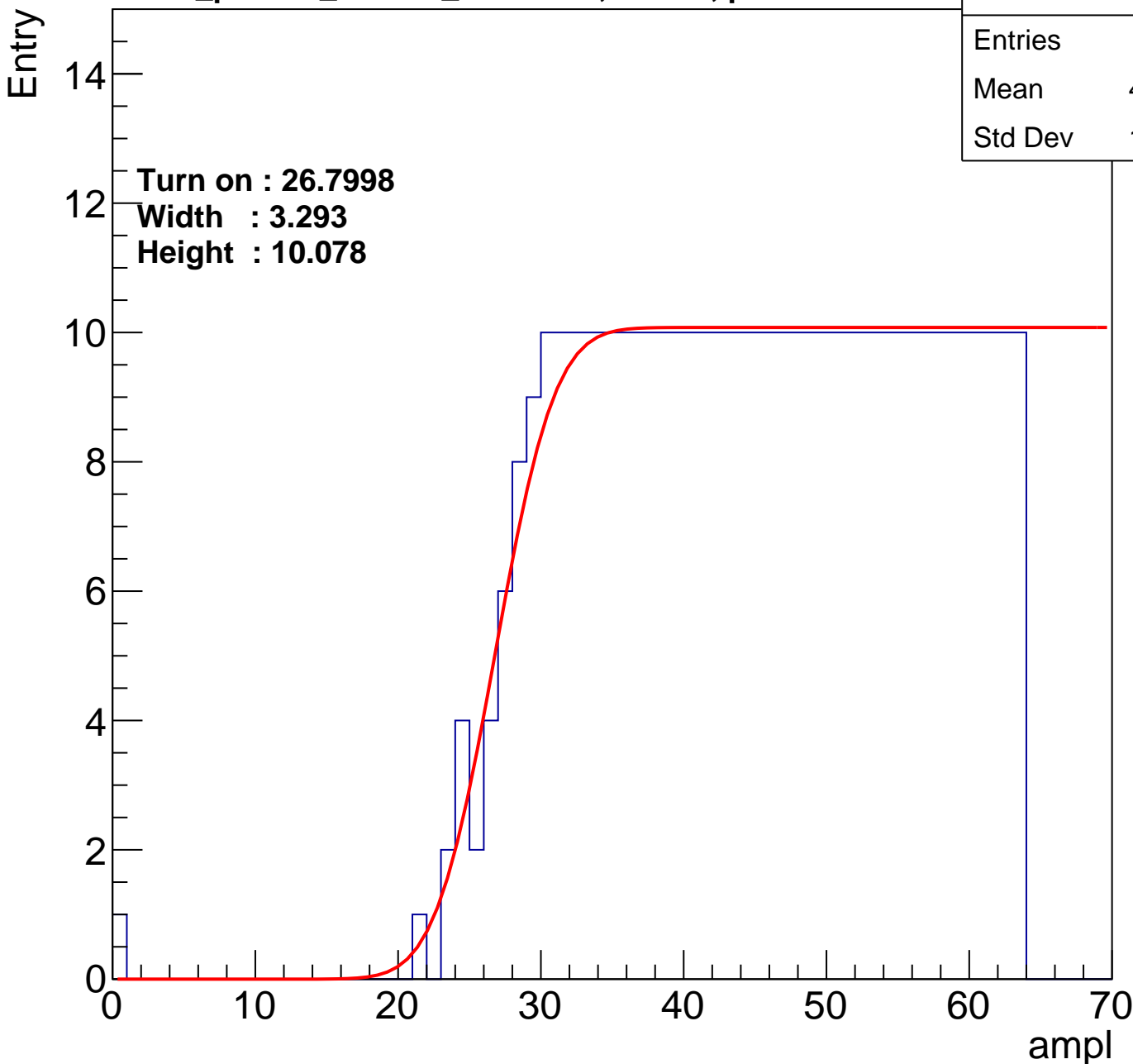
calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.7998

Width : 3.293

Height : 10.078



B1L002S, U4-ch25

calib_packv5_042523_0143.root, FC#10, port B3

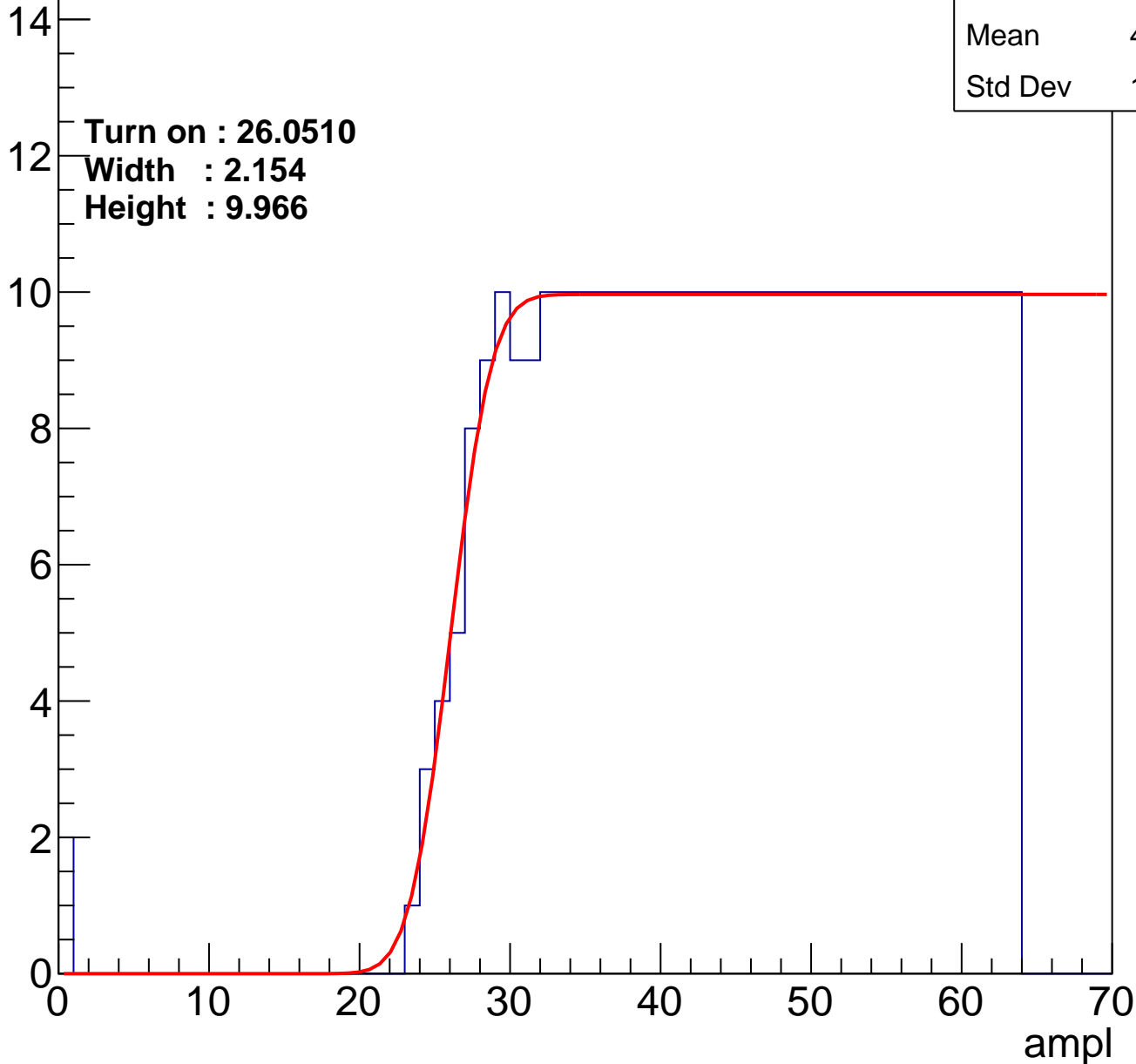
Entry

Entries	380
Mean	44.29
Std Dev	11.46

Turn on : 26.0510

Width : 2.154

Height : 9.966



B1L002S, U4-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.23
Std Dev	11.68

Turn on : 26.4924

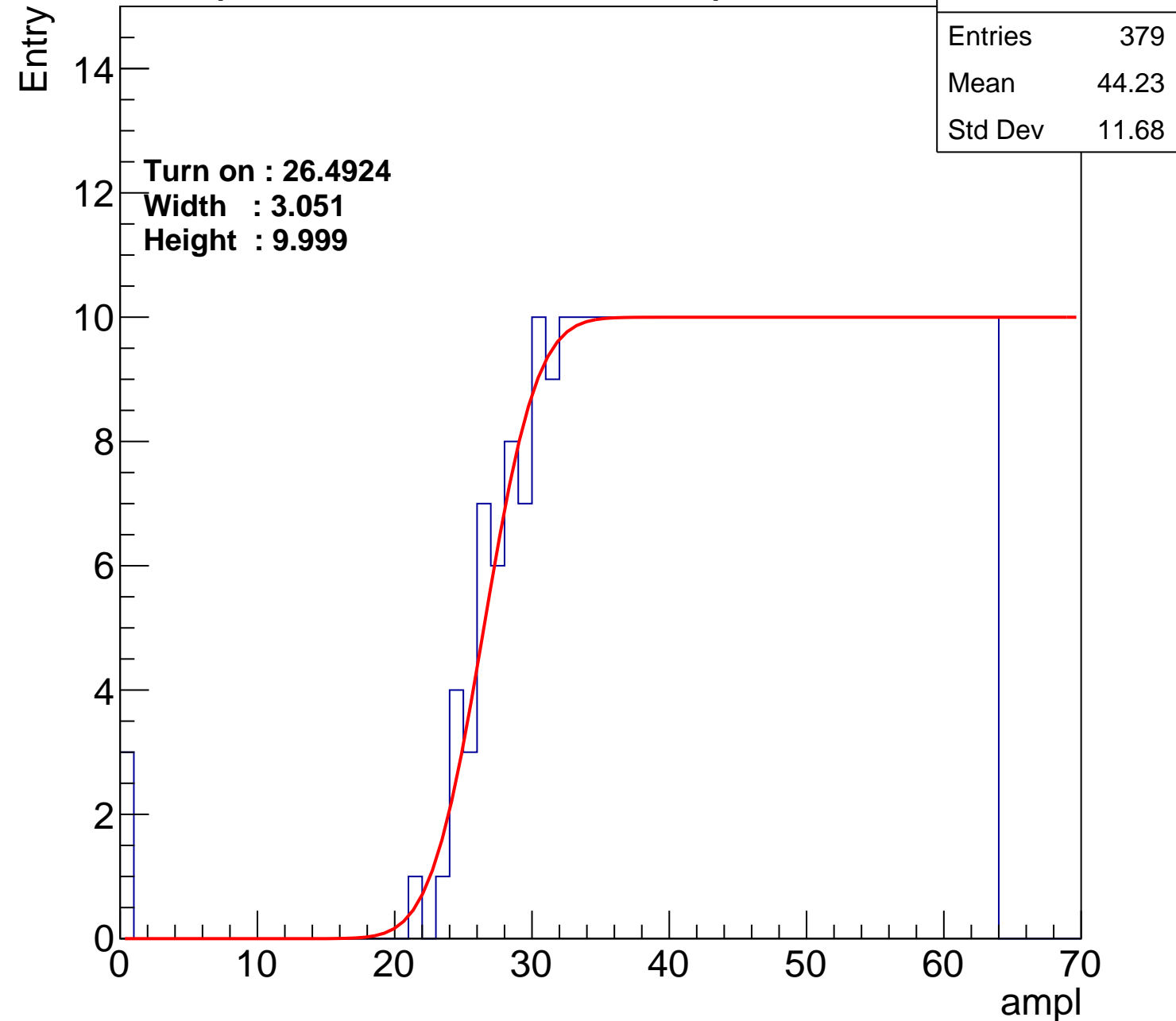
Width : 3.051

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch27

calib_packv5_042523_0143.root, FC#10, port B3

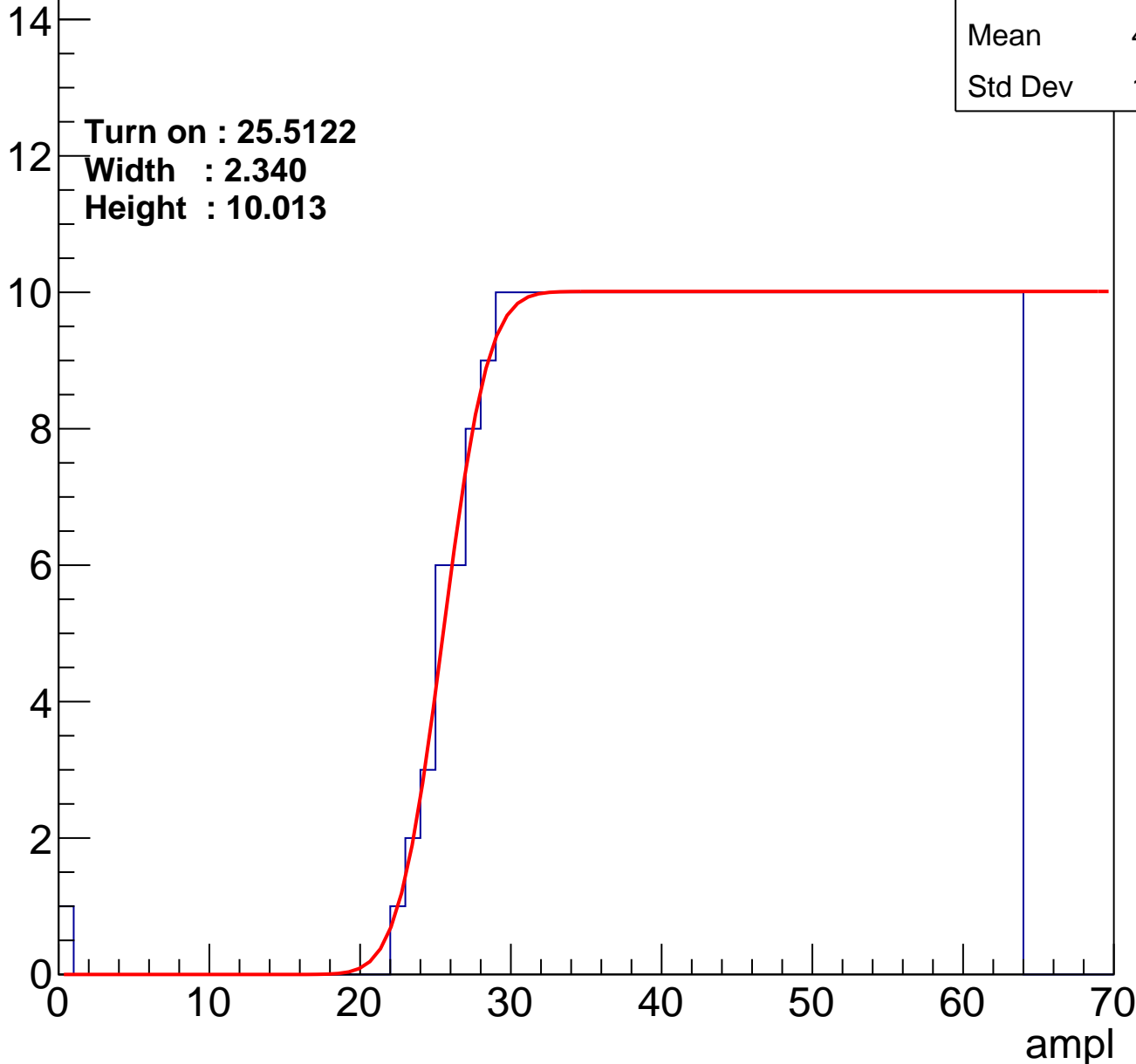
Entries	386
Mean	44.08
Std Dev	11.42

Turn on : 25.5122

Width : 2.340

Height : 10.013

Entry



B1L002S, U4-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.68

Turn on : 24.9517

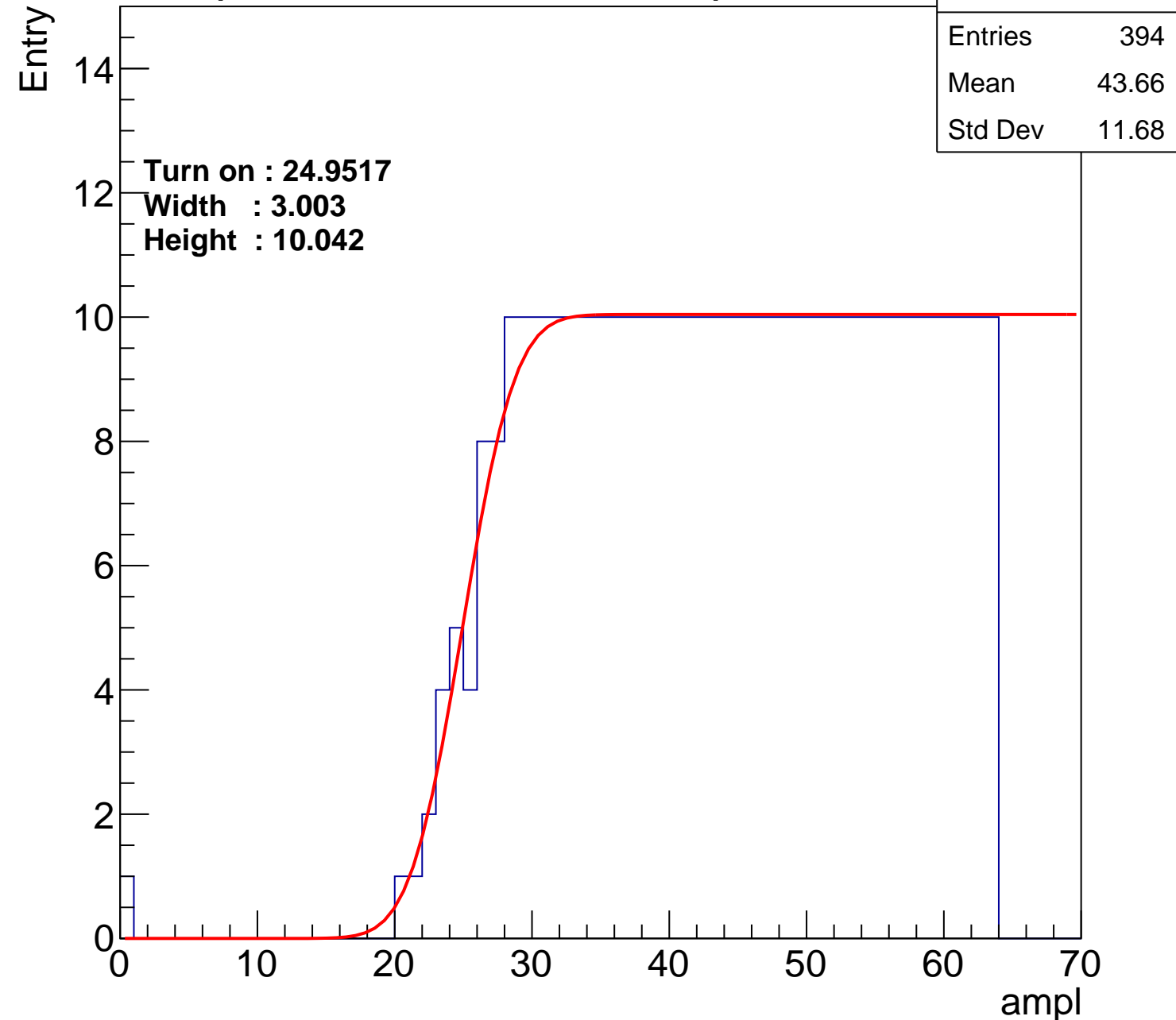
Width : 3.003

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch29

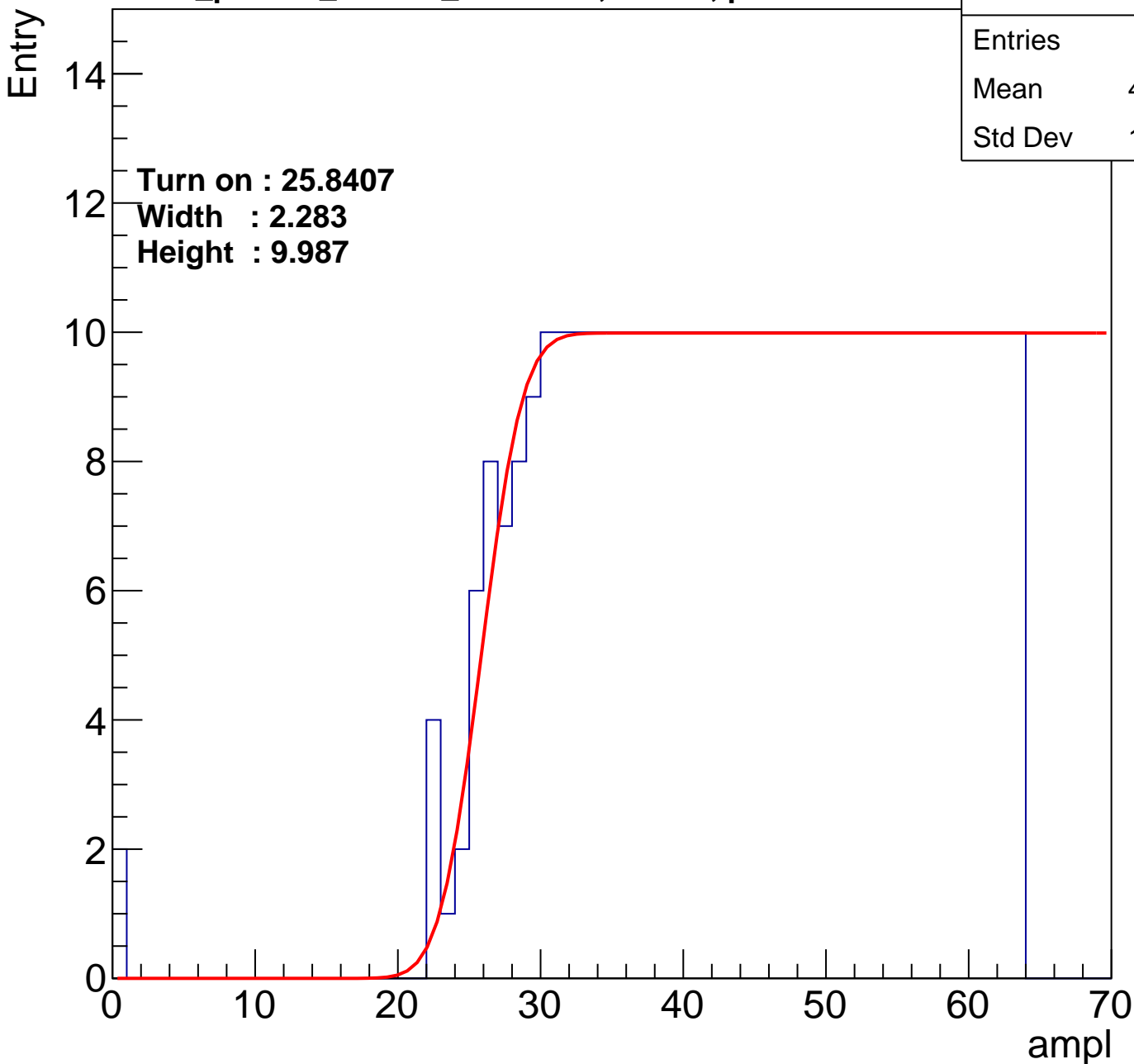
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.8407

Width : 2.283

Height : 9.987

Entries	387
Mean	43.93
Std Dev	11.68



B1L002S, U4-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.72
Std Dev	11.63

Turn on : 25.1805

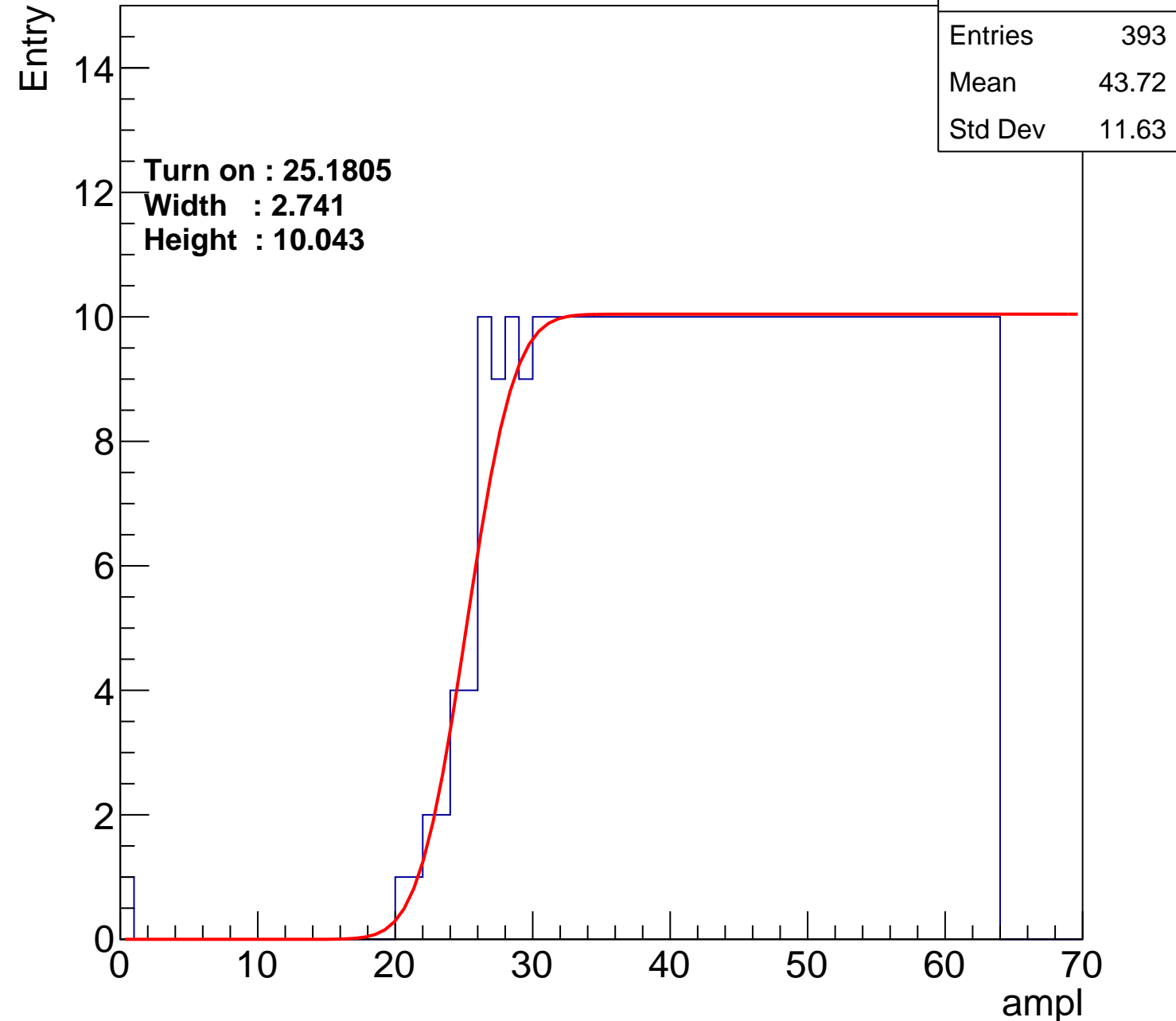
Width : 2.741

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.4102

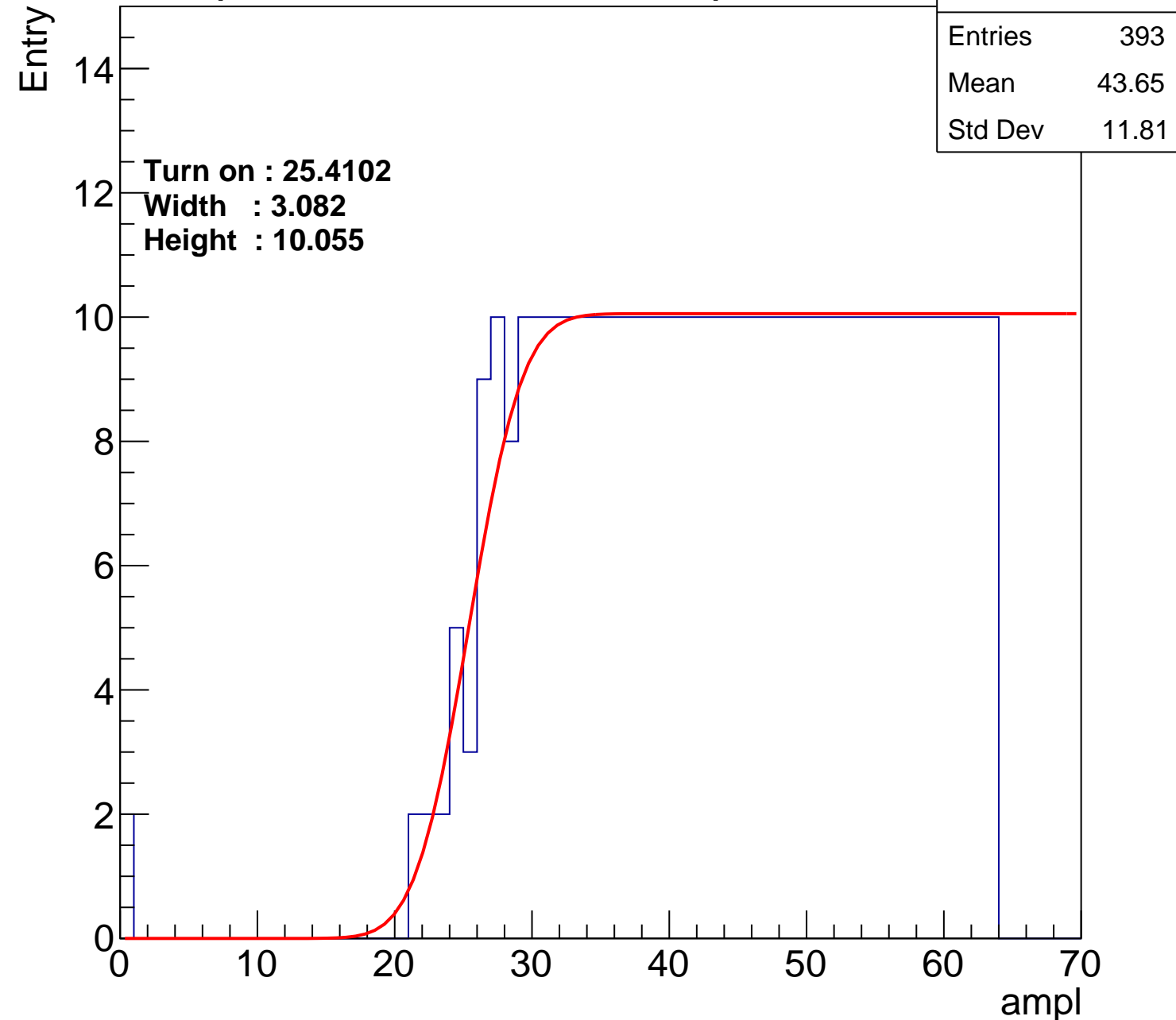
Width : 3.082

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 24.9694

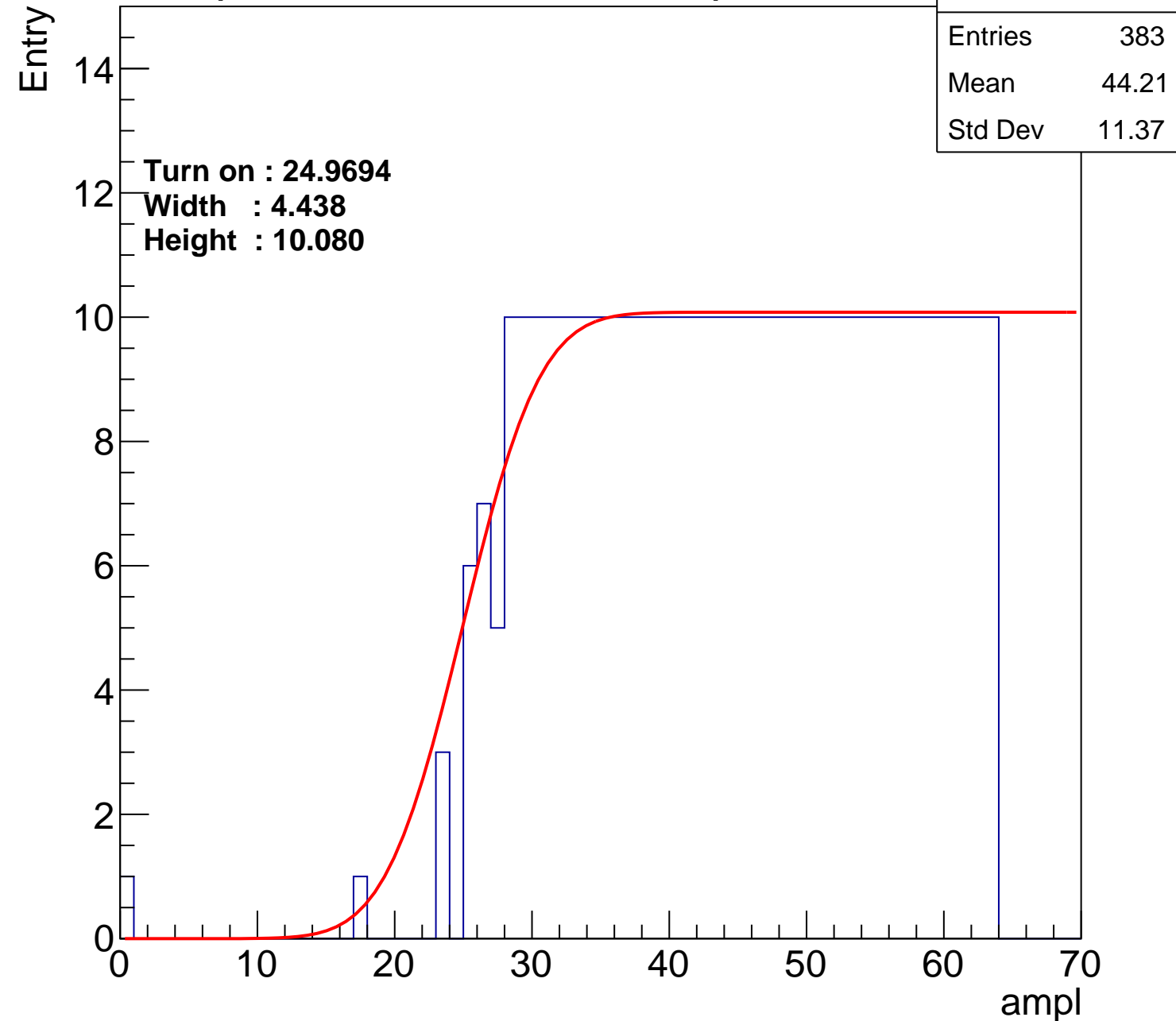
Width : 4.438

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.72
Std Dev	11.91

Turn on : 25.1486

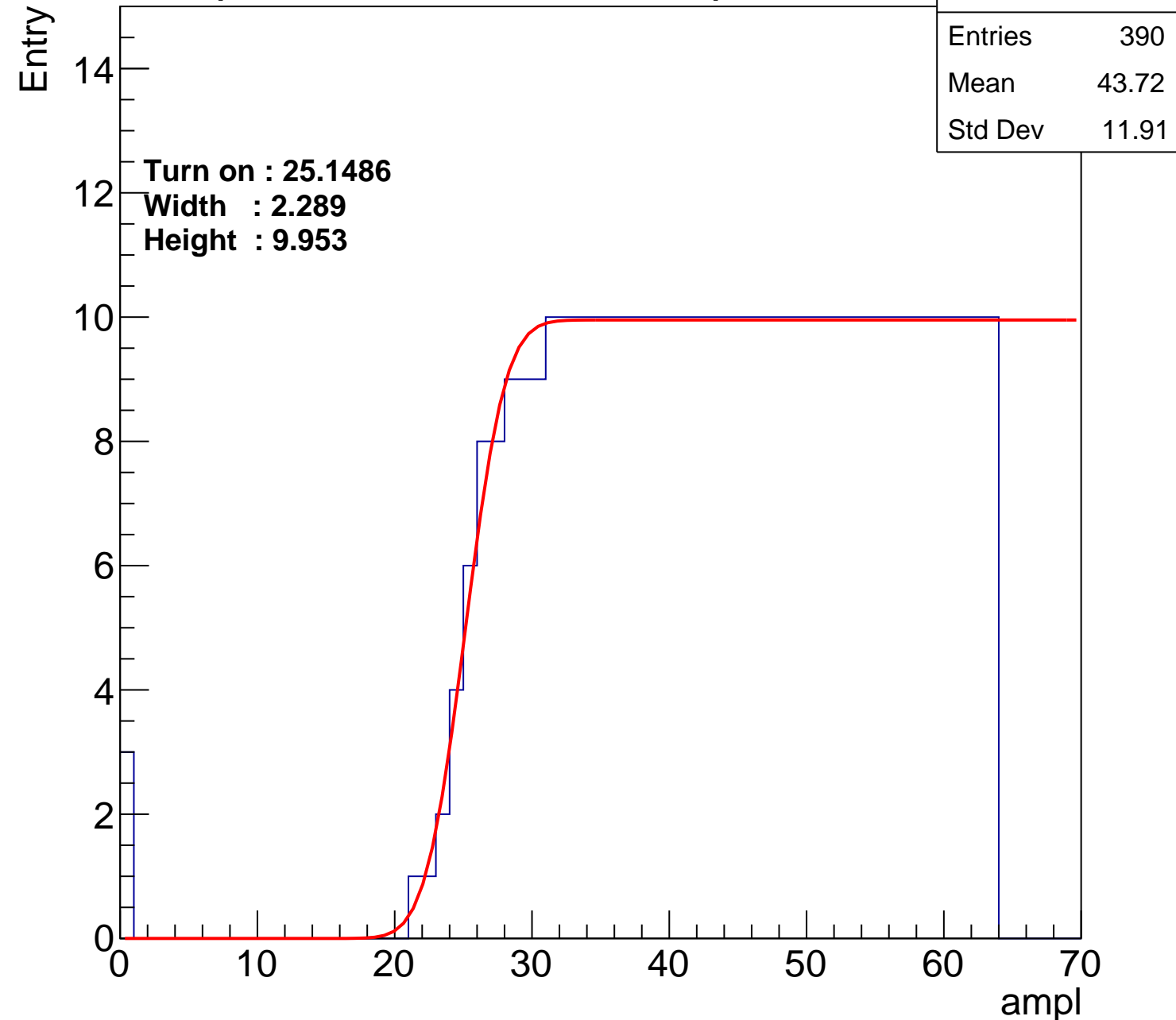
Width : 2.289

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.78

Turn on : 25.2923

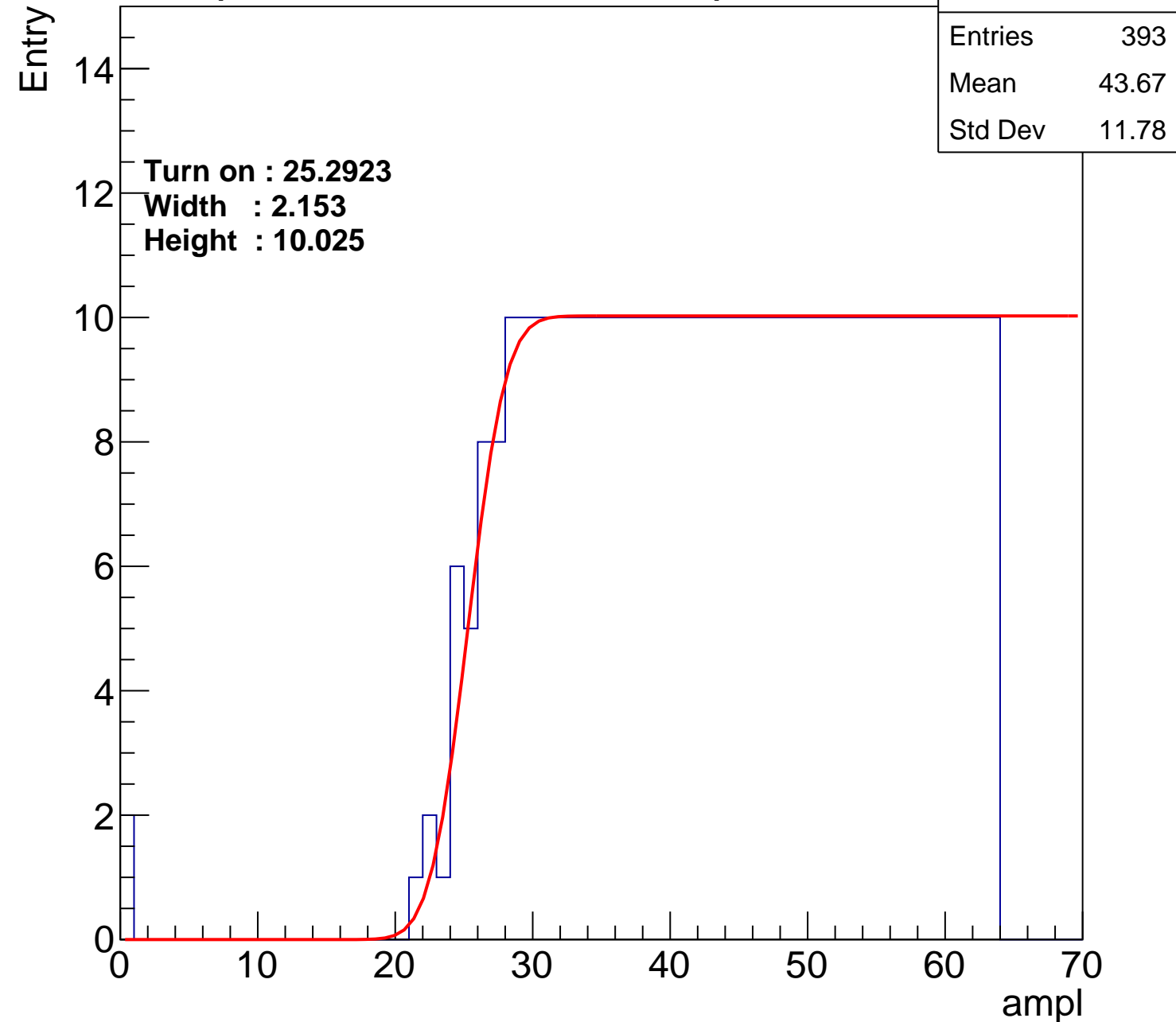
Width : 2.153

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.2864

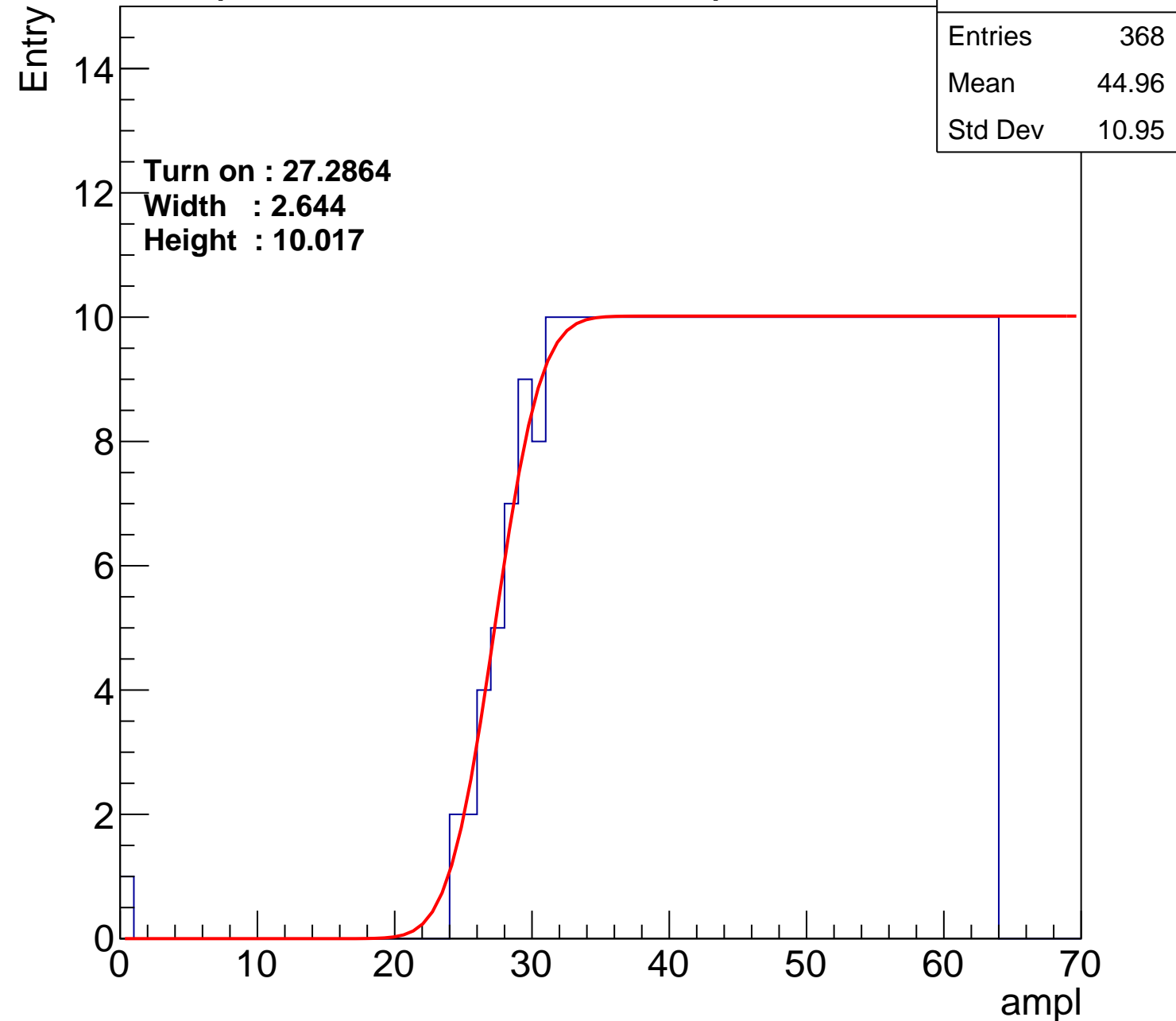
Width : 2.644

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch36

calib_packv5_042523_0143.root, FC#10, port B3

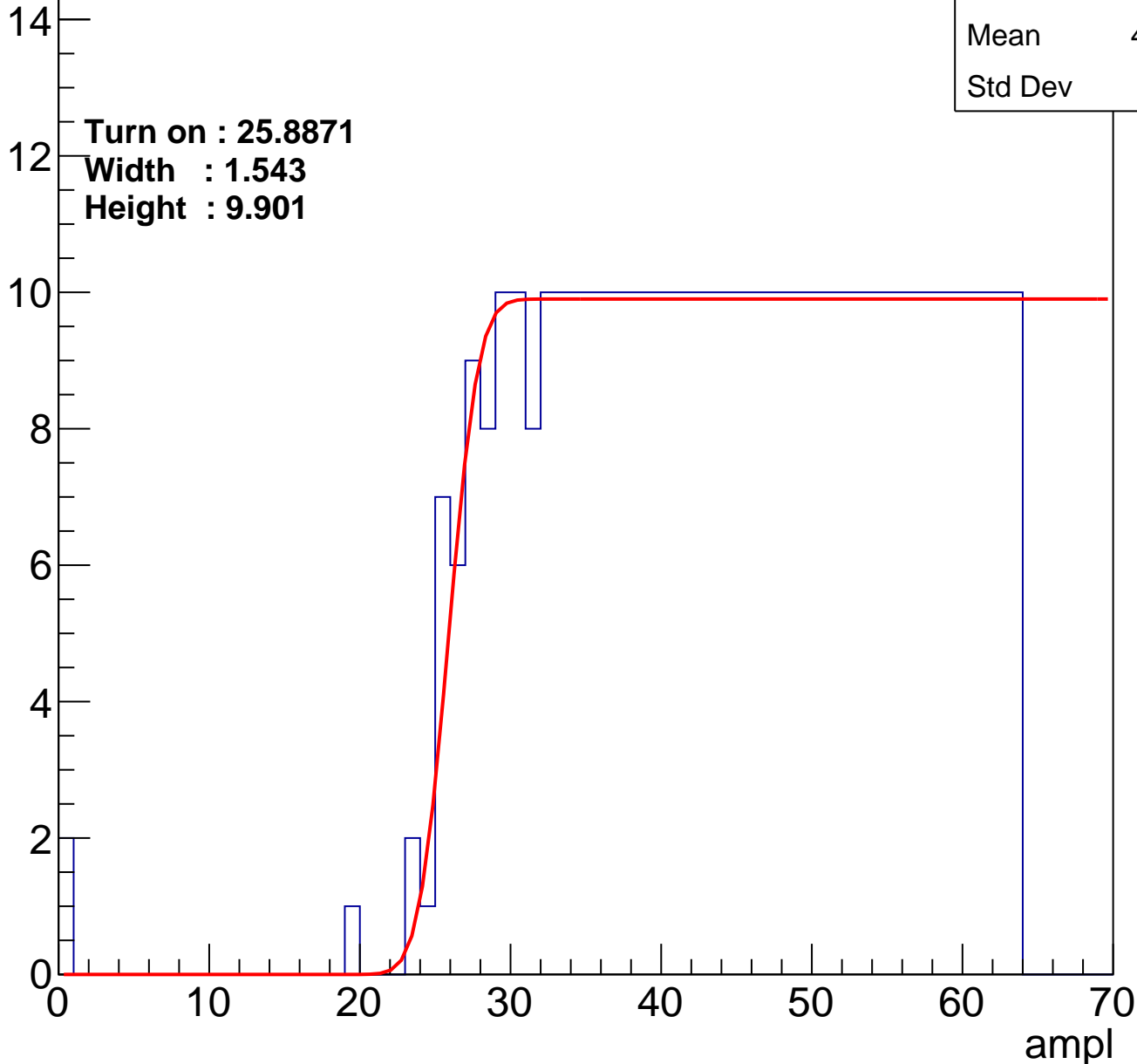
Entry

Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 25.8871

Width : 1.543

Height : 9.901



B1L002S, U4-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 26.2584

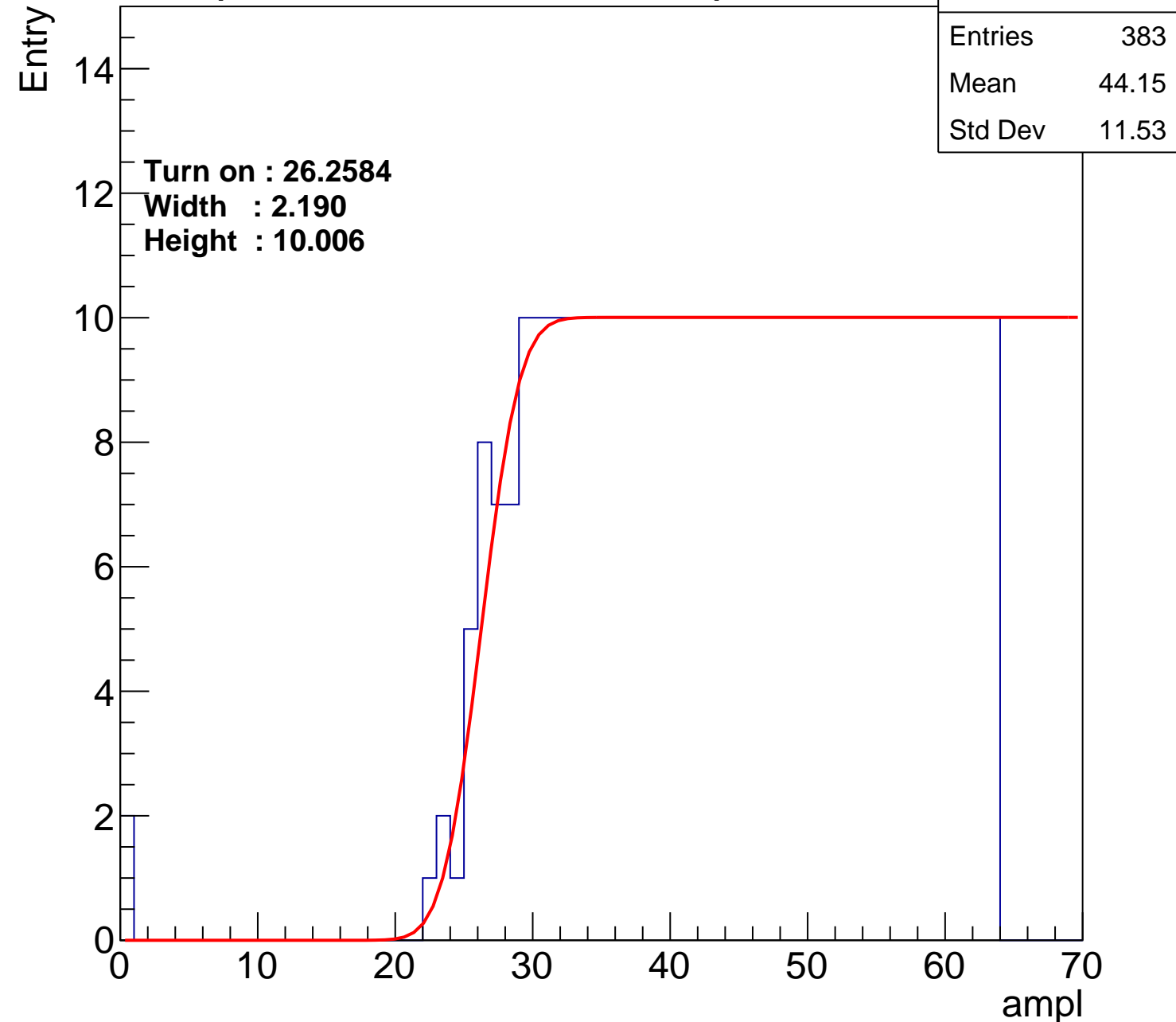
Width : 2.190

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch38

calib_packv5_042523_0143.root, FC#10, port B3

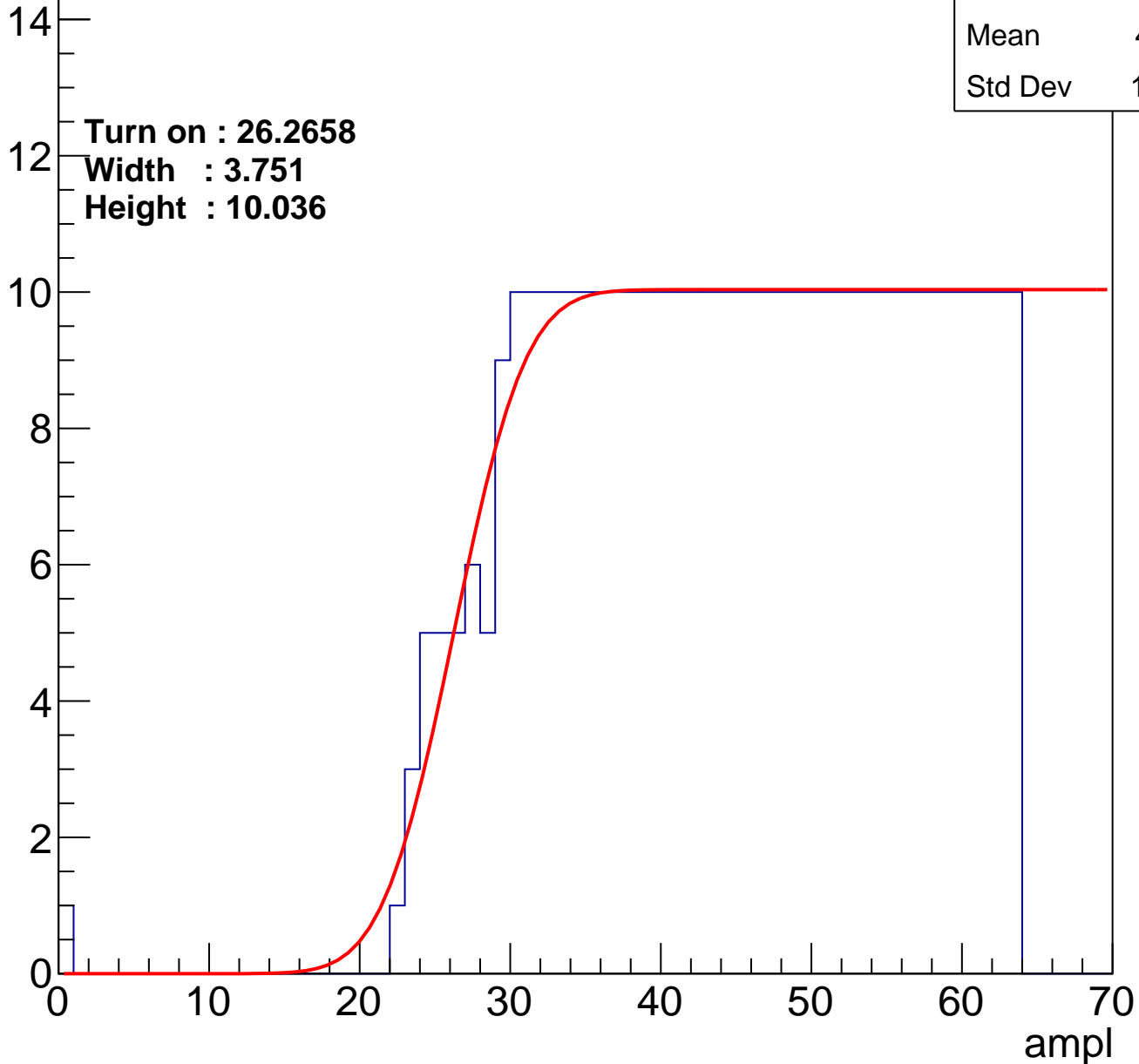
Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.2658

Width : 3.751

Height : 10.036

Entry



B1L002S, U4-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.62
Std Dev	11.64

Turn on : 27.9489

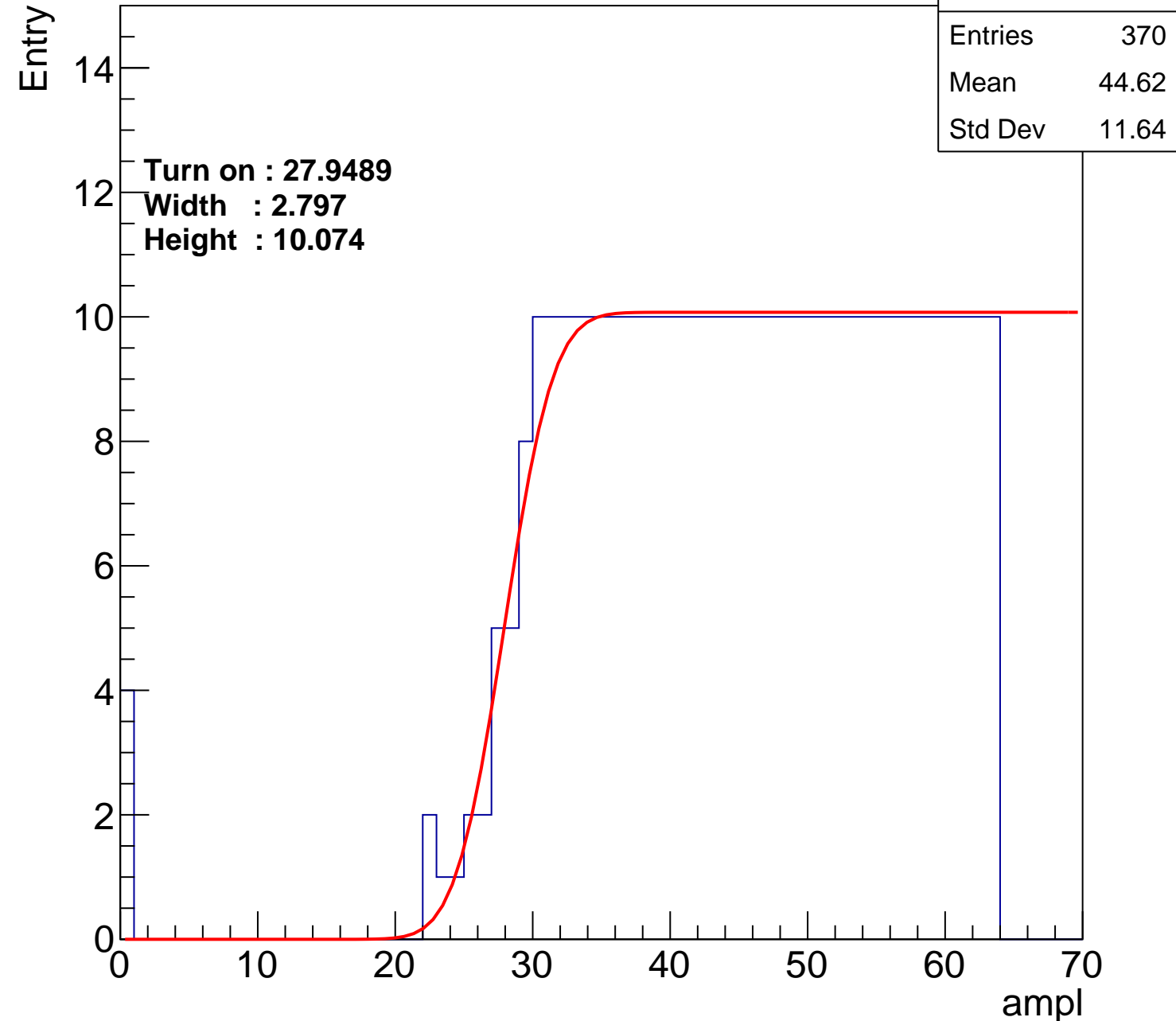
Width : 2.797

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.25
Std Dev	11.4

Turn on : 26.5077

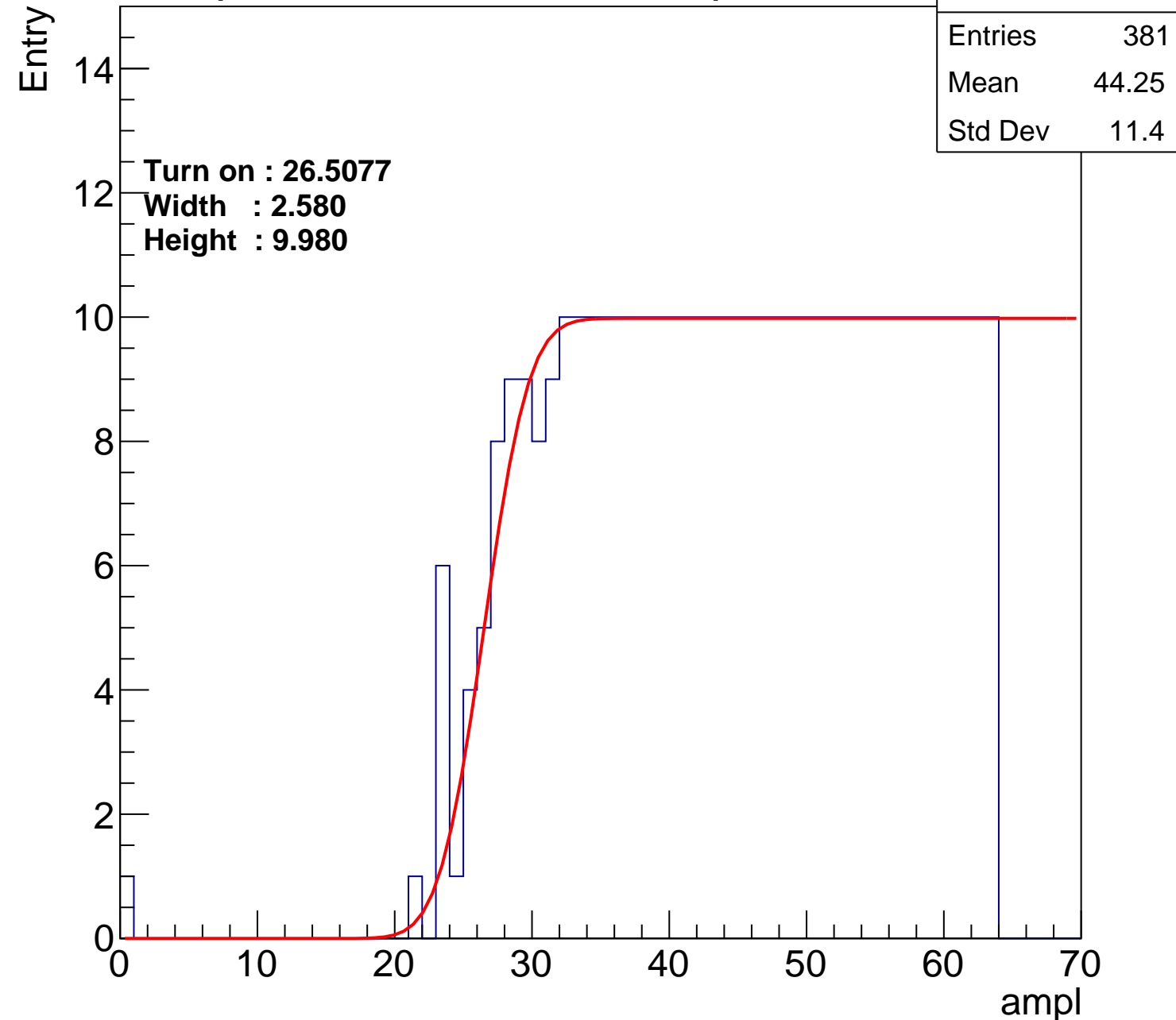
Width : 2.580

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.58
Std Dev	12.09

Turn on : 25.4987

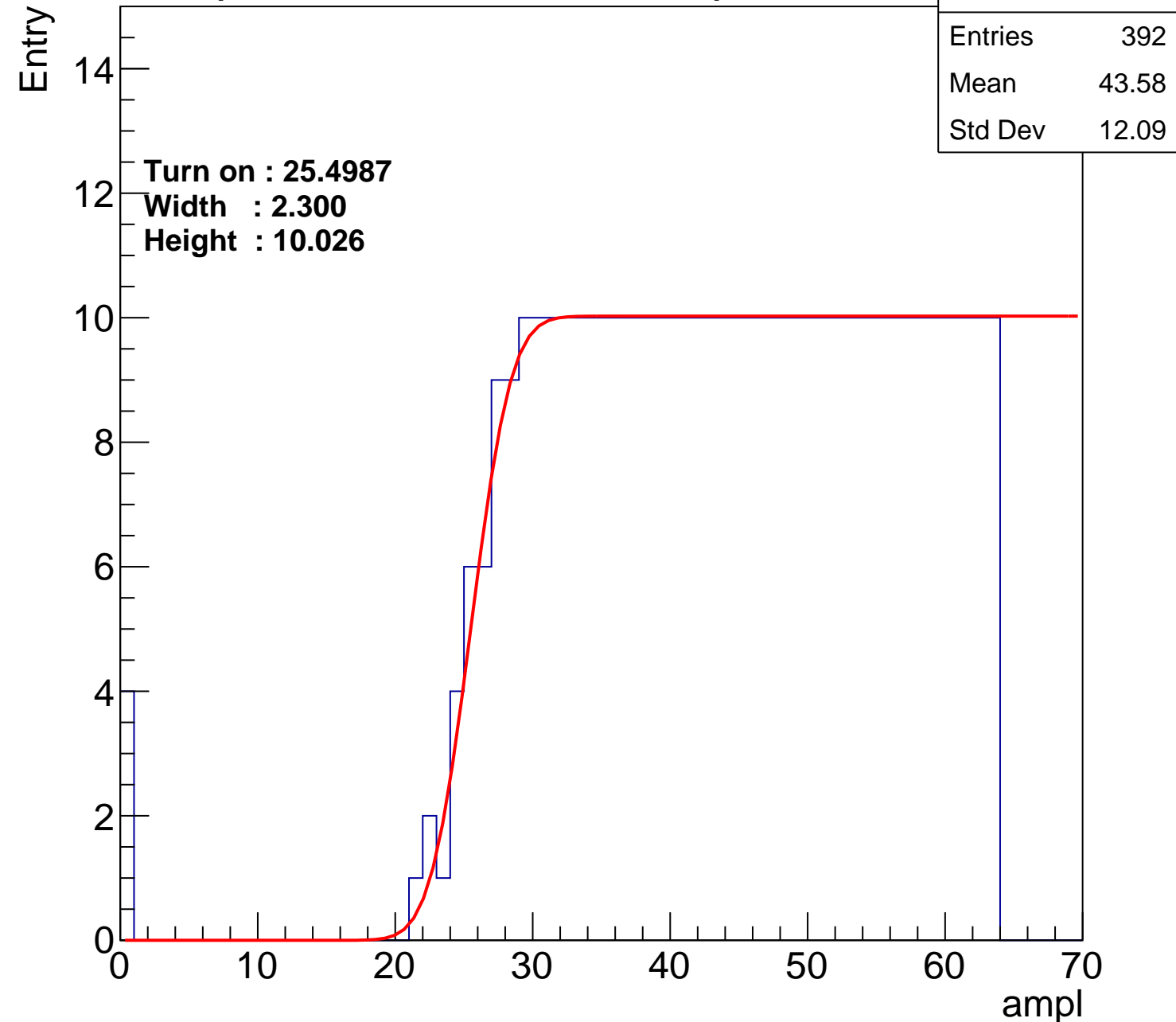
Width : 2.300

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.87
Std Dev	12.22

Turn on : 23.2545

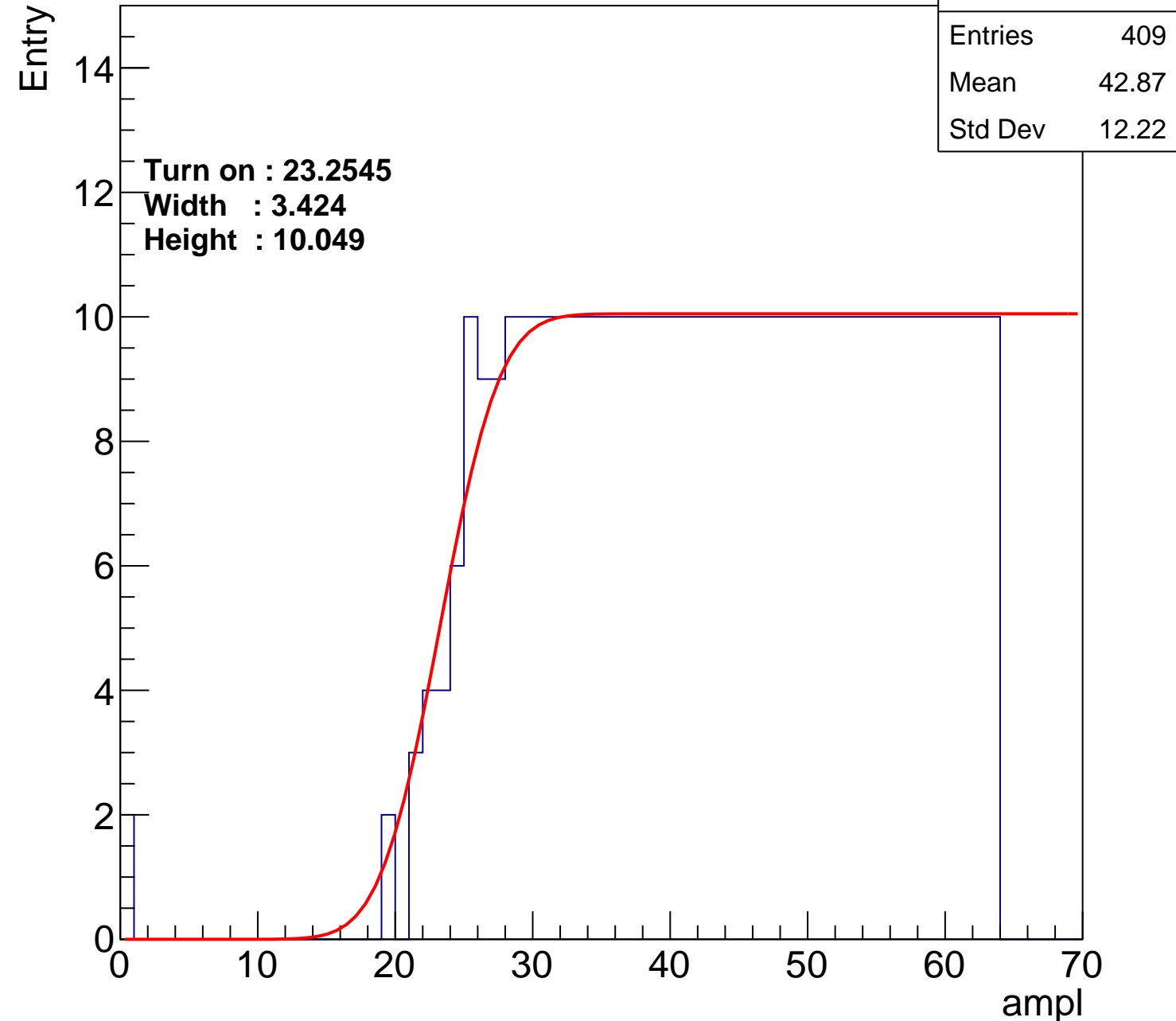
Width : 3.424

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 25.6132

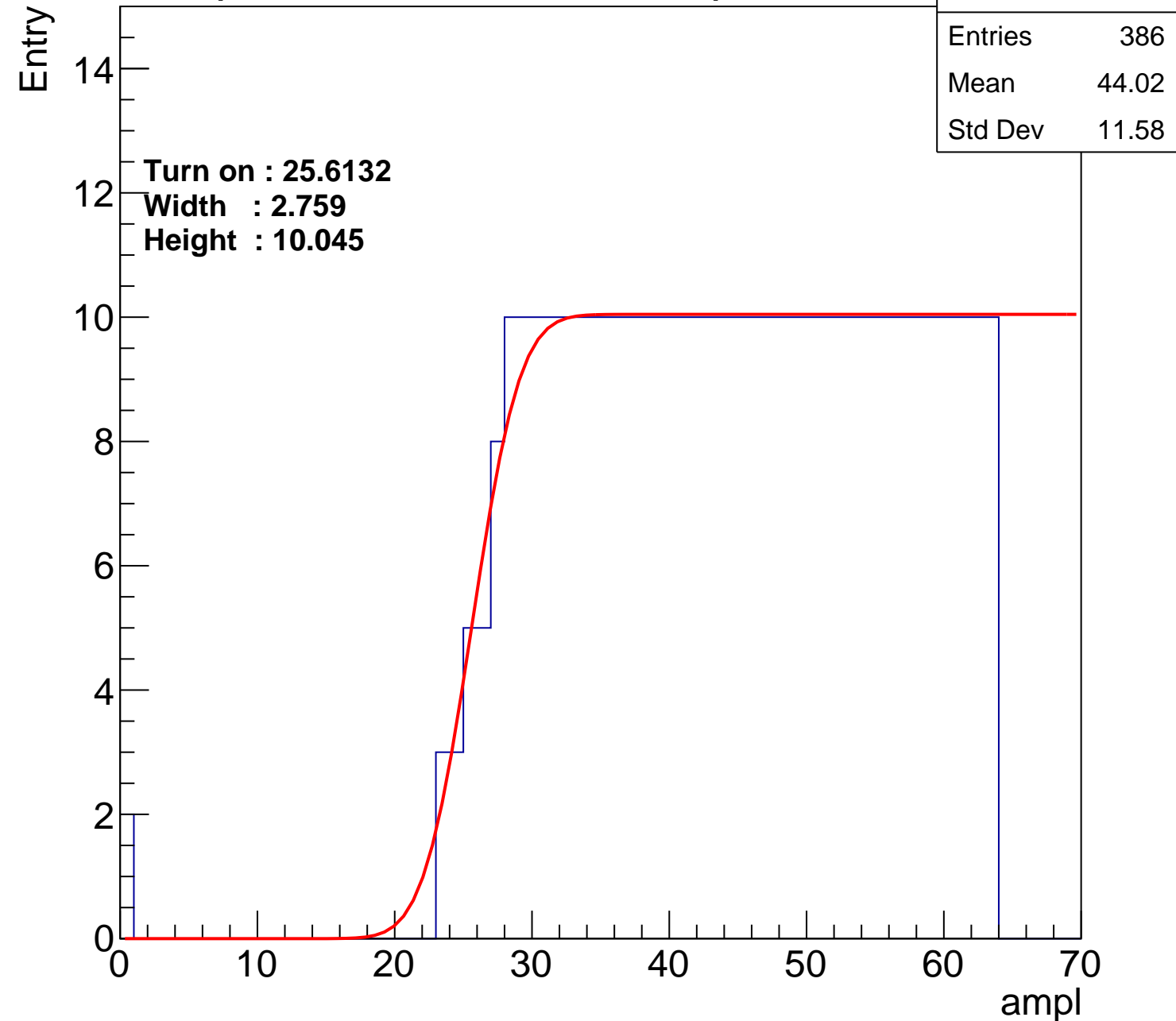
Width : 2.759

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.7
Std Dev	11.91

Turn on : 25.2368

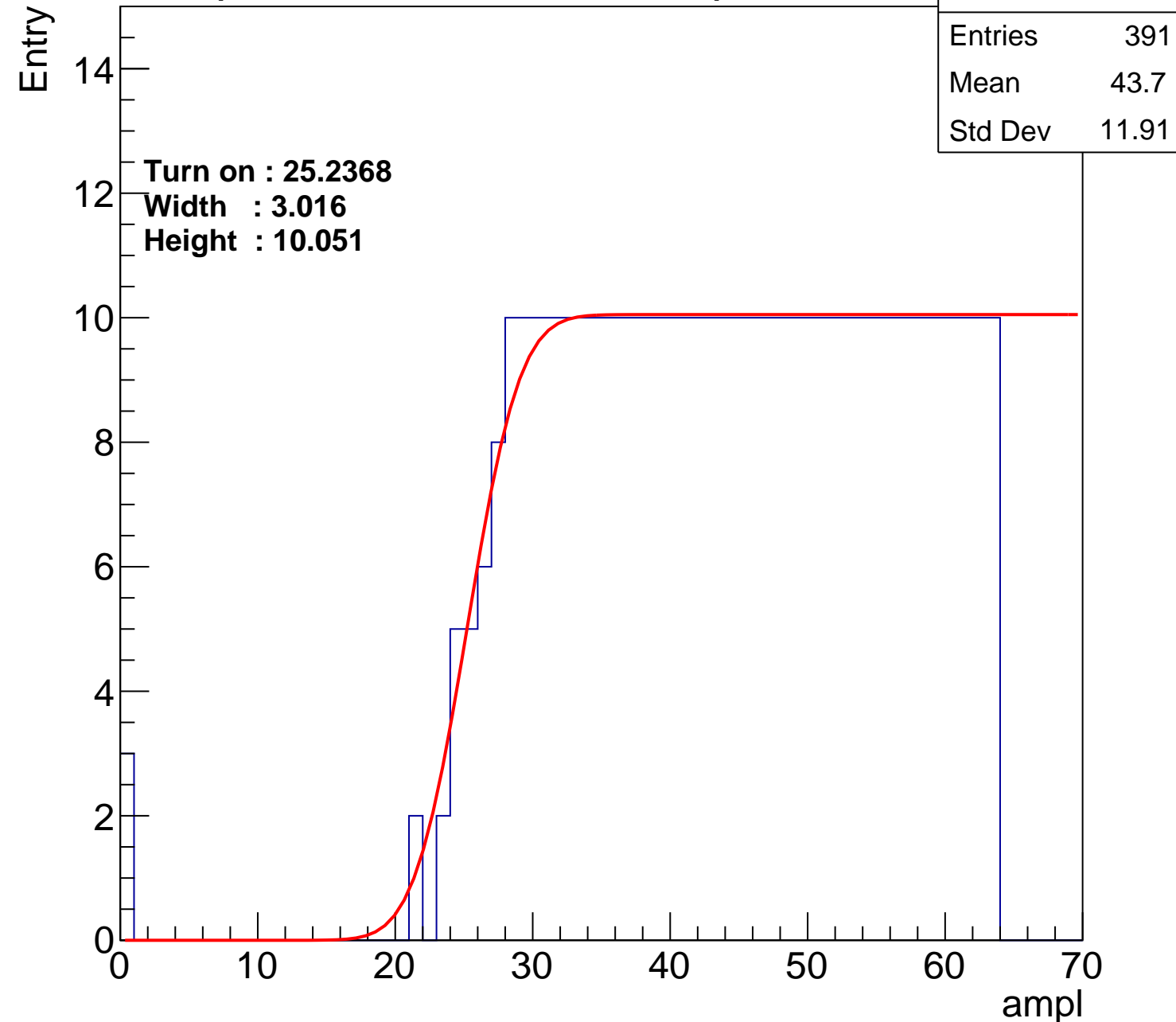
Width : 3.016

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 24.9832

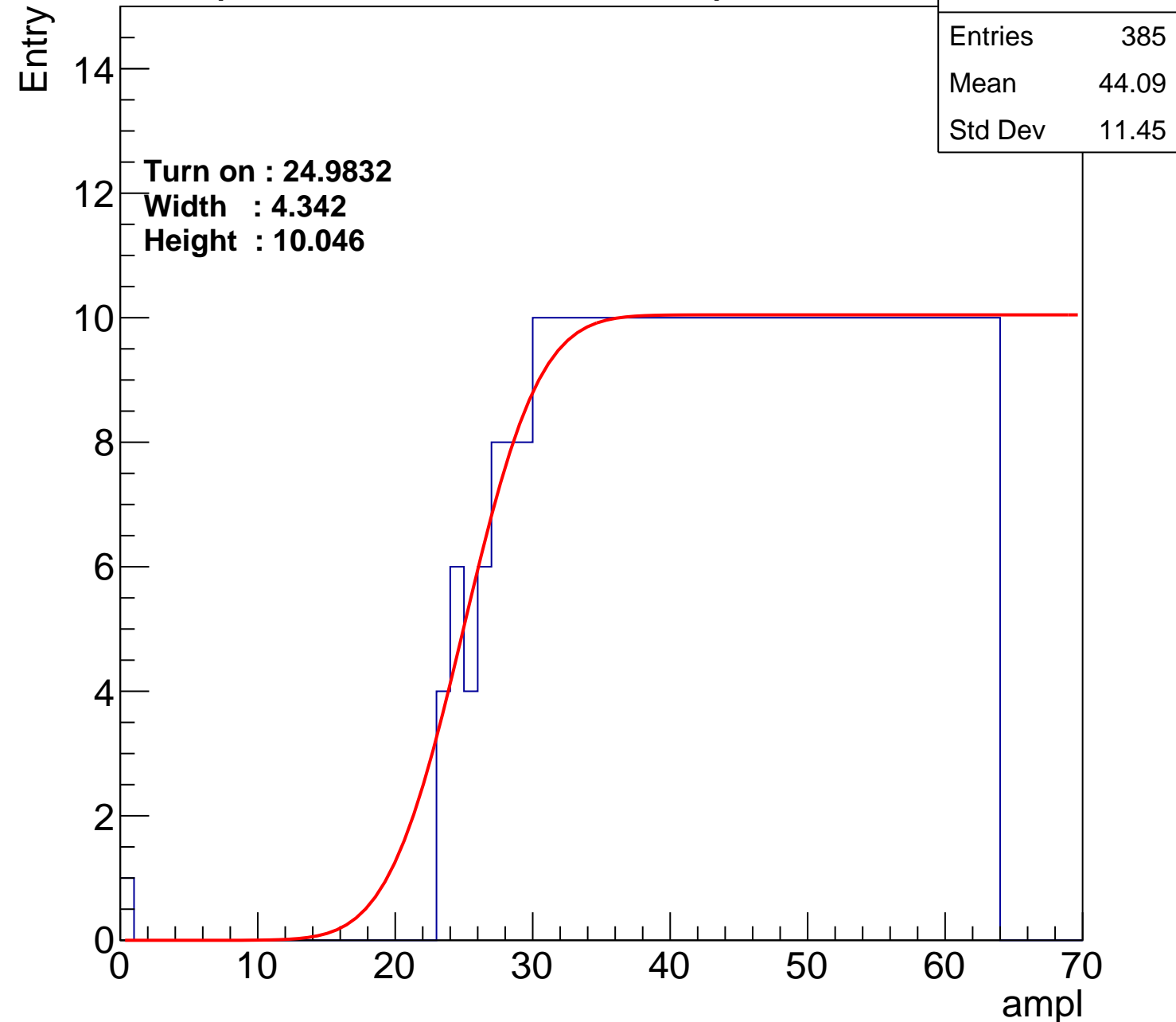
Width : 4.342

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.89
Std Dev	11.52

Turn on : 25.0791

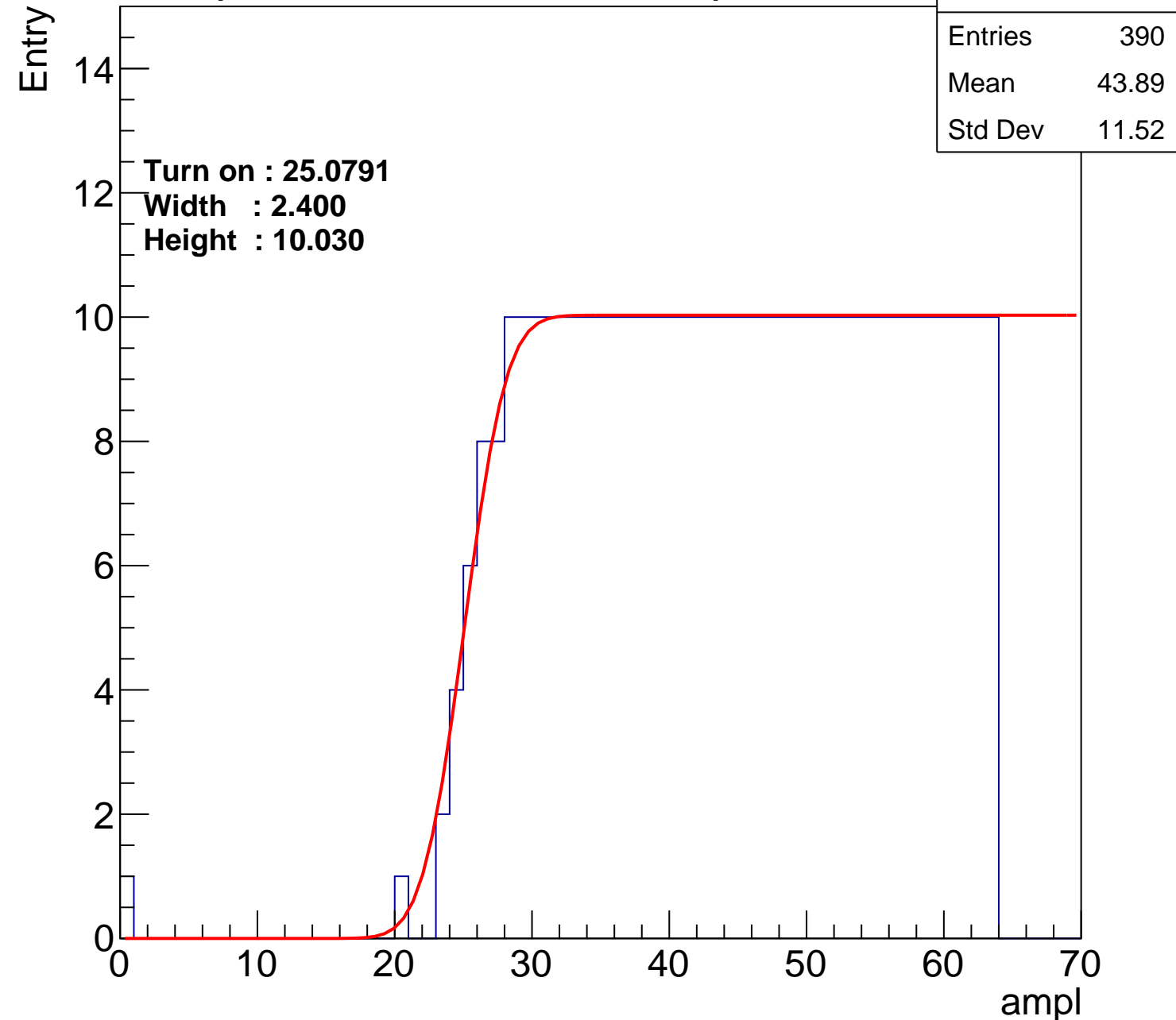
Width : 2.400

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 25.5308

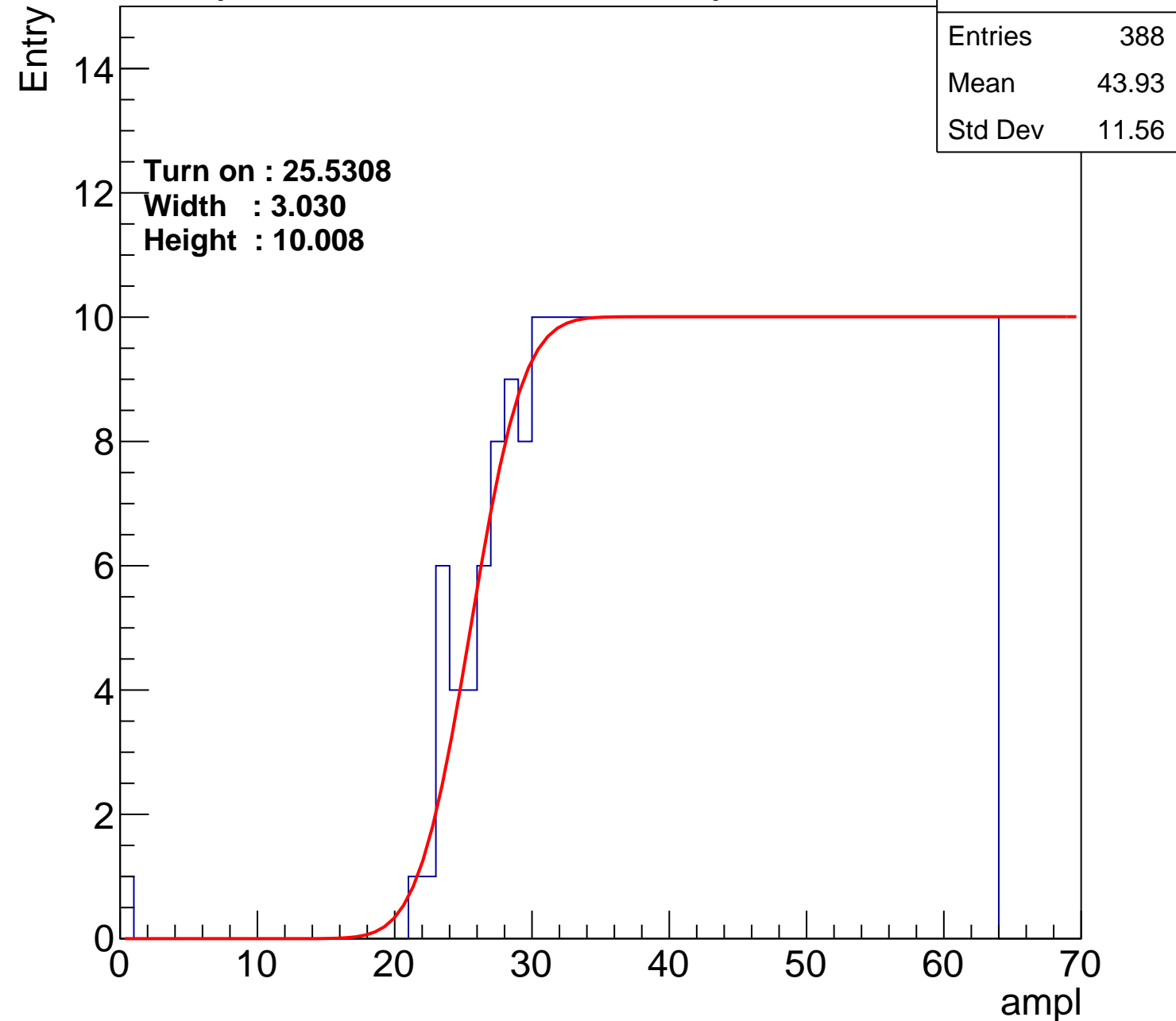
Width : 3.030

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.24
Std Dev	11.38

Turn on : 26.5001

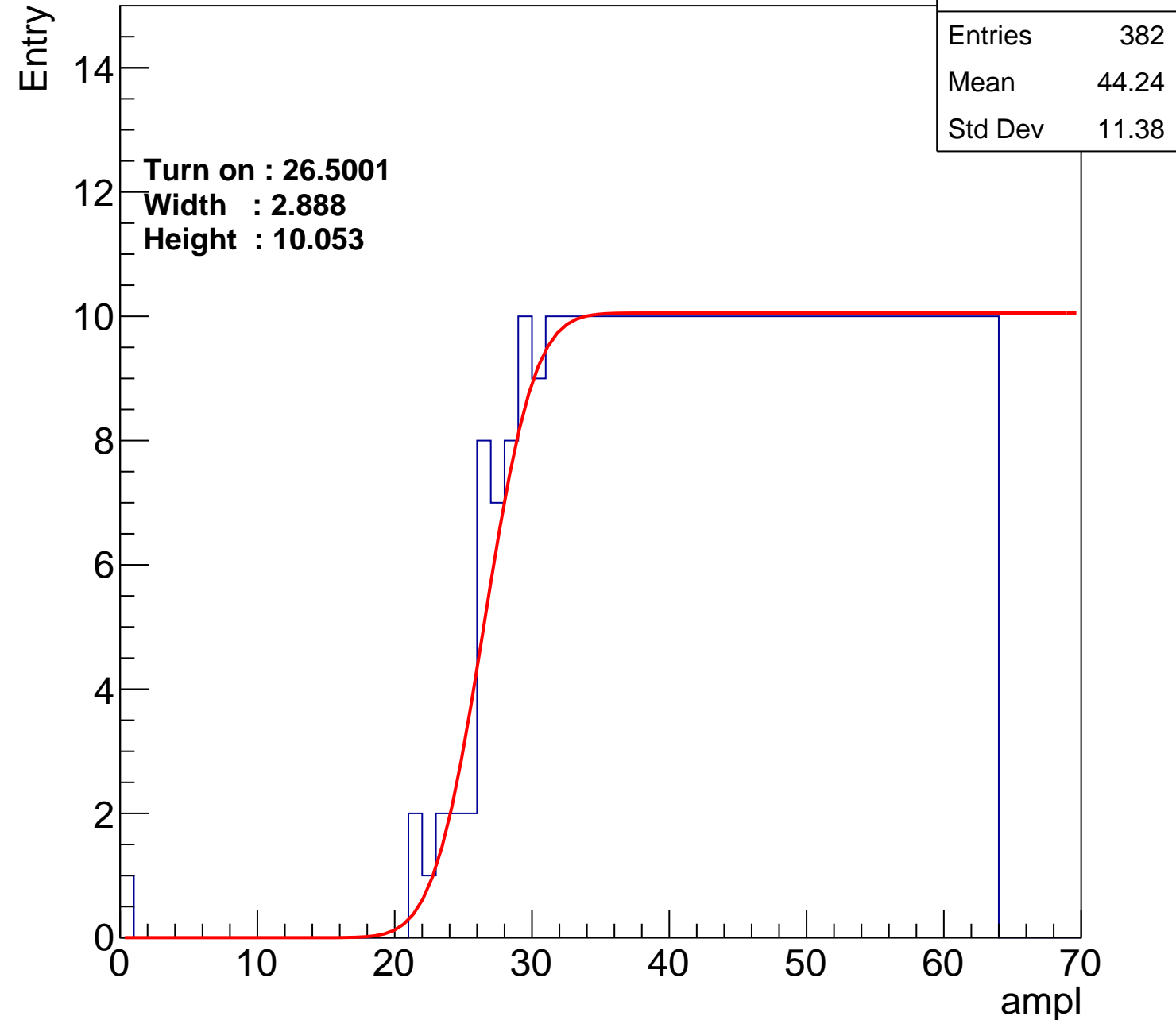
Width : 2.888

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch49

calib_packv5_042523_0143.root, FC#10, port B3

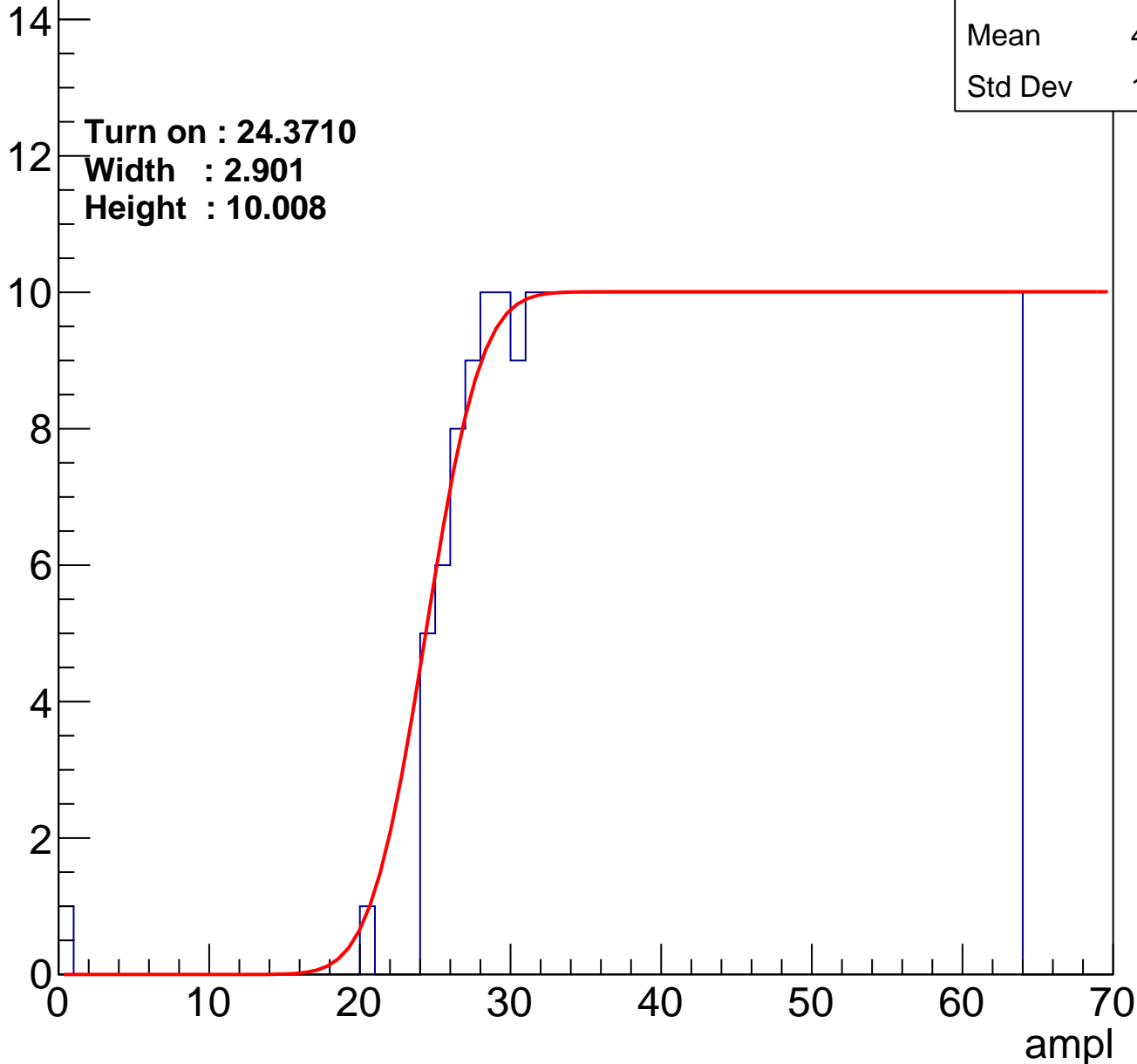
Entries	389
Mean	43.94
Std Dev	11.49

Turn on : 24.3710

Width : 2.901

Height : 10.008

Entry



B1L002S, U4-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.97
Std Dev	11.61

Turn on : 25.8744

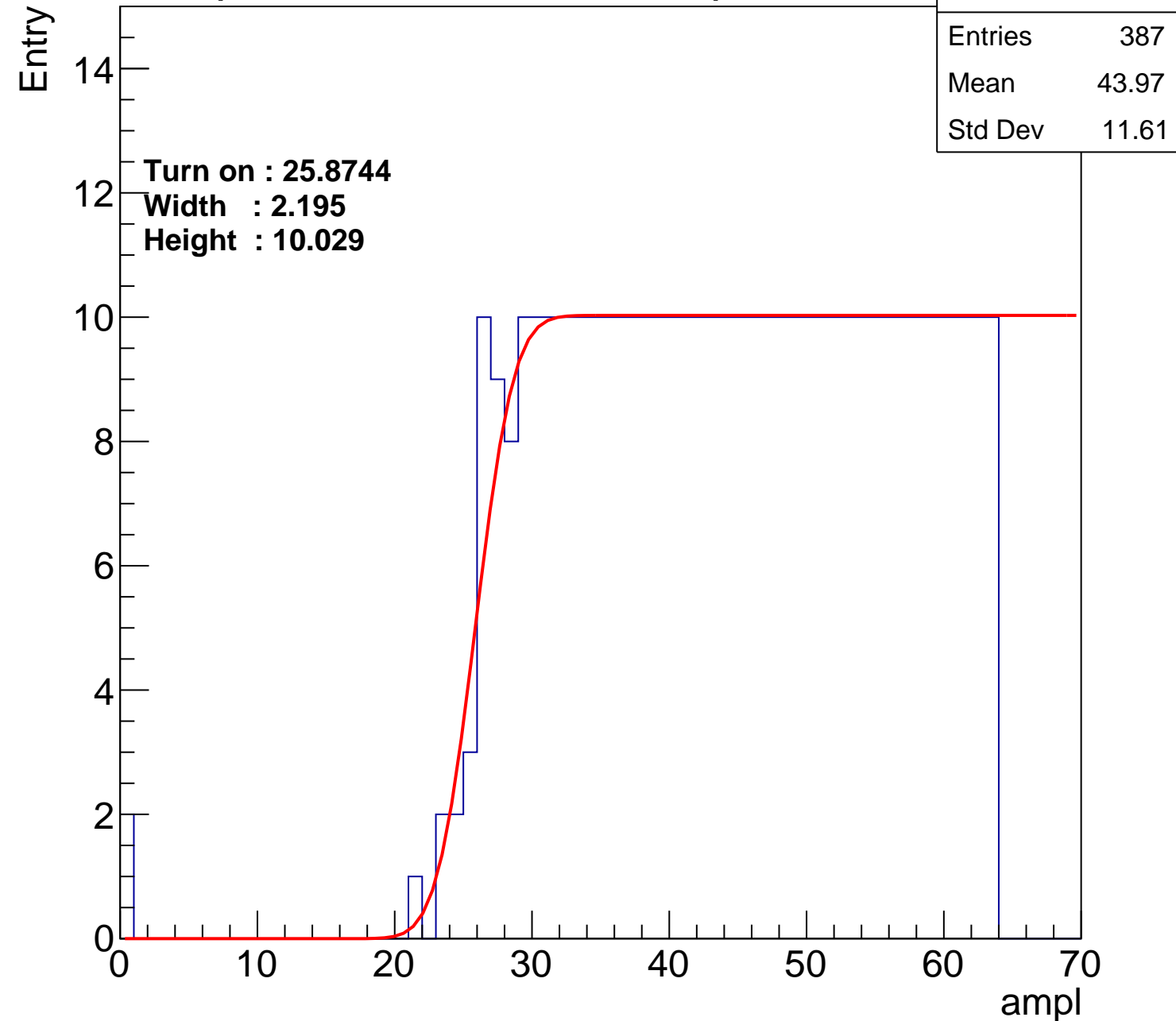
Width : 2.195

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch51

calib_packv5_042523_0143.root, FC#10, port B3

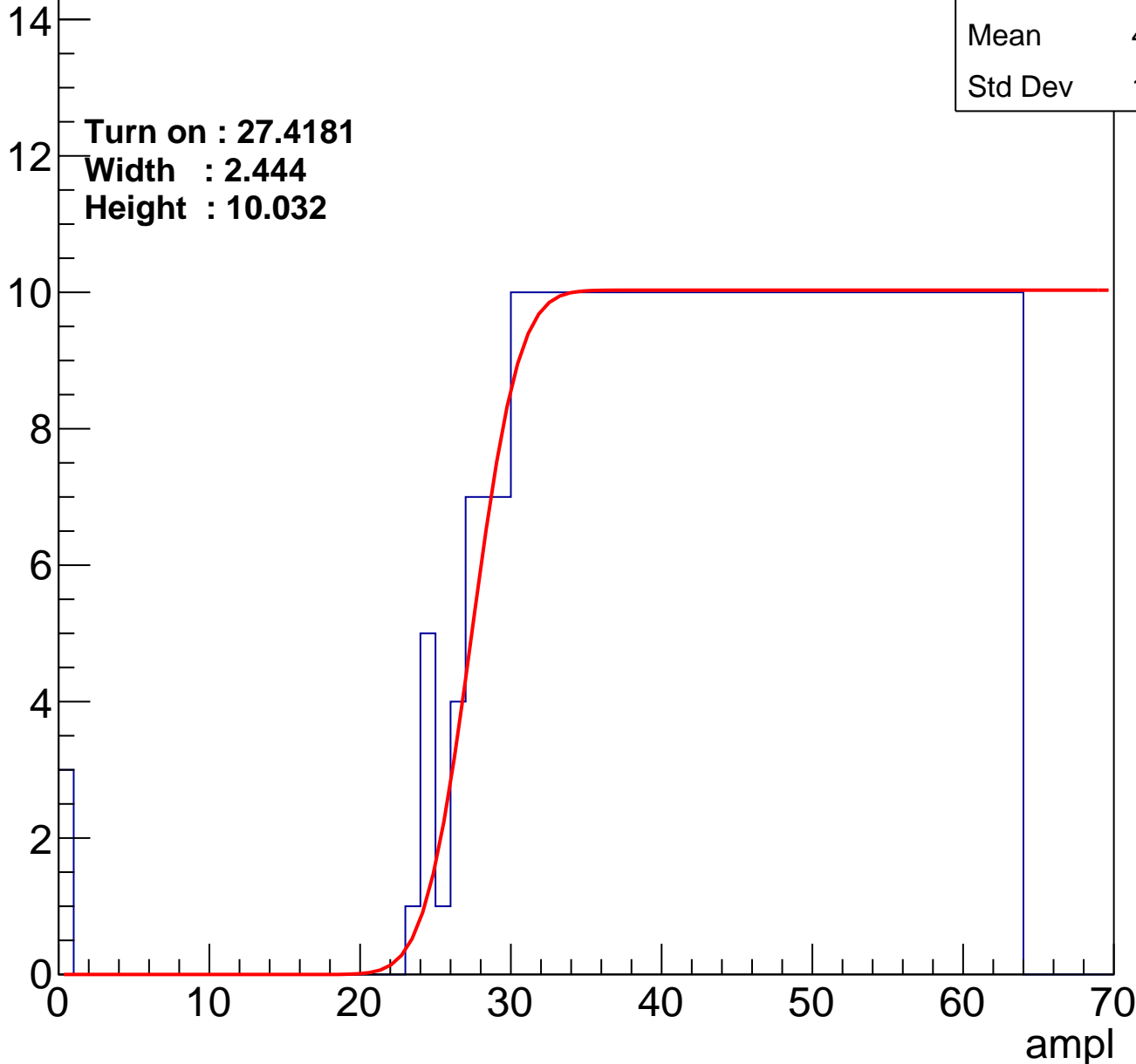
Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 27.4181

Width : 2.444

Height : 10.032

Entry



B1L002S, U4-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.02

Turn on : 25.0544

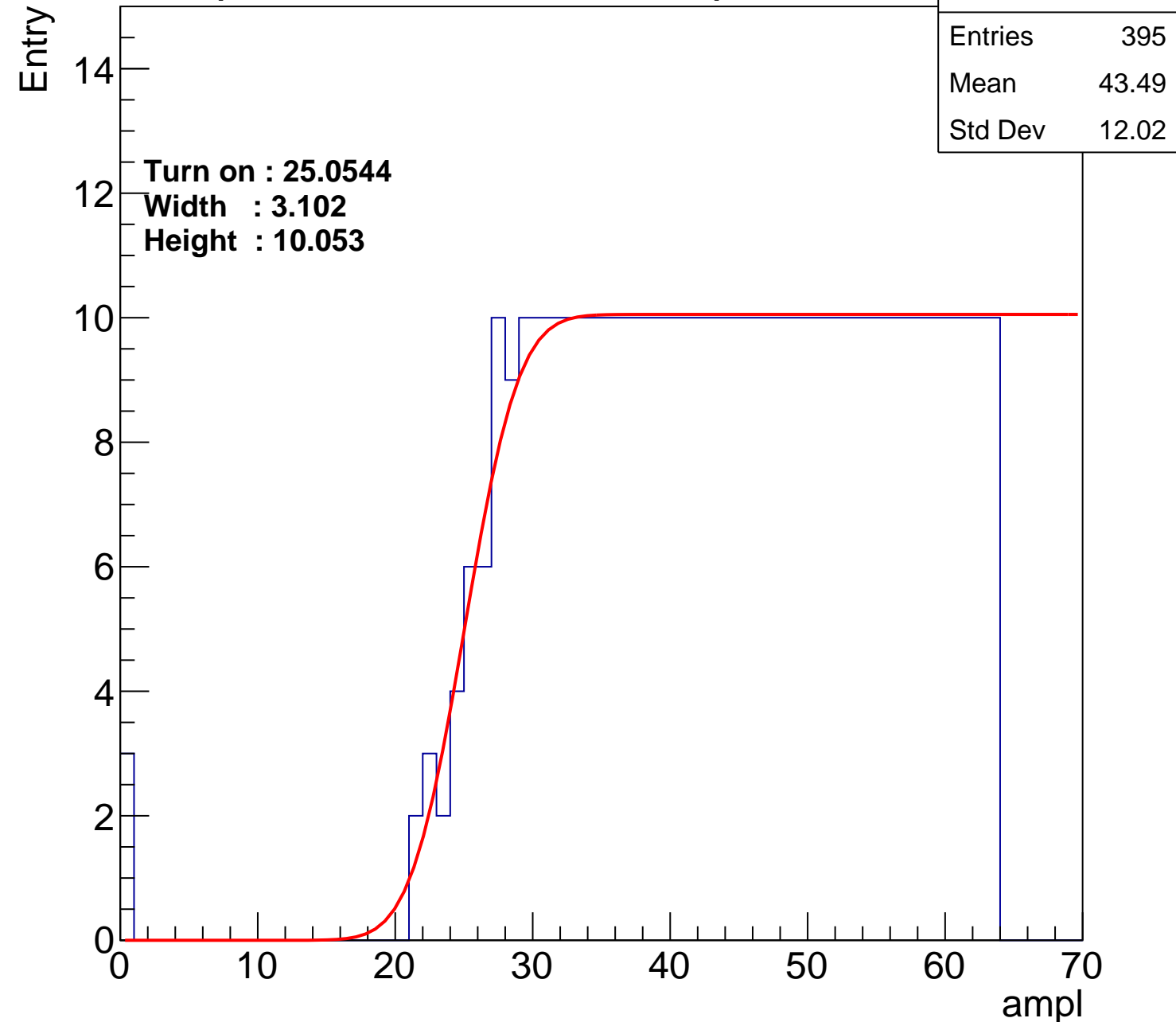
Width : 3.102

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 24.9550

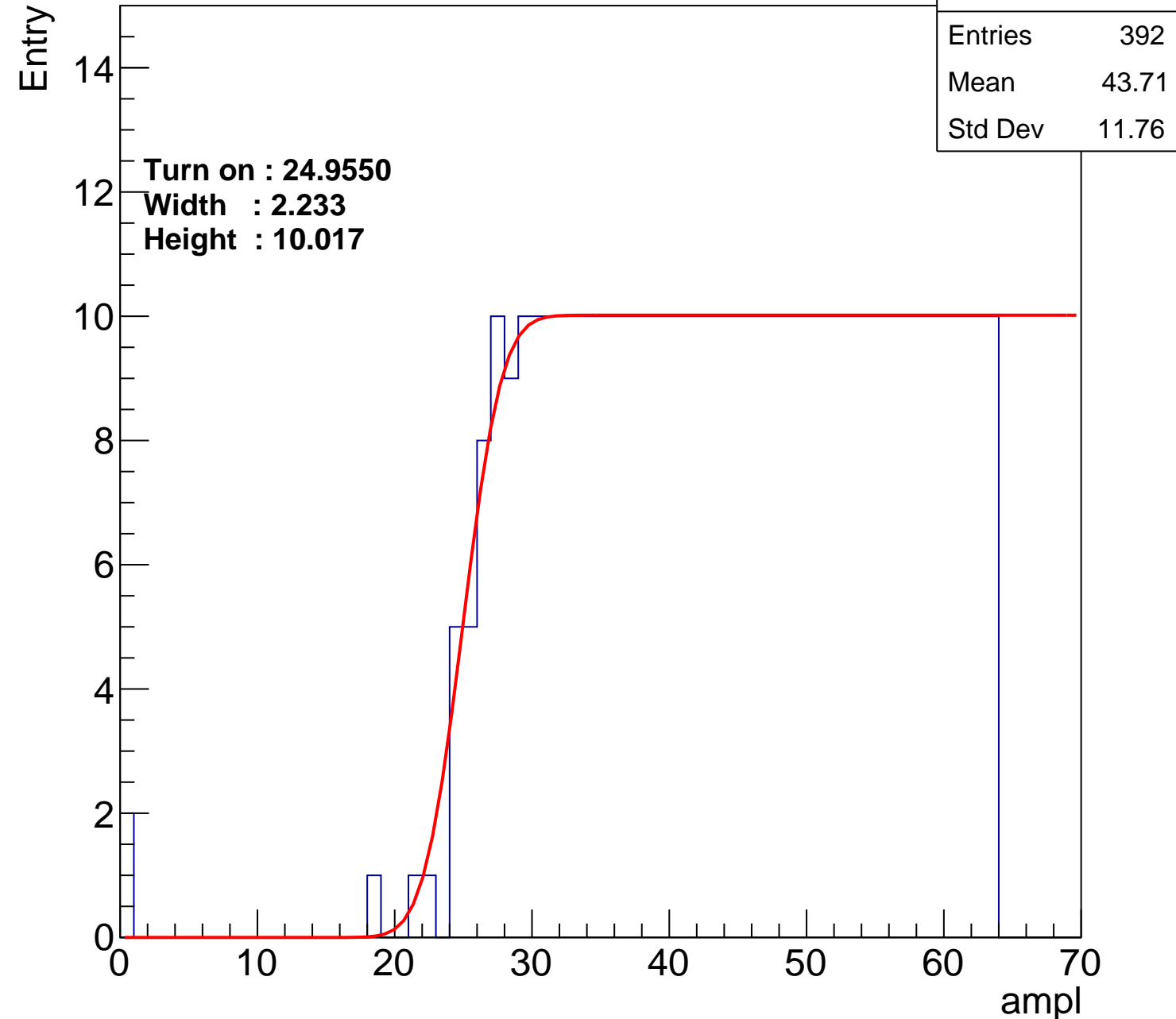
Width : 2.233

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.6866

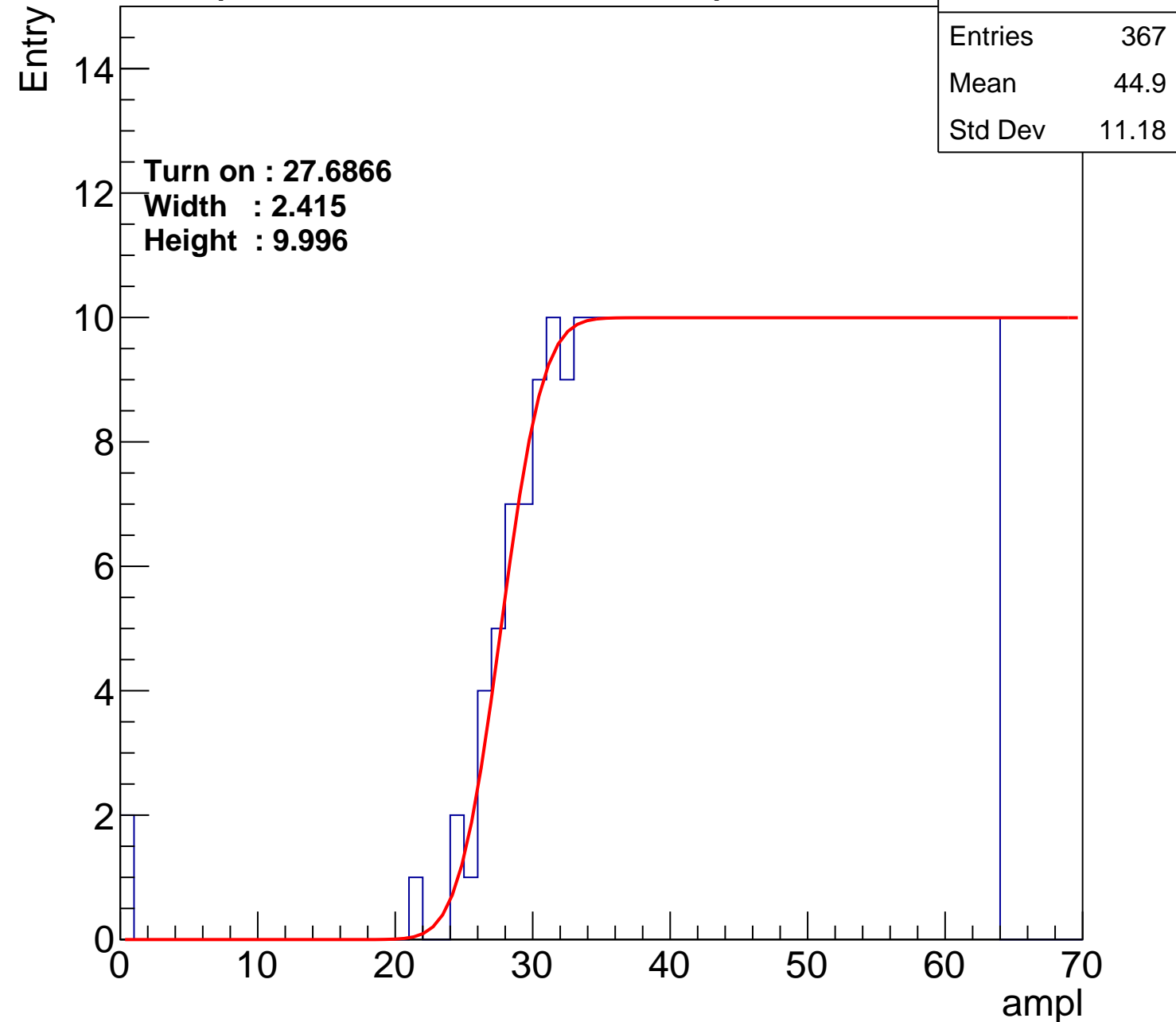
Width : 2.415

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.36
Std Dev	12.34

Turn on : 24.8148

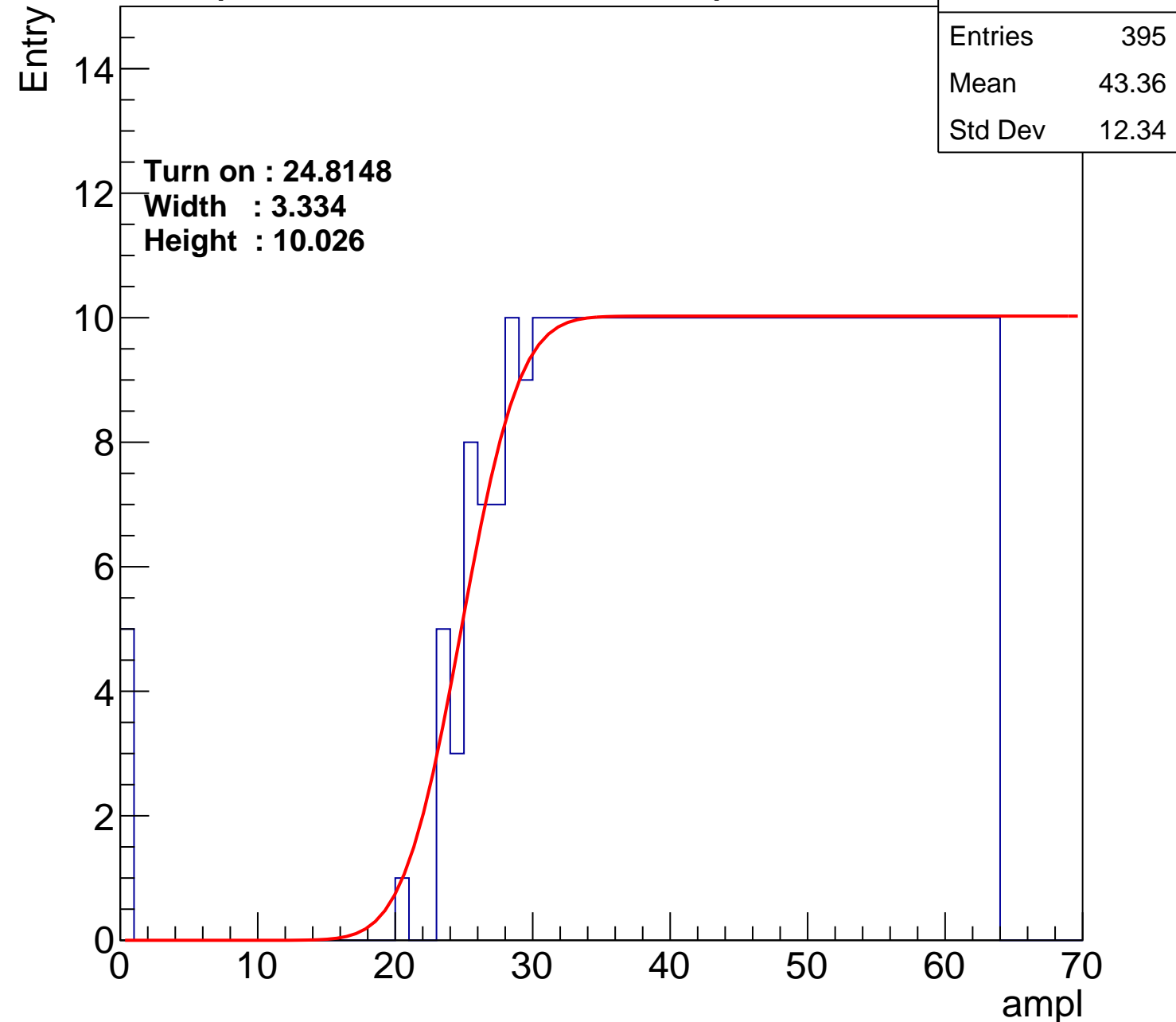
Width : 3.334

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.04
Std Dev	11.62

Turn on : 26.4802

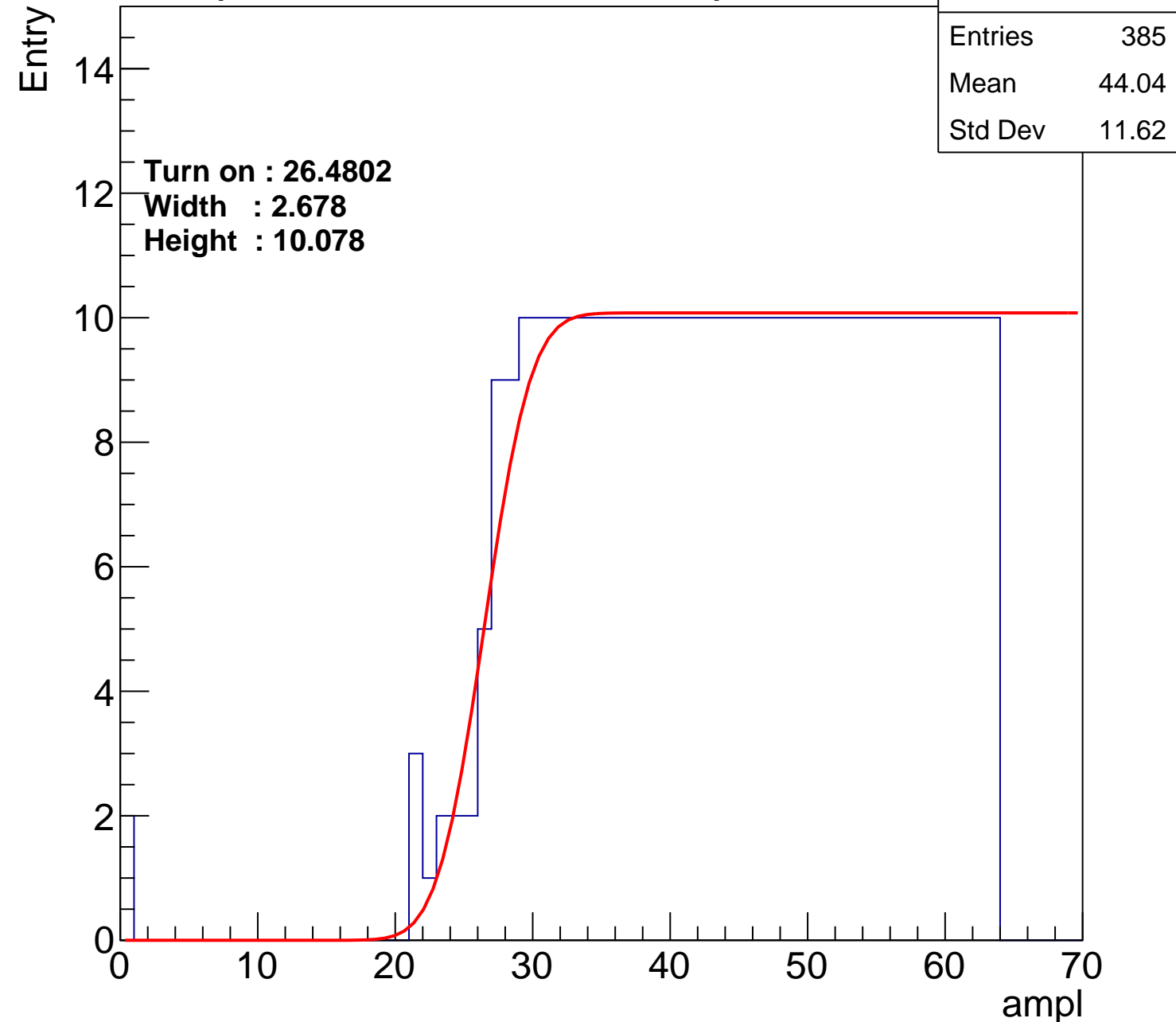
Width : 2.678

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch57

calib_packv5_042523_0143.root, FC#10, port B3

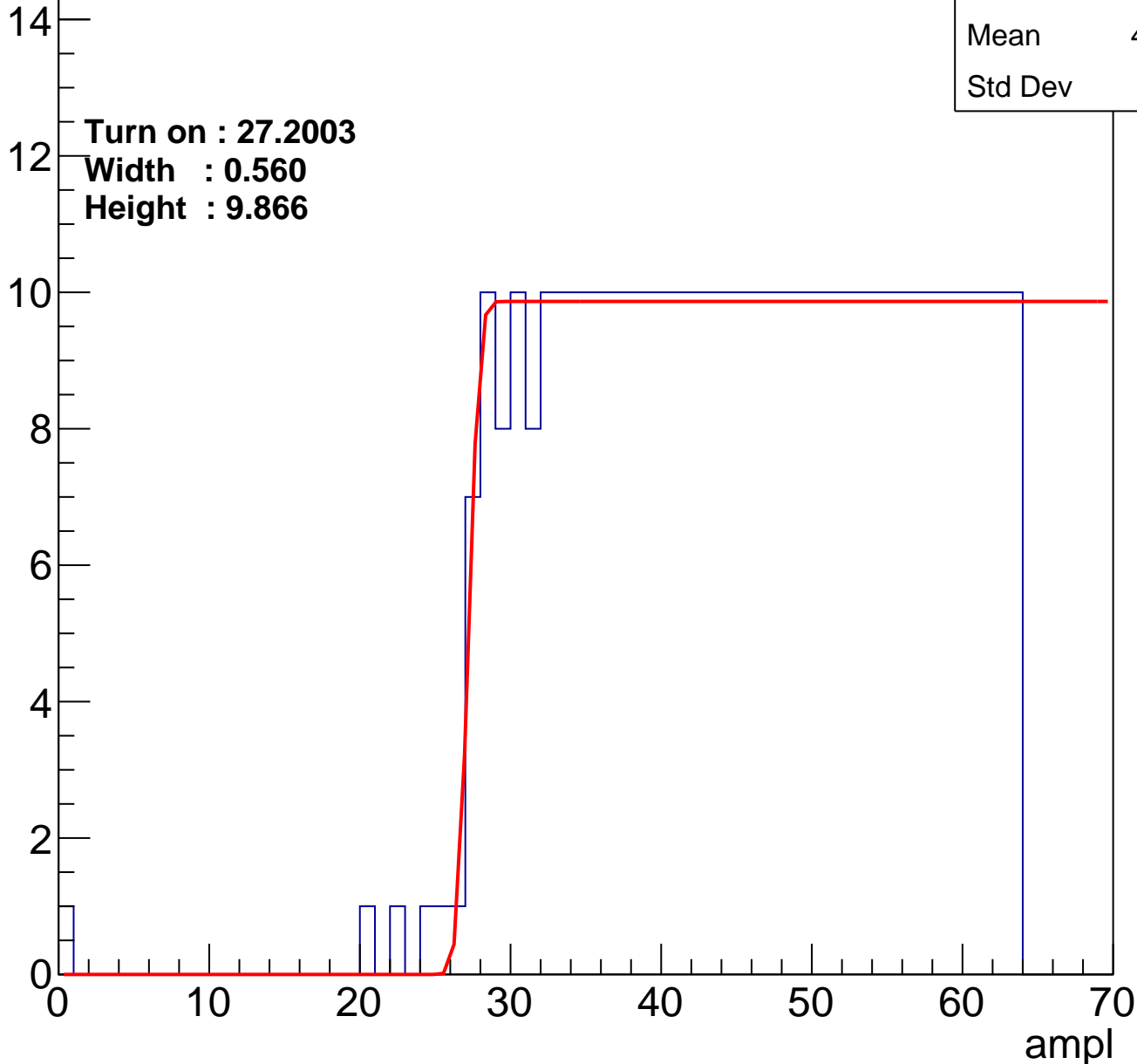
Entry

Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.2003

Width : 0.560

Height : 9.866



B1L002S, U4-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.05
Std Dev	12.14

Turn on : 23.8689

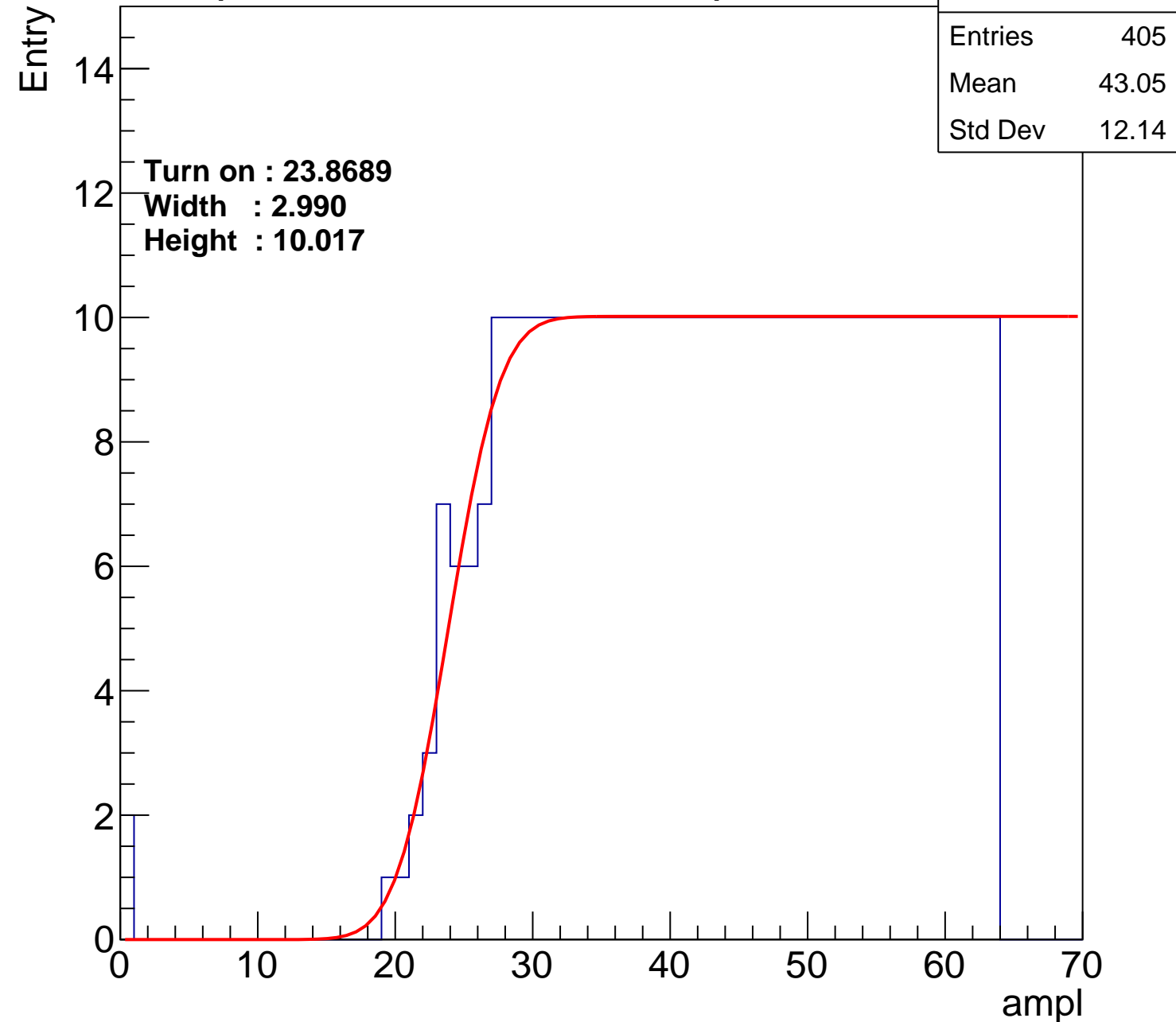
Width : 2.990

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.02
Std Dev	11.5

Turn on : 25.5657

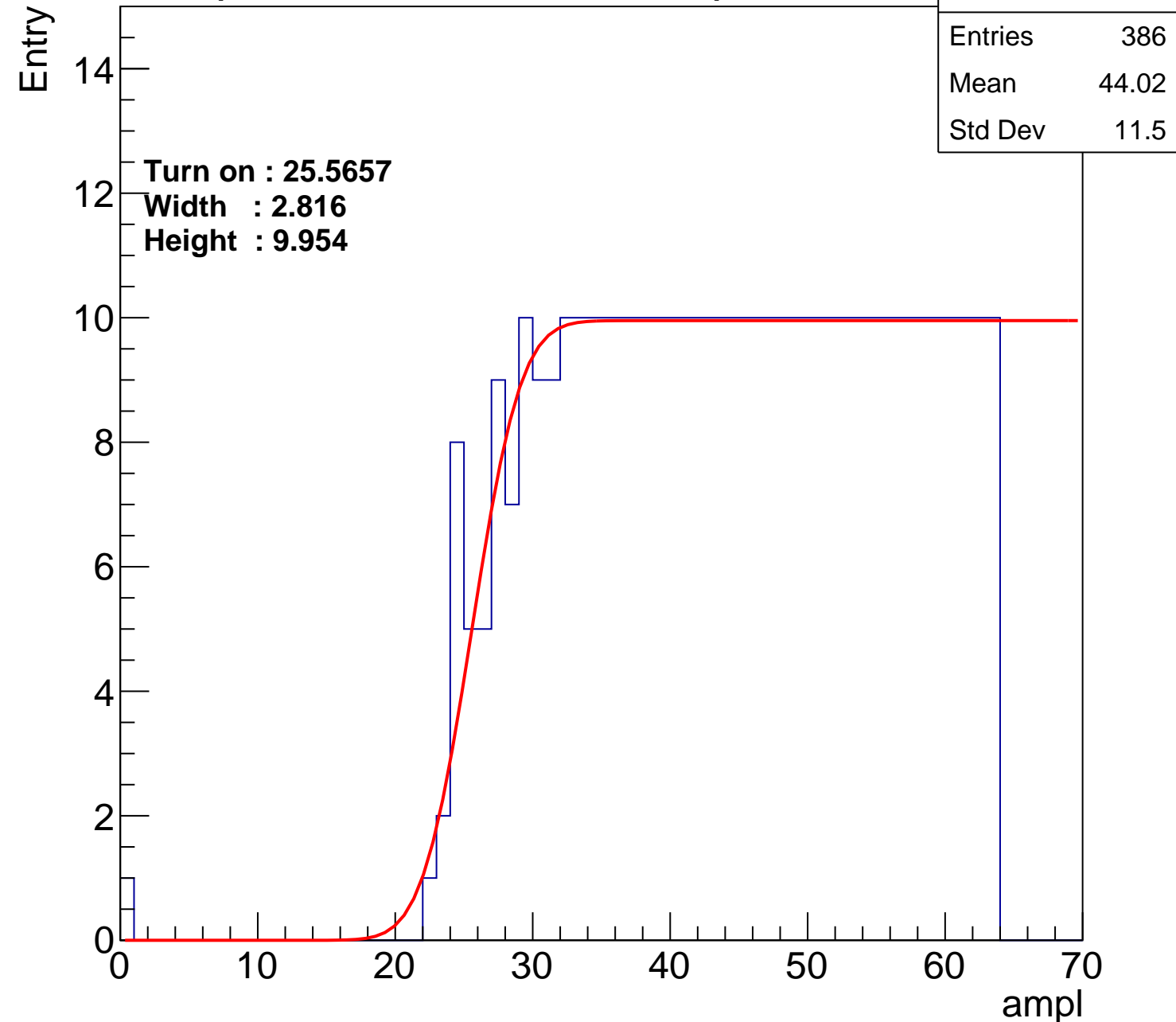
Width : 2.816

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.0389

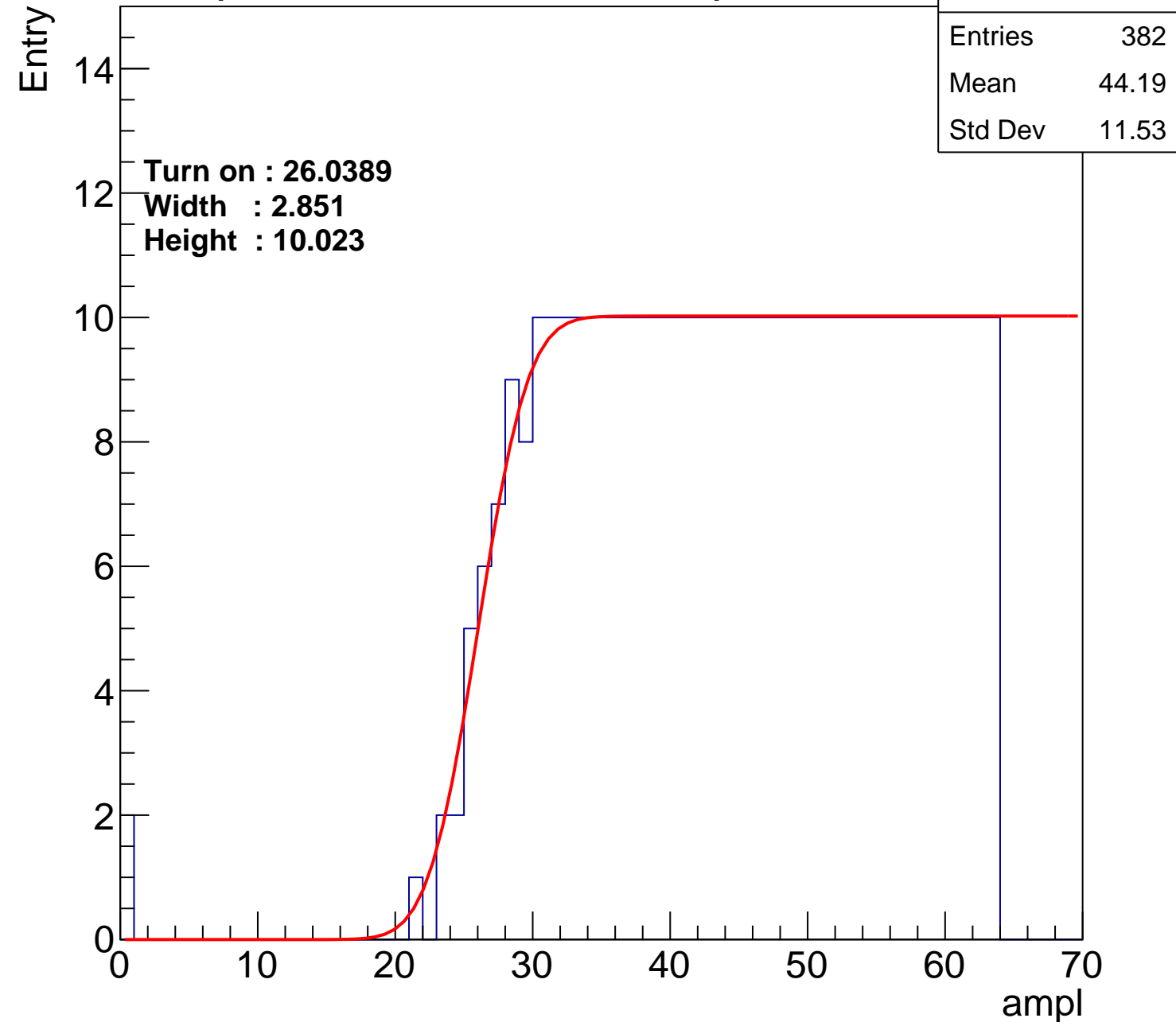
Width : 2.851

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch61

calib_packv5_042523_0143.root, FC#10, port B3

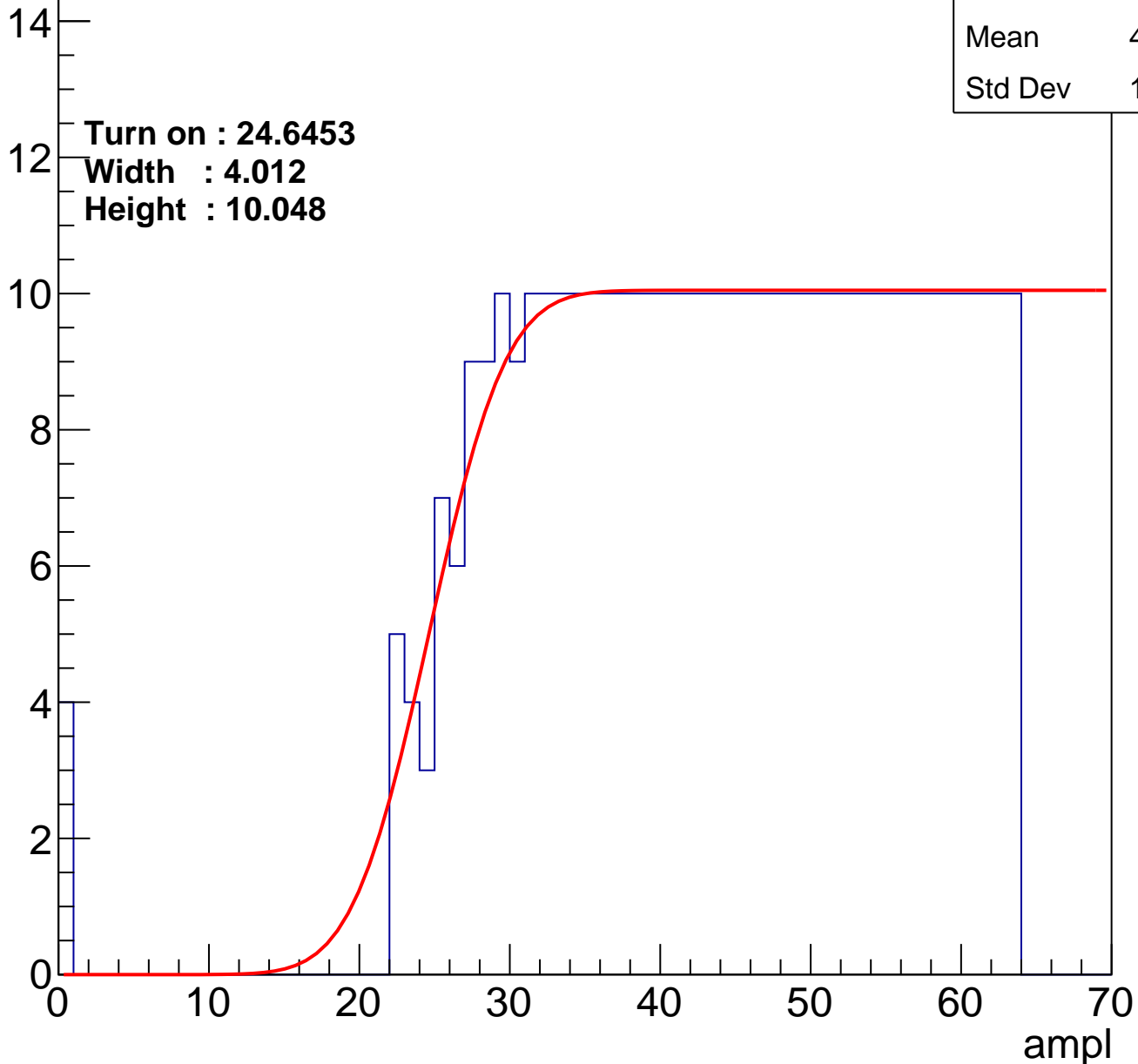
Entries	396
Mean	43.36
Std Dev	12.23

Turn on : 24.6453

Width : 4.012

Height : 10.048

Entry



B1L002S, U4-ch62

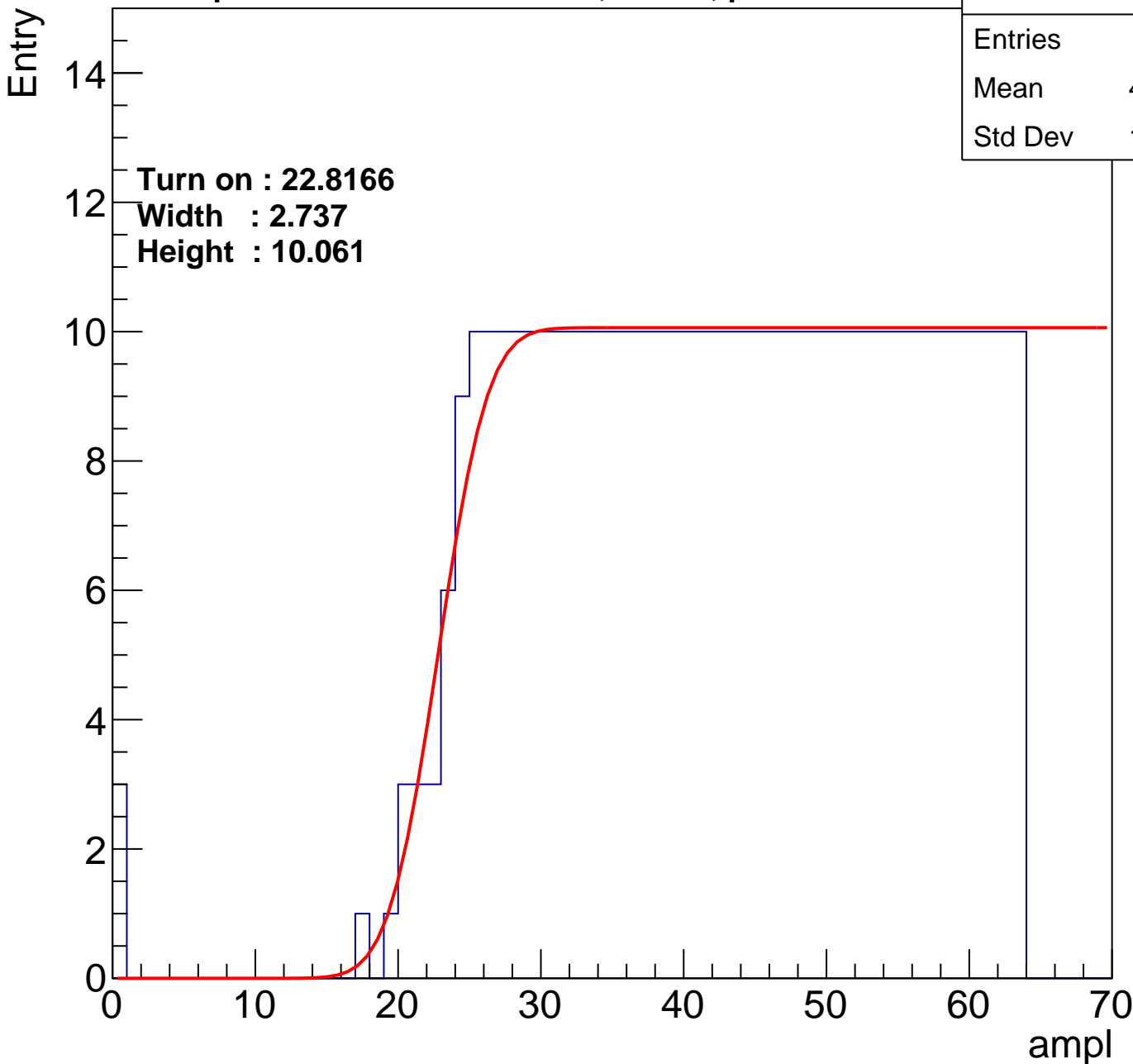
calib_packv5_042523_0143.root, FC#10, port B3

Entries	419
Mean	42.34
Std Dev	12.59

Turn on : 22.8166

Width : 2.737

Height : 10.061



B1L002S, U4-ch63

calib_packv5_042523_0143.root, FC#10, port B3

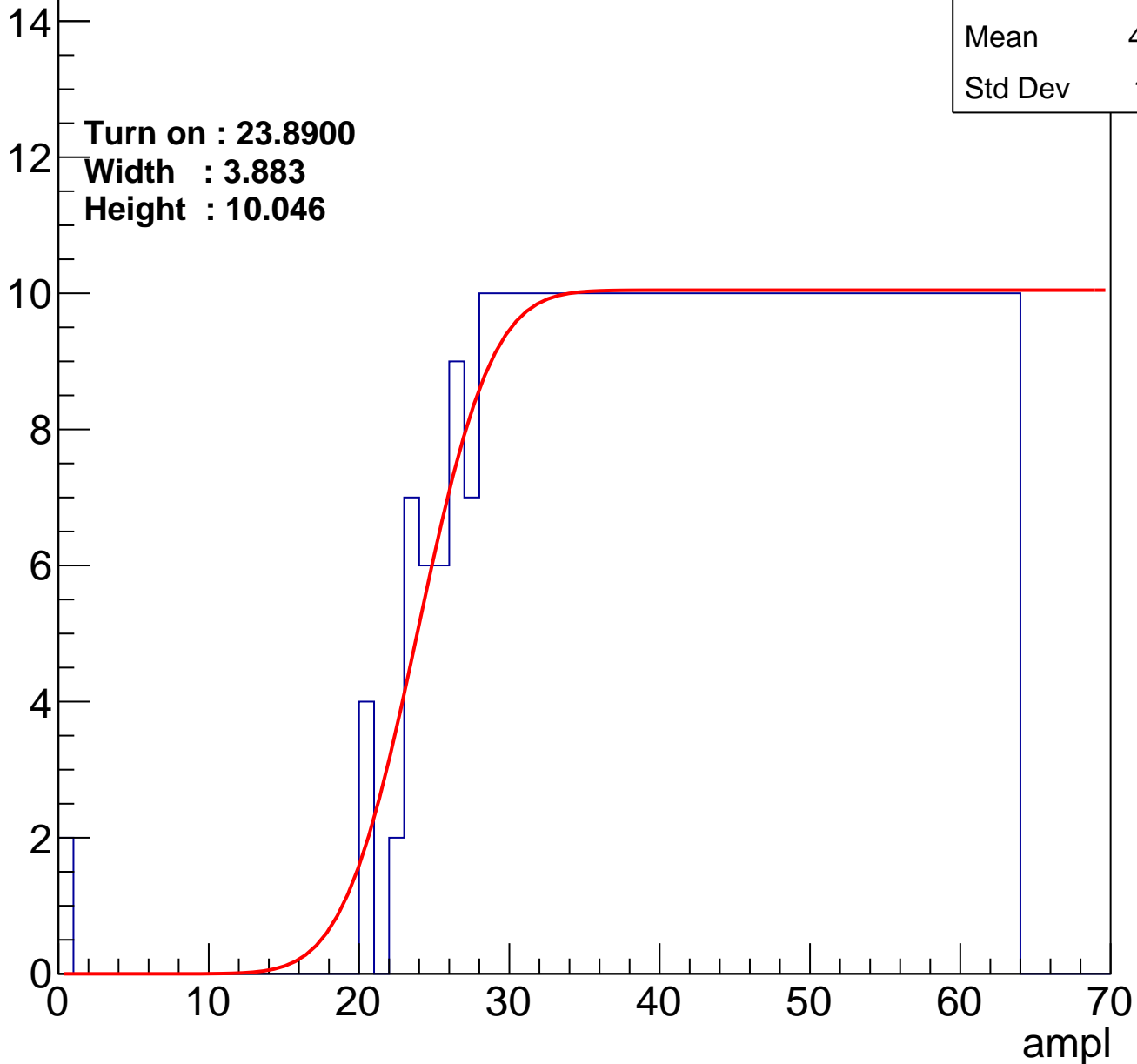
Entries	403
Mean	43.13
Std Dev	12.11

Turn on : 23.8900

Width : 3.883

Height : 10.046

Entry



B1L002S, U4-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	362
Mean	45.16
Std Dev	11.04

Turn on : 28.2745

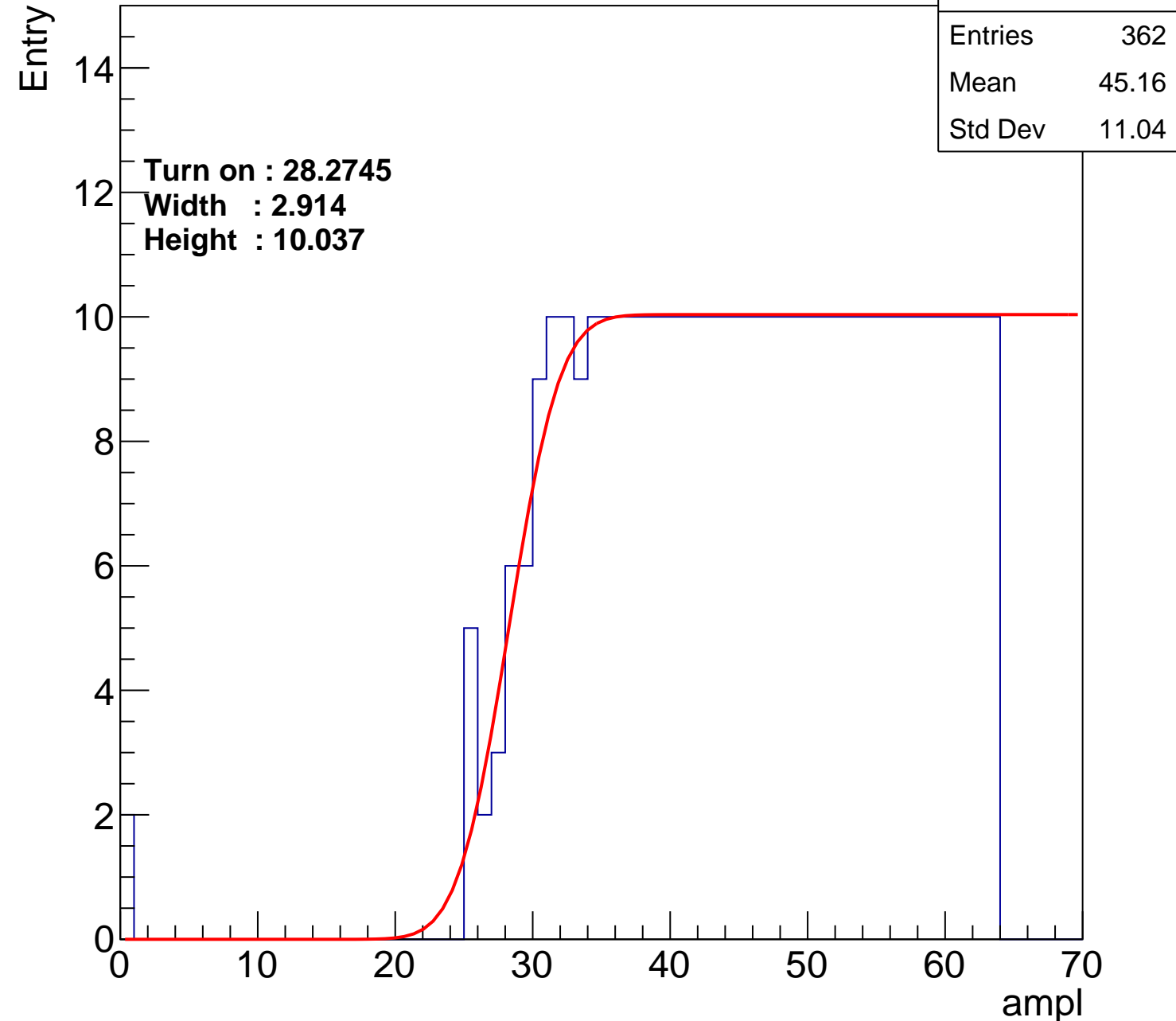
Width : 2.914

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.4577

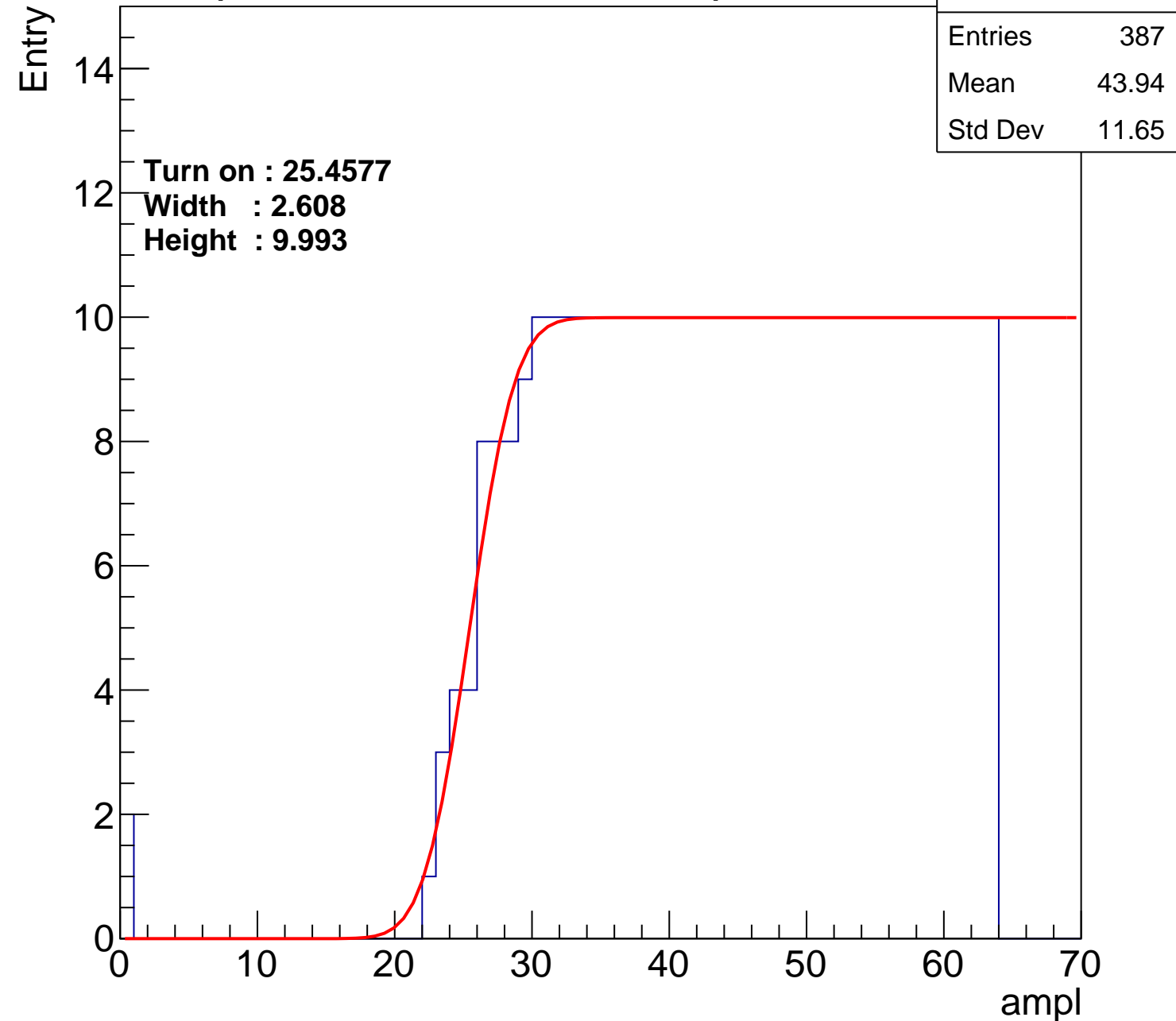
Width : 2.608

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.02
Std Dev	11.51

Turn on : 25.7080

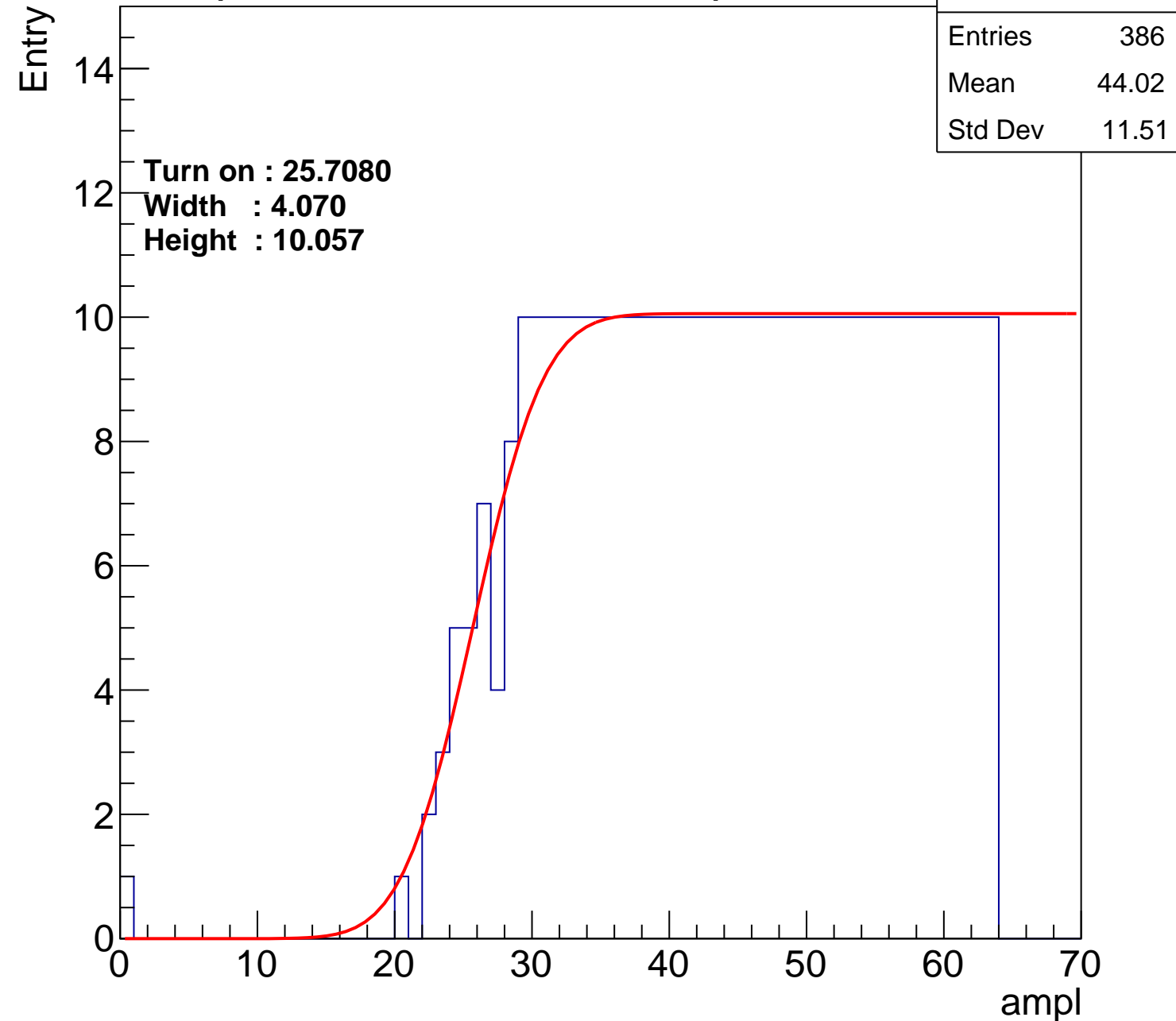
Width : 4.070

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.79
Std Dev	11.19

Turn on : 27.1415

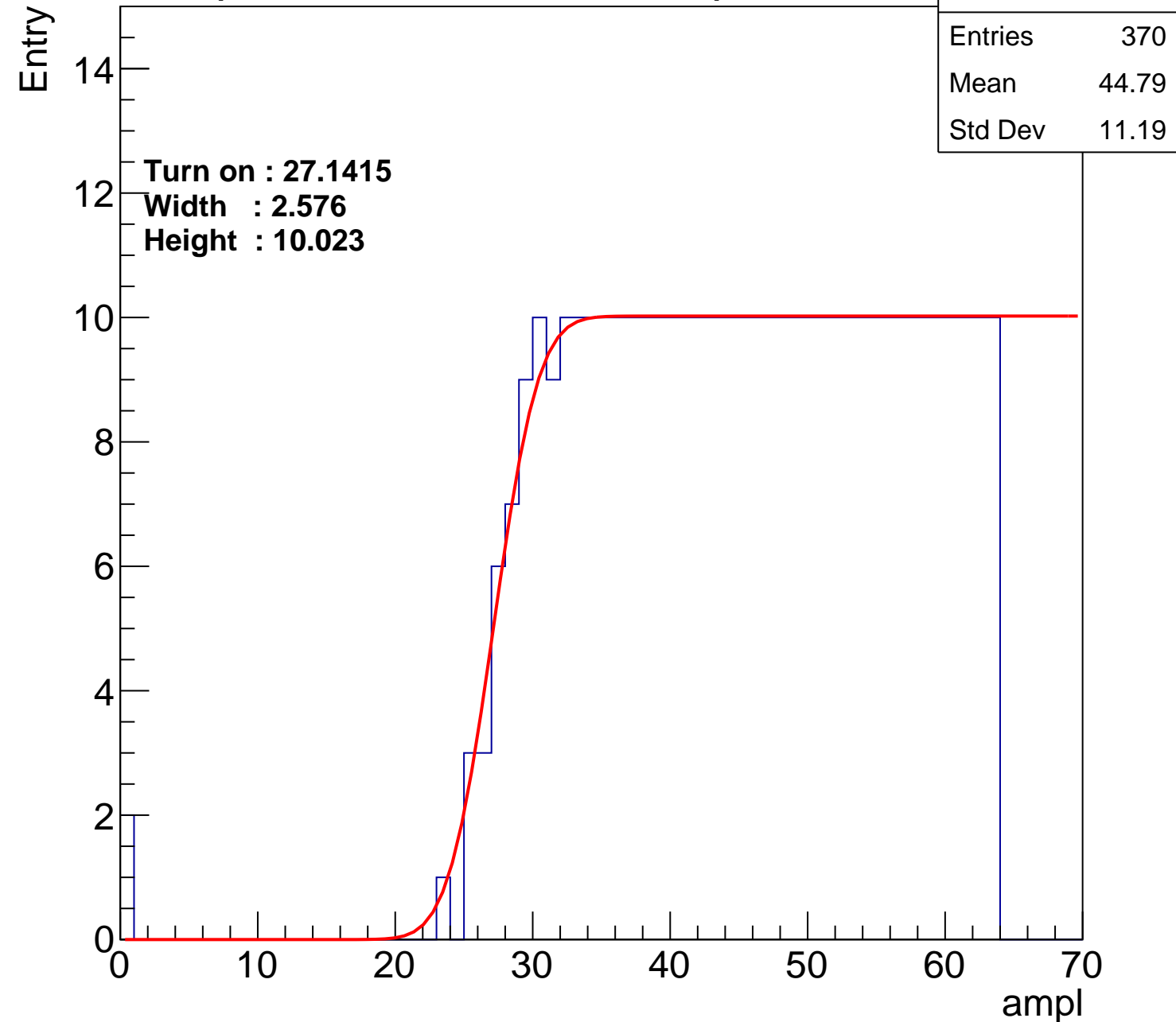
Width : 2.576

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch68

calib_packv5_042523_0143.root, FC#10, port B3

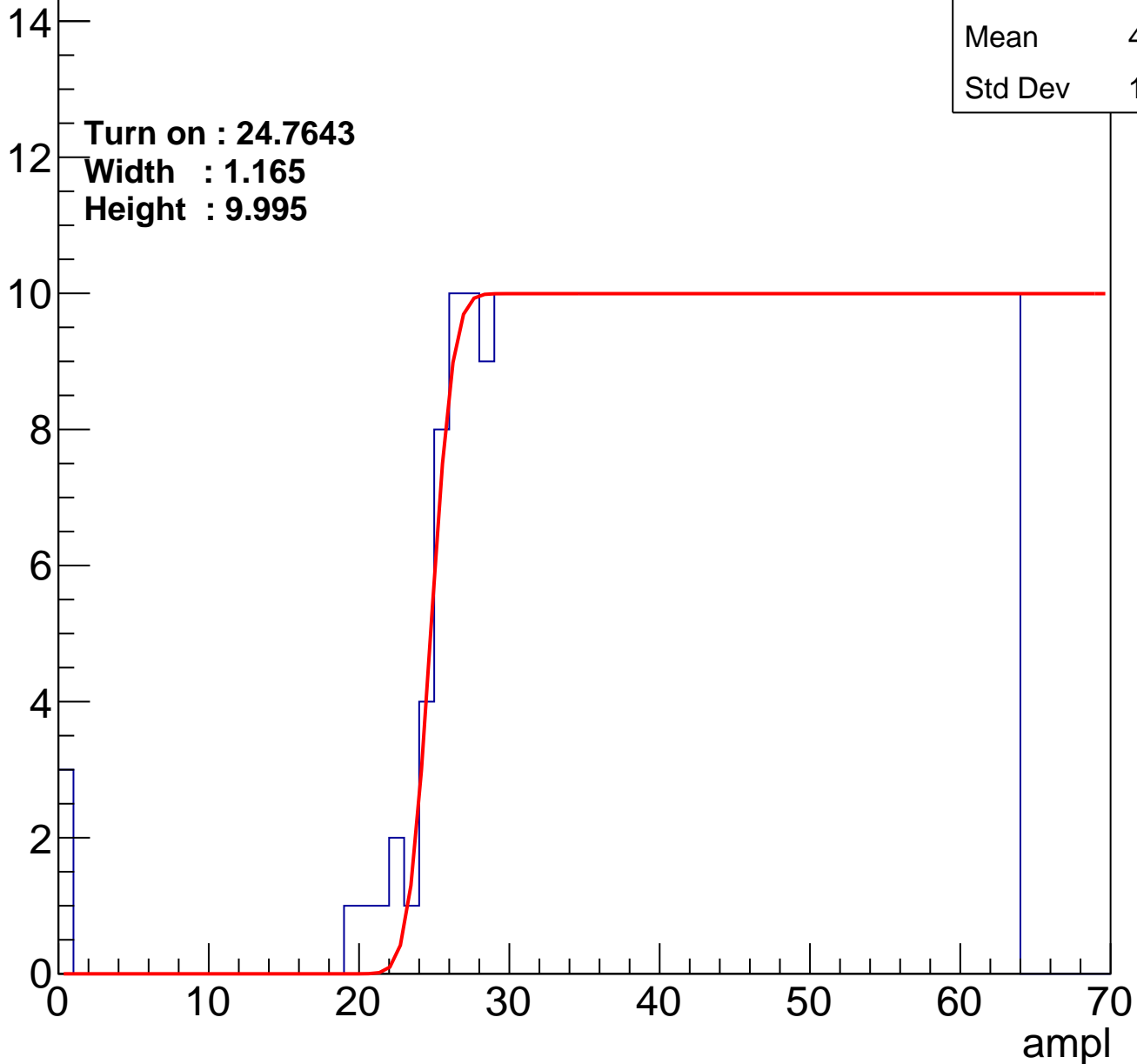
Entries	400
Mean	43.26
Std Dev	12.12

Turn on : 24.7643

Width : 1.165

Height : 9.995

Entry



B1L002S, U4-ch69

calib_packv5_042523_0143.root, FC#10, port B3

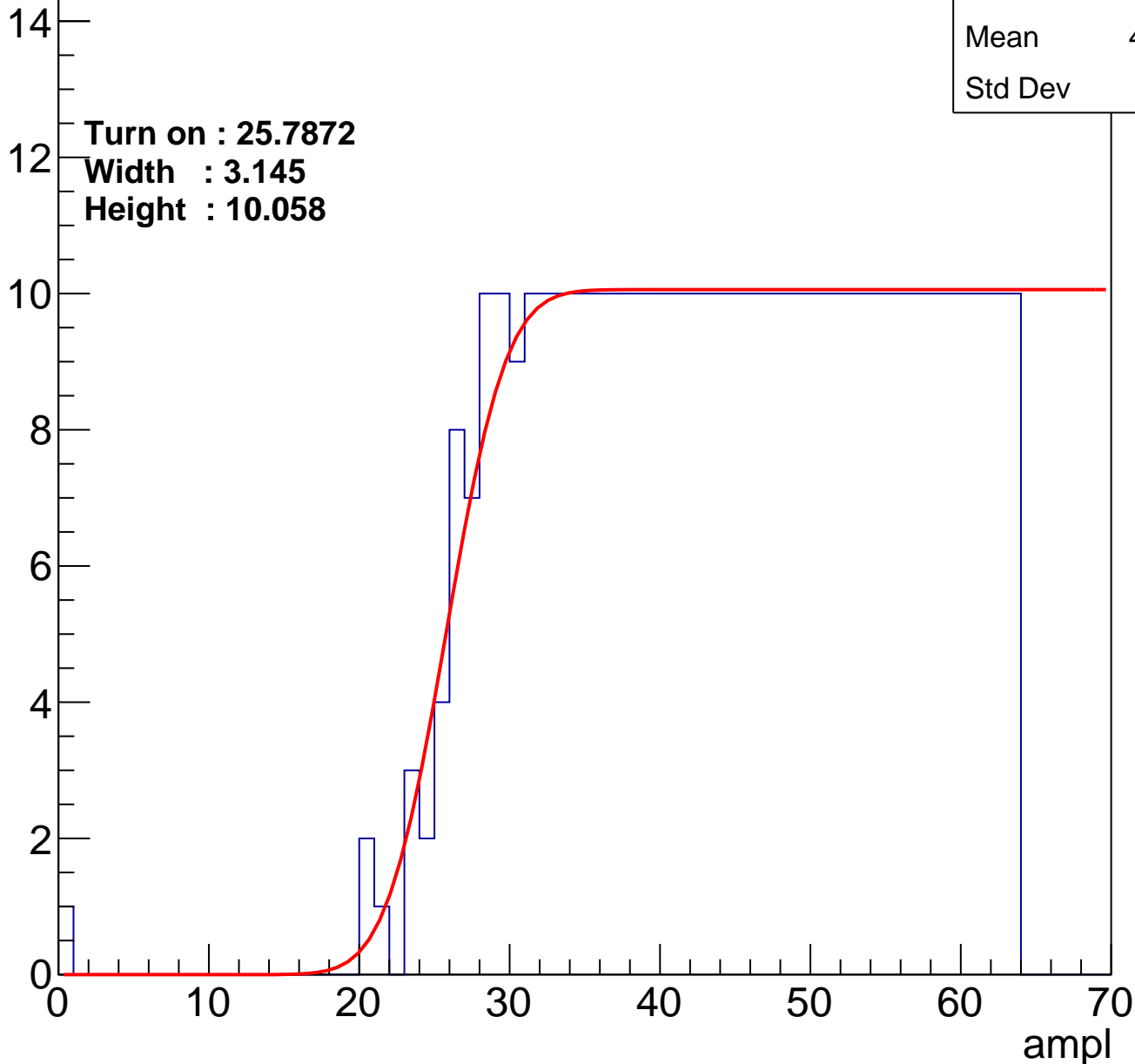
Entry

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.7872

Width : 3.145

Height : 10.058



B1L002S, U4-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 26.0414

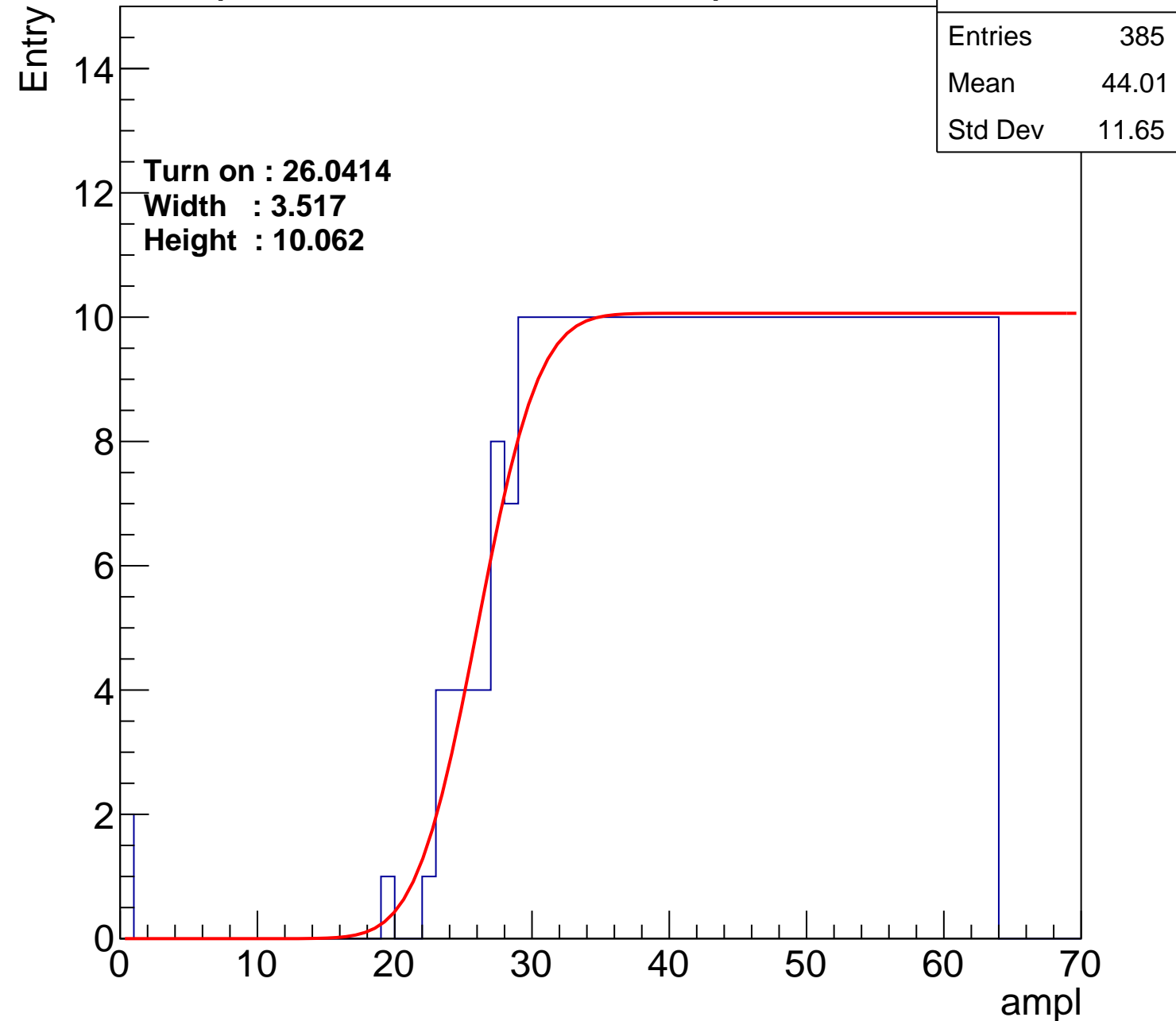
Width : 3.517

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch71

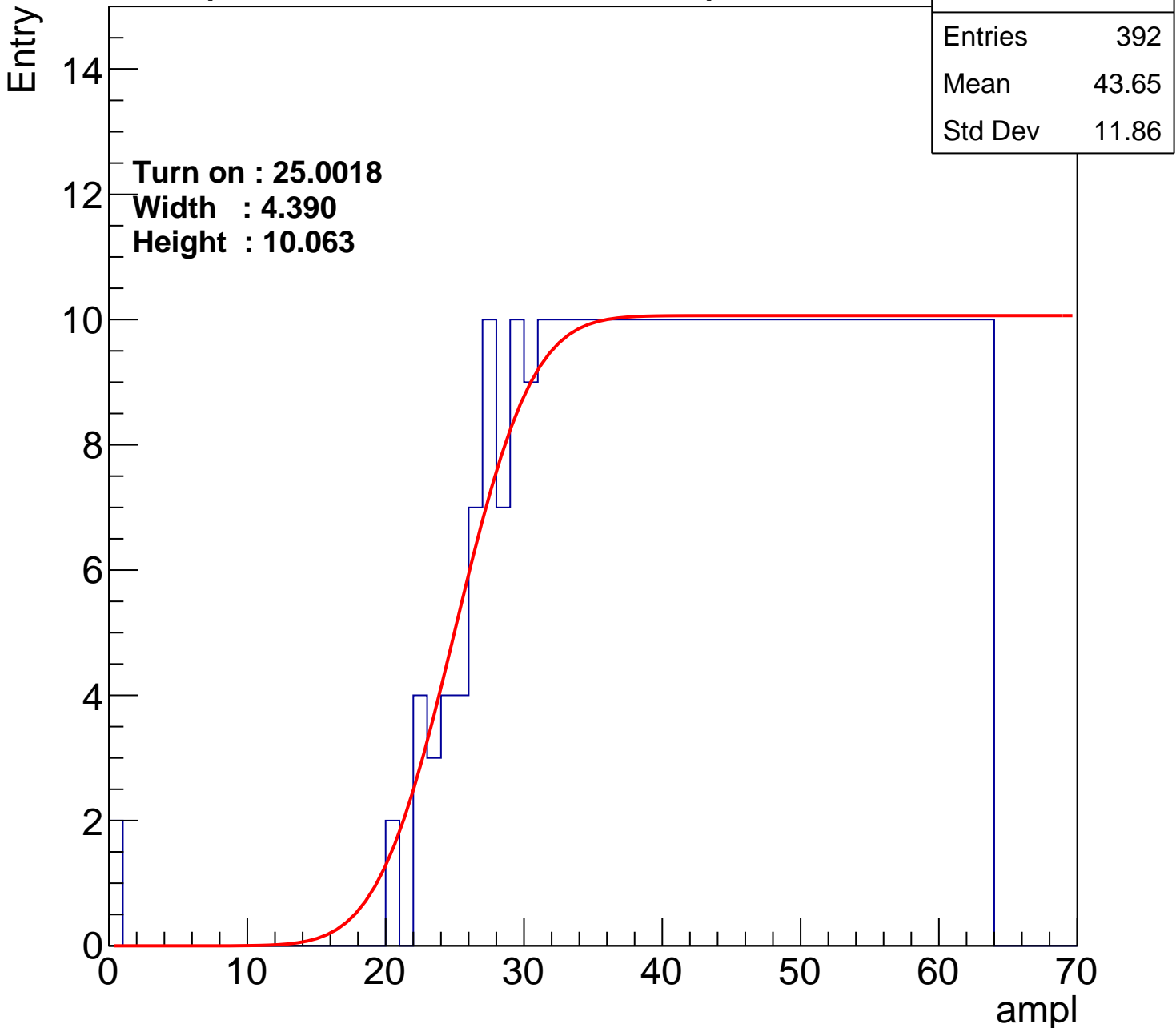
calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.0018

Width : 4.390

Height : 10.063



B1L002S, U4-ch72

calib_packv5_042523_0143.root, FC#10, port B3

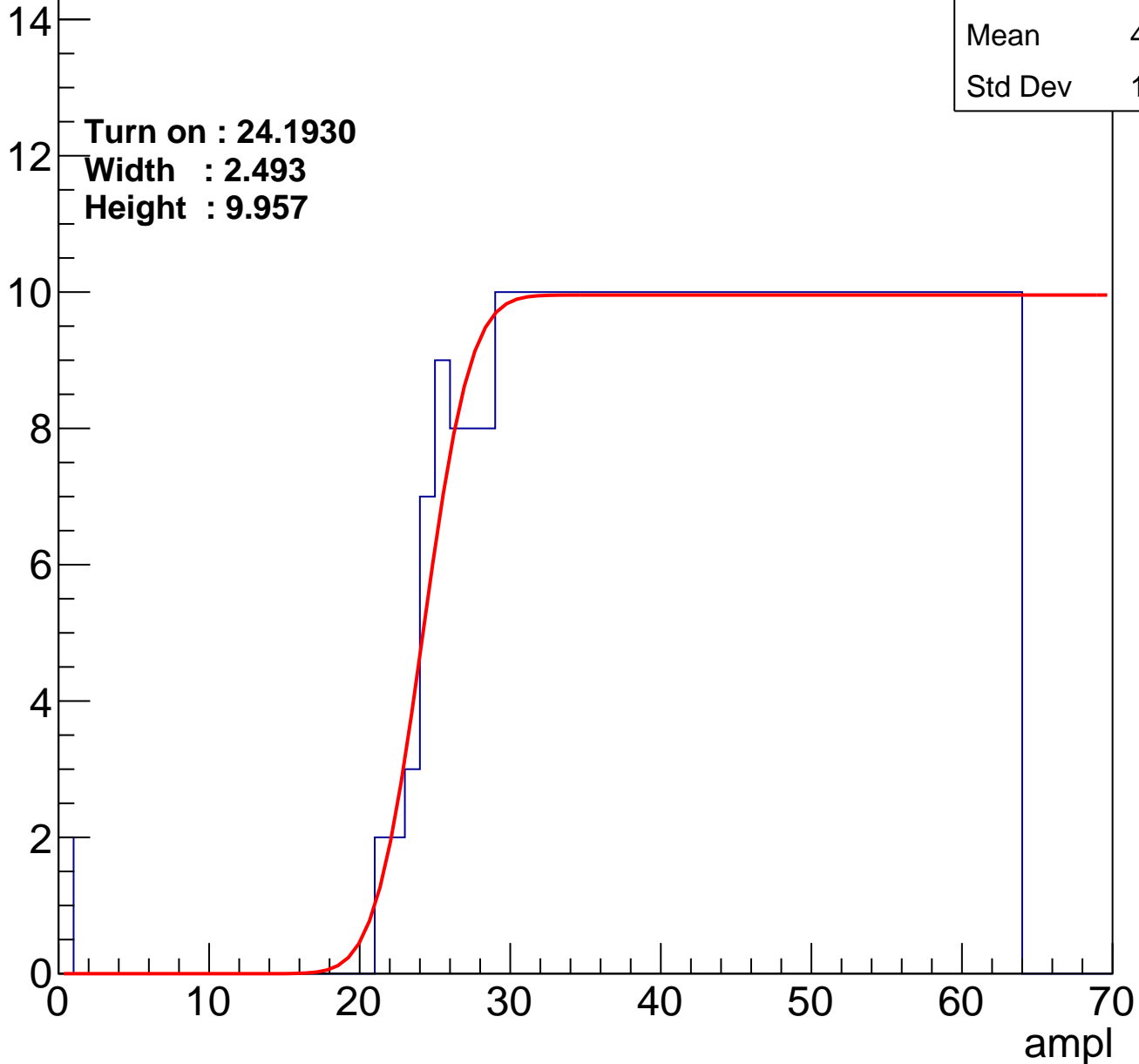
Entries	399
Mean	43.35
Std Dev	11.97

Turn on : 24.1930

Width : 2.493

Height : 9.957

Entry



B1L002S, U4-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.33
Std Dev	12.23

Turn on : 25.0261

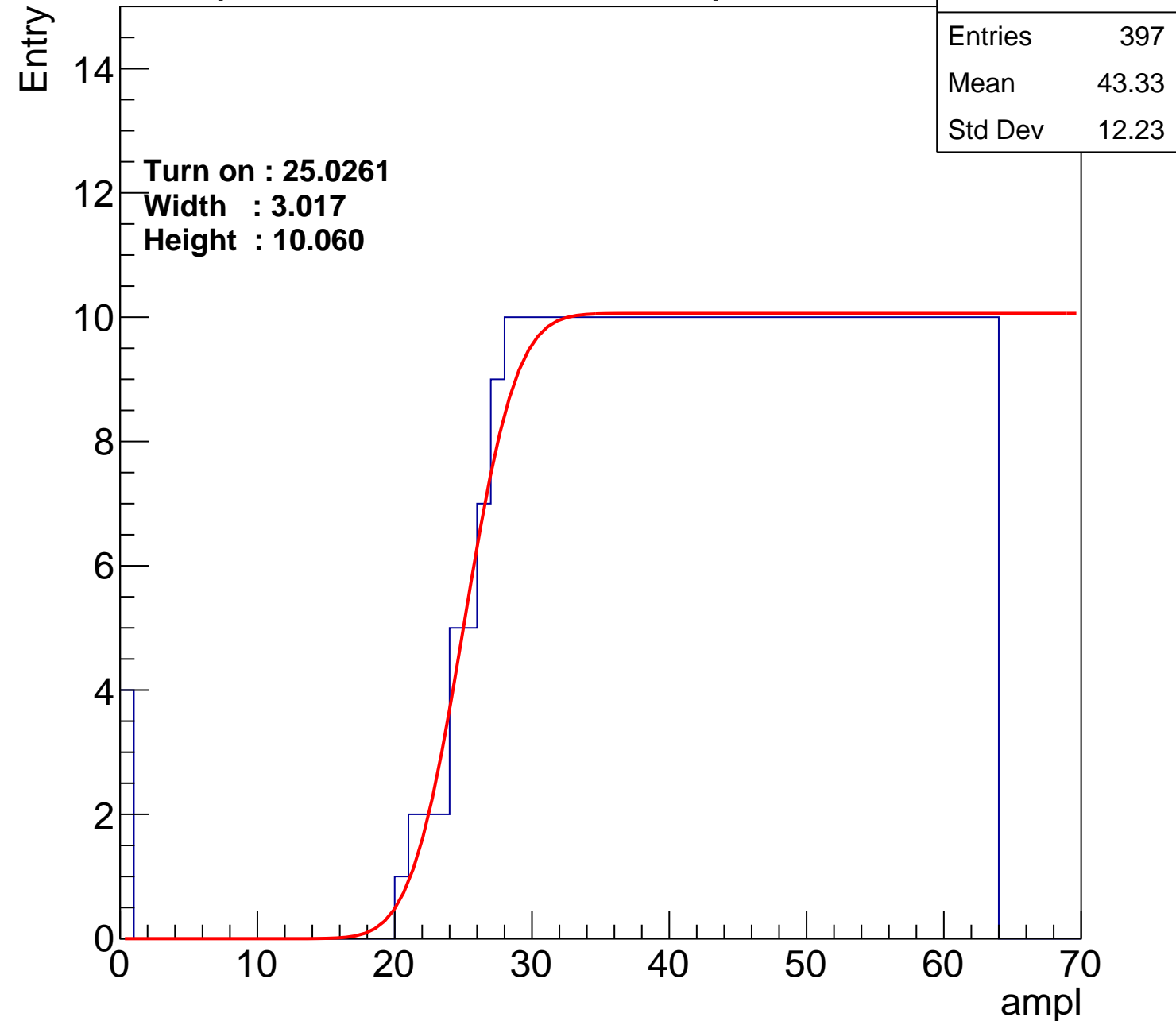
Width : 3.017

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.7937

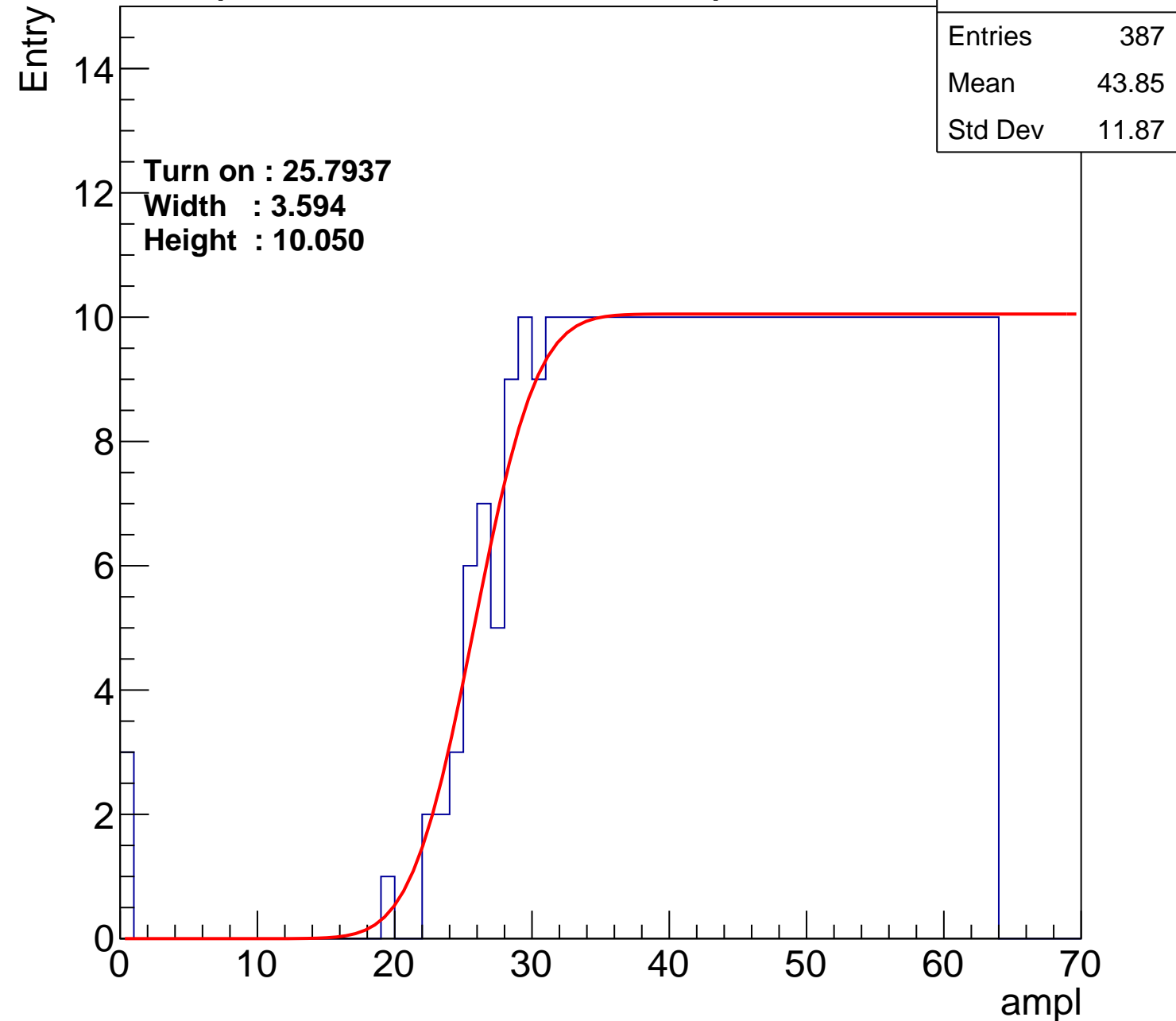
Width : 3.594

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.86
Std Dev	11.29

Turn on : 27.5773

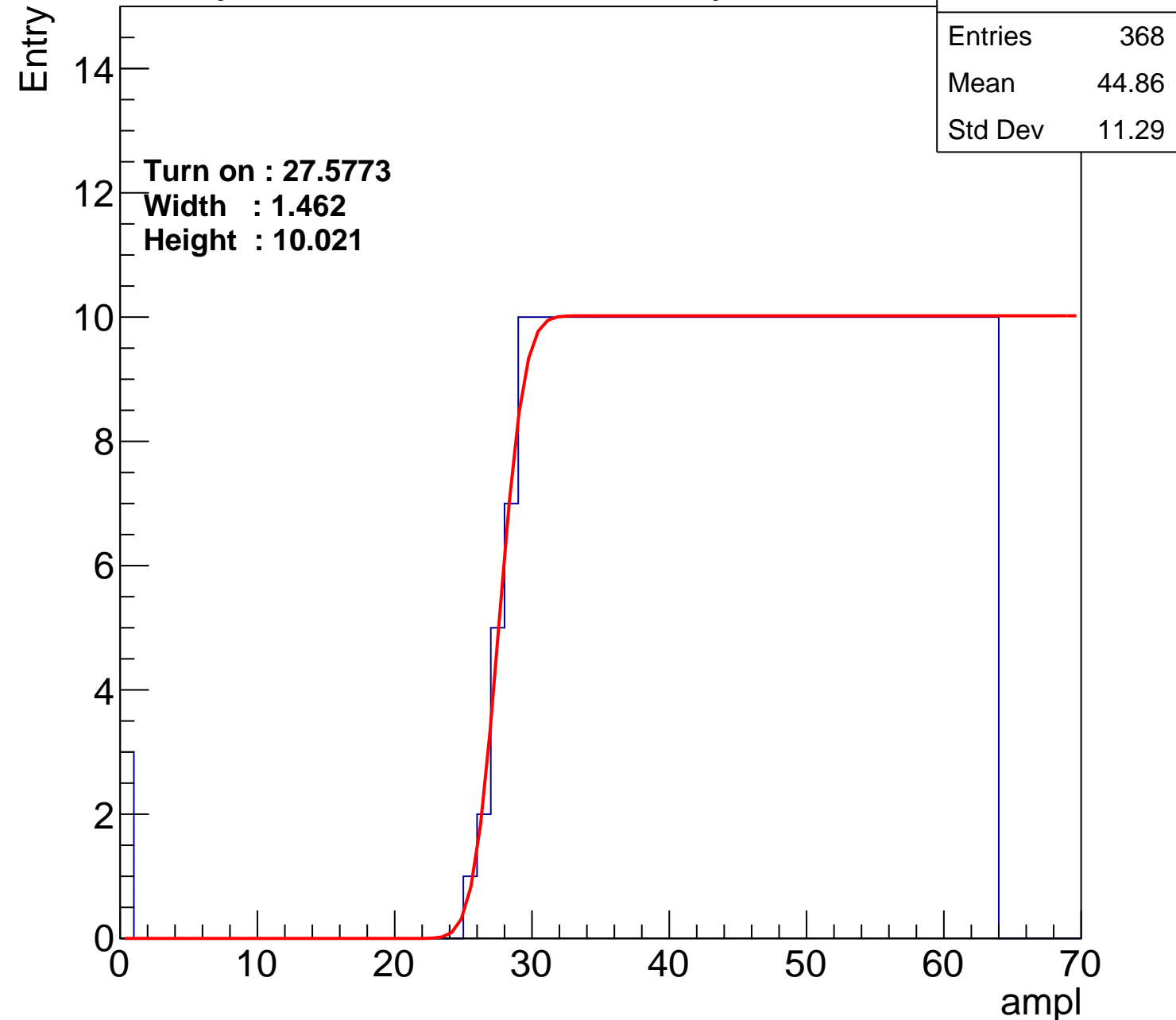
Width : 1.462

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.59

Turn on : 25.6784

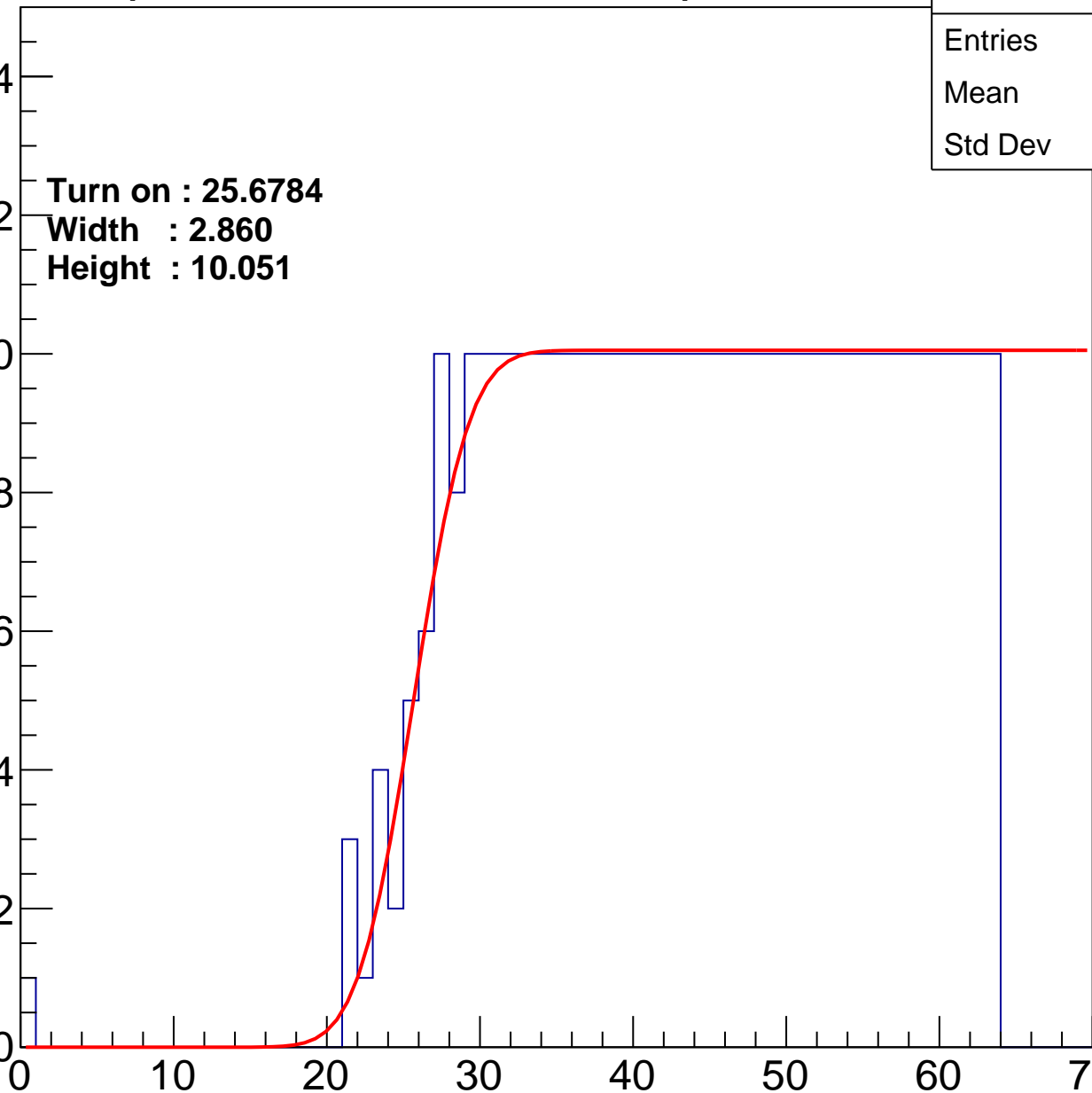
Width : 2.860

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.29
Std Dev	11.34

Turn on : 25.9896

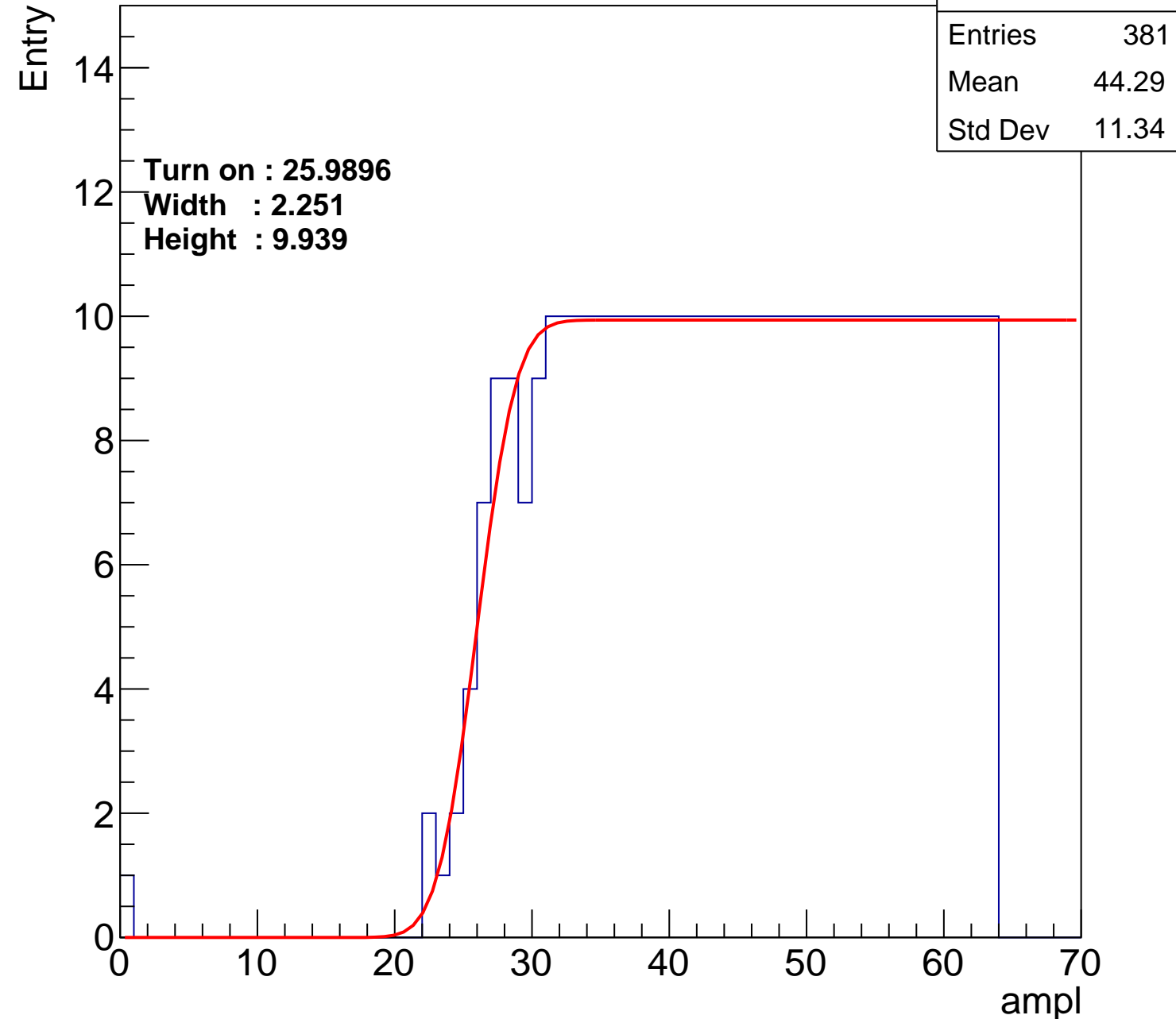
Width : 2.251

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.02
Std Dev	12.51

Turn on : 24.6710

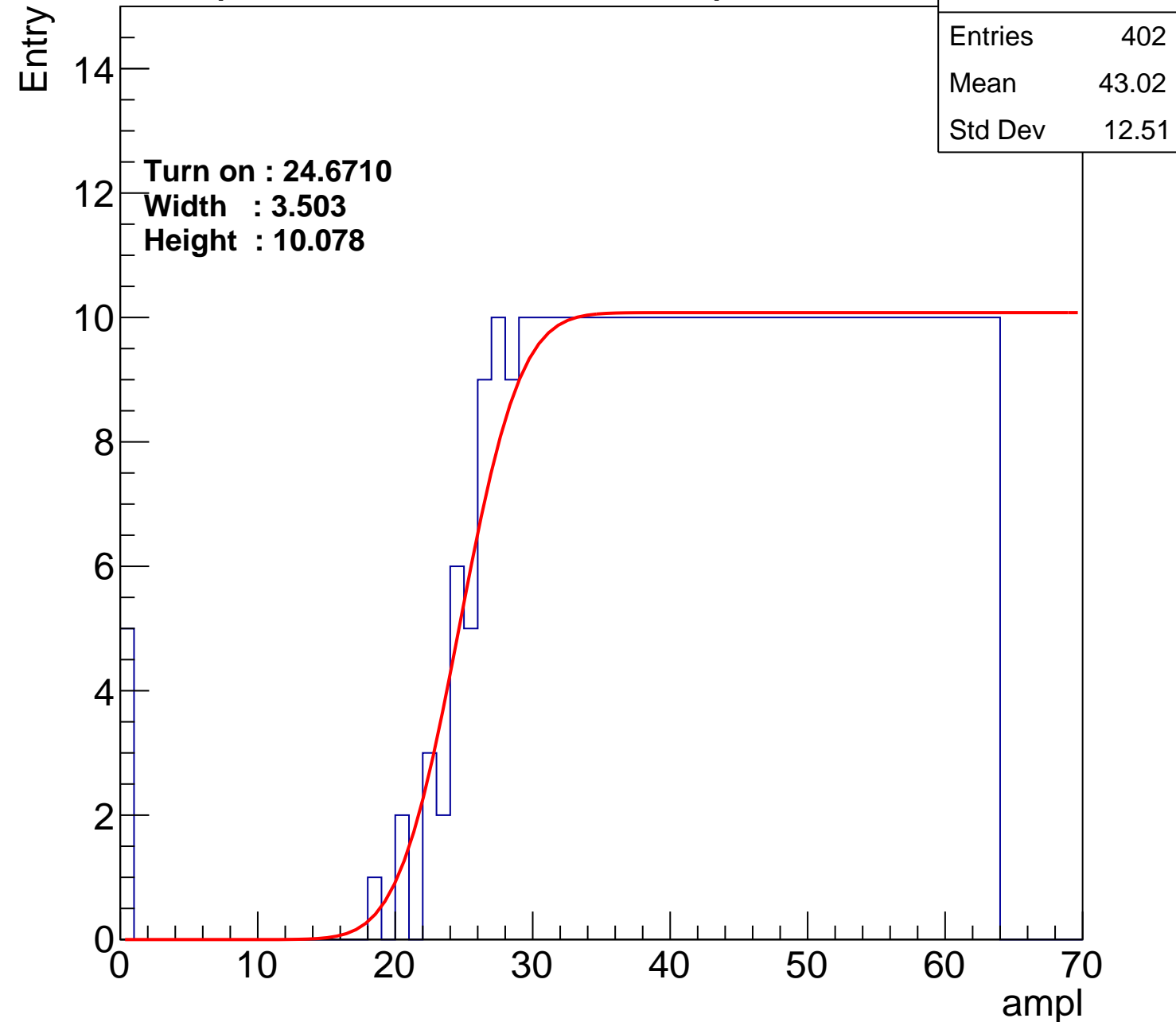
Width : 3.503

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.77

Turn on : 25.4557

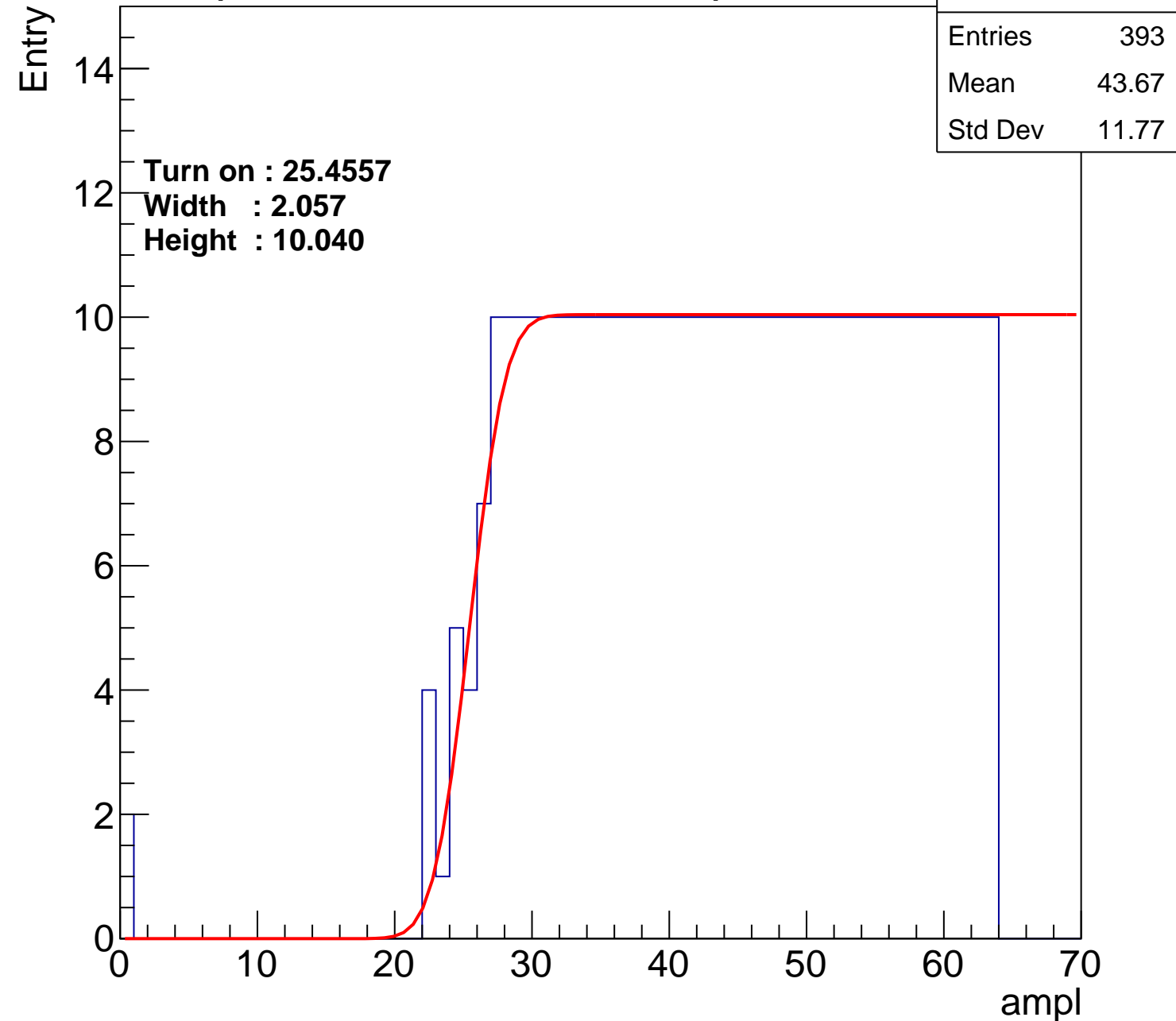
Width : 2.057

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.43
Std Dev	12.02

Turn on : 24.2360

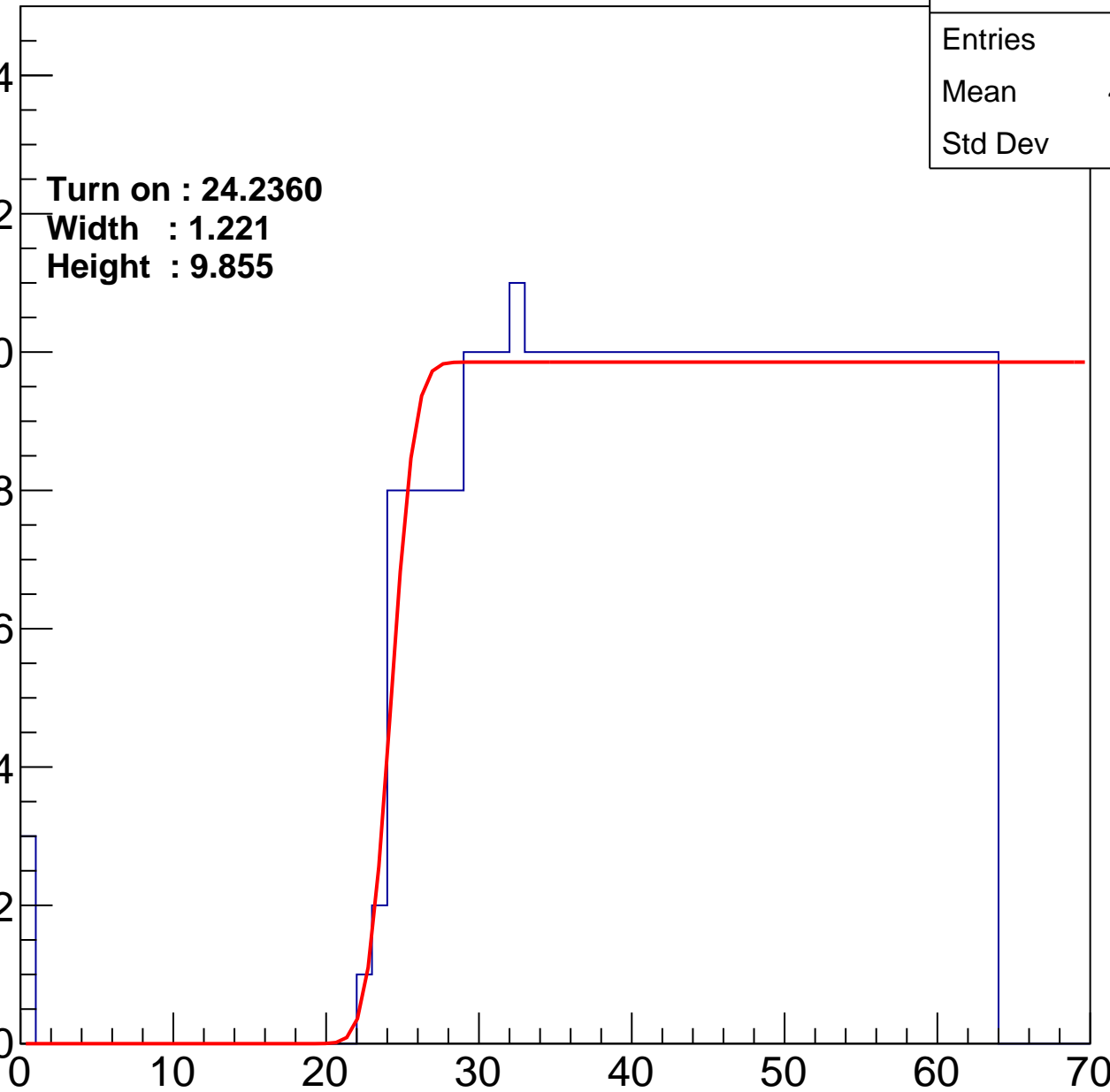
Width : 1.221

Height : 9.855

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.7833

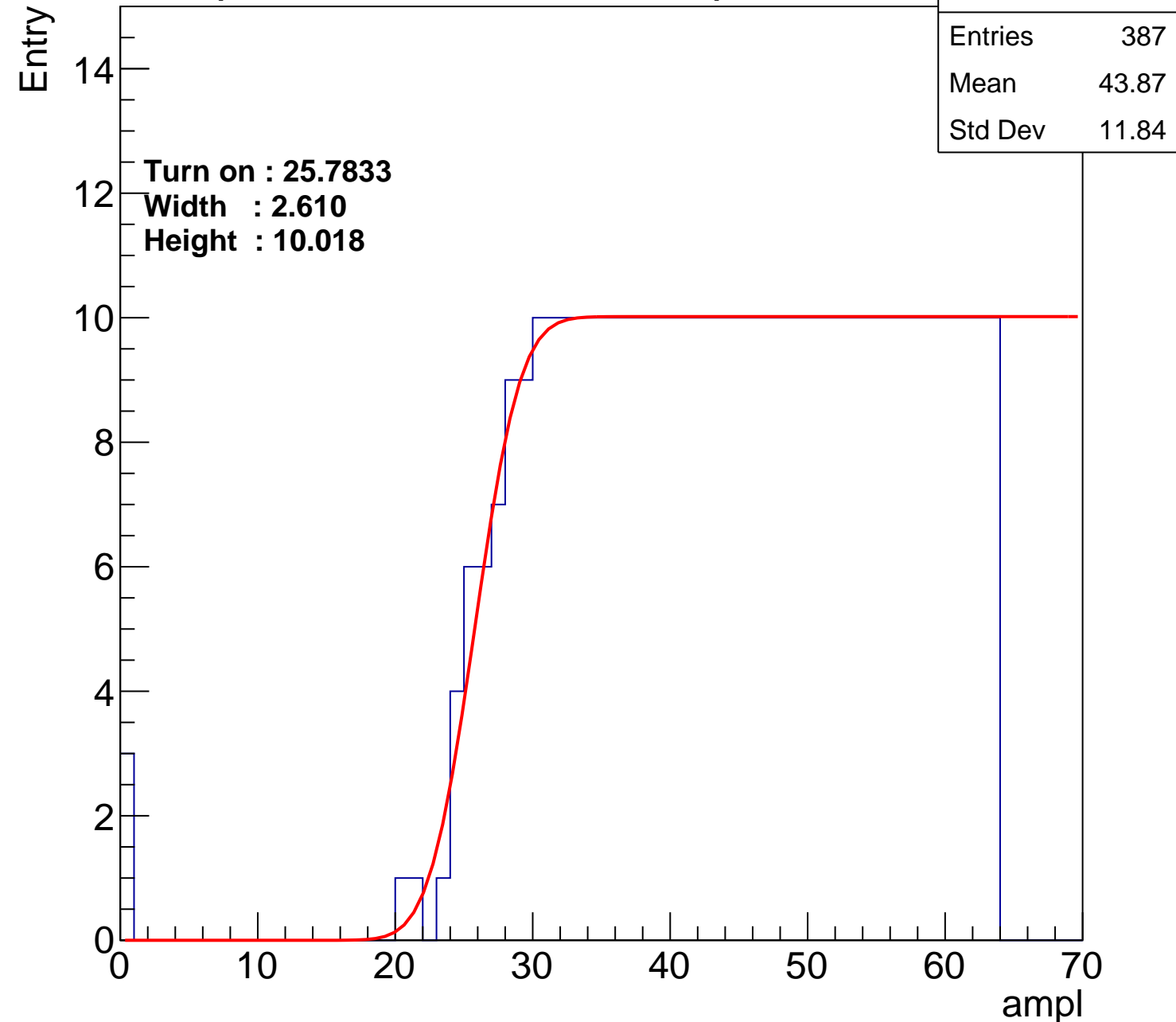
Width : 2.610

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.49
Std Dev	11.37

Turn on : 26.9777

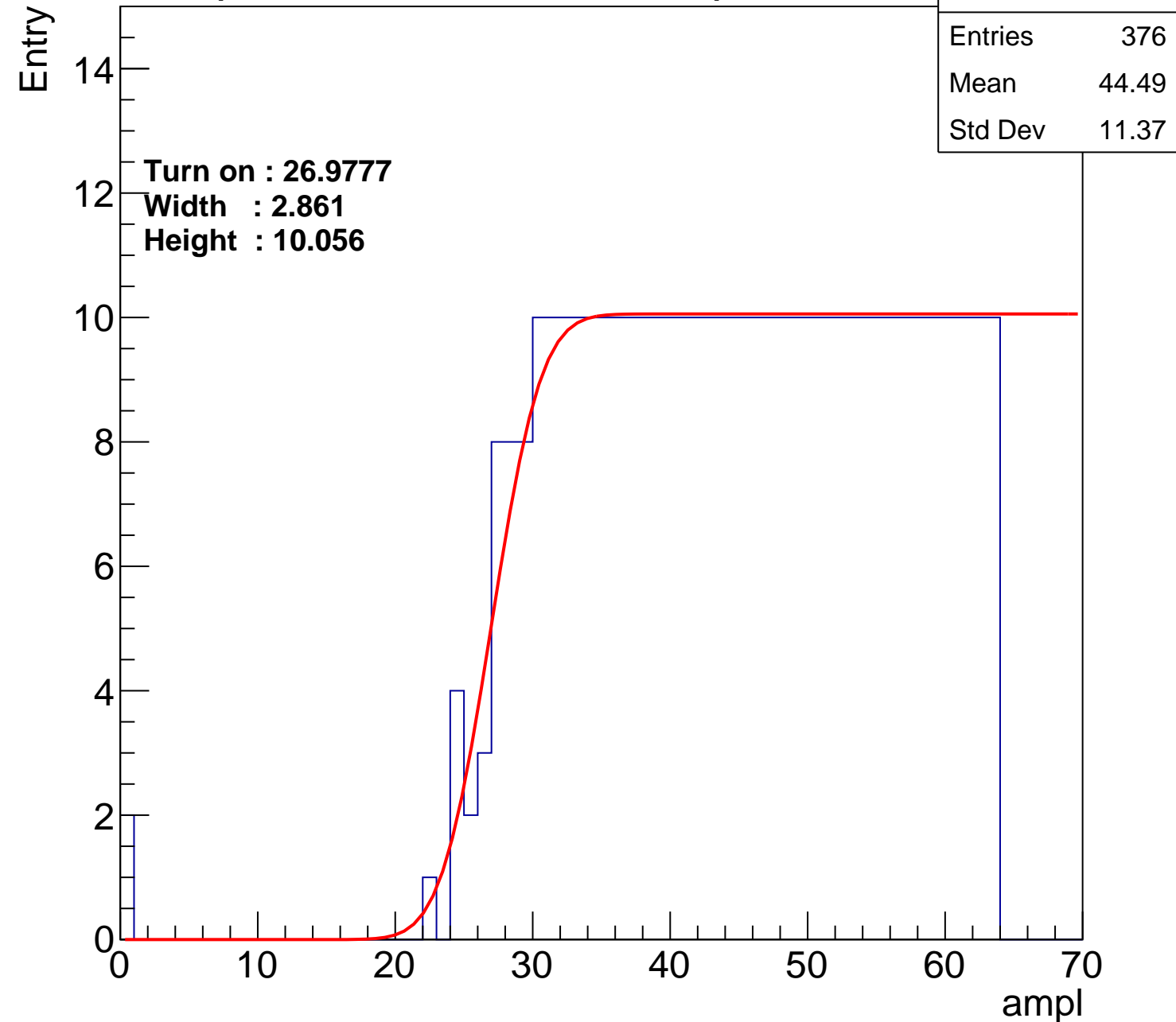
Width : 2.861

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.02
Std Dev	11.65

Turn on : 25.8836

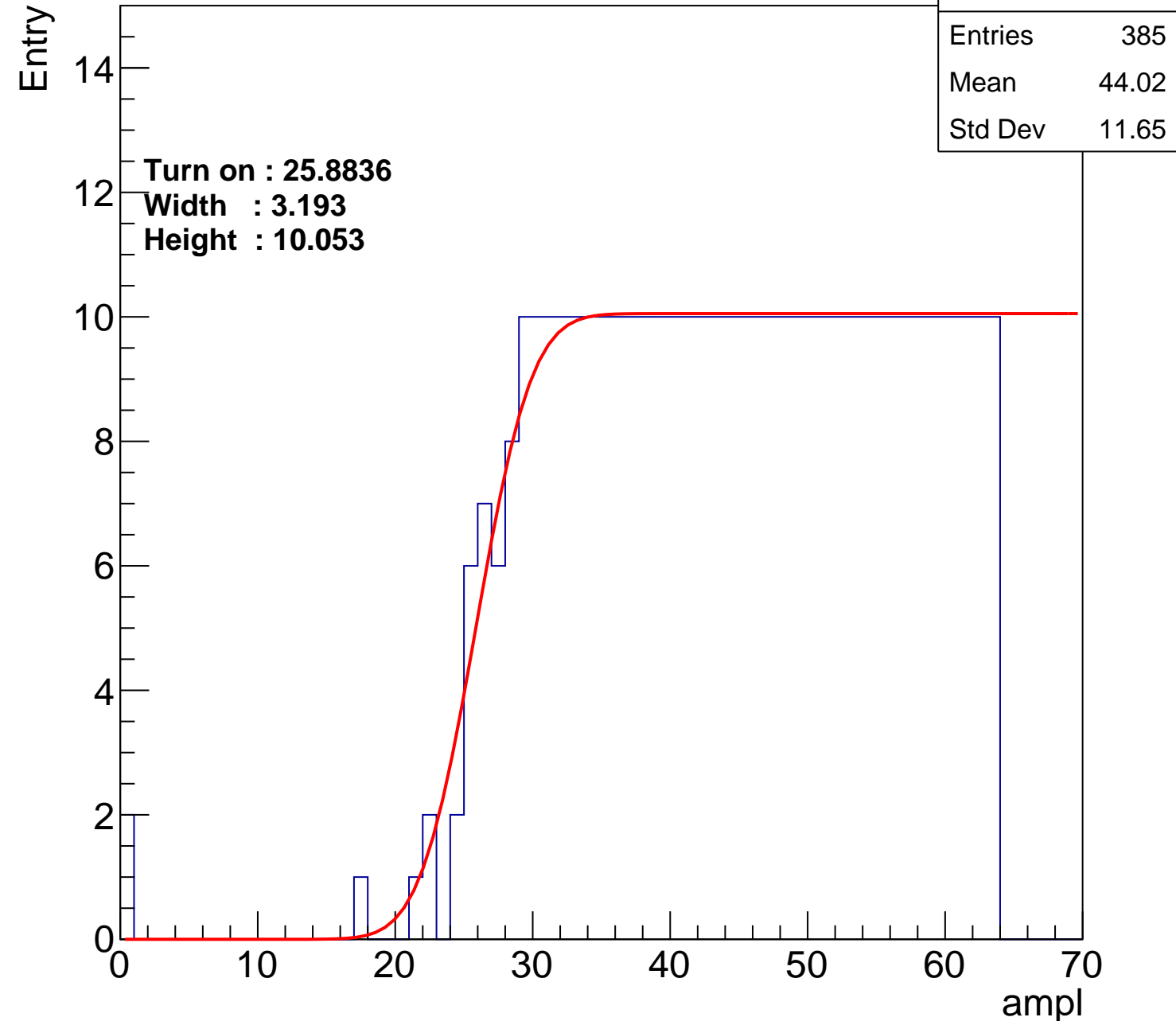
Width : 3.193

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch84

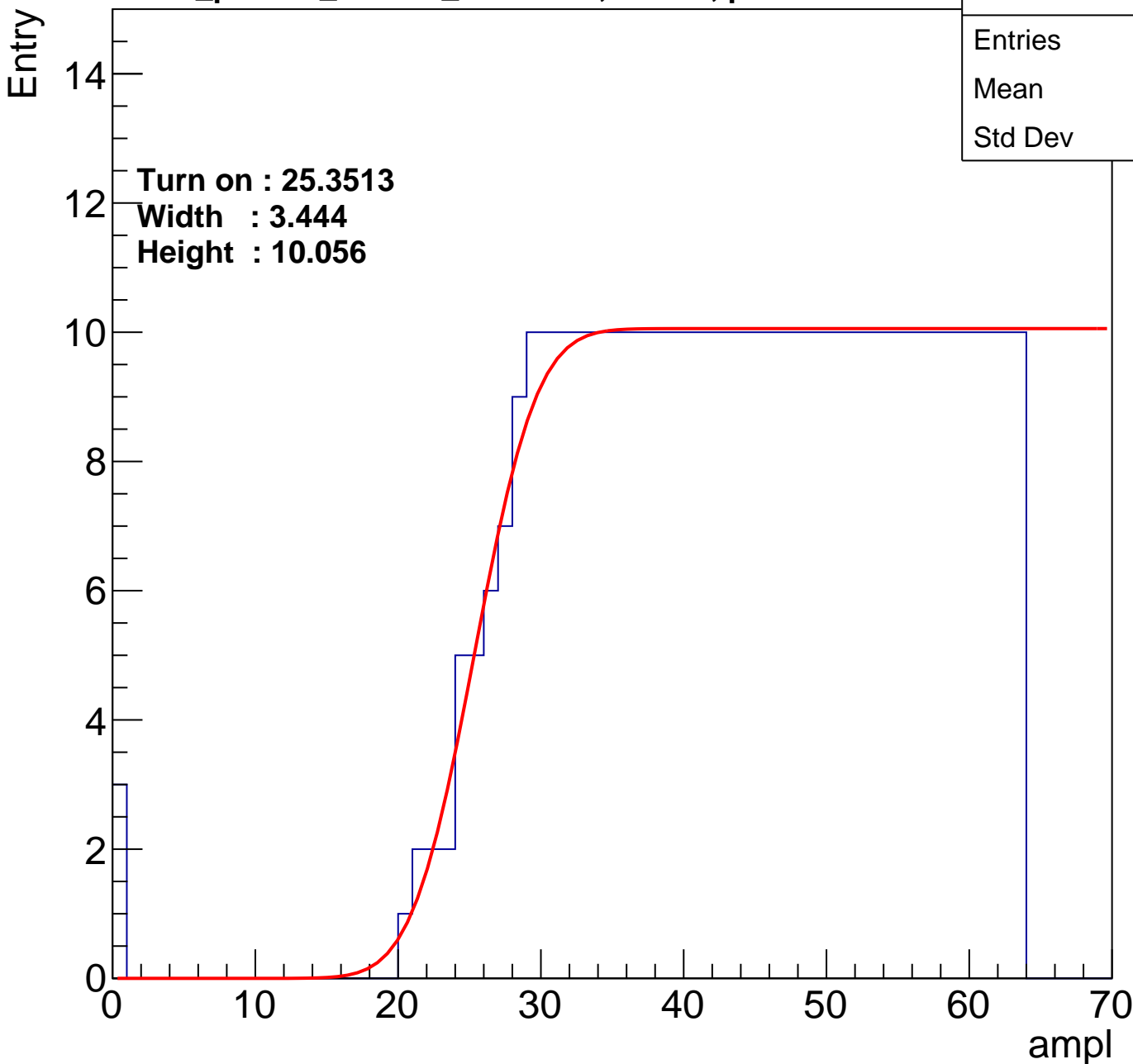
calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.61
Std Dev	12

Turn on : 25.3513

Width : 3.444

Height : 10.056



B1L002S, U4-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.6
Std Dev	12

Turn on : 25.0879

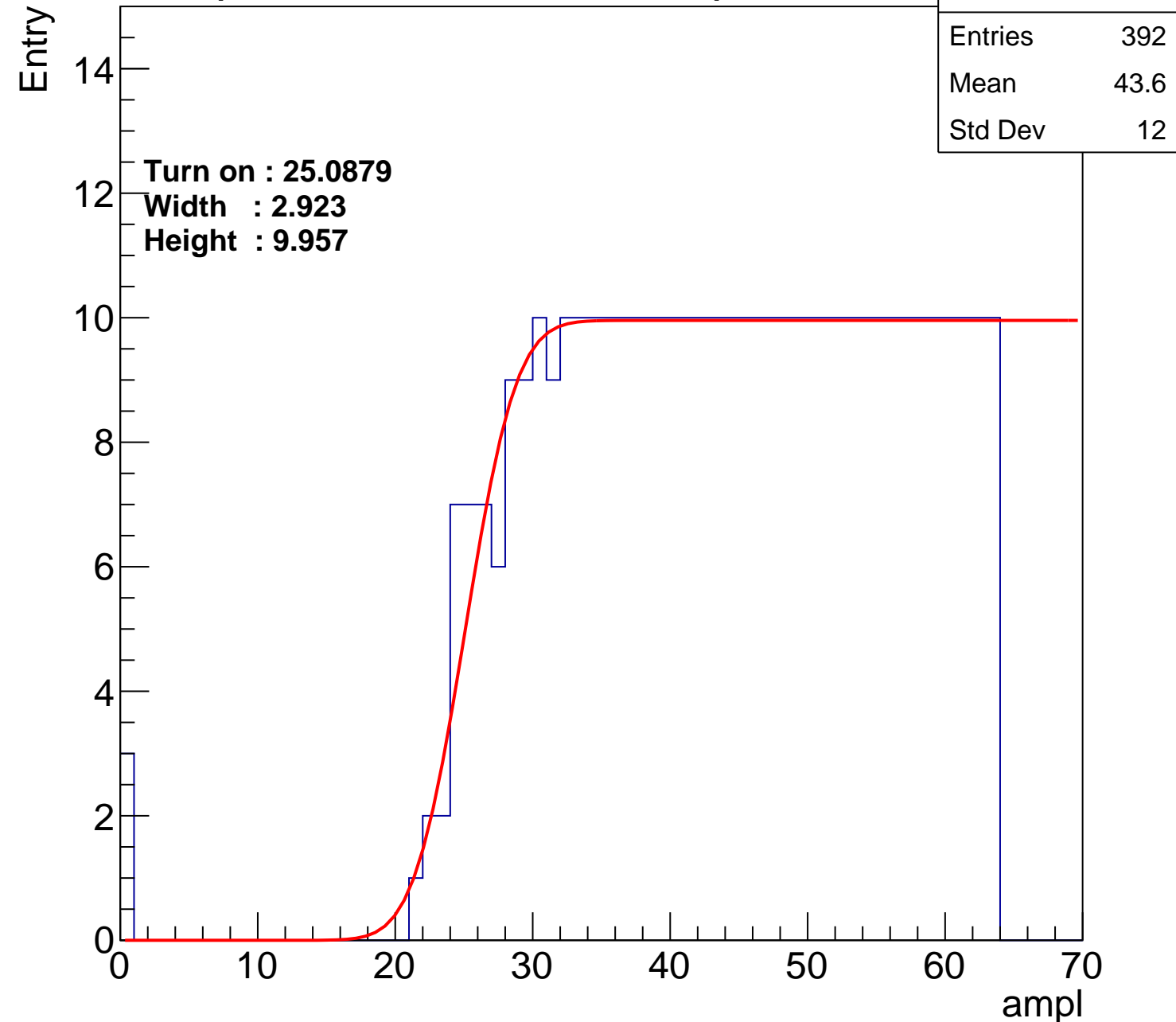
Width : 2.923

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch86

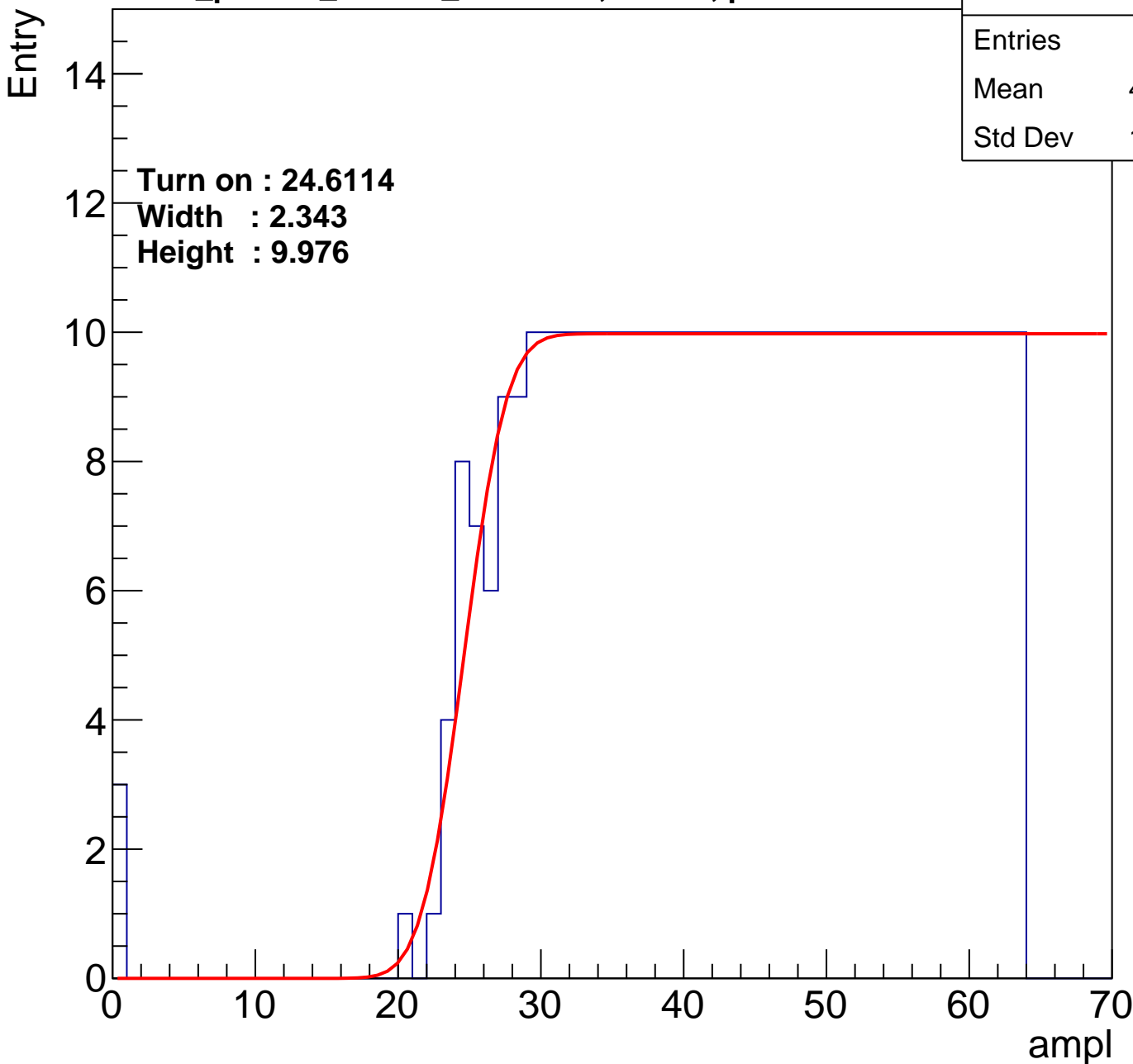
calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.35
Std Dev	12.09

Turn on : 24.6114

Width : 2.343

Height : 9.976



B1L002S, U4-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.24
Std Dev	12.14

Turn on : 24.2270

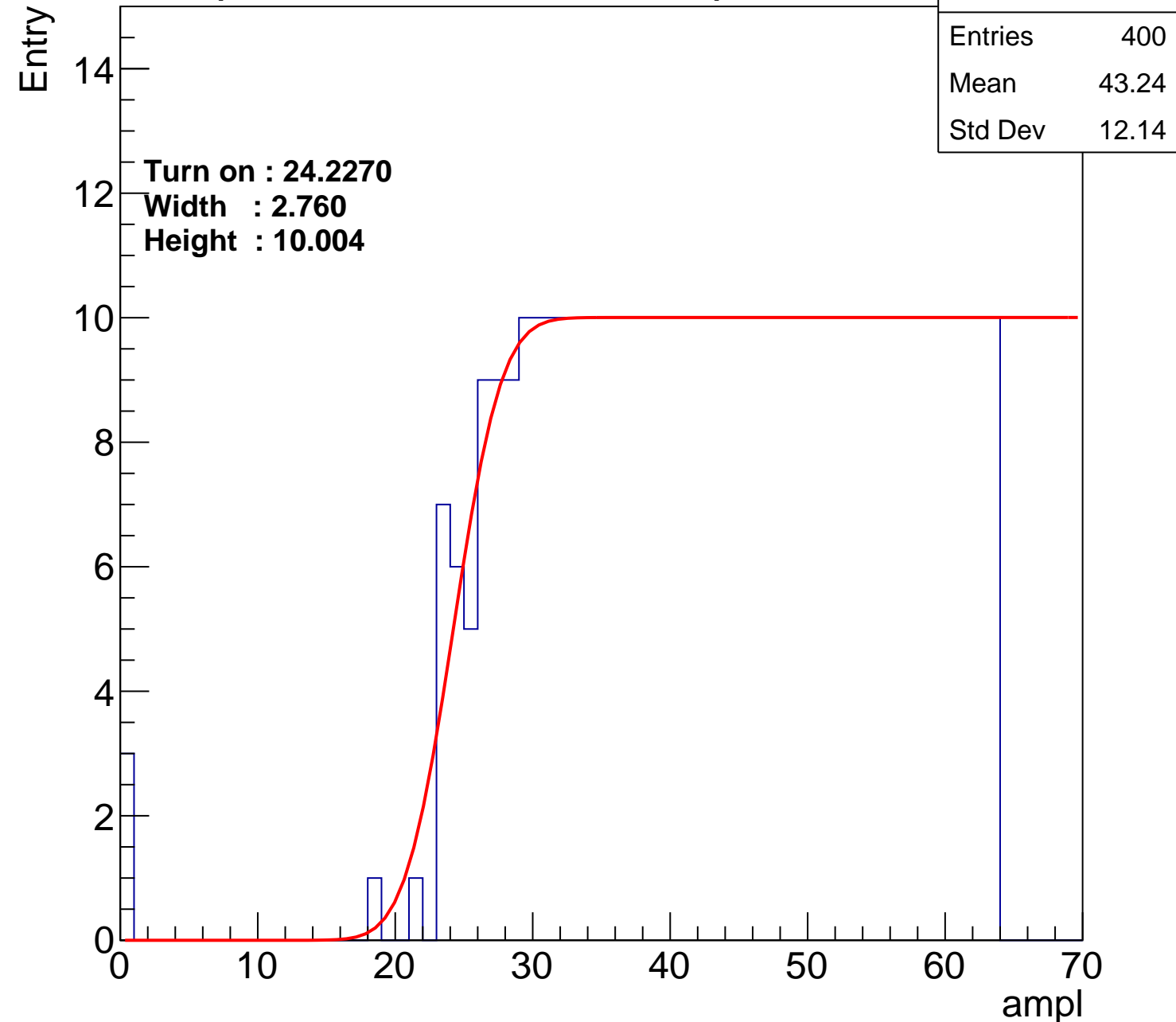
Width : 2.760

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch88

calib_packv5_042523_0143.root, FC#10, port B3

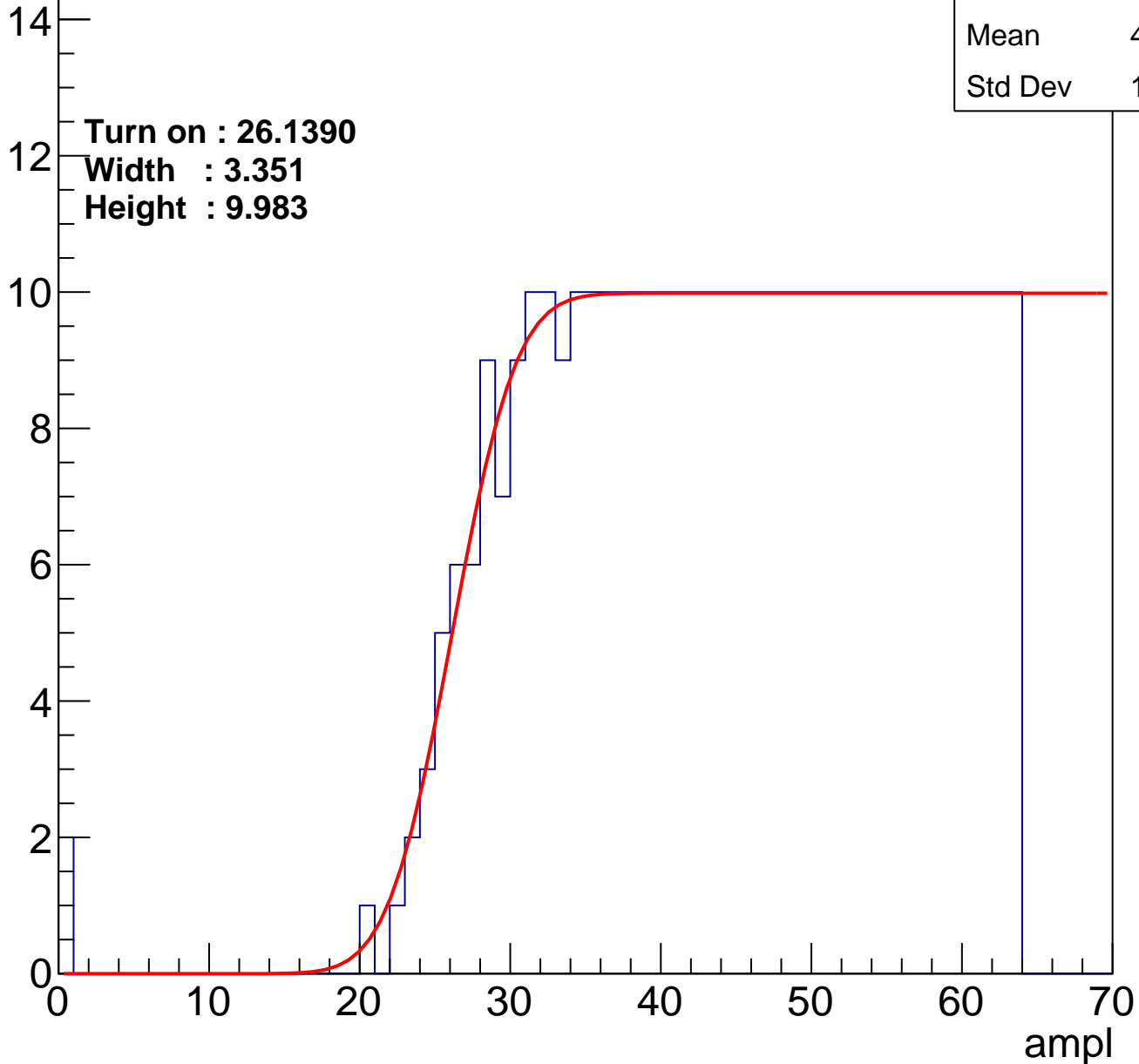
Entries	380
Mean	44.22
Std Dev	11.57

Turn on : 26.1390

Width : 3.351

Height : 9.983

Entry



B1L002S, U4-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.66
Std Dev	11.63

Turn on : 27.7485

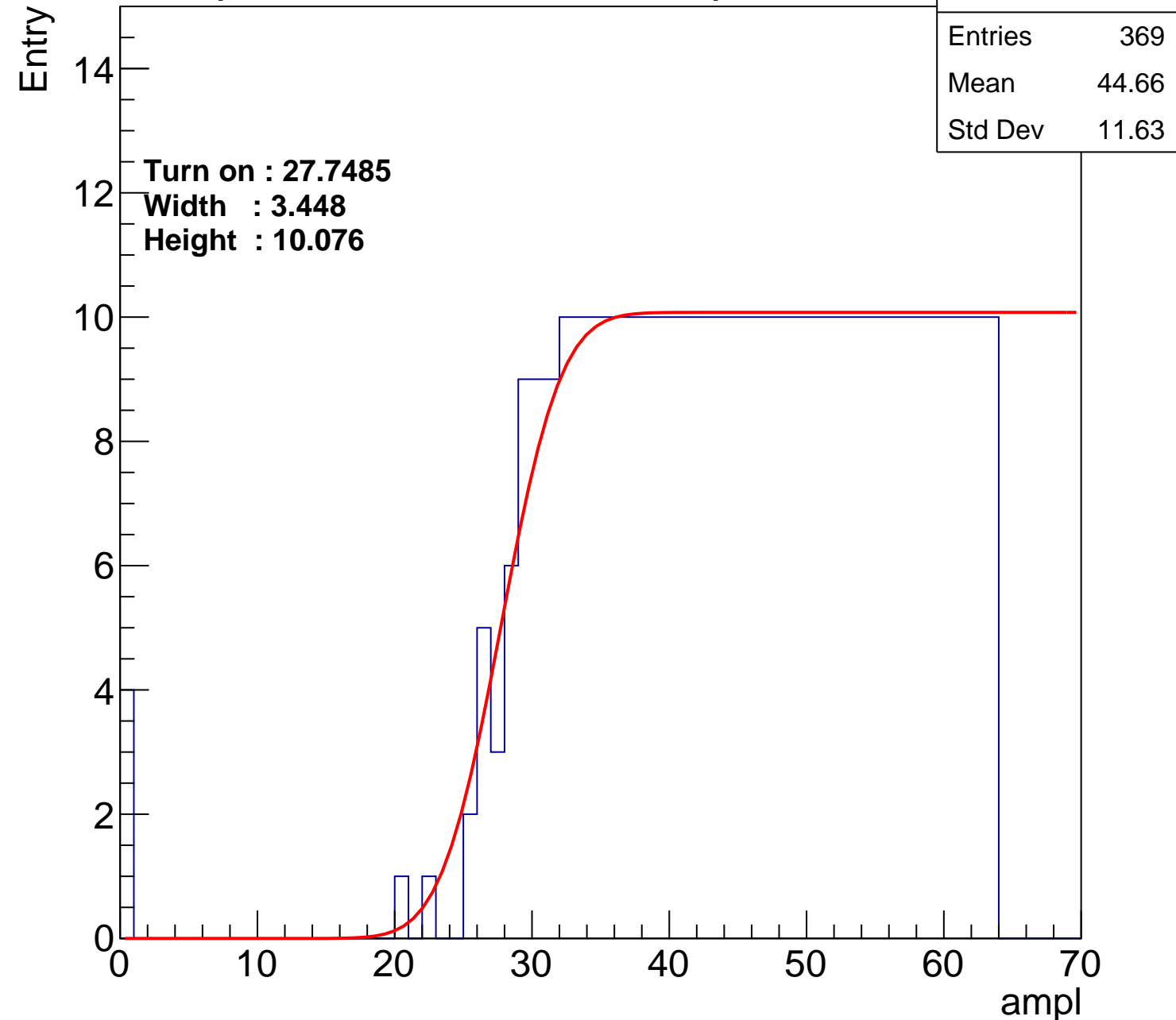
Width : 3.448

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.57
Std Dev	11.99

Turn on : 25.0878

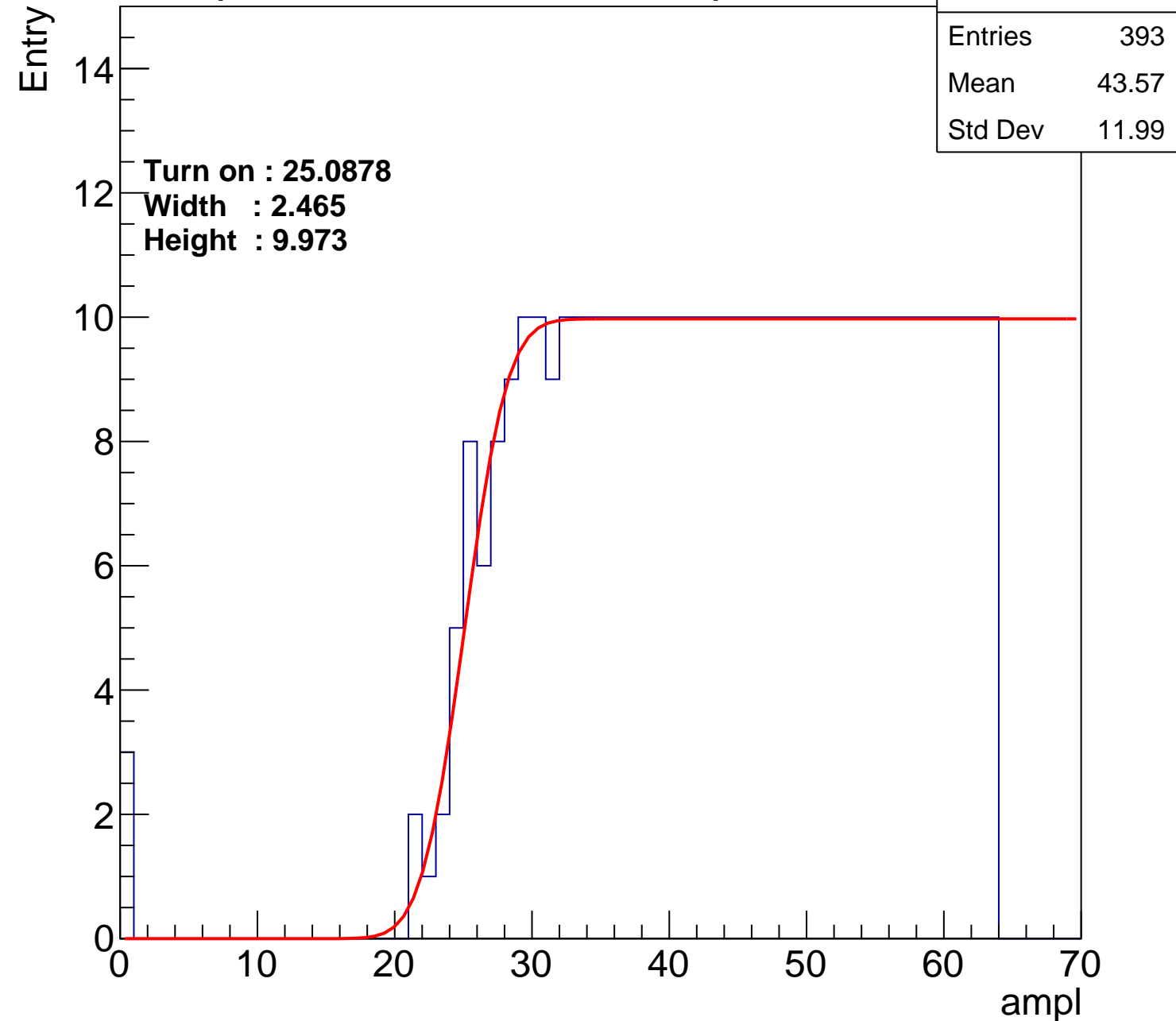
Width : 2.465

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.53
Std Dev	11.49

Turn on : 26.6608

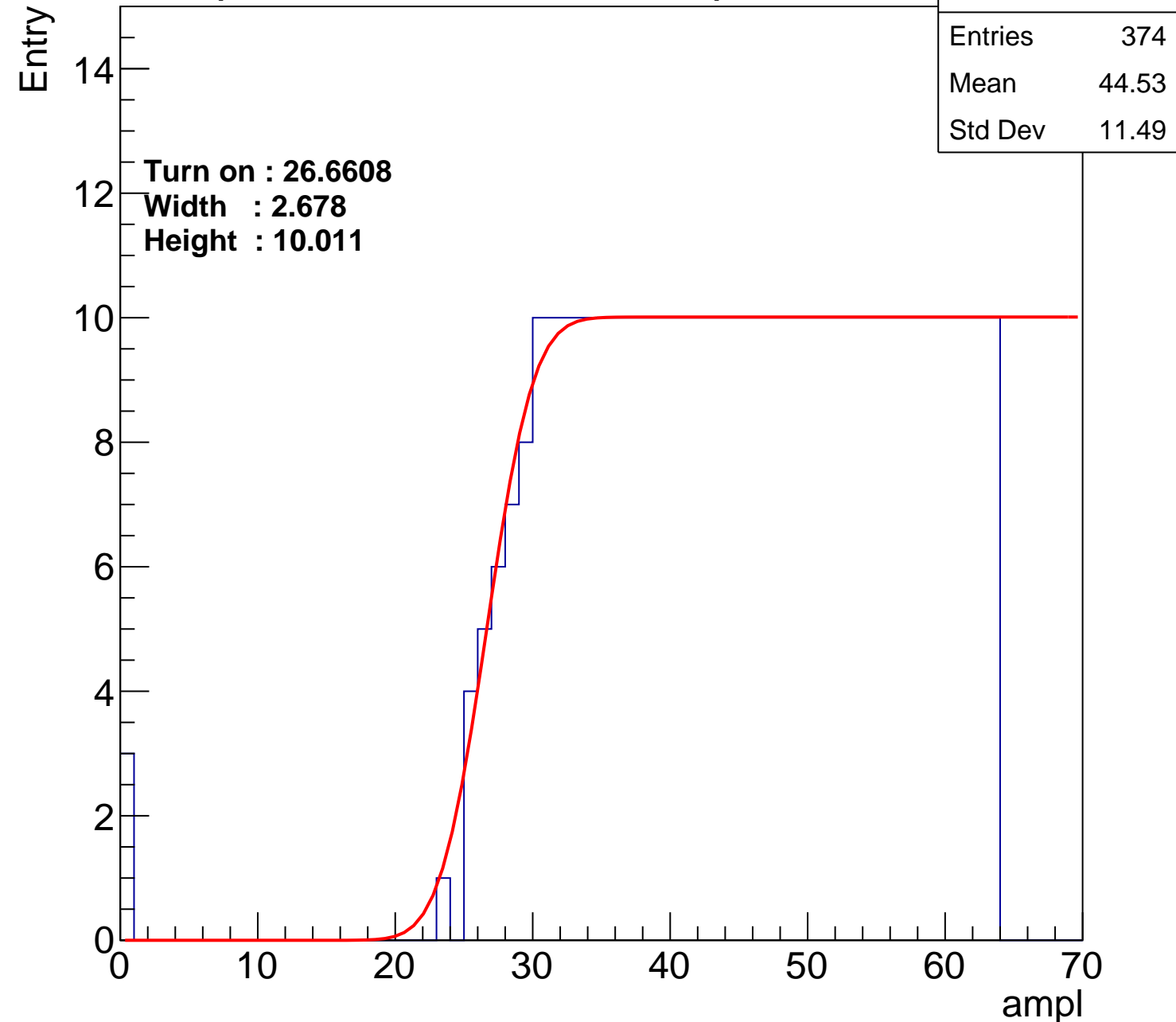
Width : 2.678

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch92

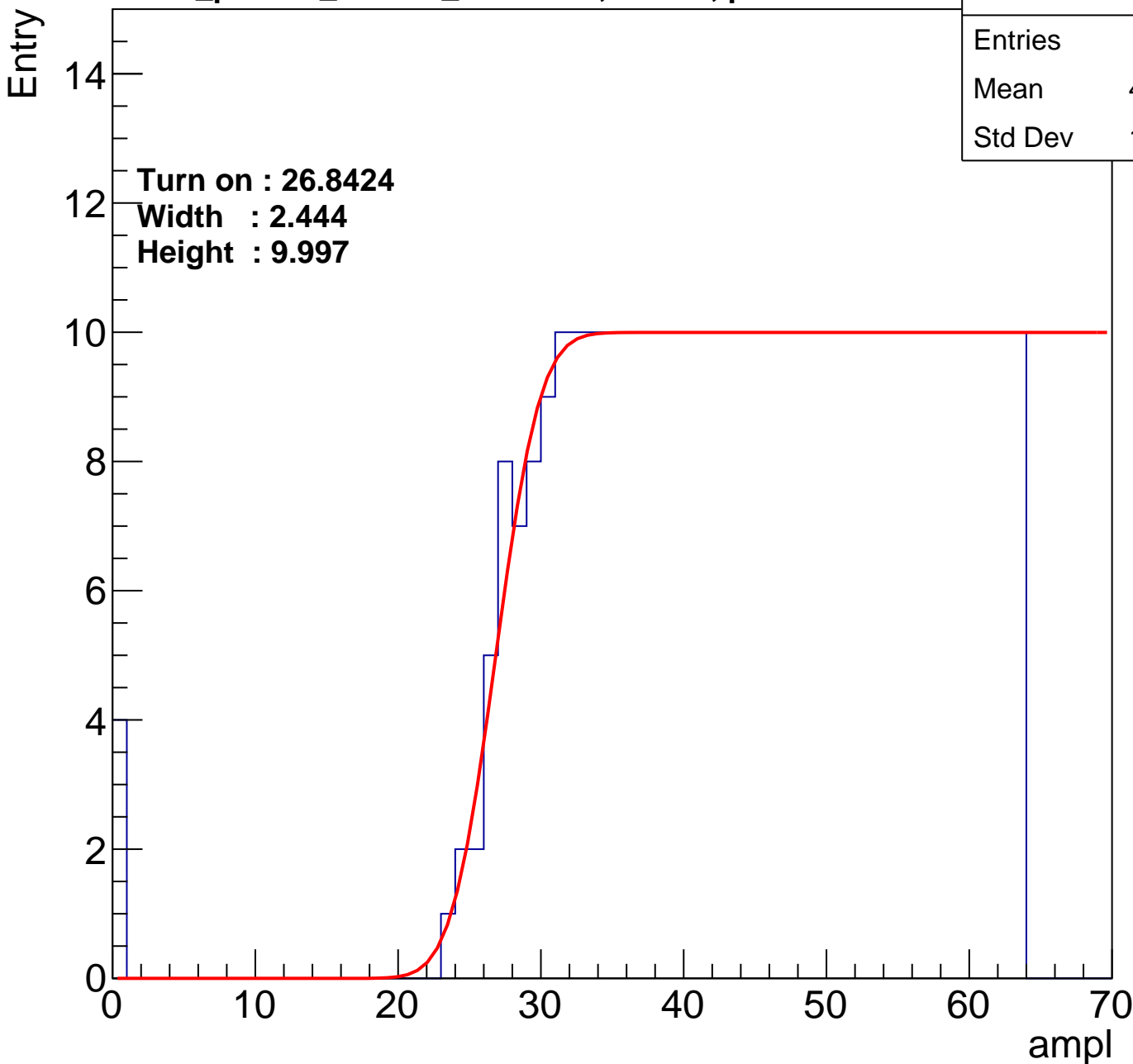
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 26.8424

Width : 2.444

Height : 9.997

Entries	376
Mean	44.35
Std Dev	11.74



B1L002S, U4-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	43.03
Std Dev	12.05

Turn on : 23.3269

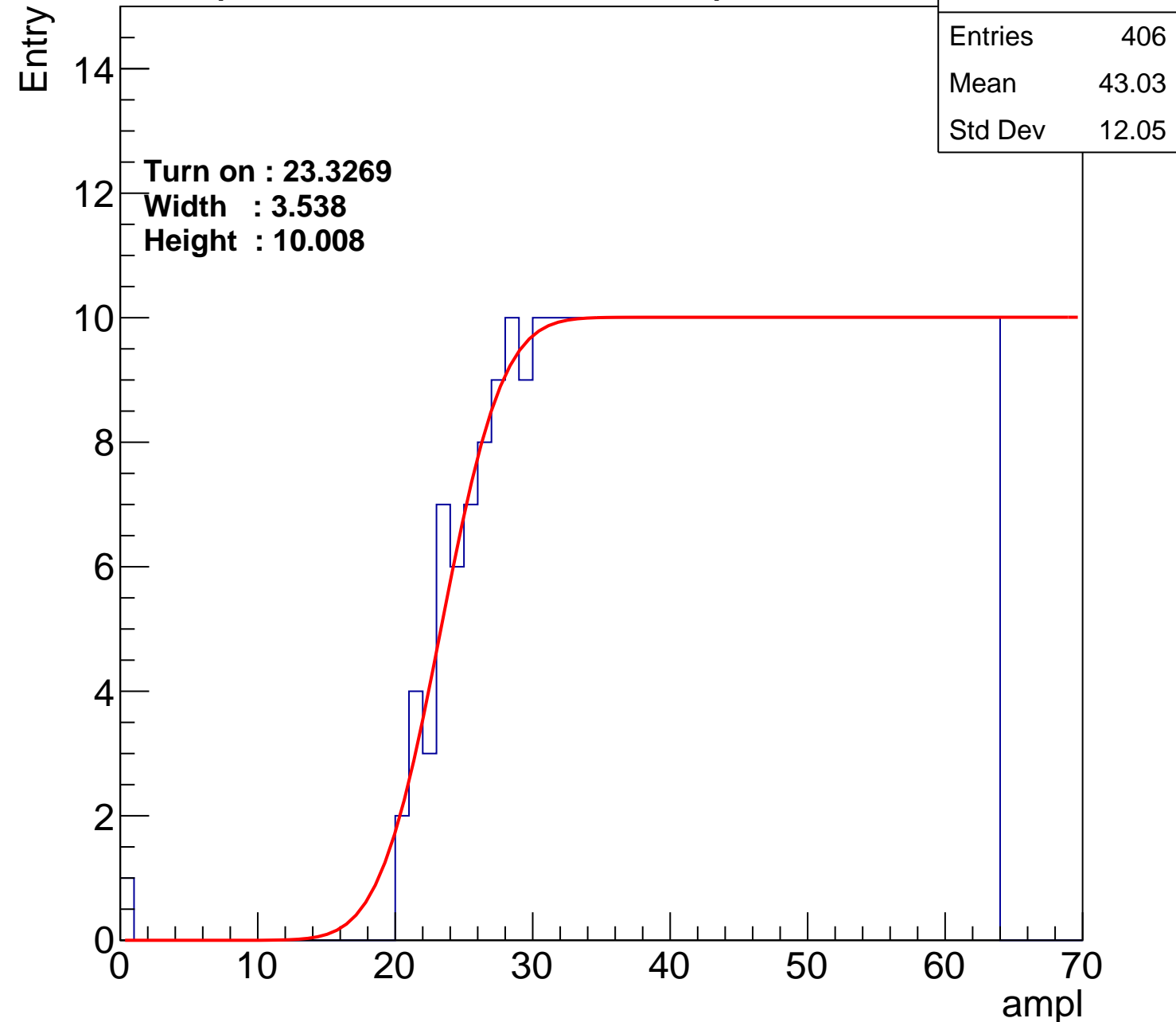
Width : 3.538

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.59
Std Dev	11.3

Turn on : 26.9051

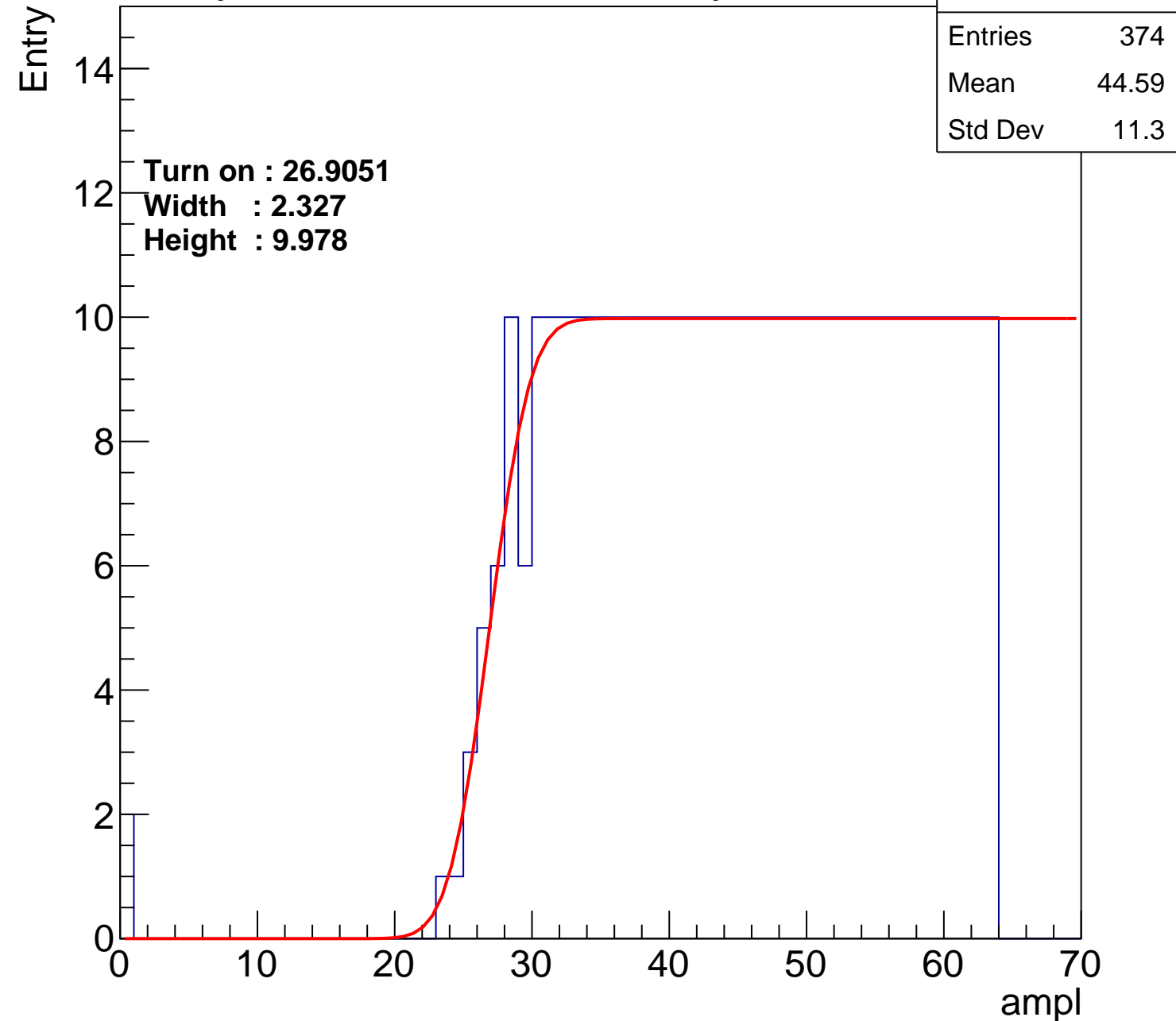
Width : 2.327

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.2014

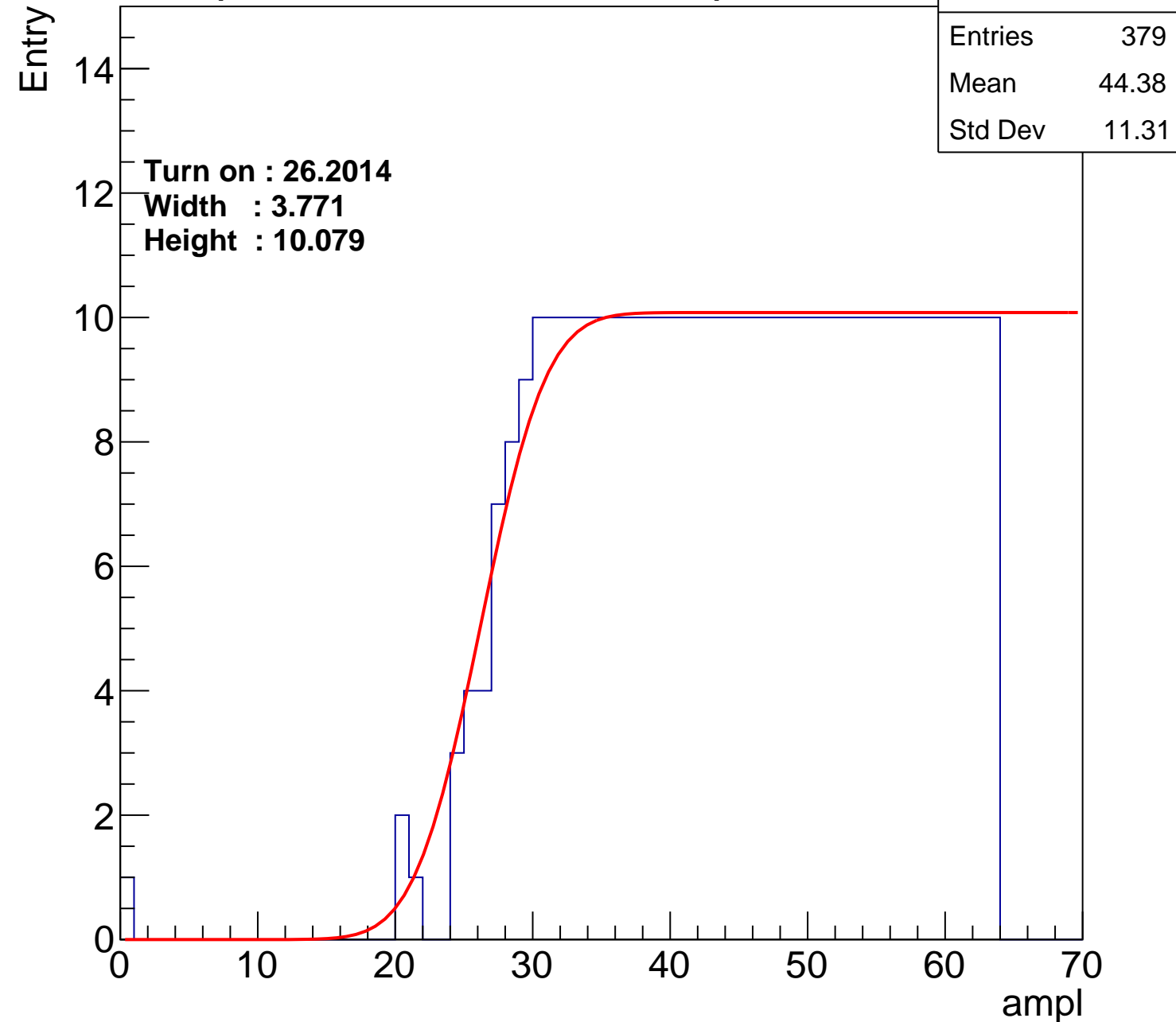
Width : 3.771

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.54
Std Dev	11.82

Turn on : 24.5456

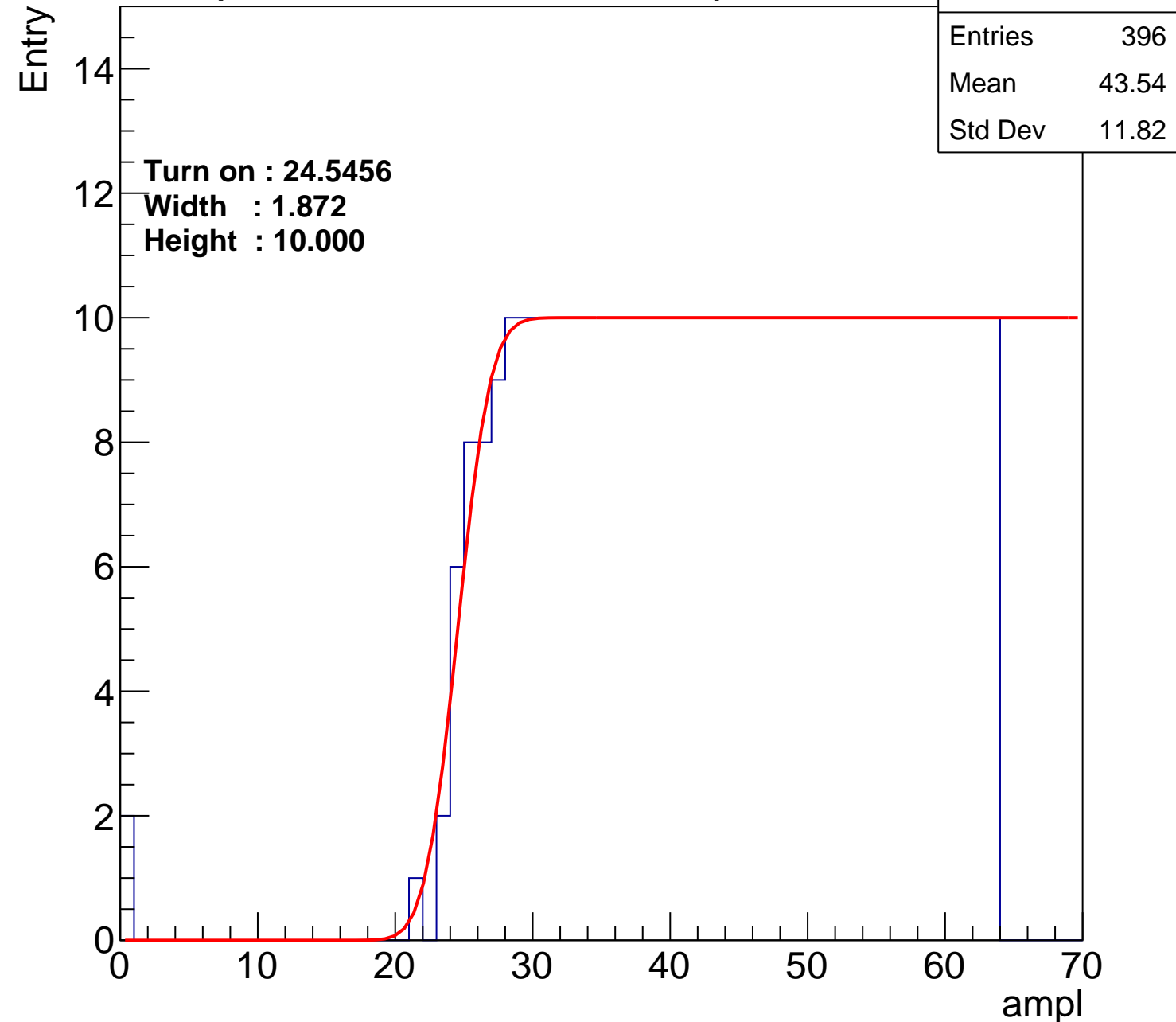
Width : 1.872

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.7918

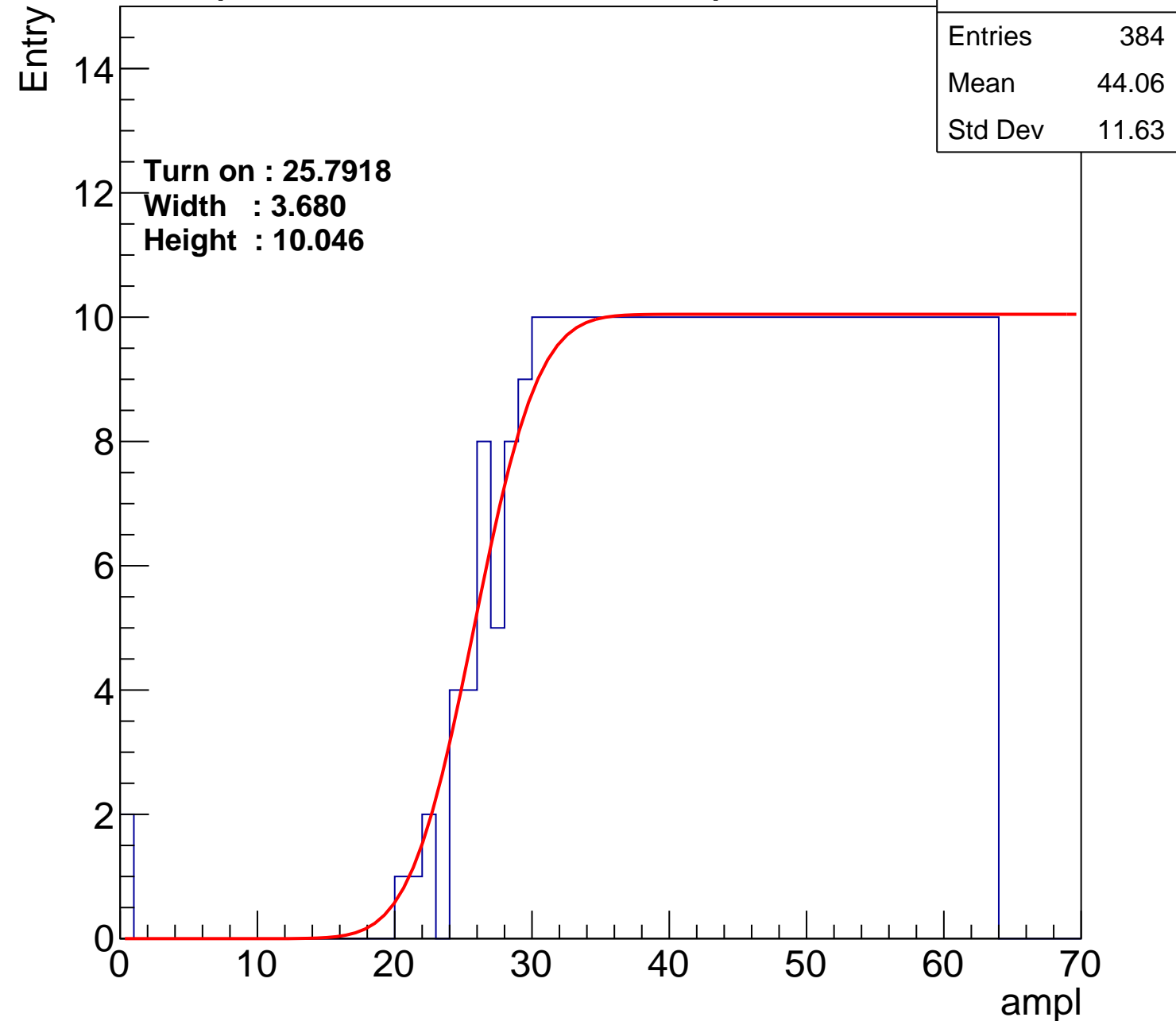
Width : 3.680

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch98

calib_packv5_042523_0143.root, FC#10, port B3

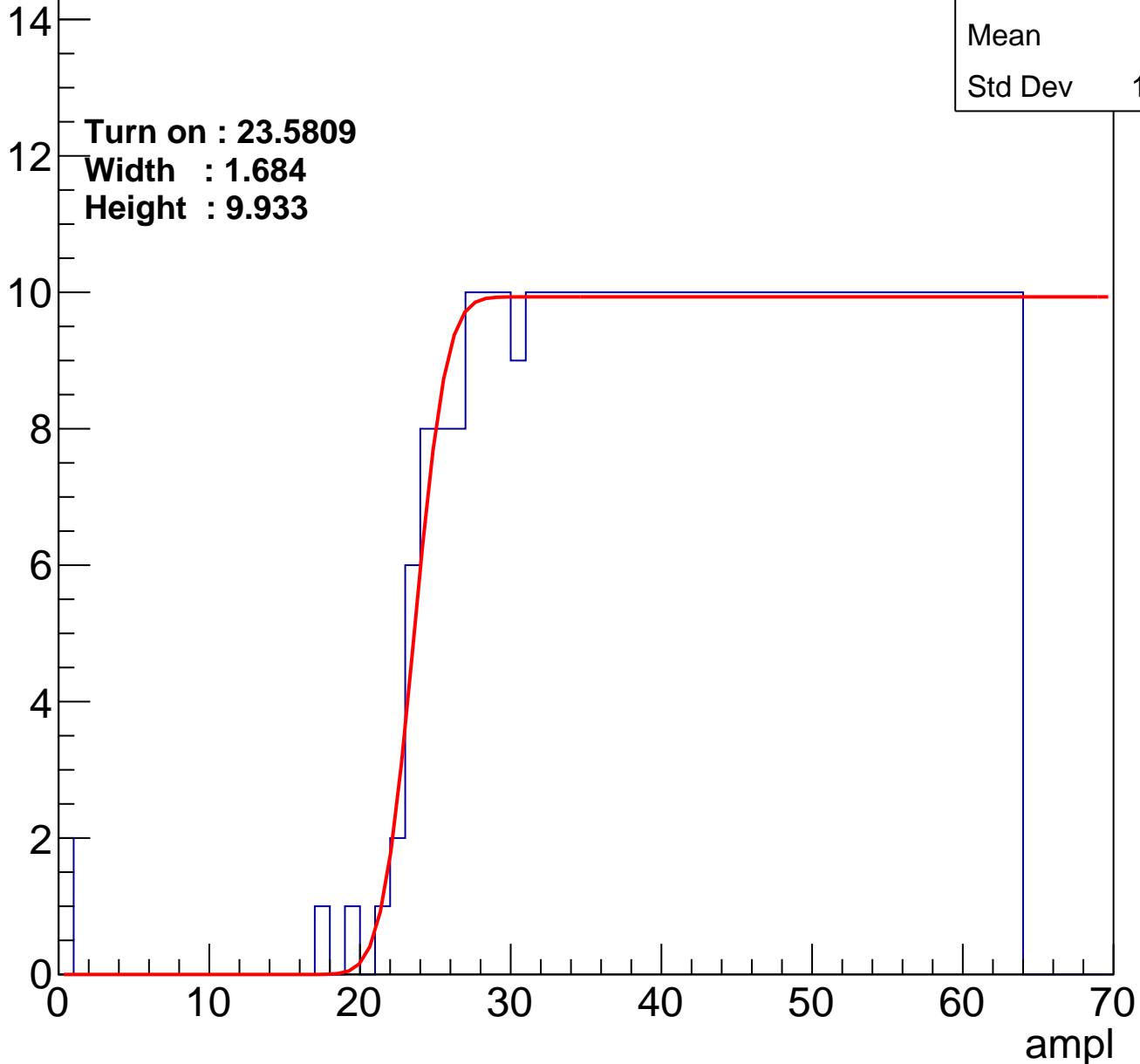
Entries	406
Mean	43
Std Dev	12.16

Turn on : 23.5809

Width : 1.684

Height : 9.933

Entry



B1L002S, U4-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.52
Std Dev	11.73

Turn on : 24.6308

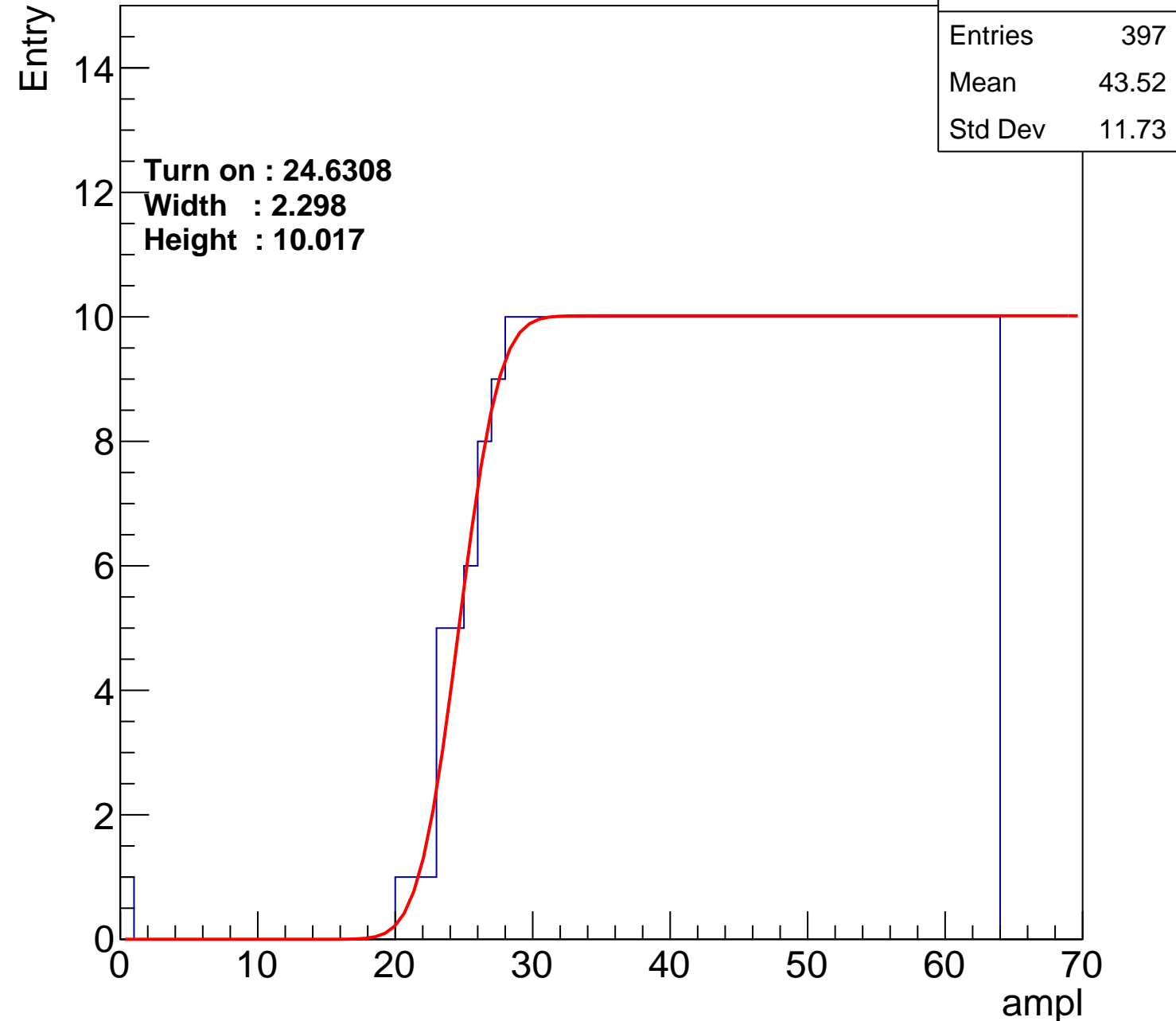
Width : 2.298

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.93
Std Dev	11.5

Turn on : 25.1756

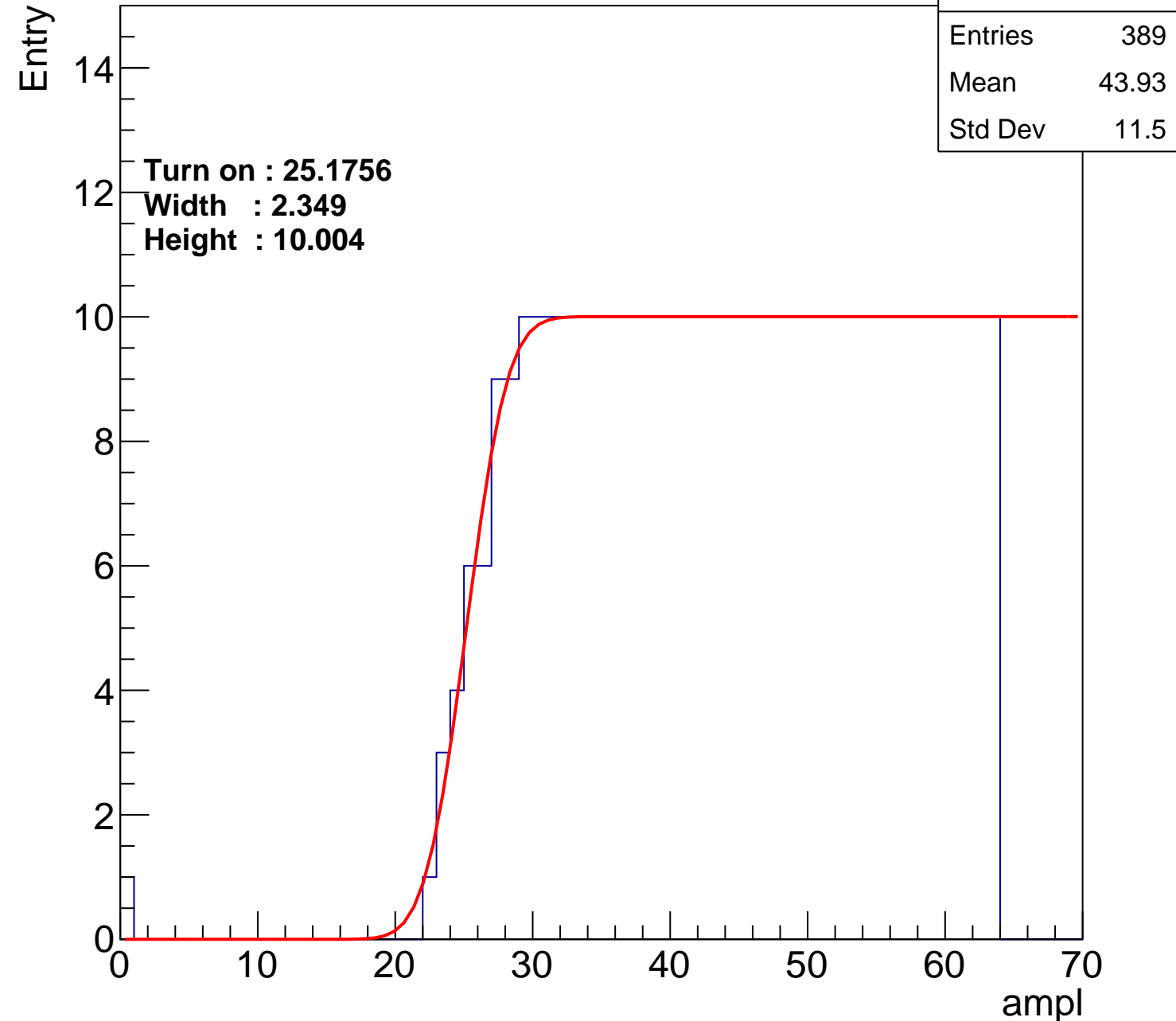
Width : 2.349

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.04
Std Dev	11.47

Turn on : 25.3392

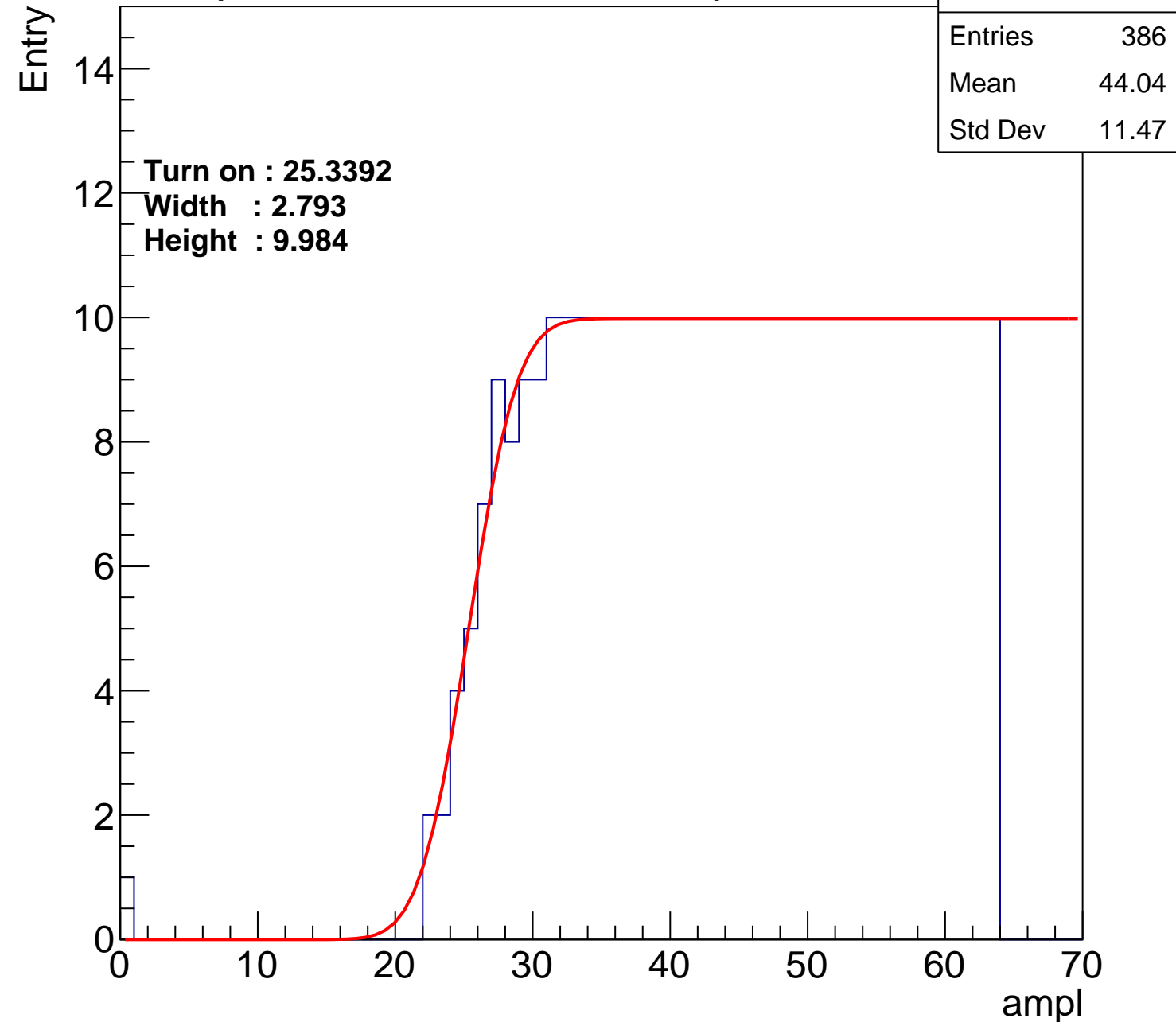
Width : 2.793

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.8675

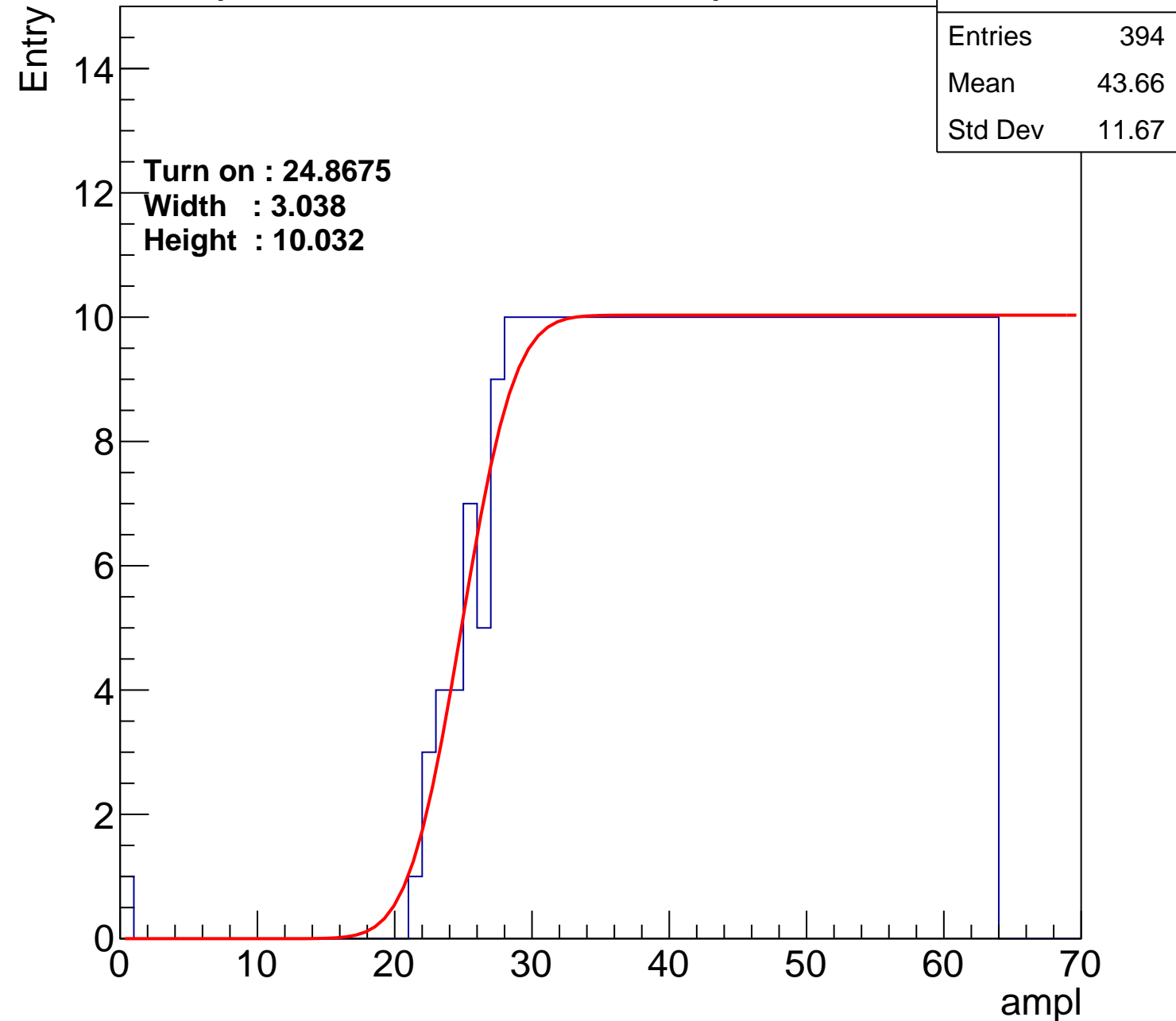
Width : 3.038

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch103

calib_packv5_042523_0143.root, FC#10, port B3

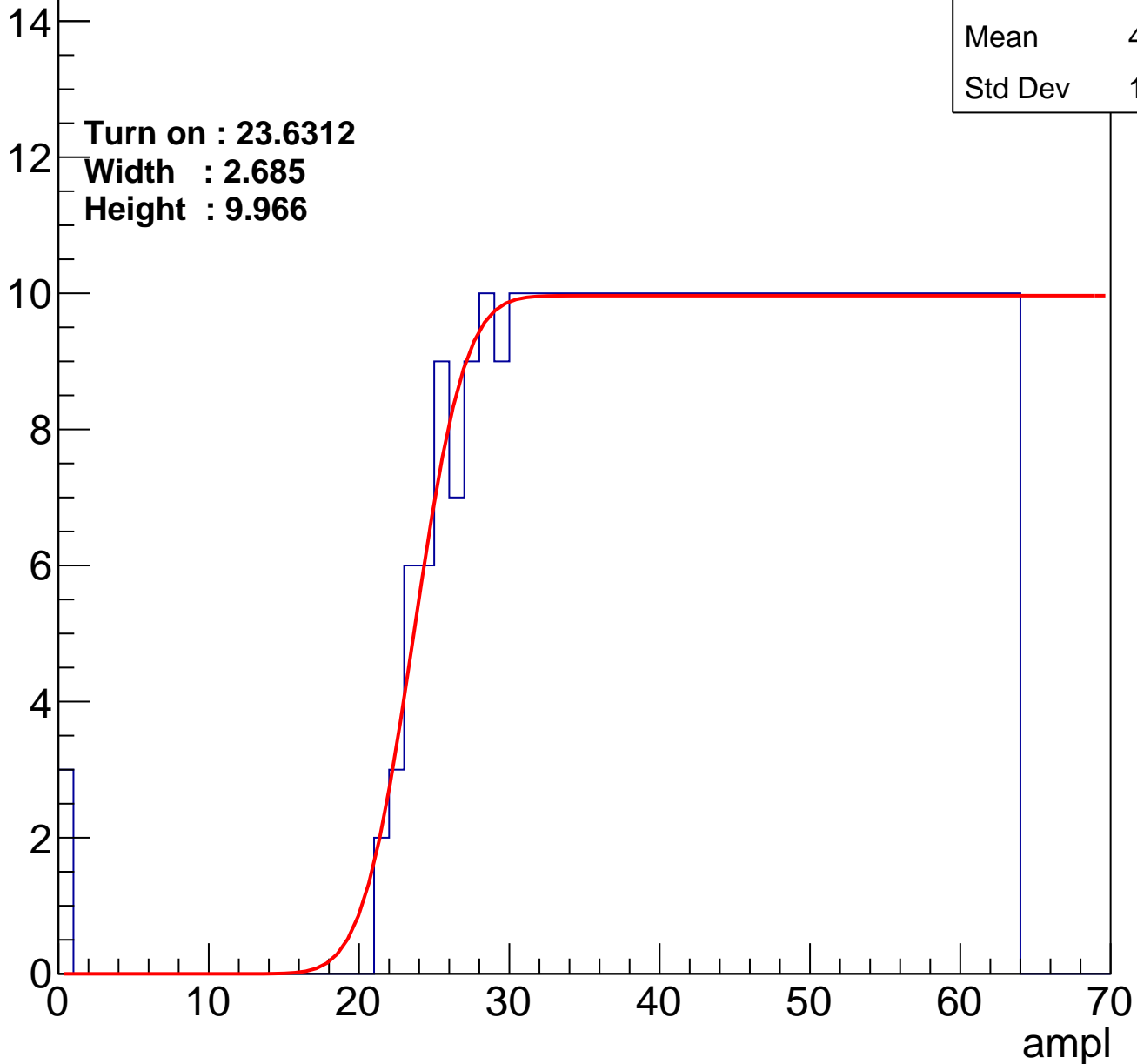
Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 23.6312

Width : 2.685

Height : 9.966

Entry



B1L002S, U4-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.91
Std Dev	12.36

Turn on : 24.5697

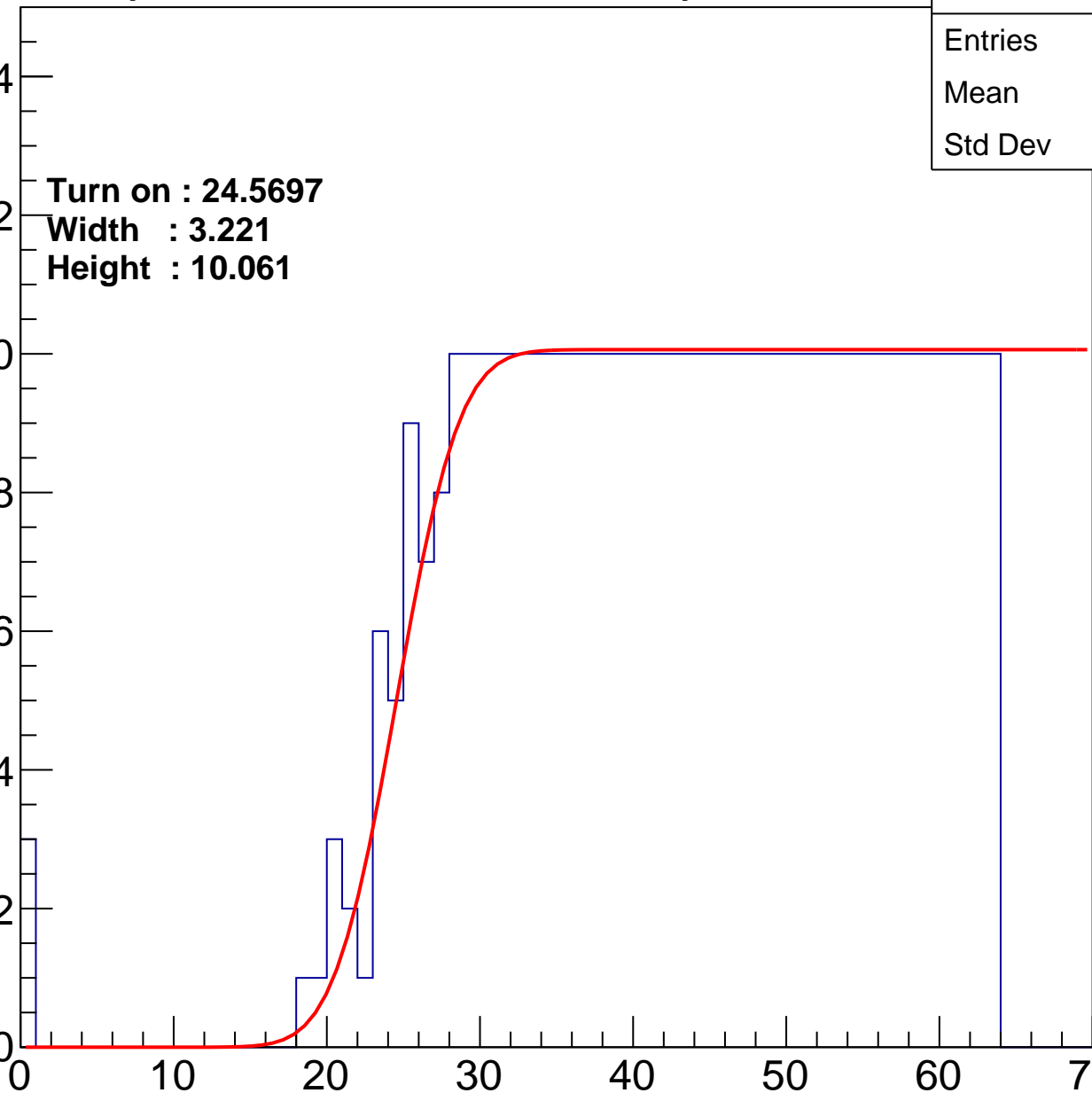
Width : 3.221

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.24
Std Dev	11.68

Turn on : 26.0401

Width : 1.426

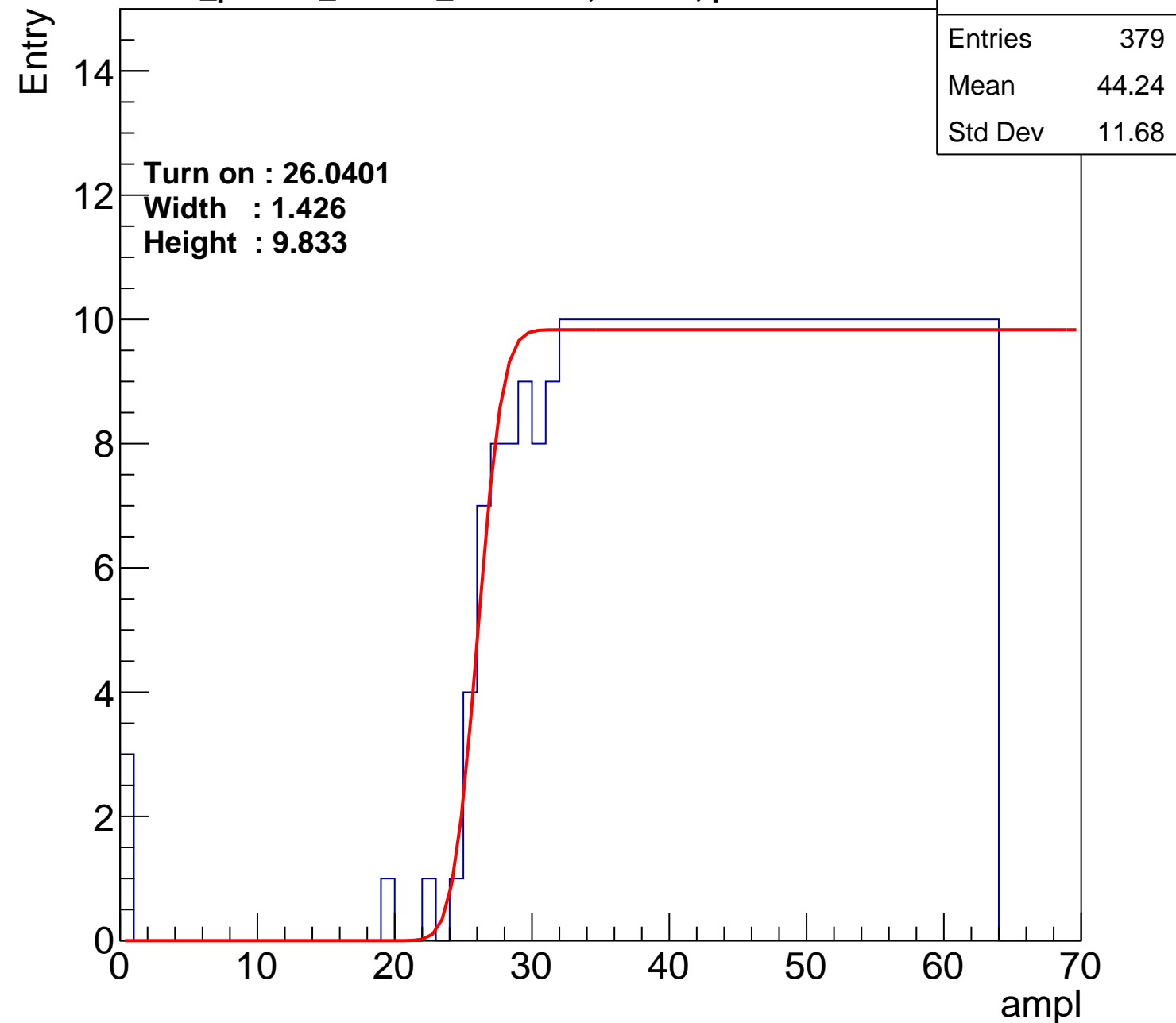
Height : 9.833

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U4-ch106

calib_packv5_042523_0143.root, FC#10, port B3

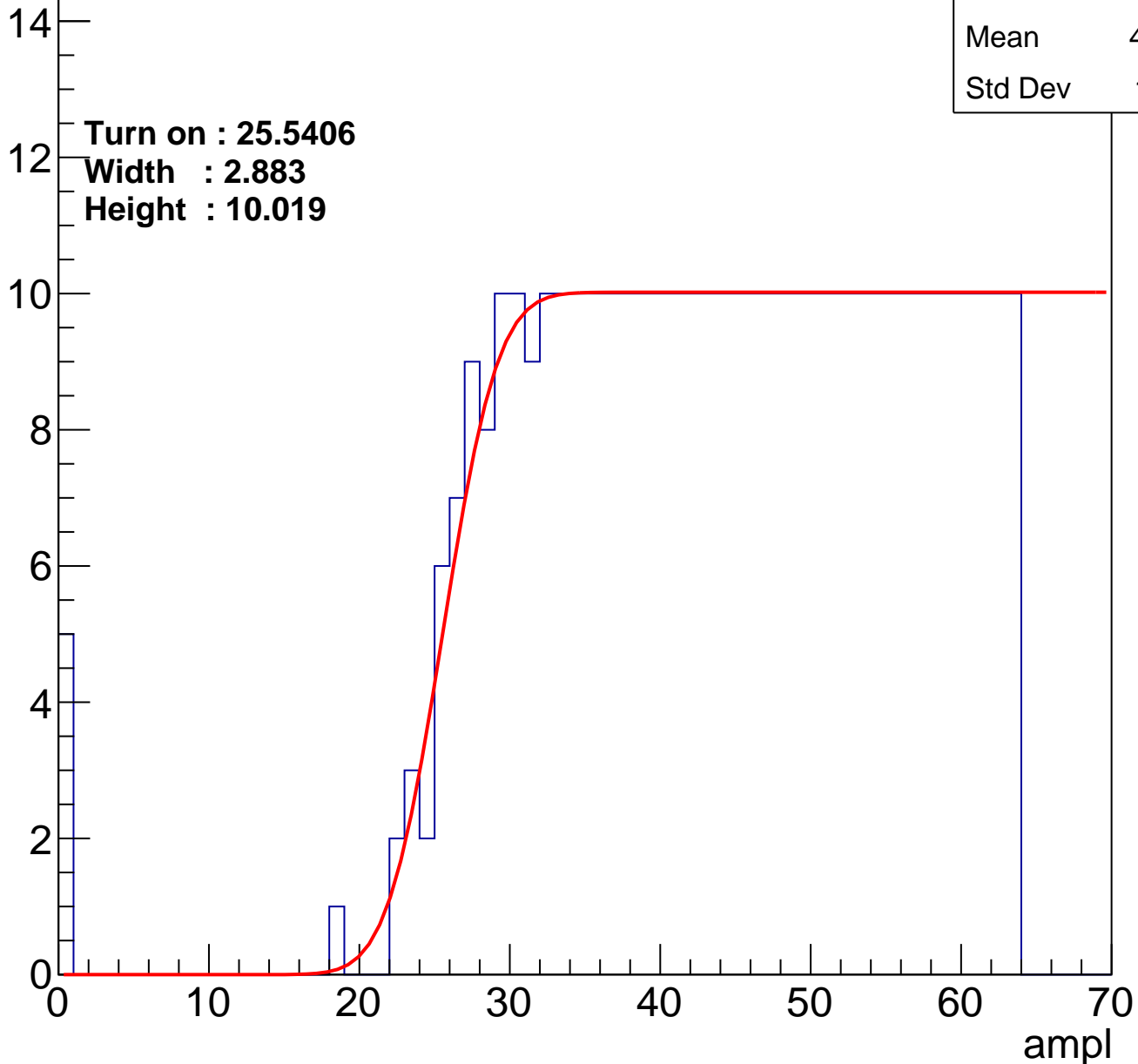
Entries	392
Mean	43.49
Std Dev	12.31

Turn on : 25.5406

Width : 2.883

Height : 10.019

Entry



B1L002S, U4-ch107

calib_packv5_042523_0143.root, FC#10, port B3

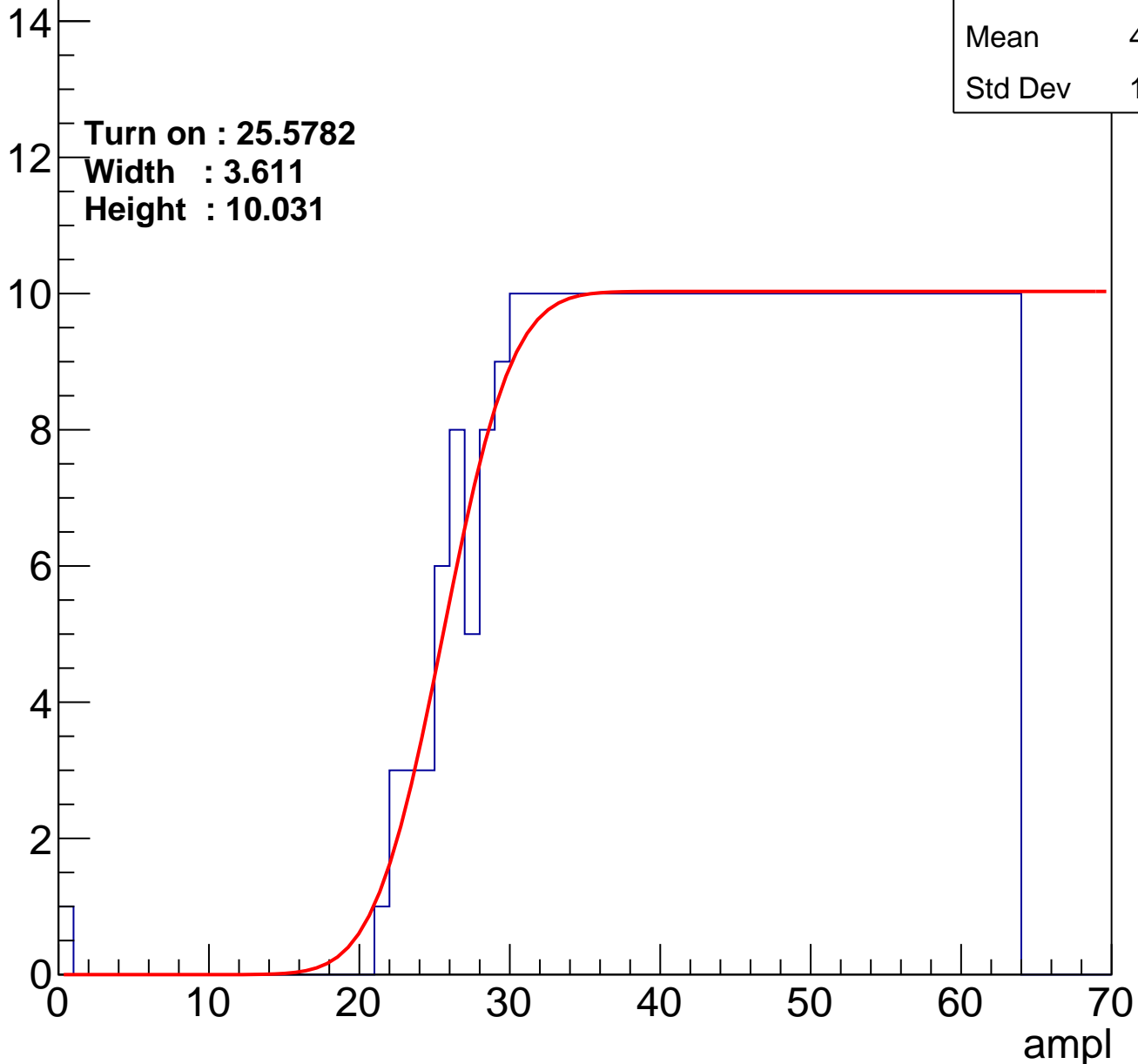
Entries	387
Mean	43.97
Std Dev	11.54

Turn on : 25.5782

Width : 3.611

Height : 10.031

Entry



B1L002S, U4-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.1

Turn on : 24.8929

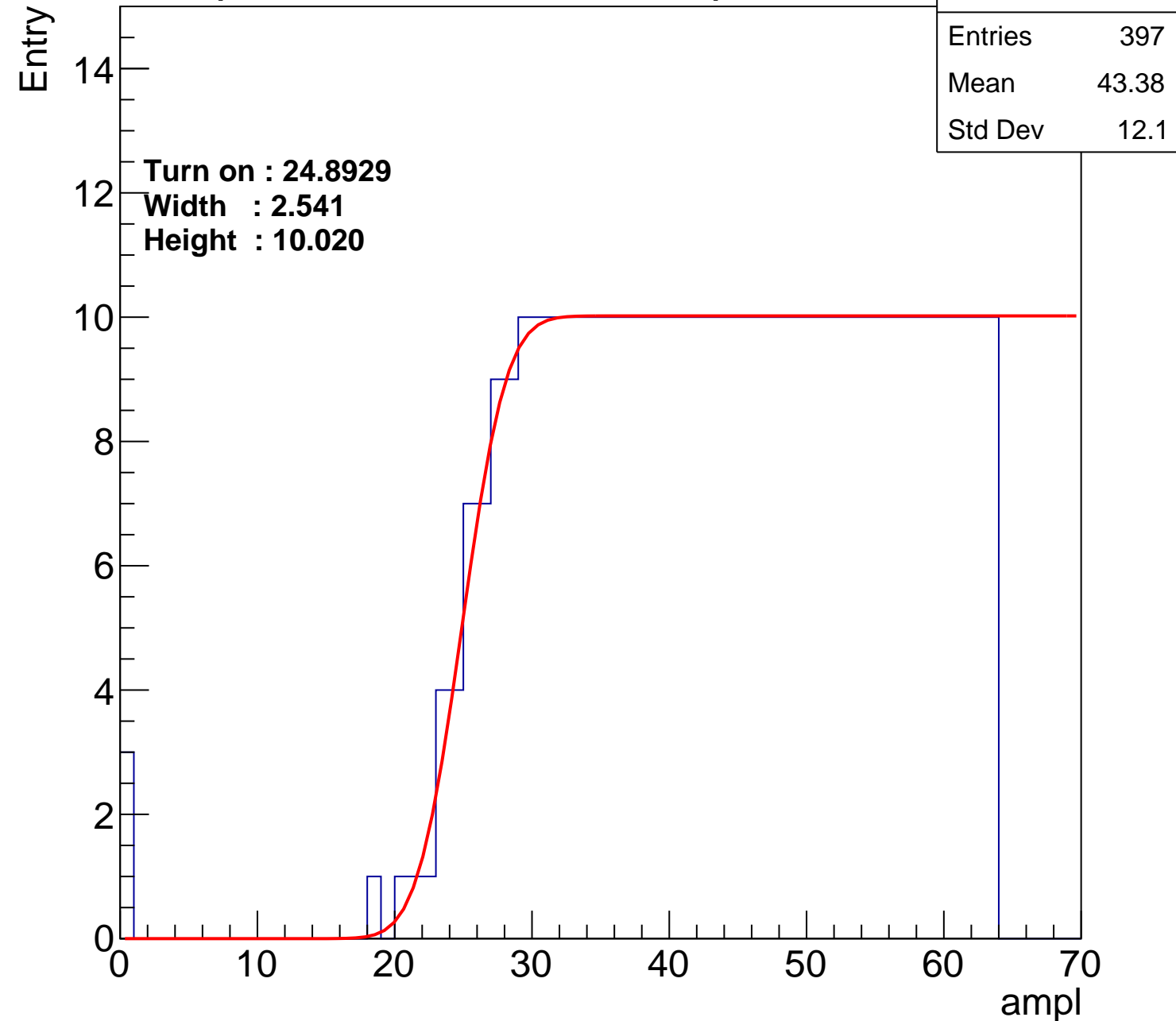
Width : 2.541

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.2508

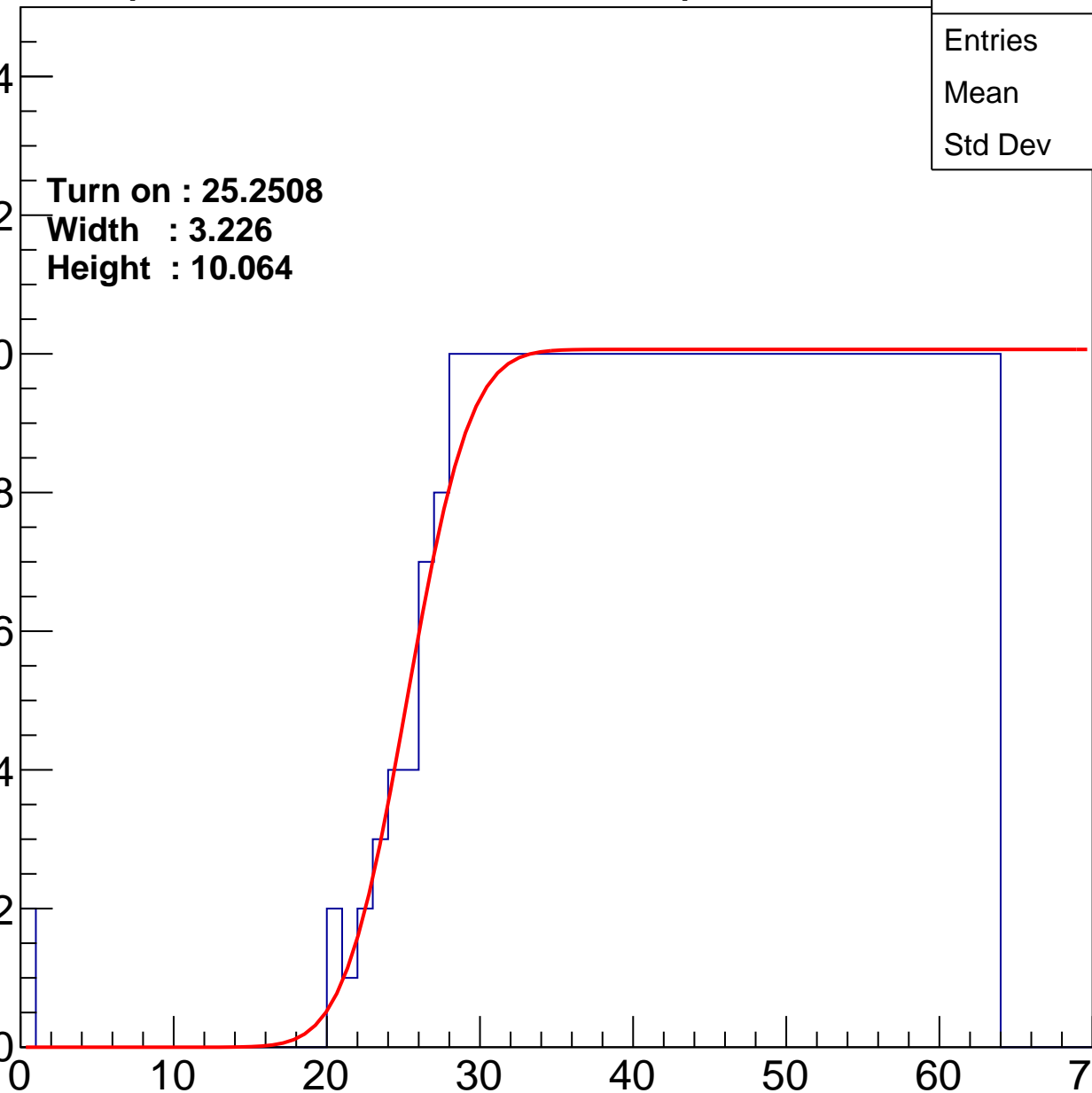
Width : 3.226

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch110

calib_packv5_042523_0143.root, FC#10, port B3

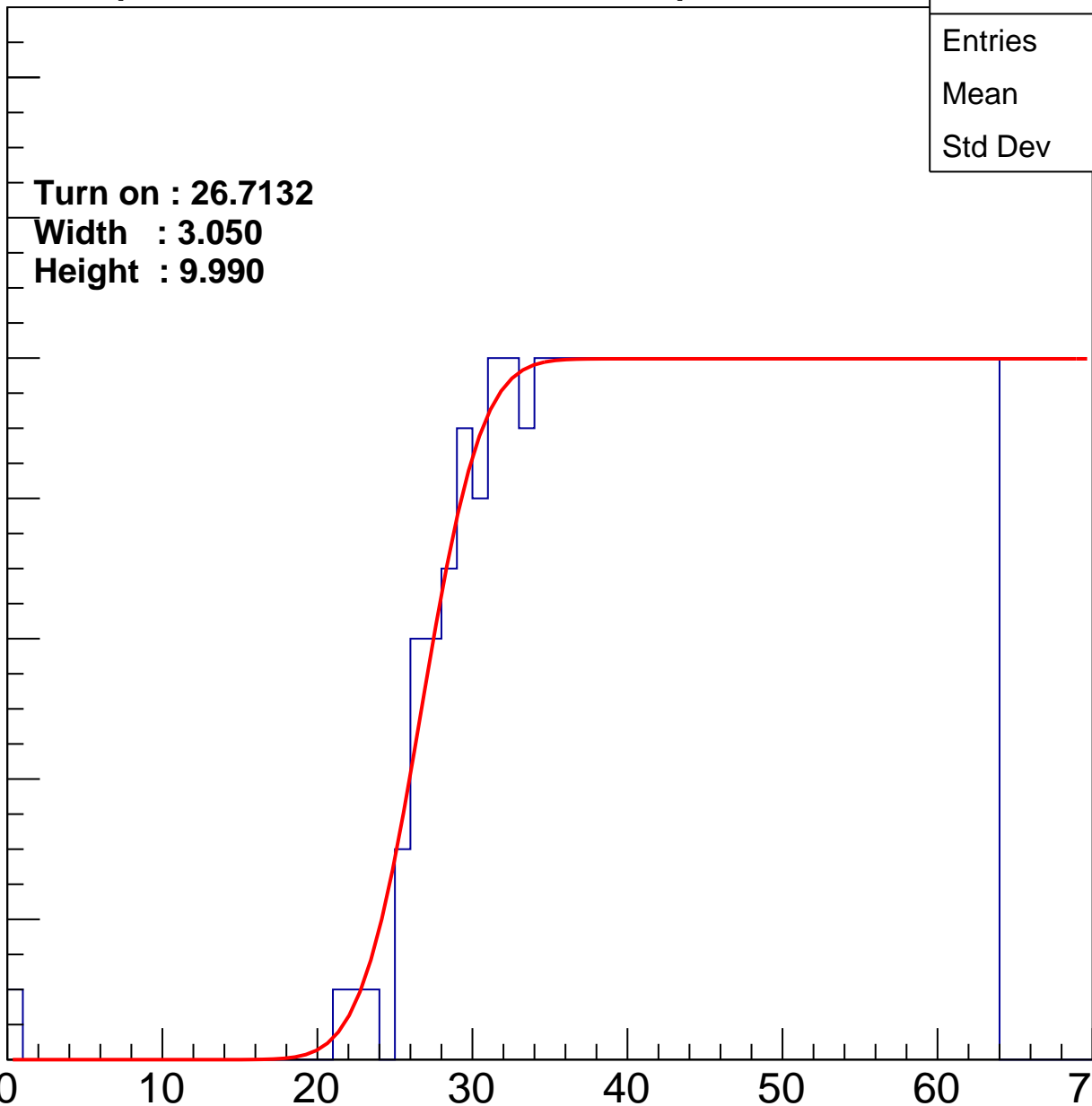
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7132
Width : 3.050
Height : 9.990

Entries	372
Mean	44.71
Std Dev	11.13

ampl



B1L002S, U4-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 24.2363

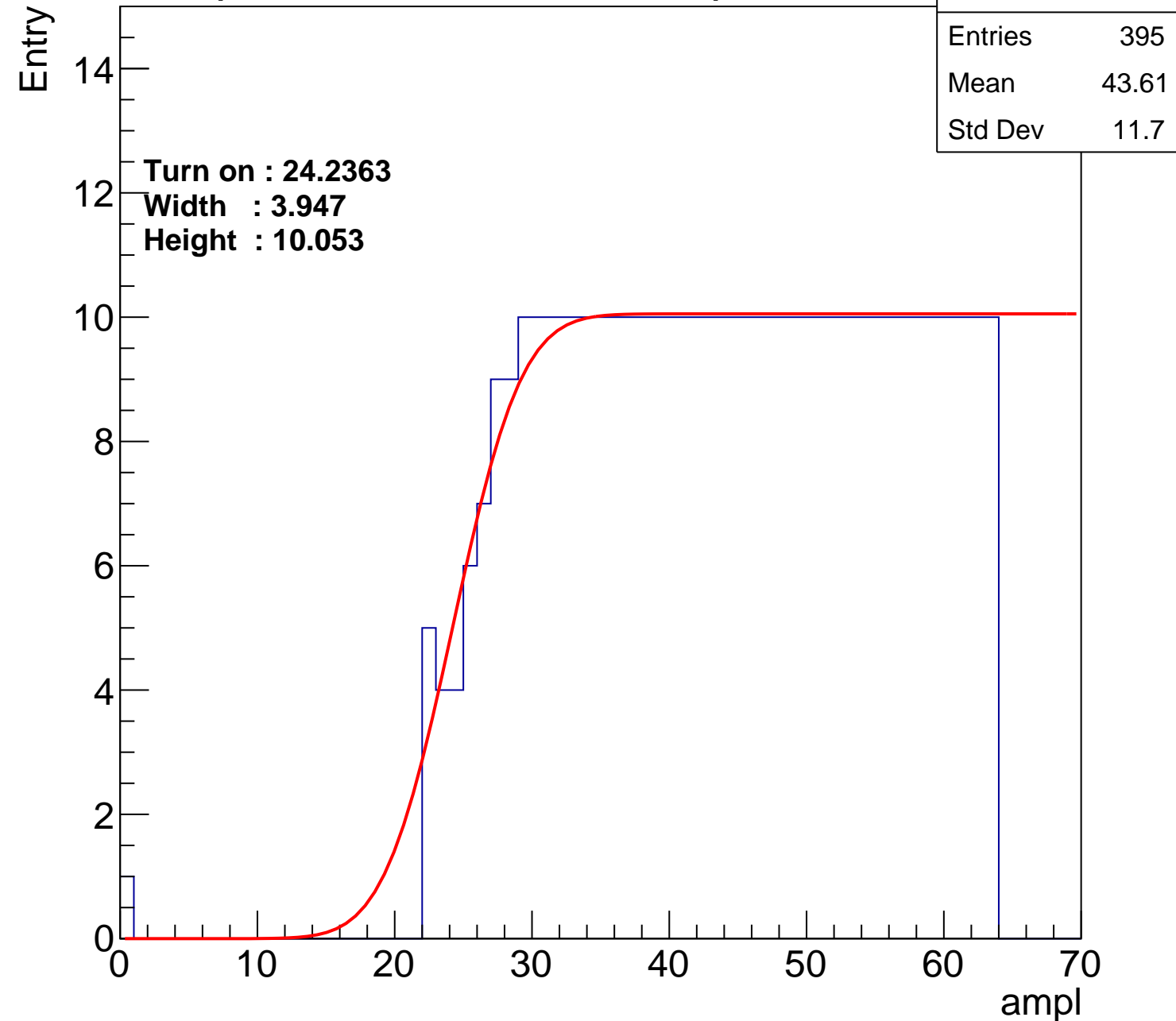
Width : 3.947

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 25.8449

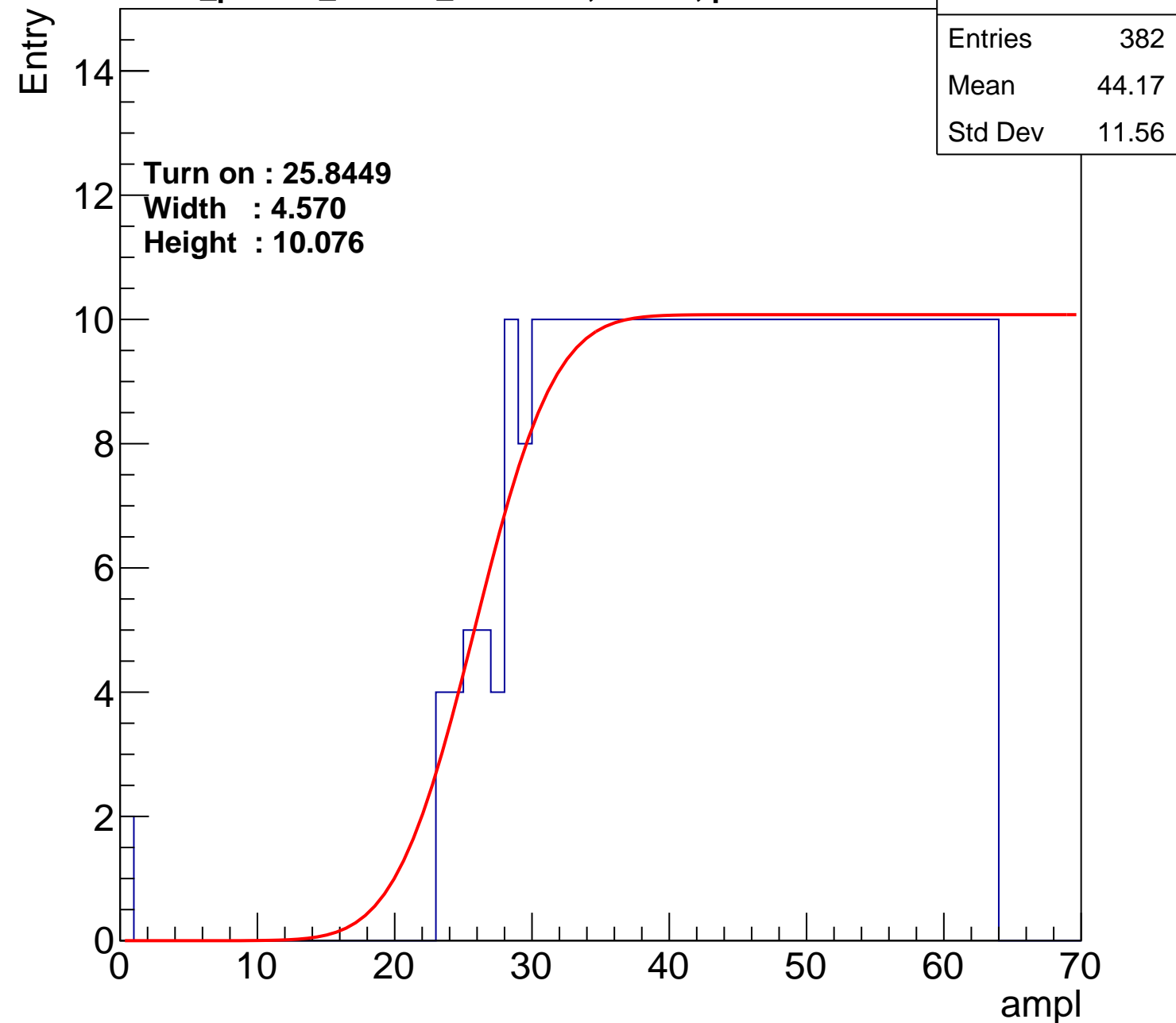
Width : 4.570

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch113

calib_packv5_042523_0143.root, FC#10, port B3

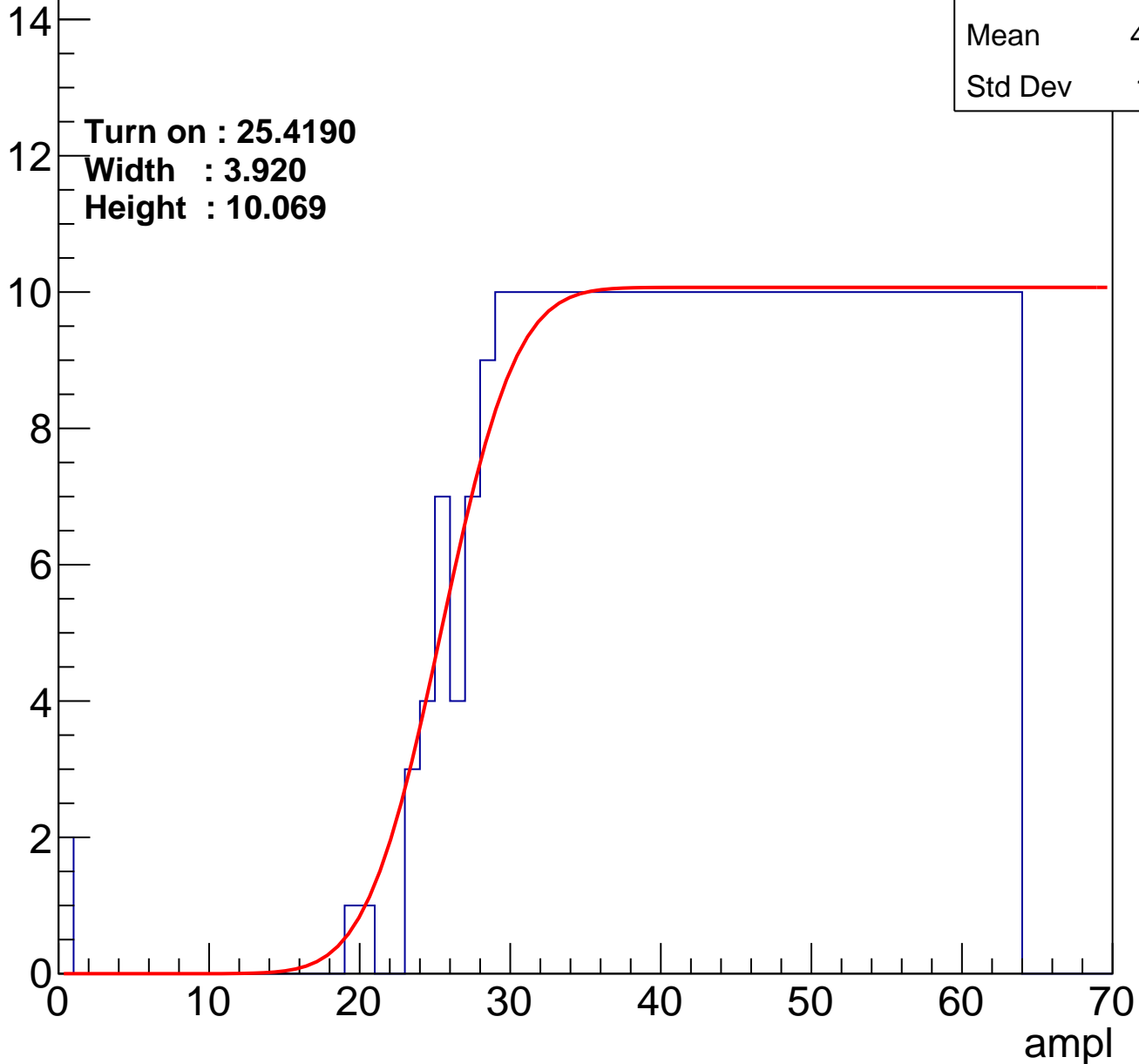
Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.4190

Width : 3.920

Height : 10.069

Entry



B1L002S, U4-ch114

calib_packv5_042523_0143.root, FC#10, port B3

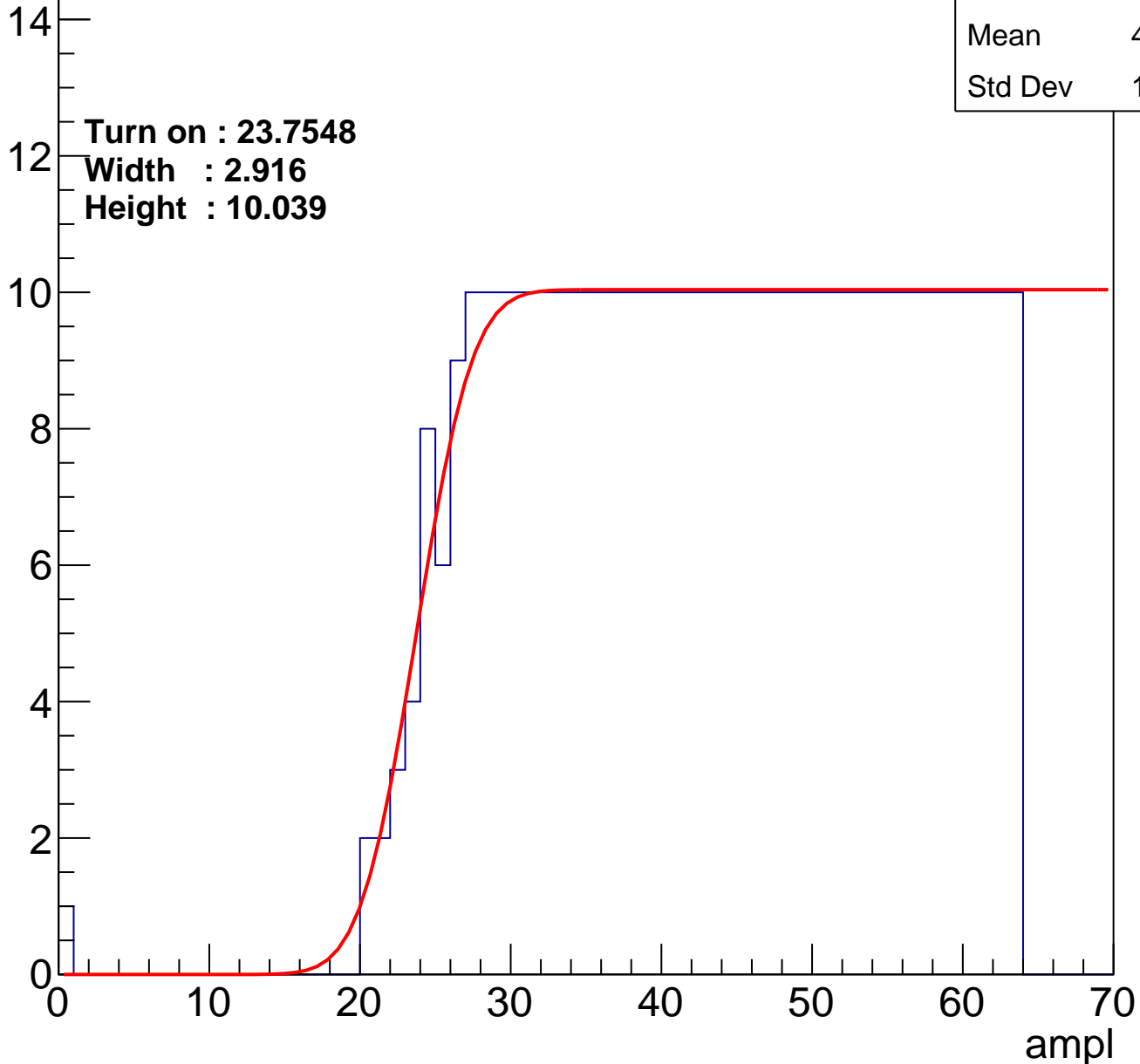
Entries	405
Mean	43.13
Std Dev	11.96

Turn on : 23.7548

Width : 2.916

Height : 10.039

Entry



B1L002S, U4-ch115

calib_packv5_042523_0143.root, FC#10, port B3

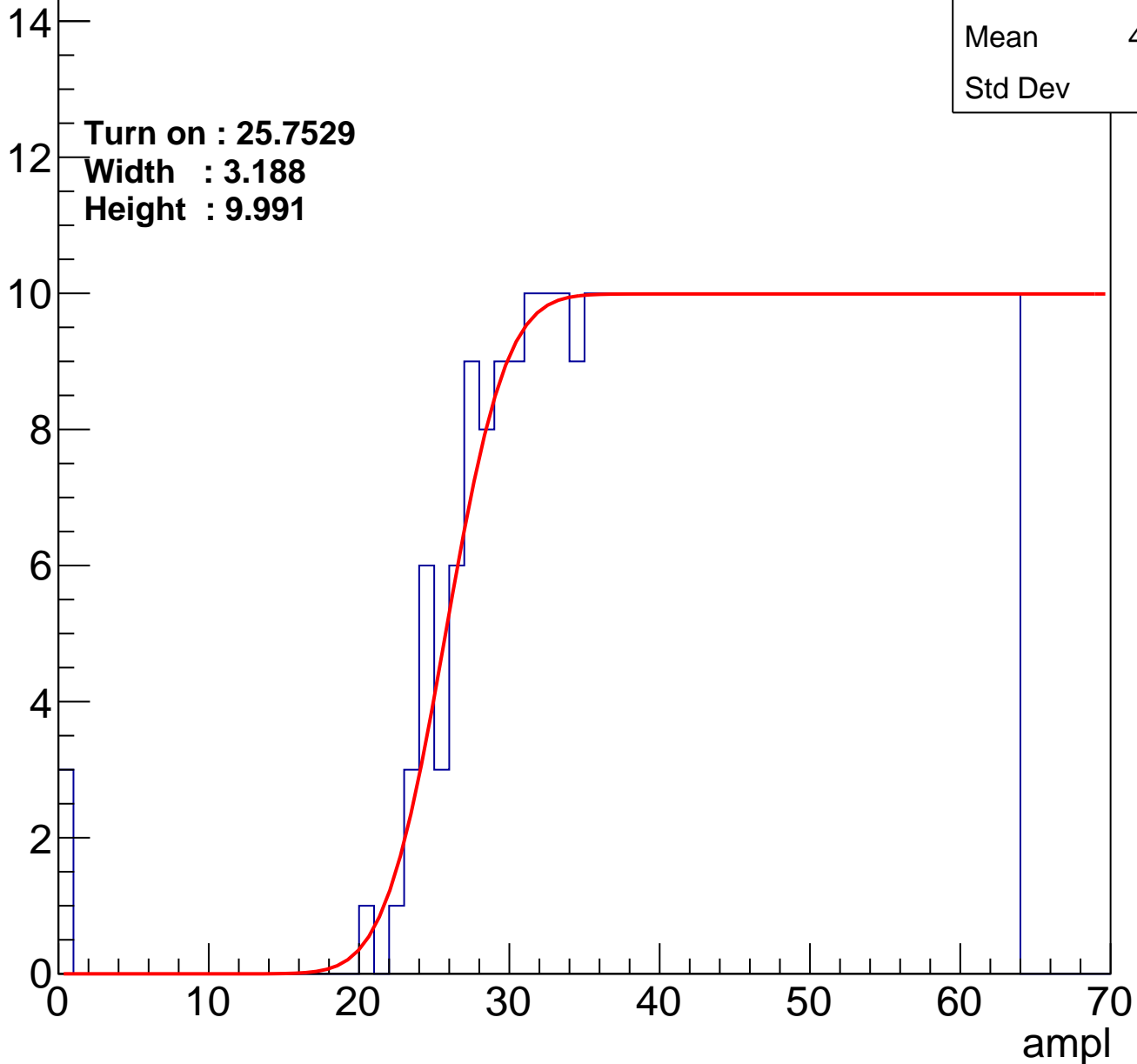
Entries	387
Mean	43.82
Std Dev	11.9

Turn on : 25.7529

Width : 3.188

Height : 9.991

Entry



B1L002S, U4-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.98
Std Dev	11.76

Turn on : 26.0013

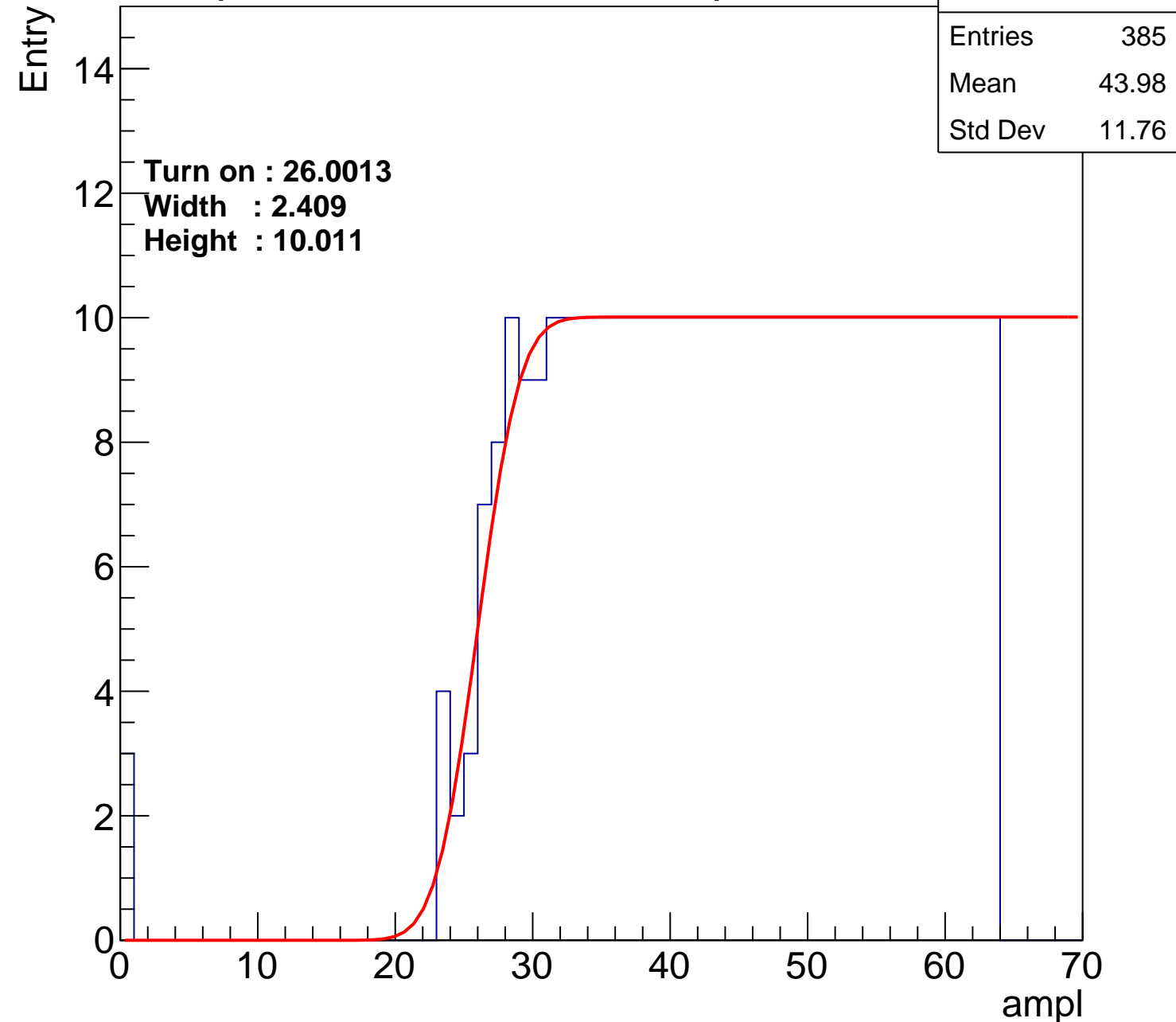
Width : 2.409

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch117

calib_packv5_042523_0143.root, FC#10, port B3

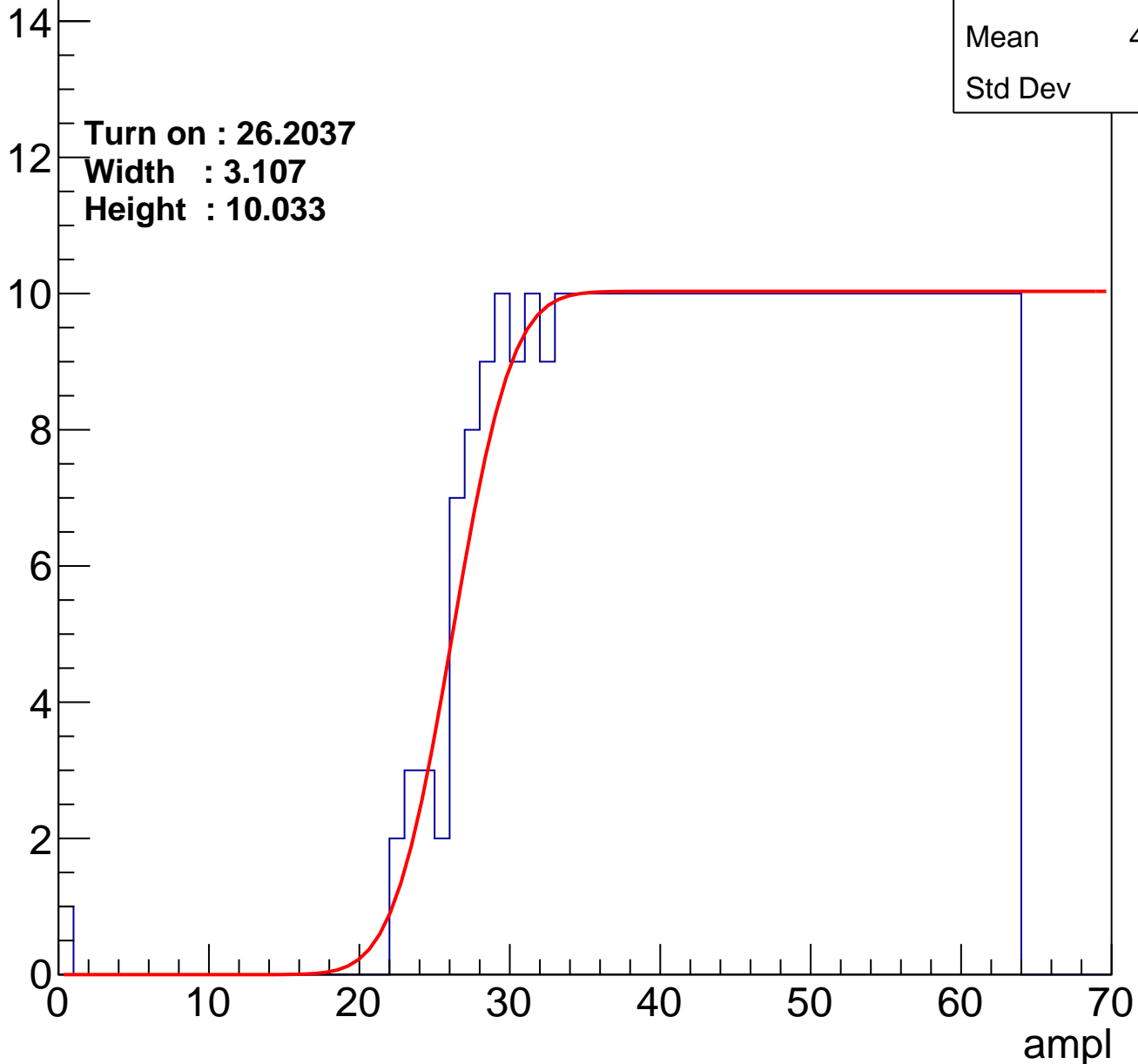
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 26.2037

Width : 3.107

Height : 10.033

Entry



B1L002S, U4-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.23
Std Dev	11.56

Turn on : 26.4677

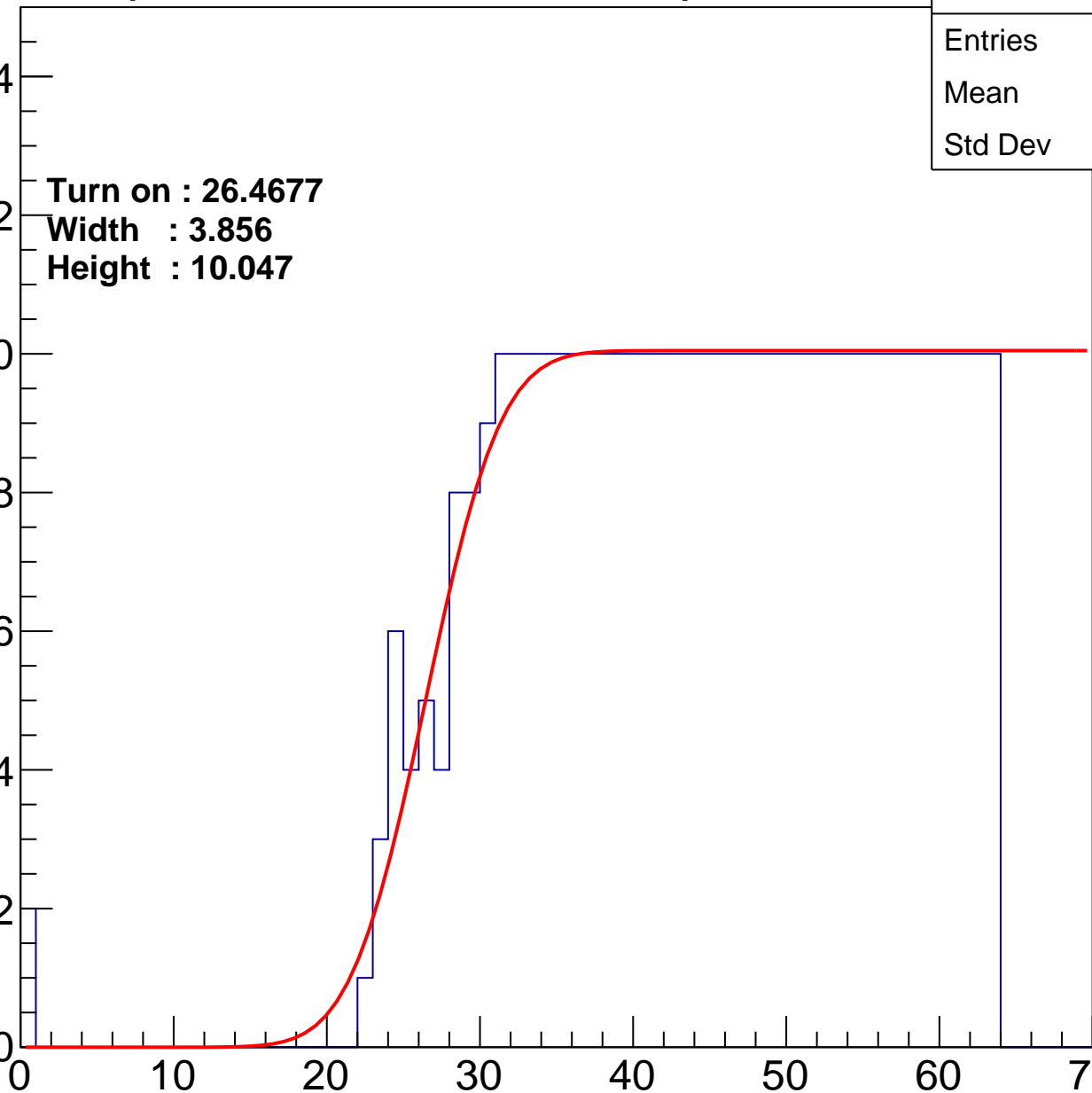
Width : 3.856

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch119

calib_packv5_042523_0143.root, FC#10, port B3

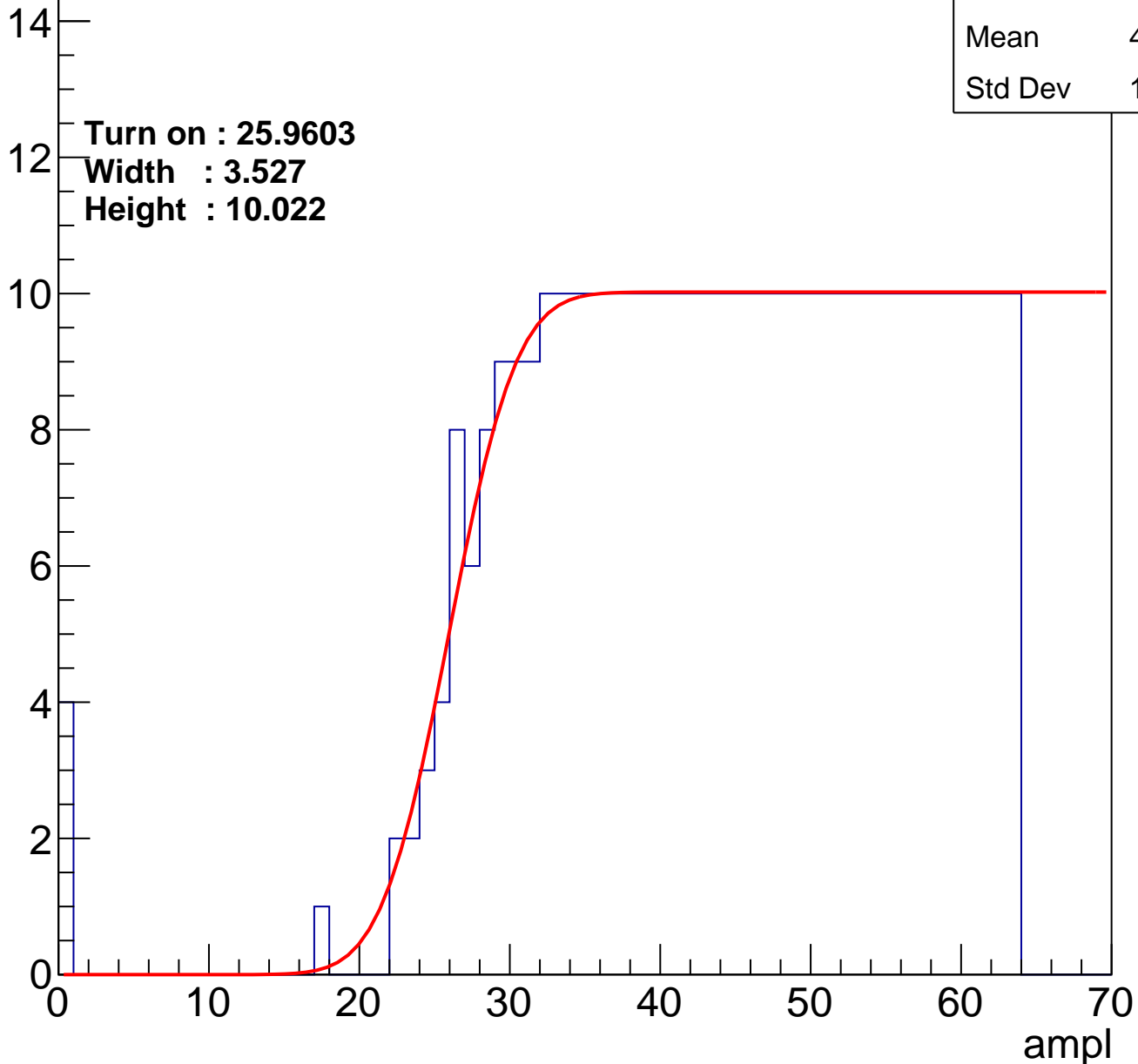
Entries	385
Mean	43.85
Std Dev	12.04

Turn on : 25.9603

Width : 3.527

Height : 10.022

Entry



B1L002S, U4-ch120

calib_packv5_042523_0143.root, FC#10, port B3

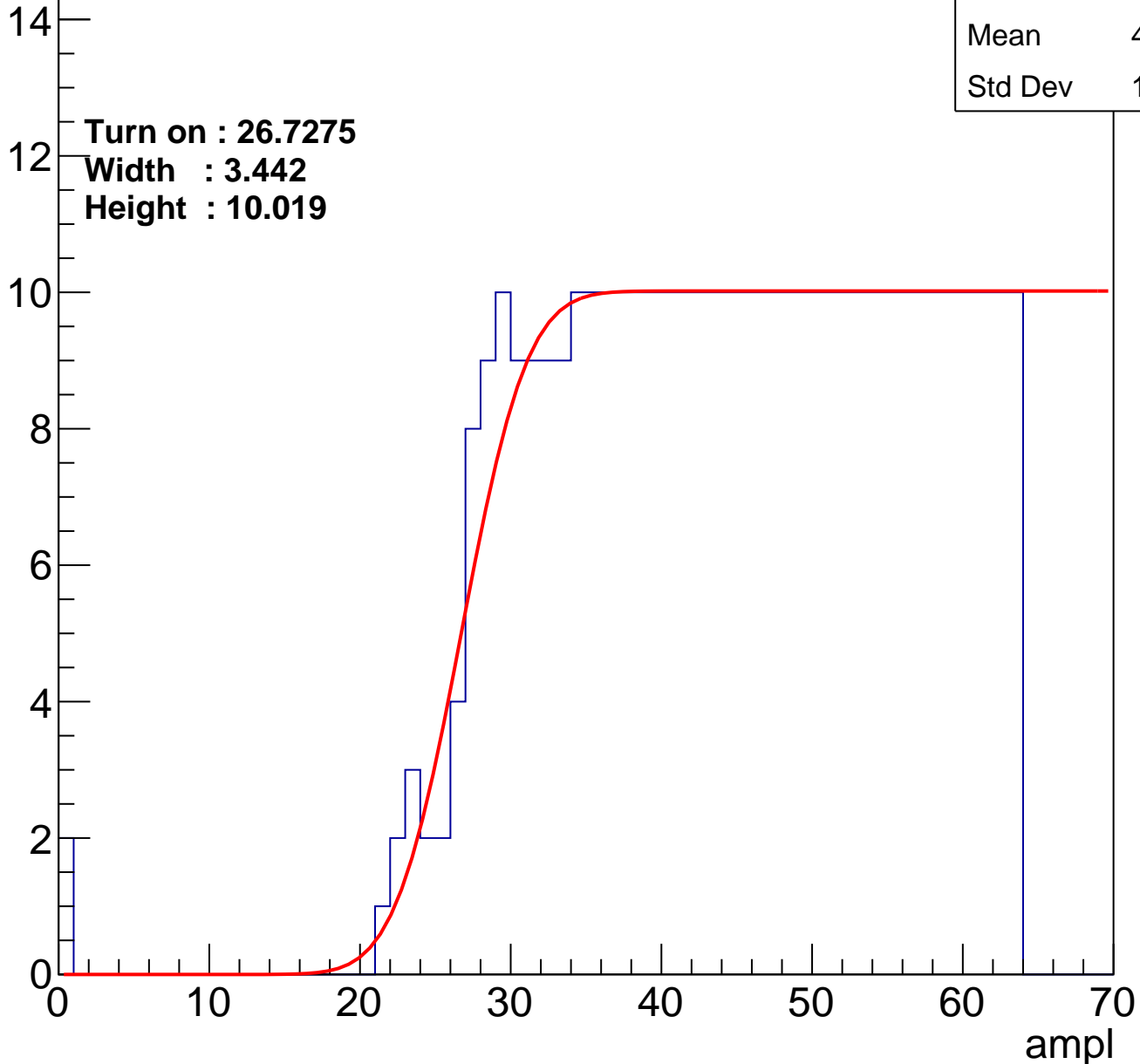
Entries	379
Mean	44.27
Std Dev	11.55

Turn on : 26.7275

Width : 3.442

Height : 10.019

Entry



B1L002S, U4-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.79
Std Dev	11.8

Turn on : 25.6390

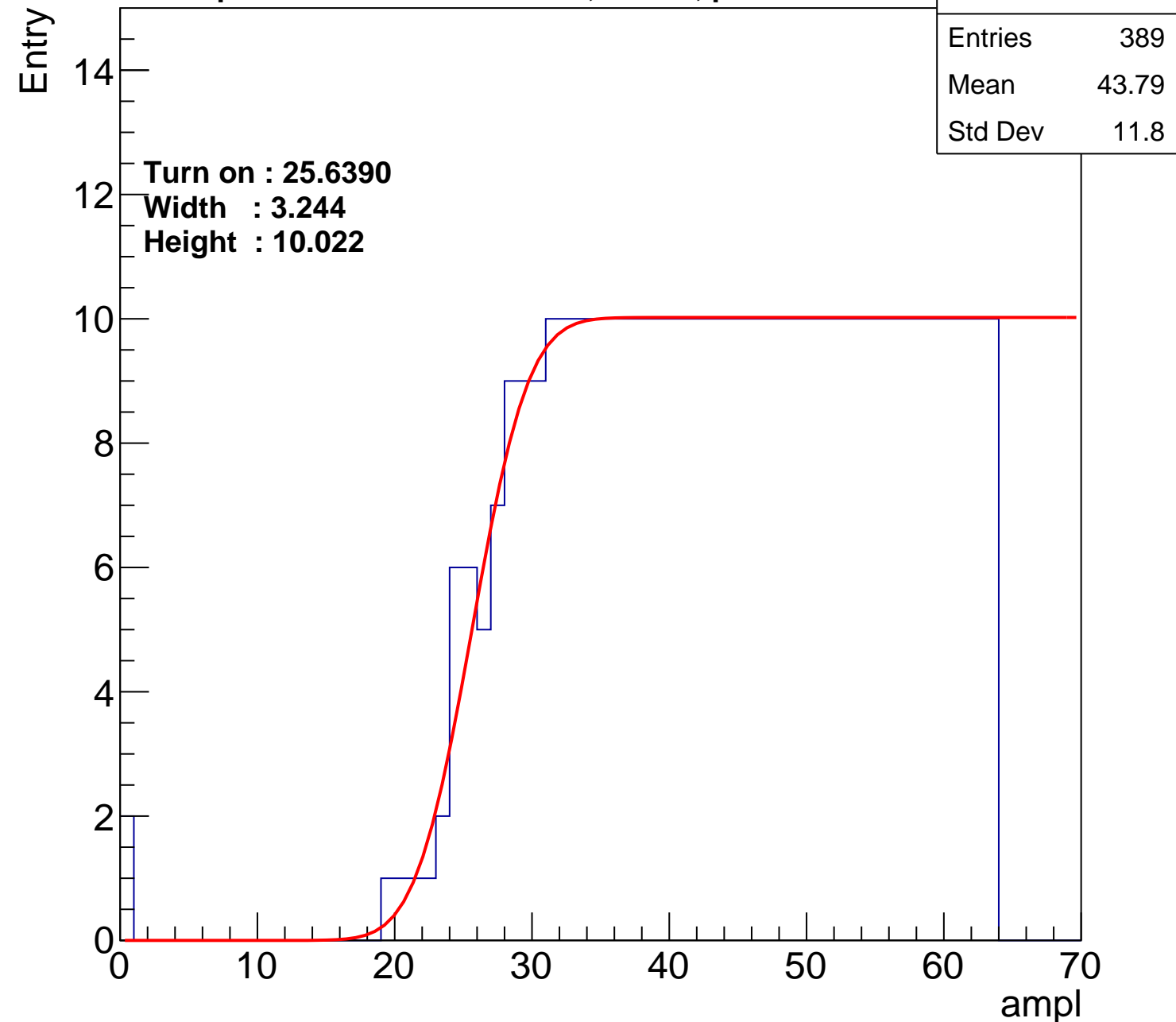
Width : 3.244

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.08
Std Dev	11.48

Turn on : 25.3034

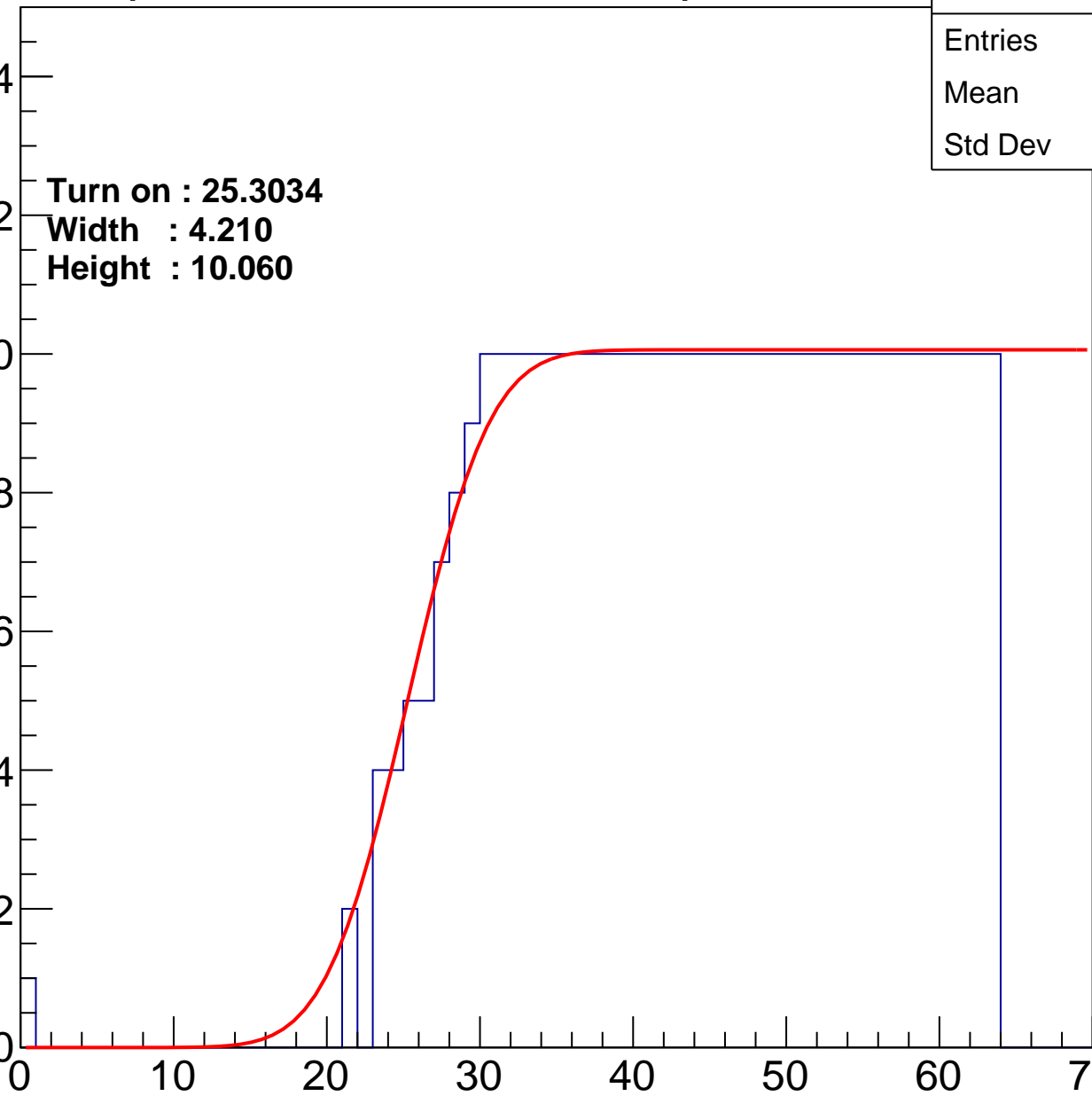
Width : 4.210

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 26.1824

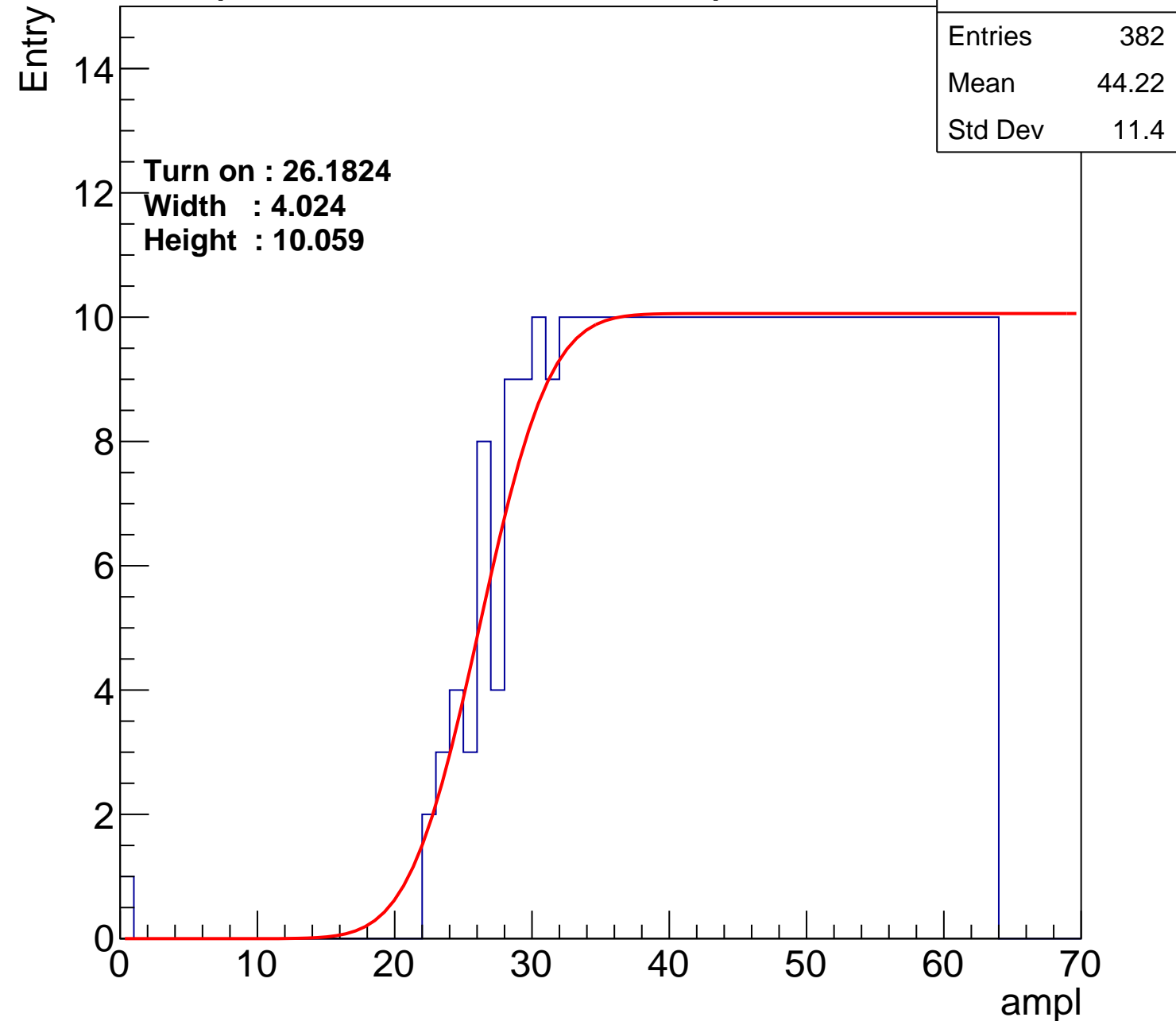
Width : 4.024

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.47
Std Dev	12.28

Turn on : 25.3369

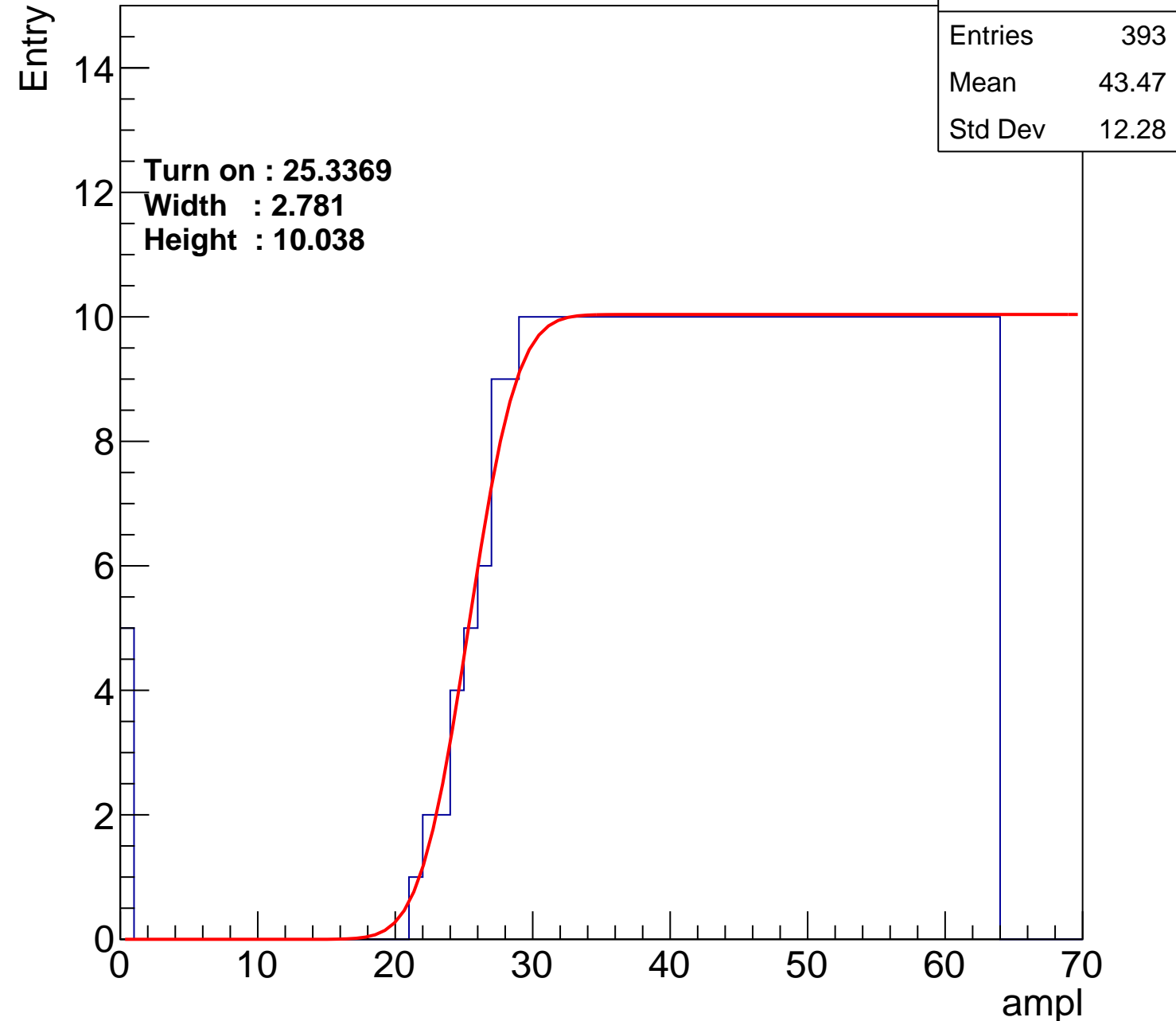
Width : 2.781

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch125

calib_packv5_042523_0143.root, FC#10, port B3

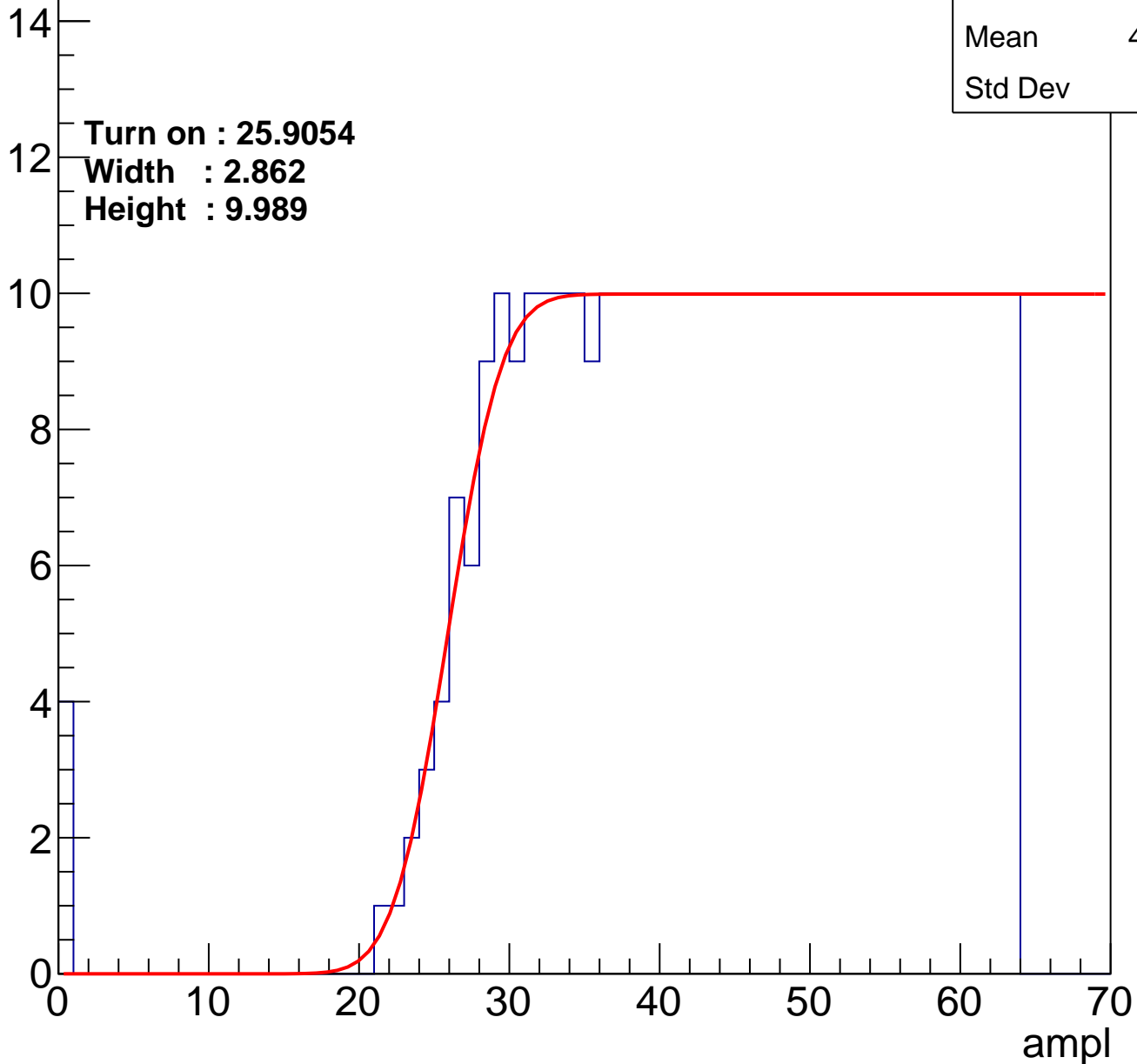
Entries	385
Mean	43.88
Std Dev	12

Turn on : 25.9054

Width : 2.862

Height : 9.989

Entry



B1L002S, U4-ch126

calib_packv5_042523_0143.root, FC#10, port B3

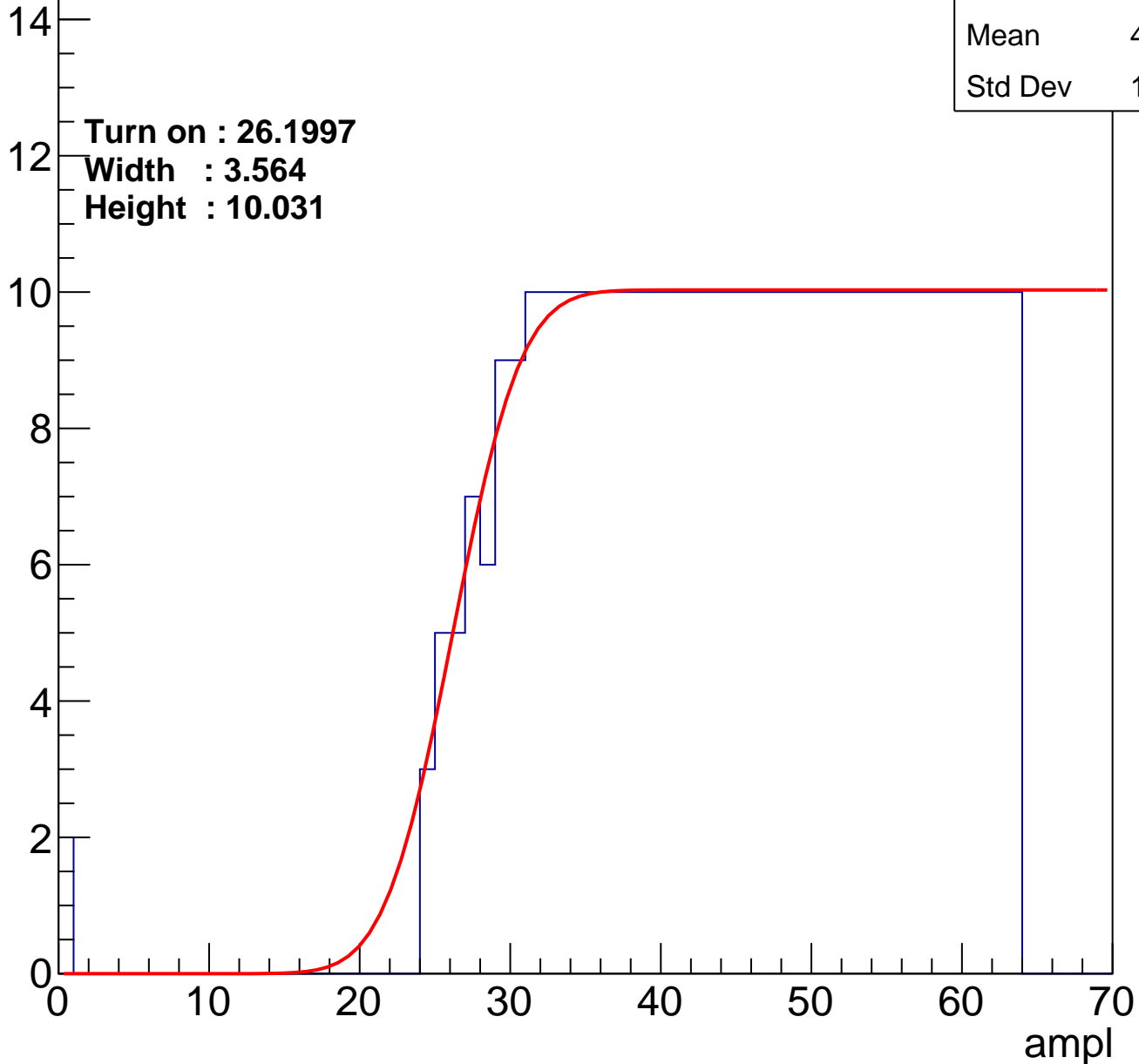
Entries	376
Mean	44.48
Std Dev	11.37

Turn on : 26.1997

Width : 3.564

Height : 10.031

Entry



B1L002S, U4-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.6087

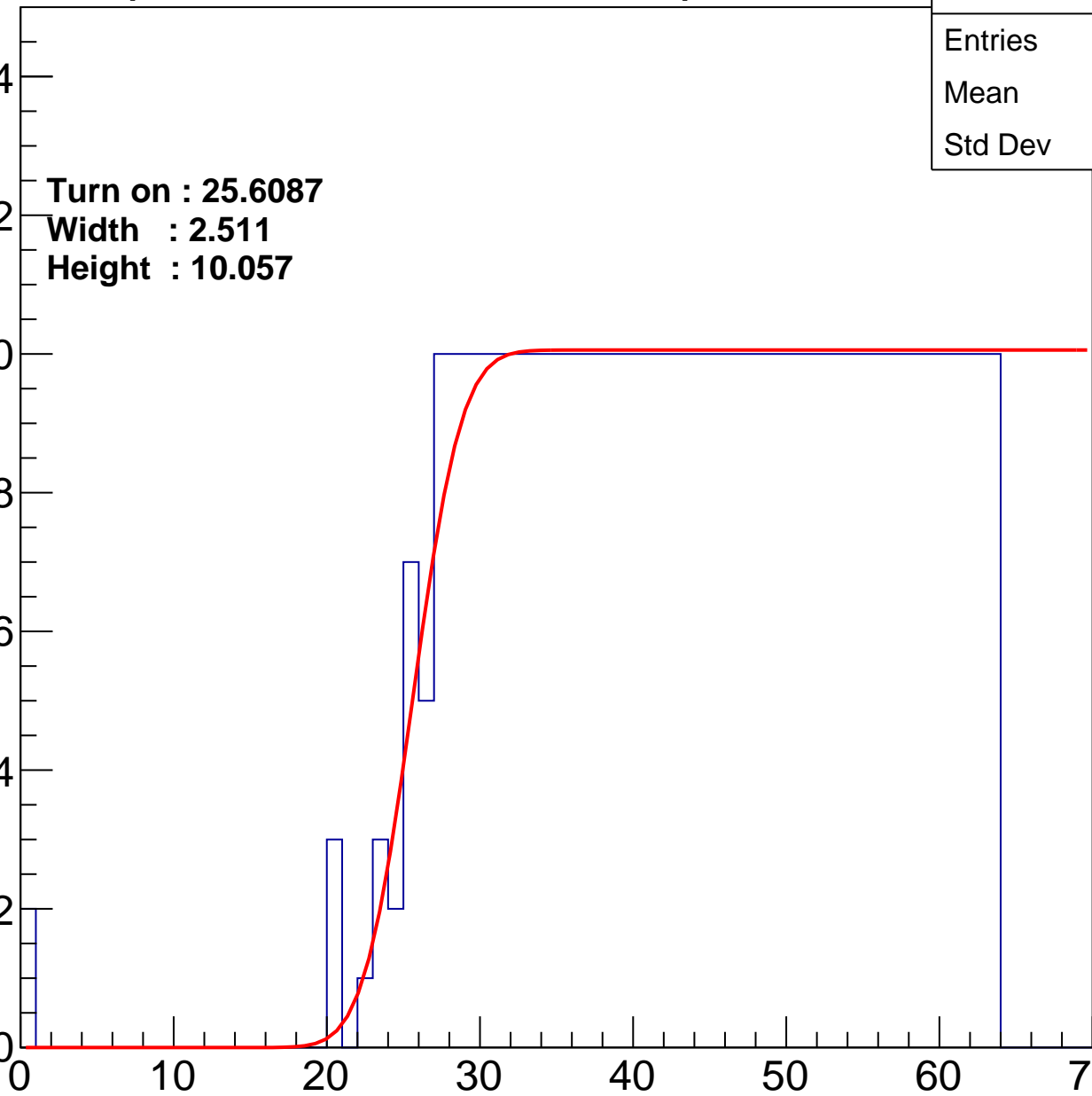
Width : 2.511

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U4-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.6087

Width : 2.511

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

