



# B0L103S, U25-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	147
Mean	54.91
Std Dev	9.109

Turn on : 49.6771

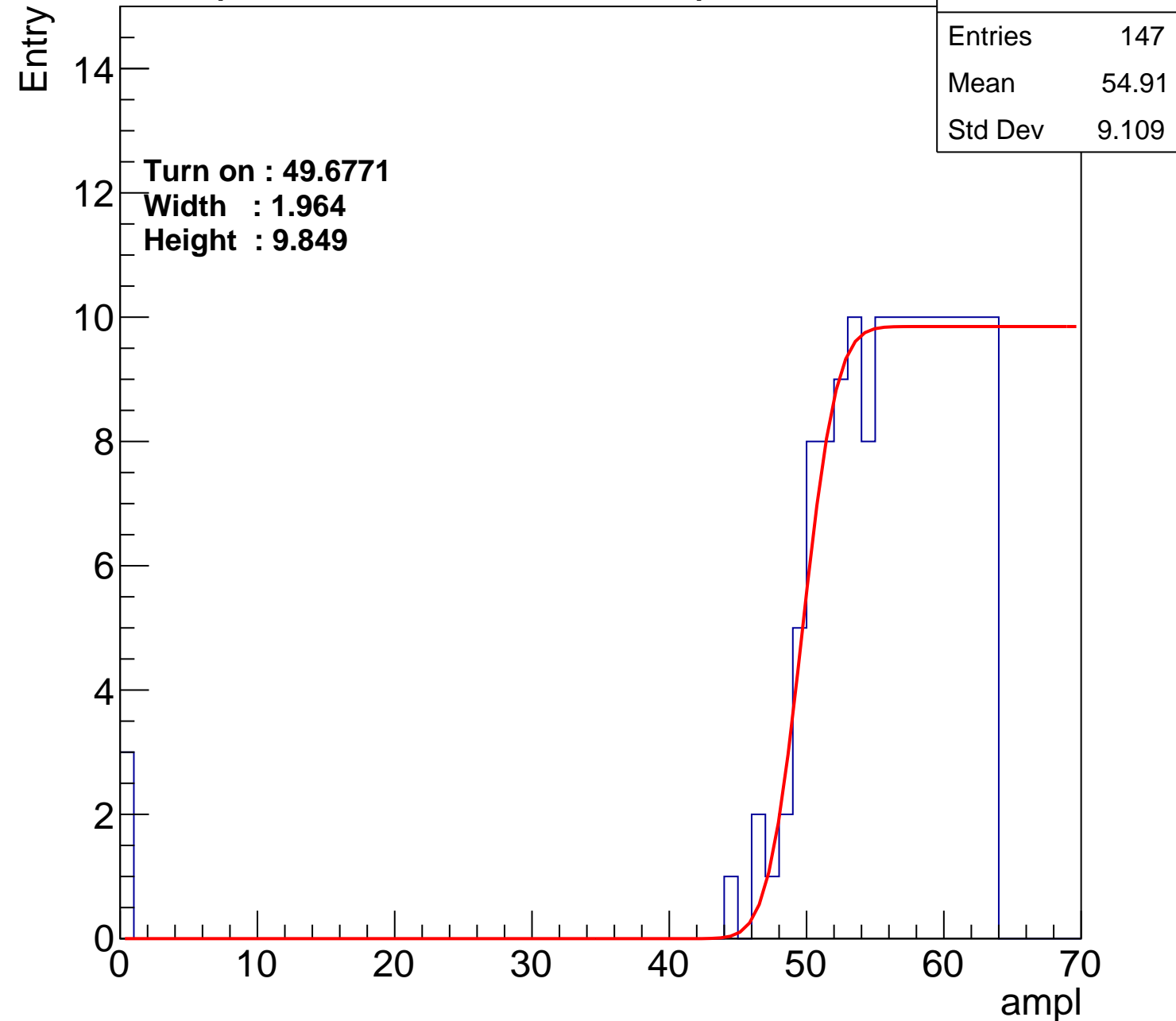
Width : 1.964

Height : 9.849

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch1

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	115
Mean	56.57
Std Dev	8.37

Turn on : 52.9071

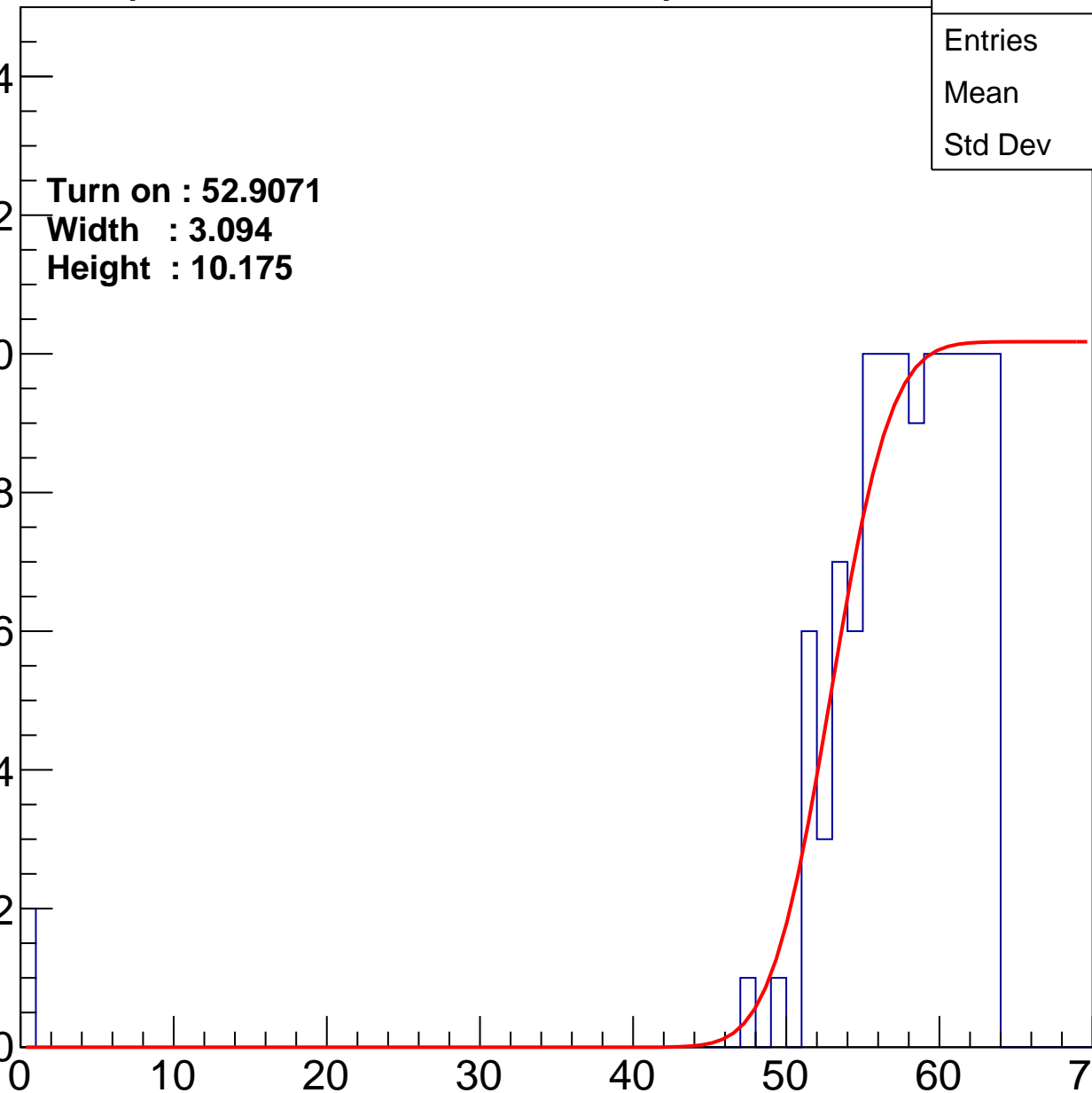
Width : 3.094

Height : 10.175

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	54.51
Std Dev	9.015

Turn on : 49.1333

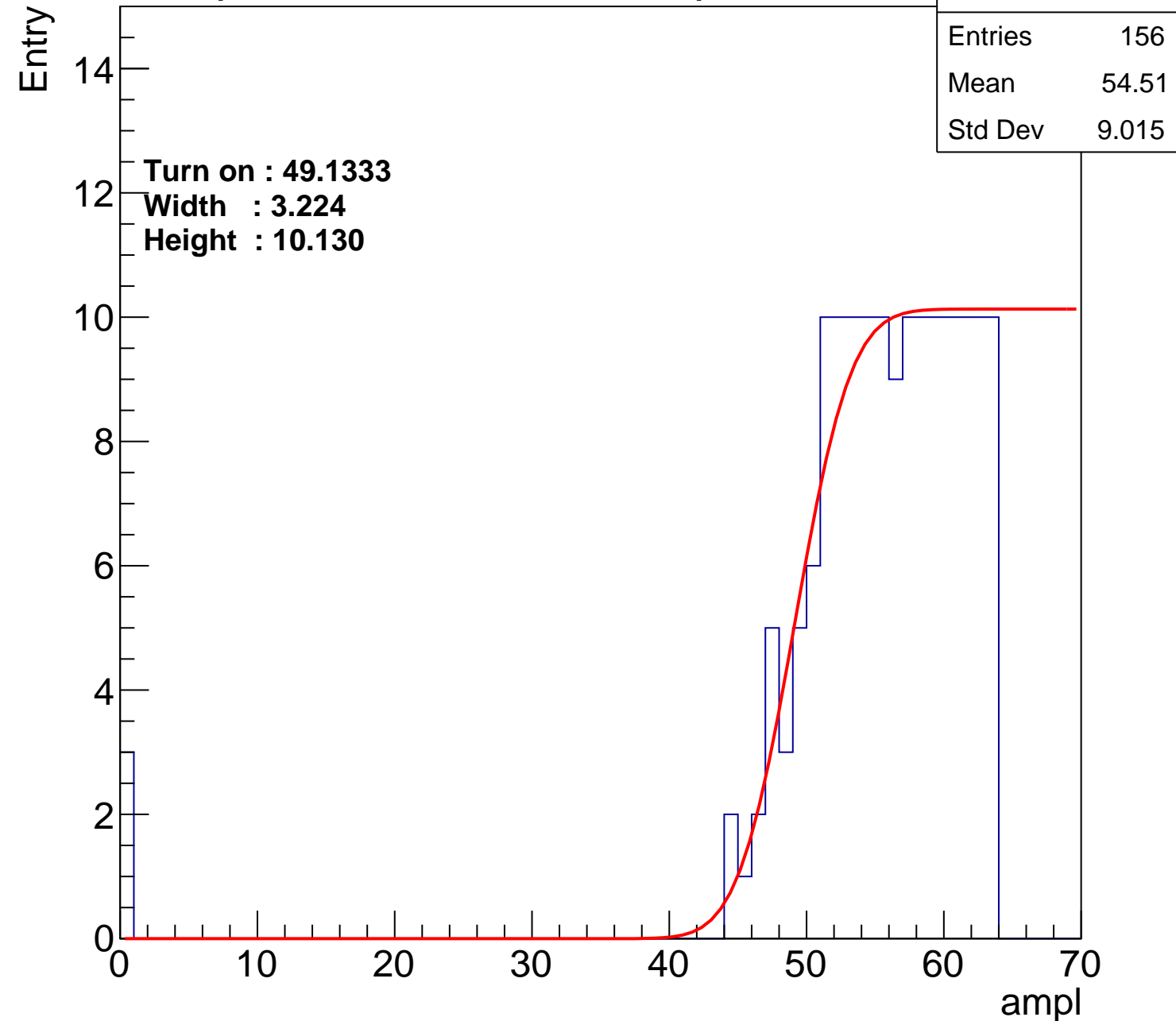
Width : 3.224

Height : 10.130

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	54.7
Std Dev	10.27

**Turn on : 50.2307**

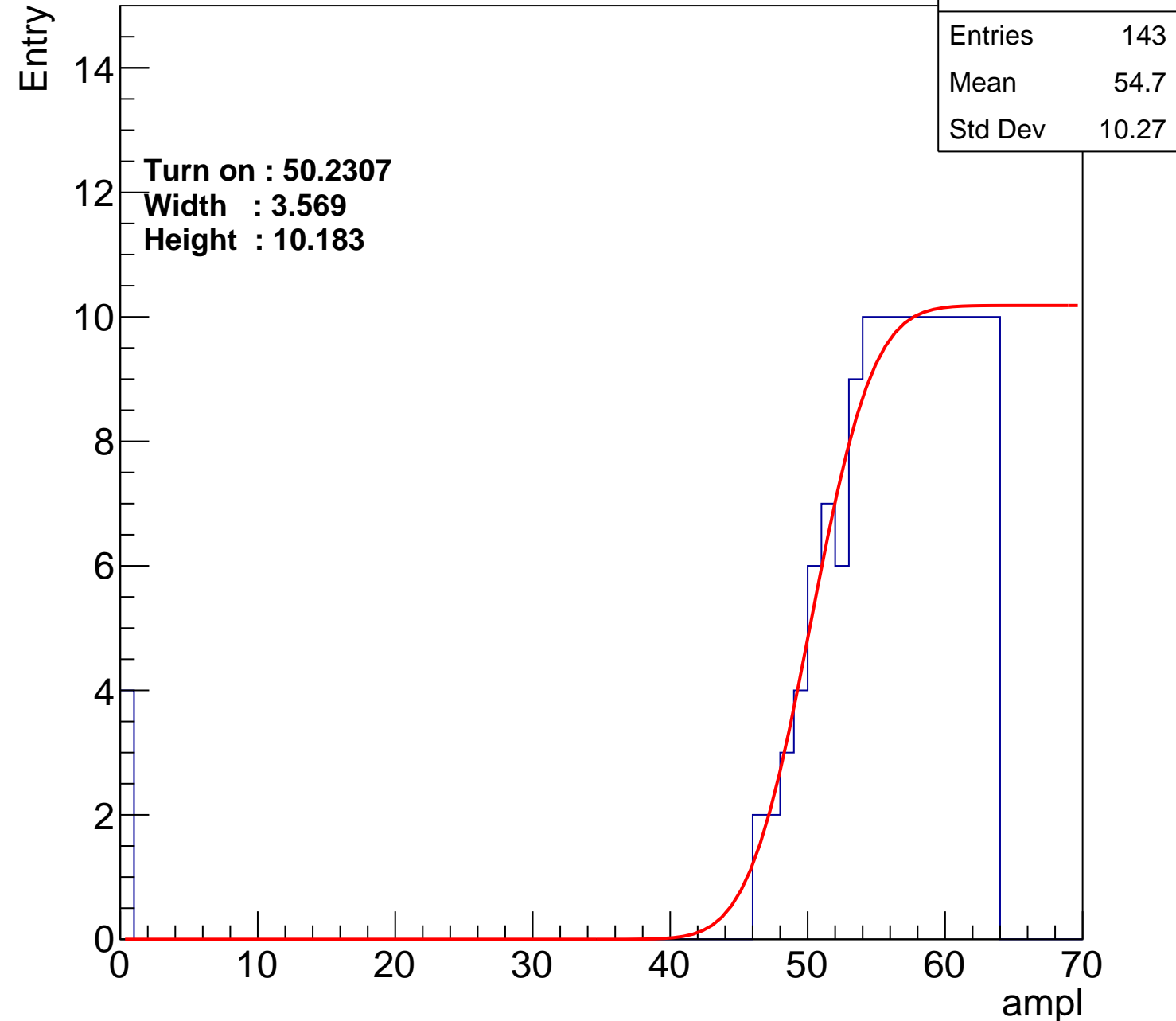
**Width : 3.569**

**Height : 10.183**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch4

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.74
Std Dev	8.094

Turn on : 51.1723

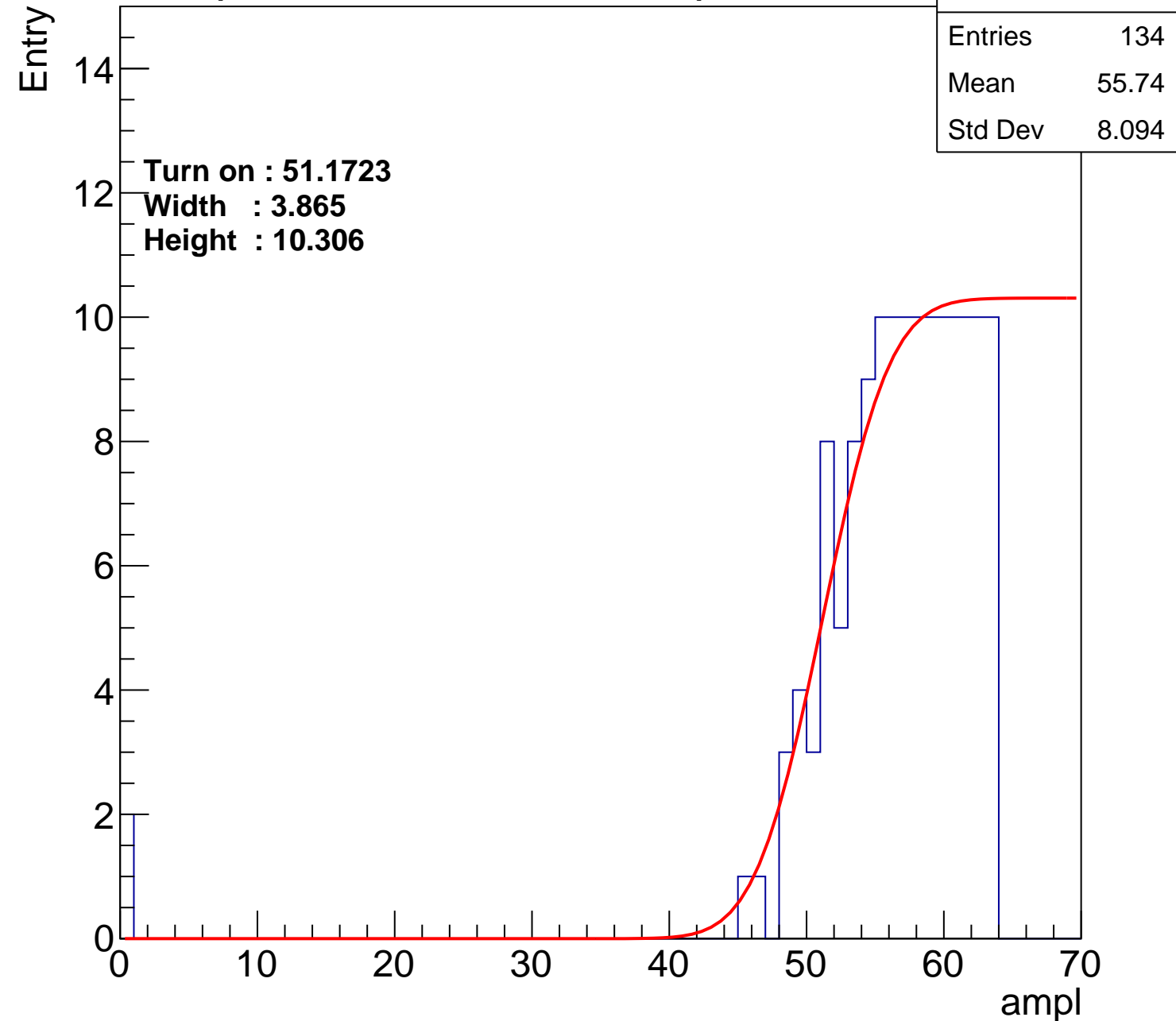
Width : 3.865

Height : 10.306

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch5

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	55.49
Std Dev	7.953

Turn on : 50.0871

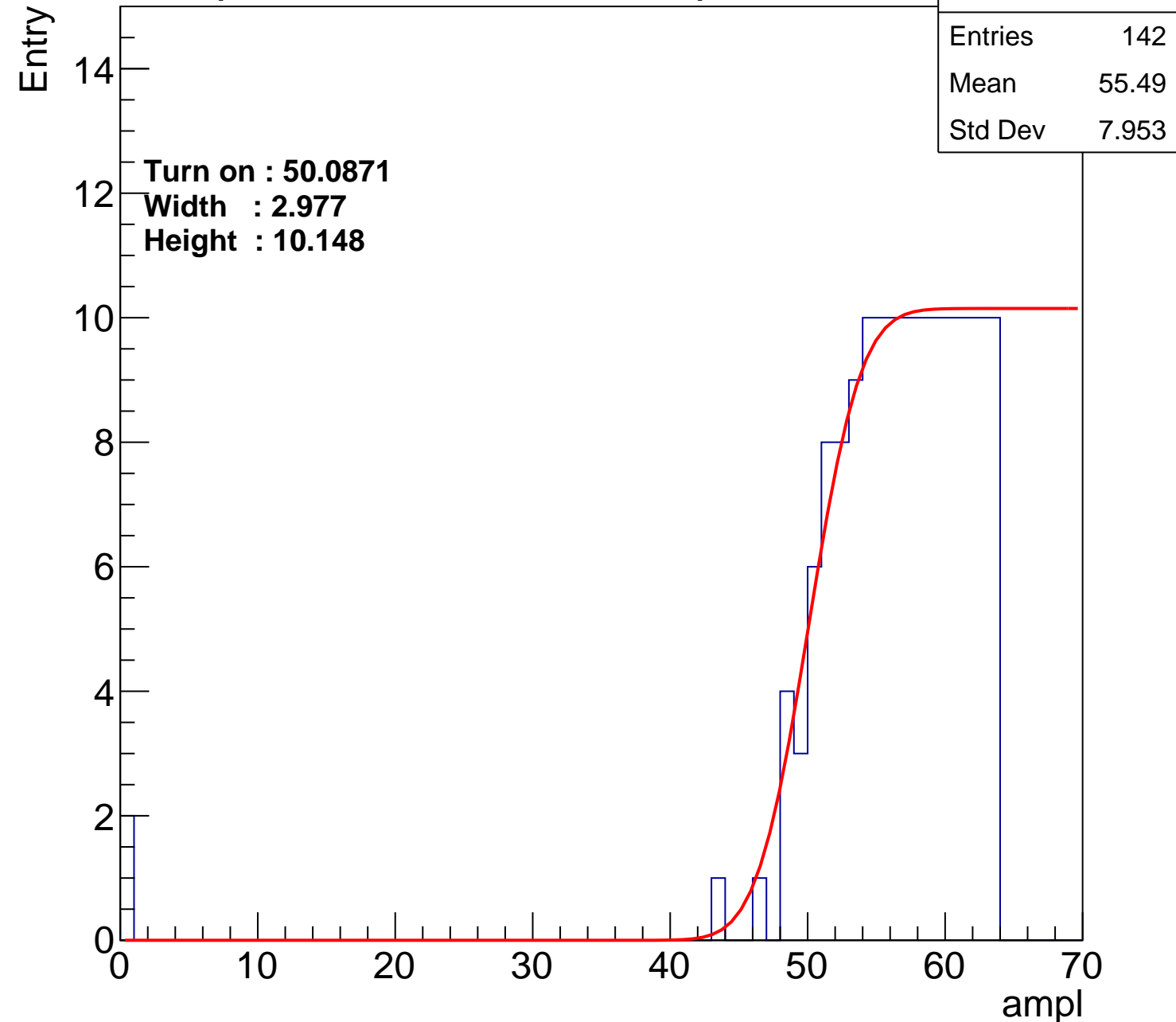
Width : 2.977

Height : 10.148

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.85
Std Dev	9.678

Turn on : 52.8234

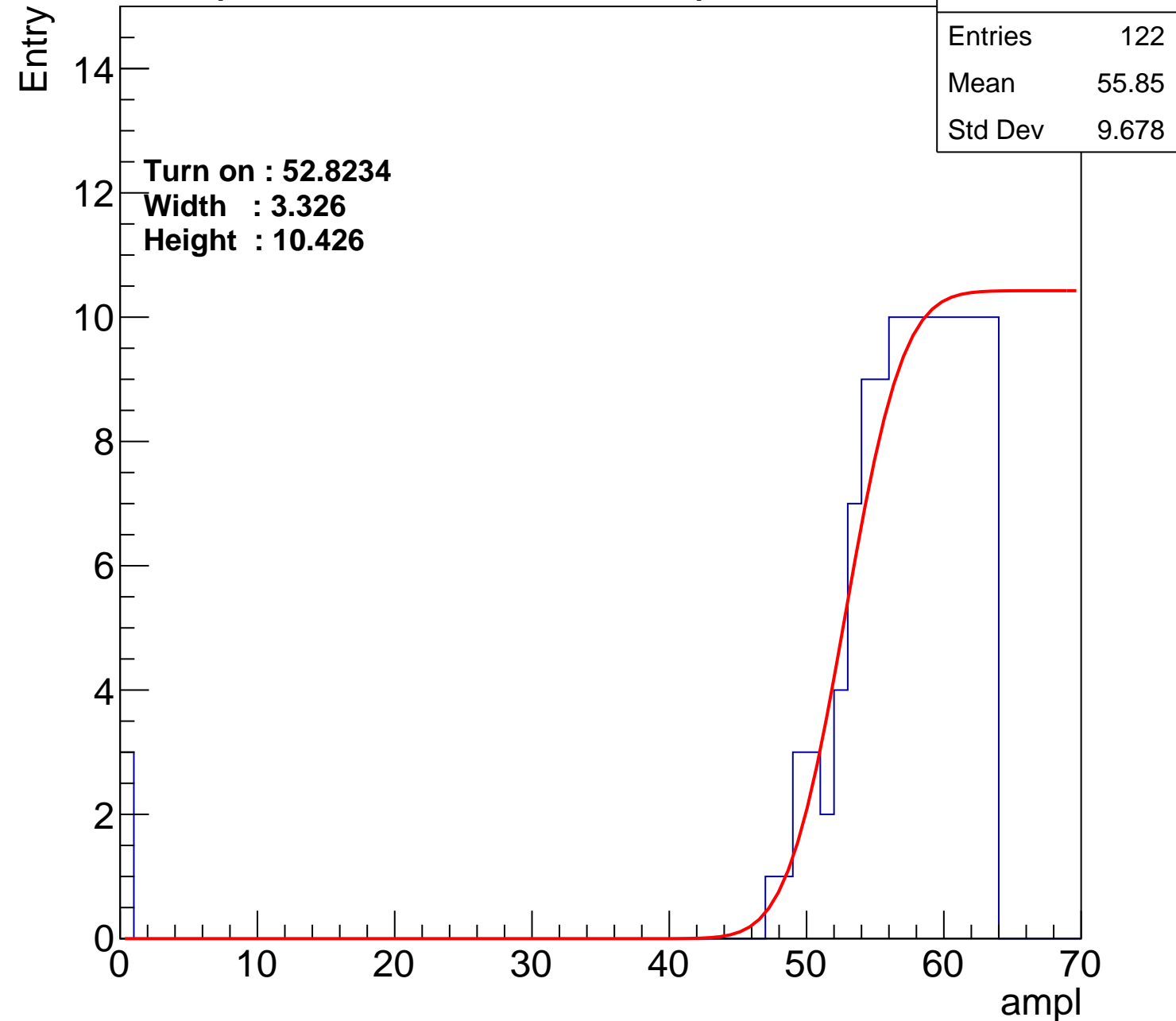
Width : 3.326

Height : 10.426

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

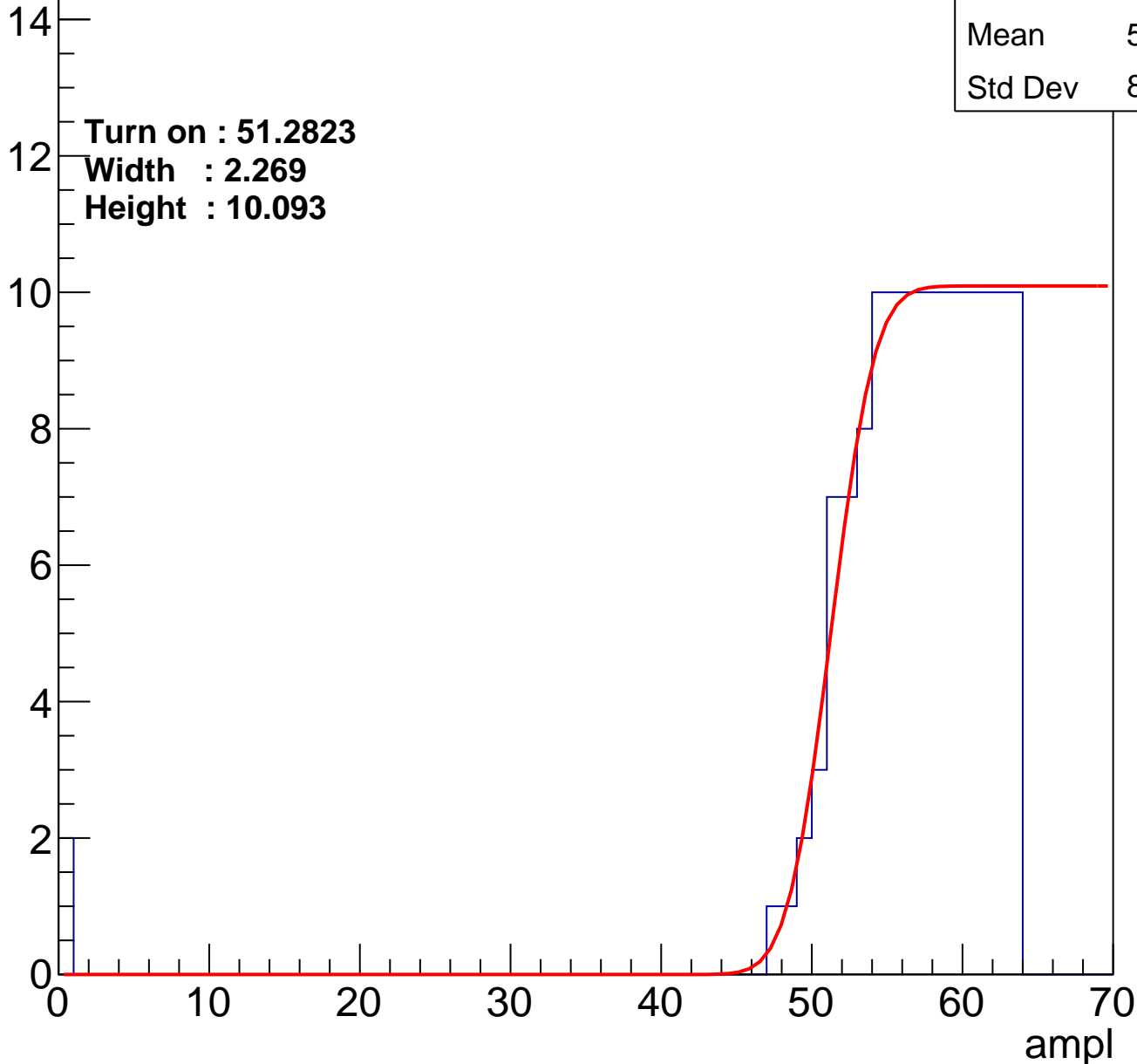
Entry

Entries	131
Mean	56.02
Std Dev	8.023

Turn on : 51.2823

Width : 2.269

Height : 10.093



# B0L103S, U25-ch8

calib\_packv5\_040323\_1717.root, FC#2, port C3

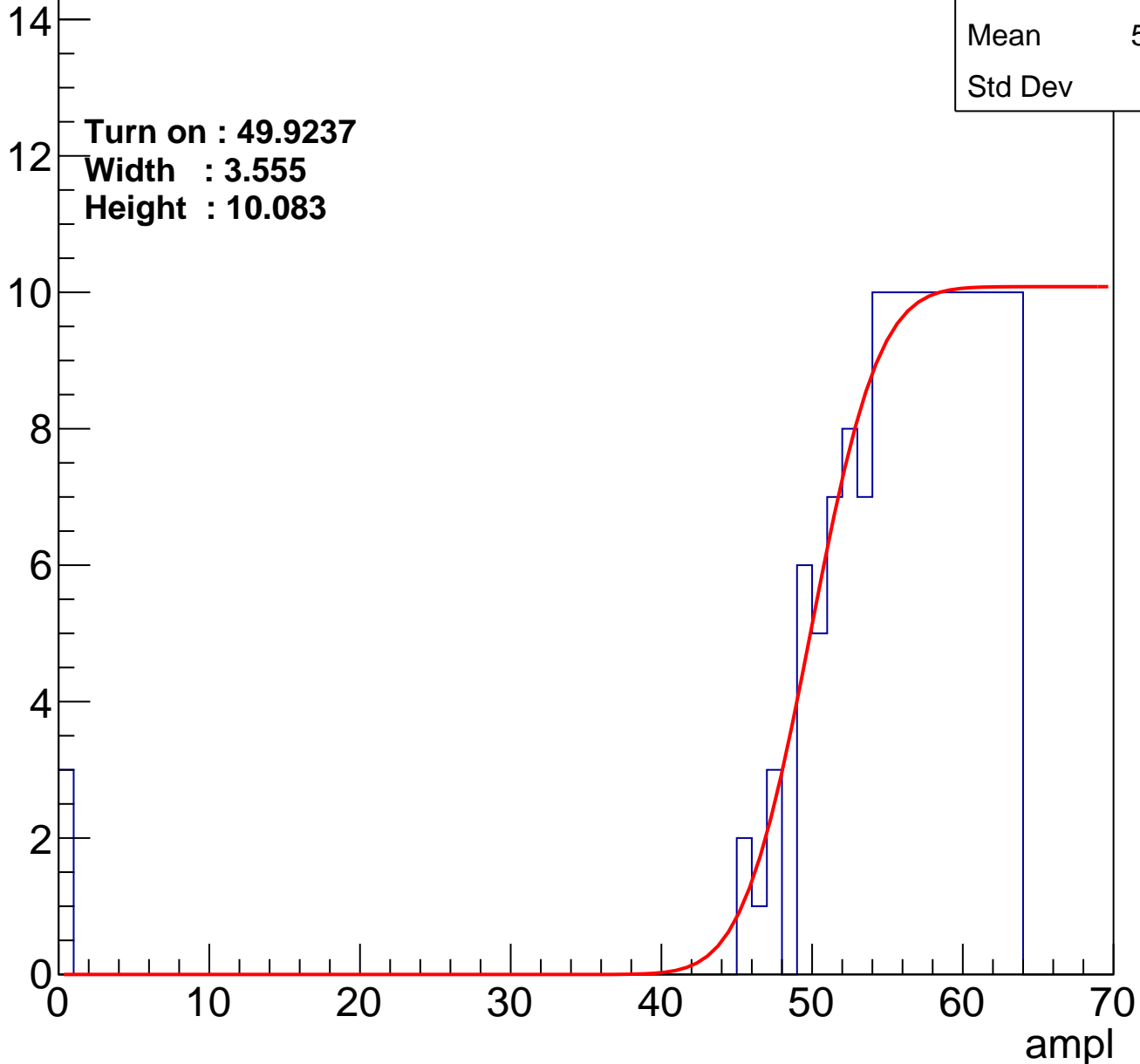
Entries	142
Mean	55.04
Std Dev	9.25

Turn on : 49.9237

Width : 3.555

Height : 10.083

Entry



# B0L103S, U25-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.68
Std Dev	8.466

Turn on : 52.6190

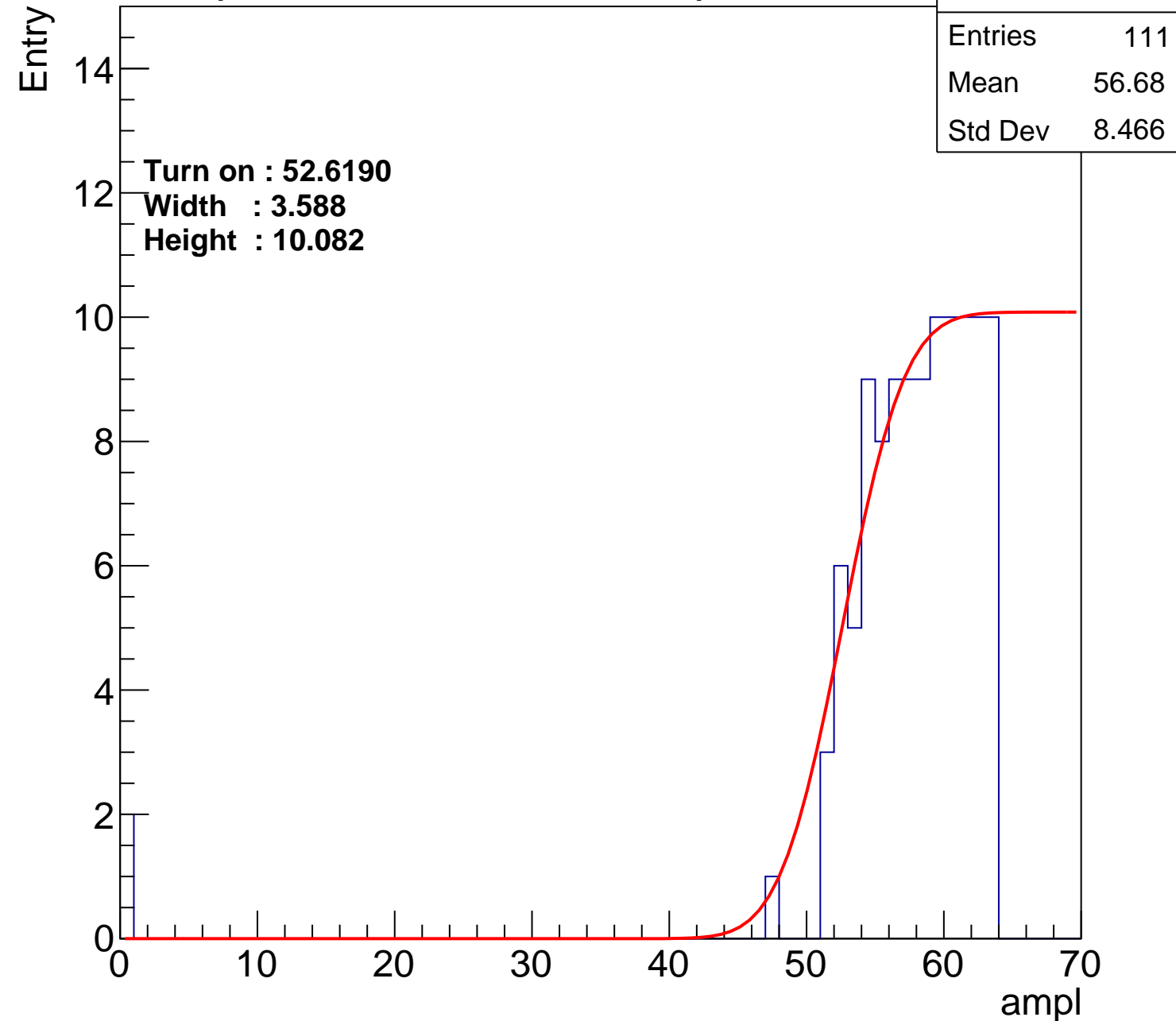
Width : 3.588

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.52
Std Dev	11.04

Turn on : 52.6716

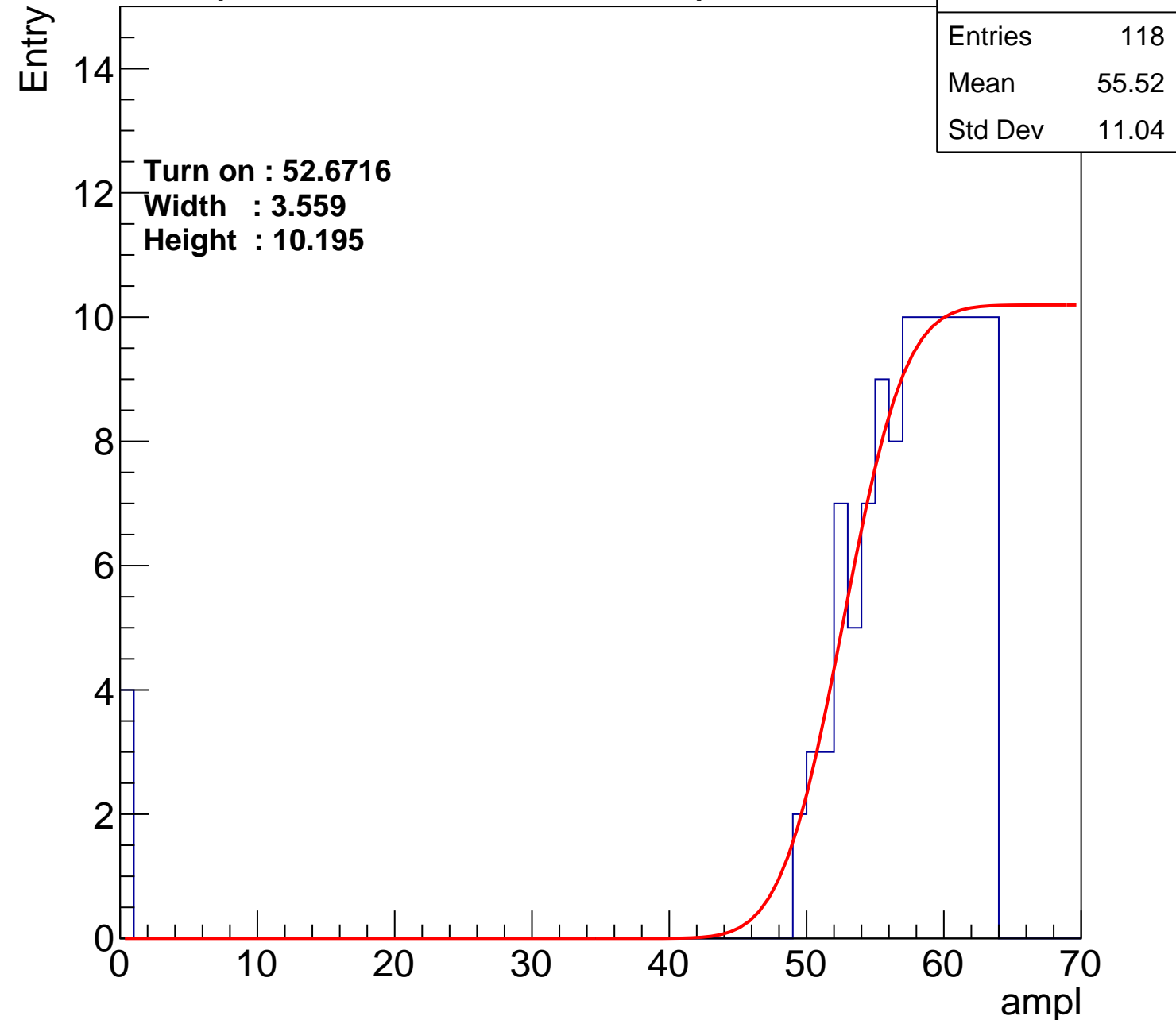
Width : 3.559

Height : 10.195

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	56.65
Std Dev	6.307

Turn on : 50.7989

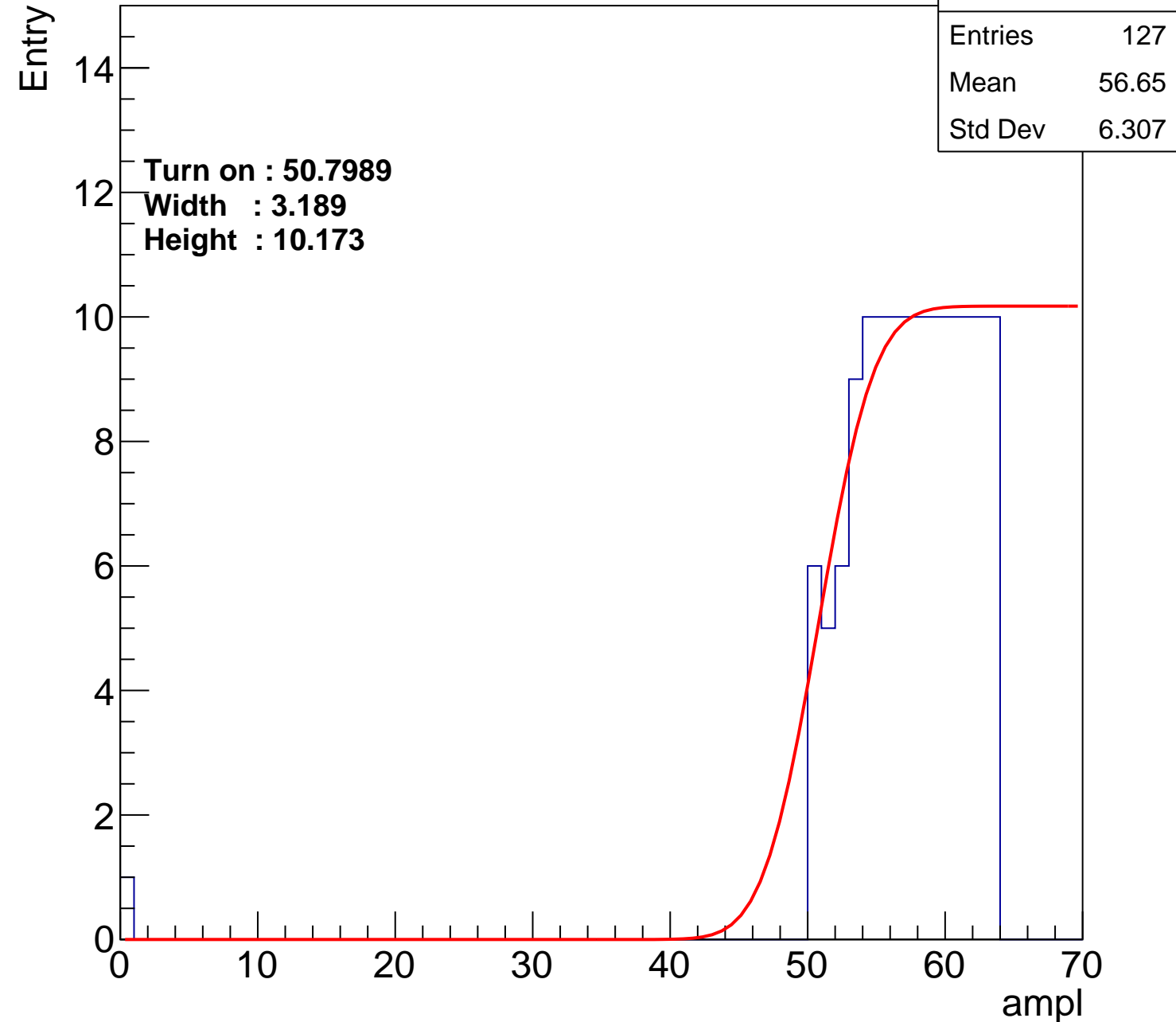
Width : 3.189

Height : 10.173

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

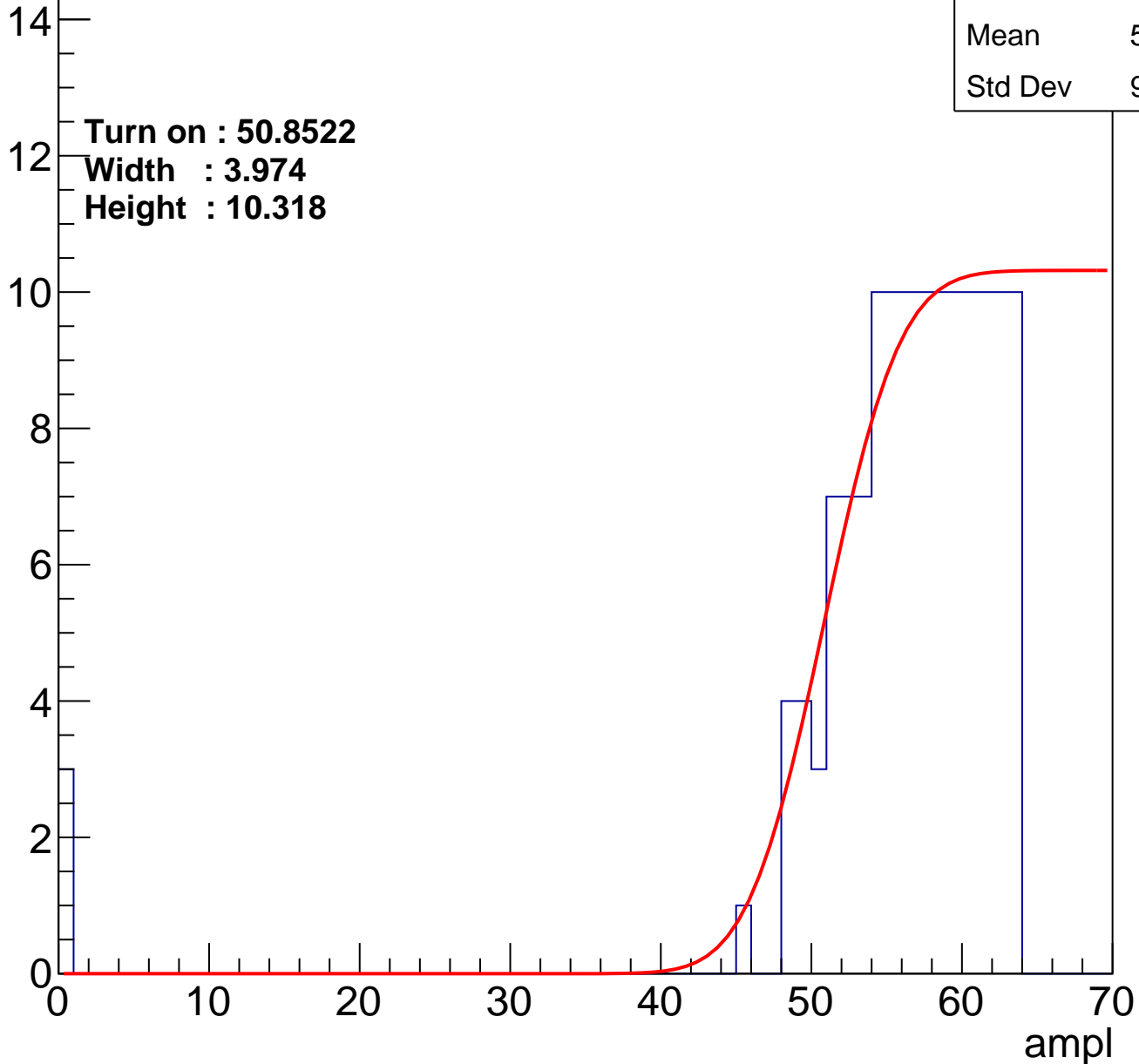
Entries	136
Mean	55.33
Std Dev	9.326

Turn on : 50.8522

Width : 3.974

Height : 10.318

Entry



# B0L103S, U25-ch13

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	57.24
Std Dev	6.545

Turn on : 52.8173

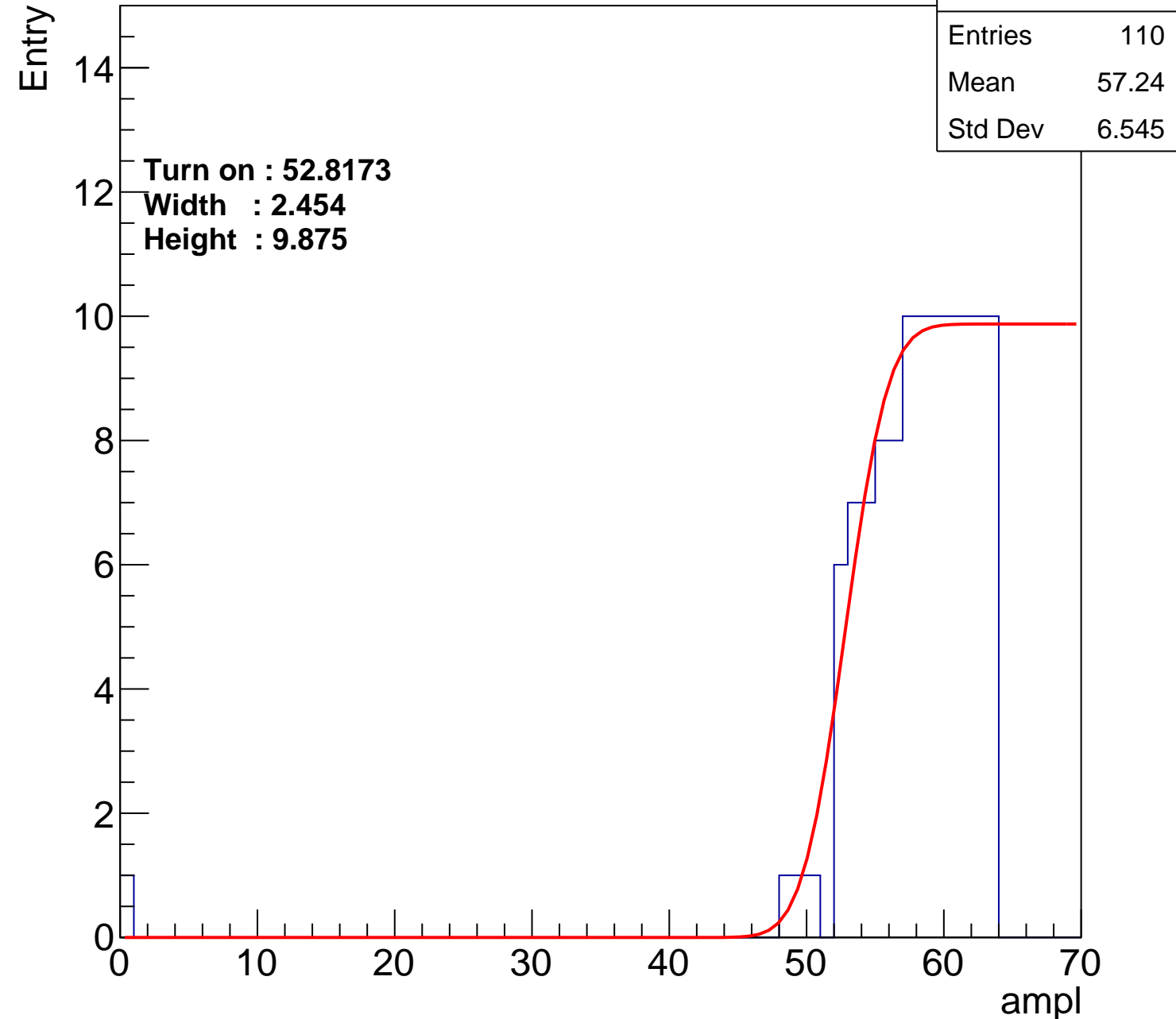
Width : 2.454

Height : 9.875

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch14

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.16
Std Dev	10.58

Turn on : 51.8966

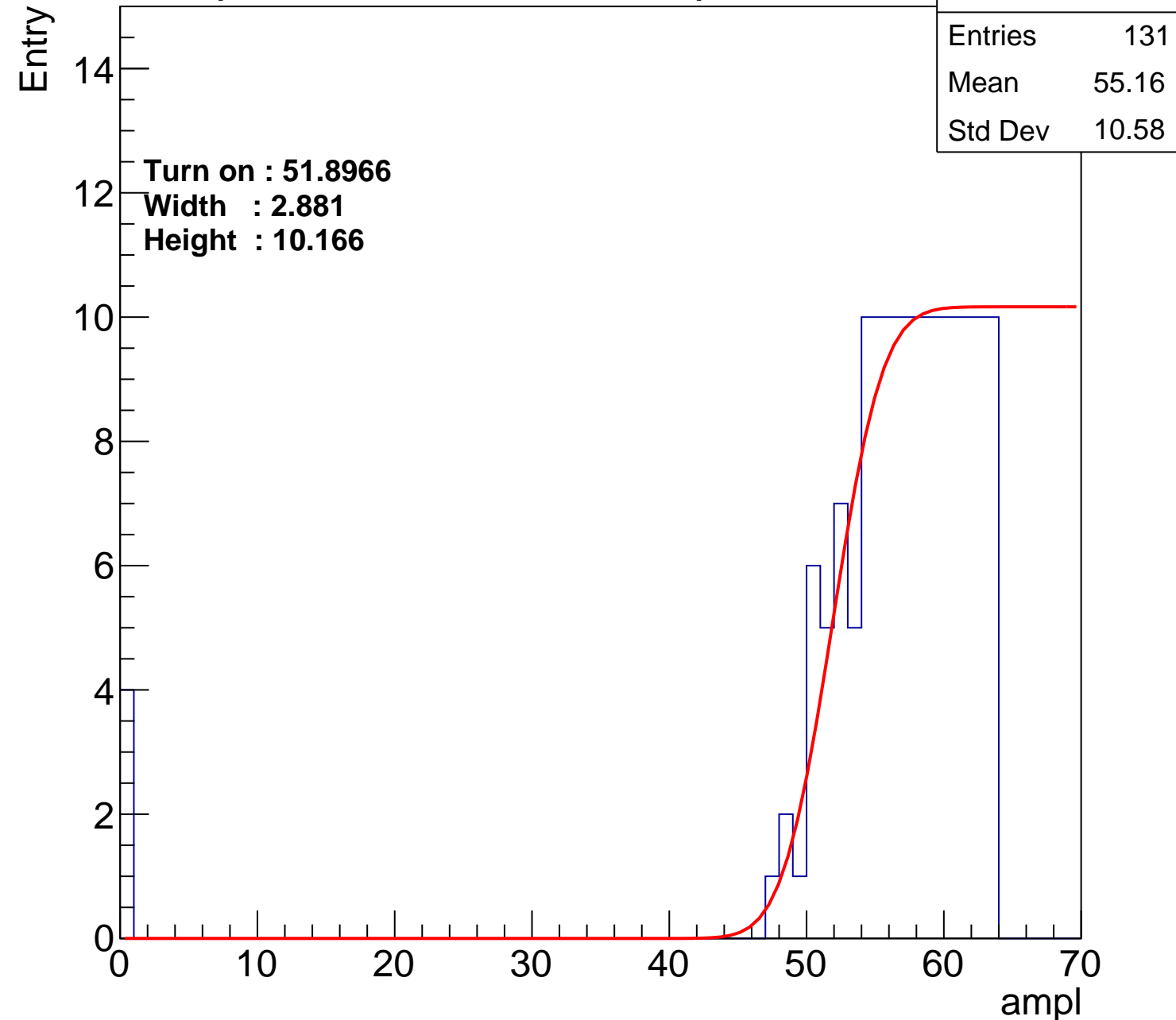
Width : 2.881

Height : 10.166

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch15

calib\_packv5\_040323\_1717.root, FC#2, port C3

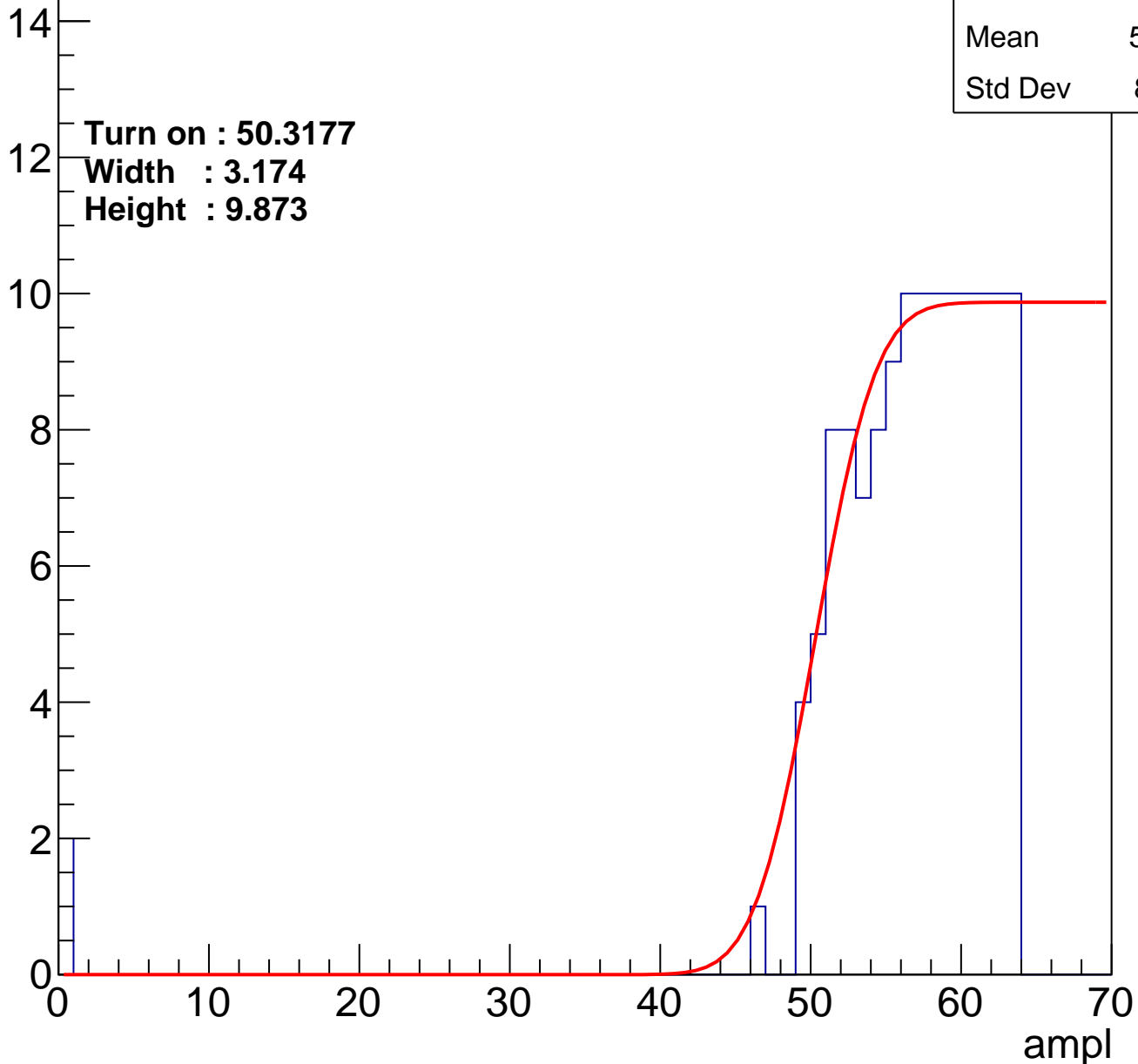
Entries	132
Mean	55.86
Std Dev	8.061

Turn on : 50.3177

Width : 3.174

Height : 9.873

Entry



# B0L103S, U25-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

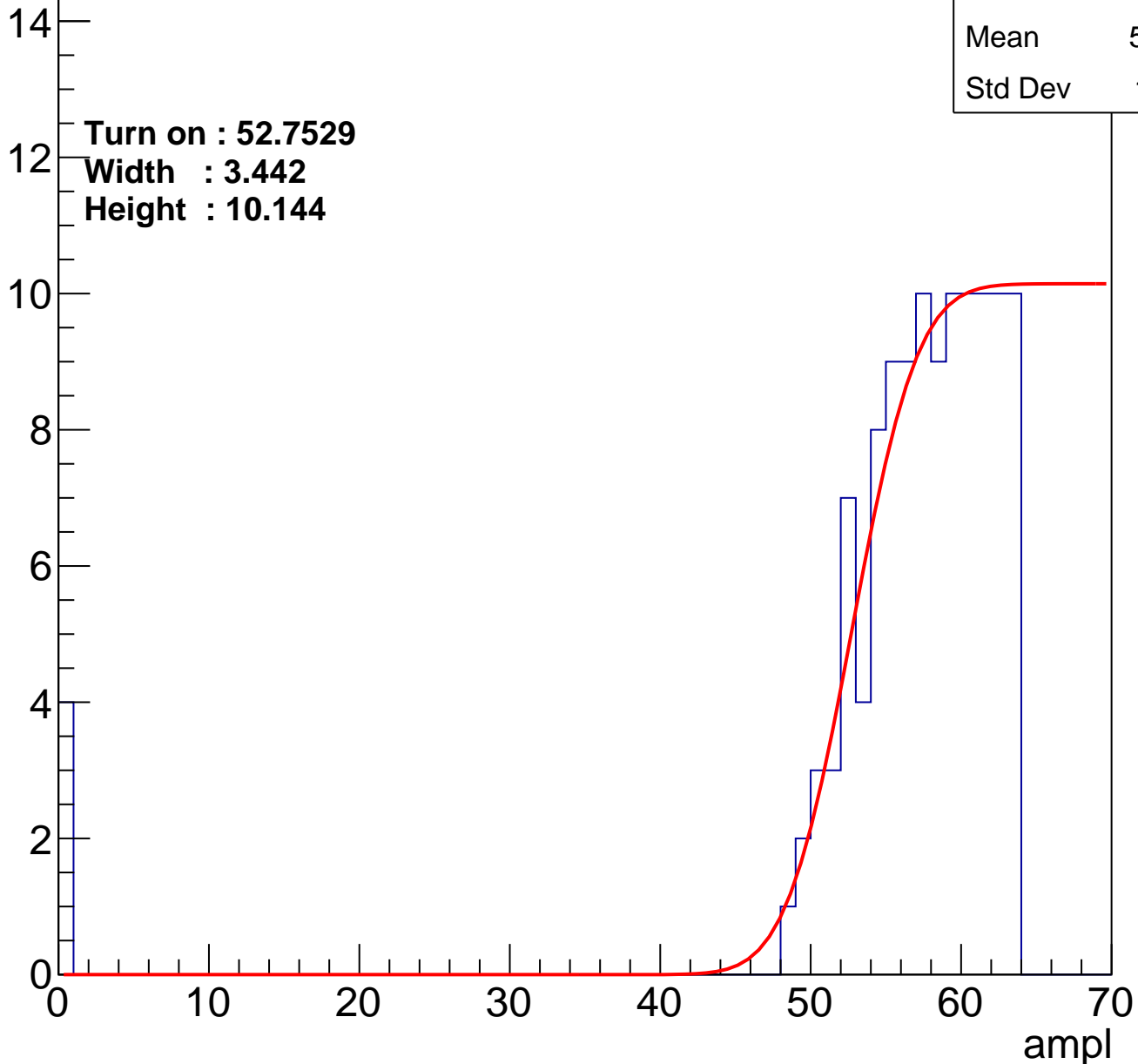
Entries	119
Mean	55.45
Std Dev	11.01

Turn on : 52.7529

Width : 3.442

Height : 10.144

Entry



# B0L103S, U25-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.89
Std Dev	9.664

Turn on : 51.3078

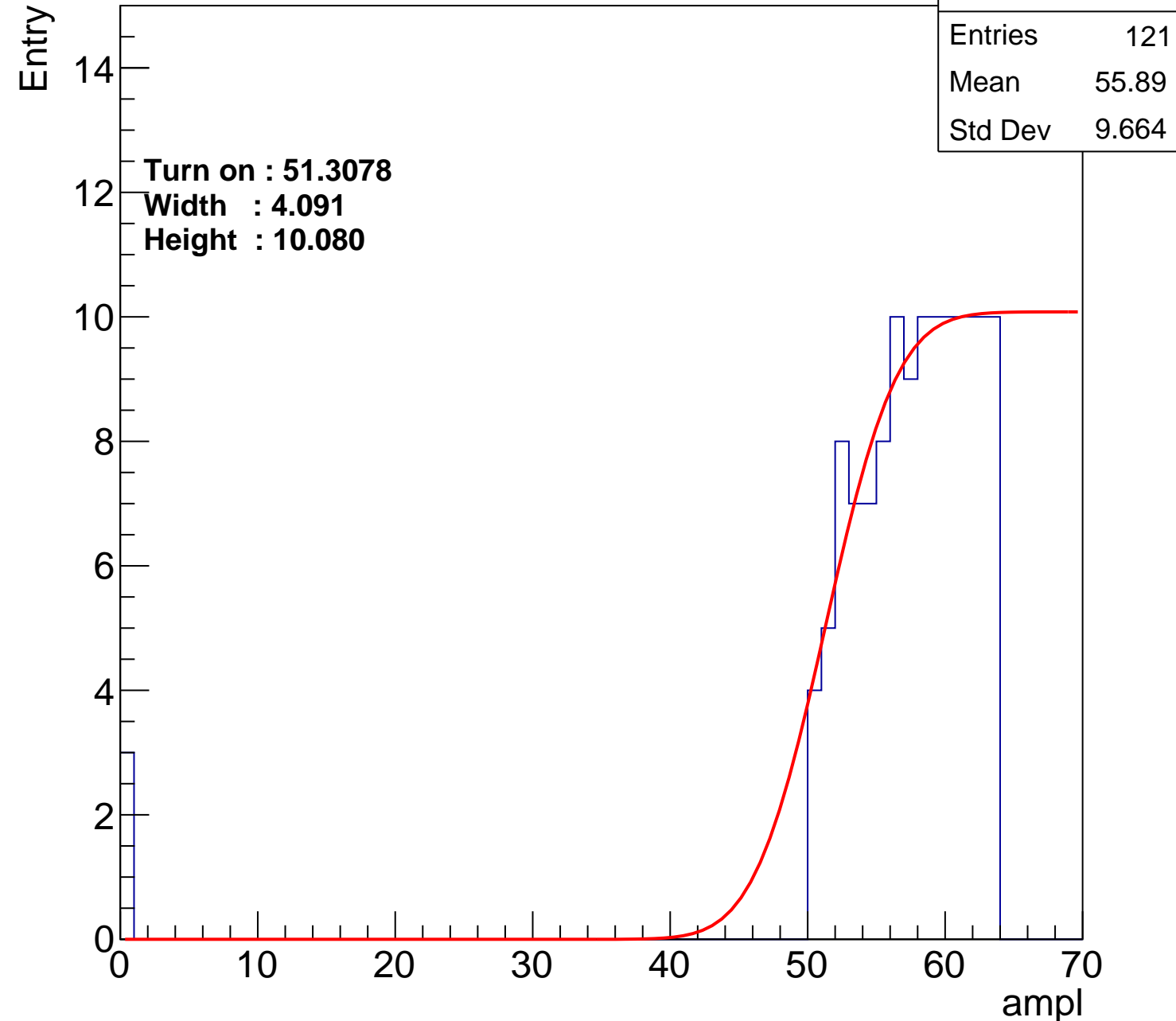
Width : 4.091

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch18

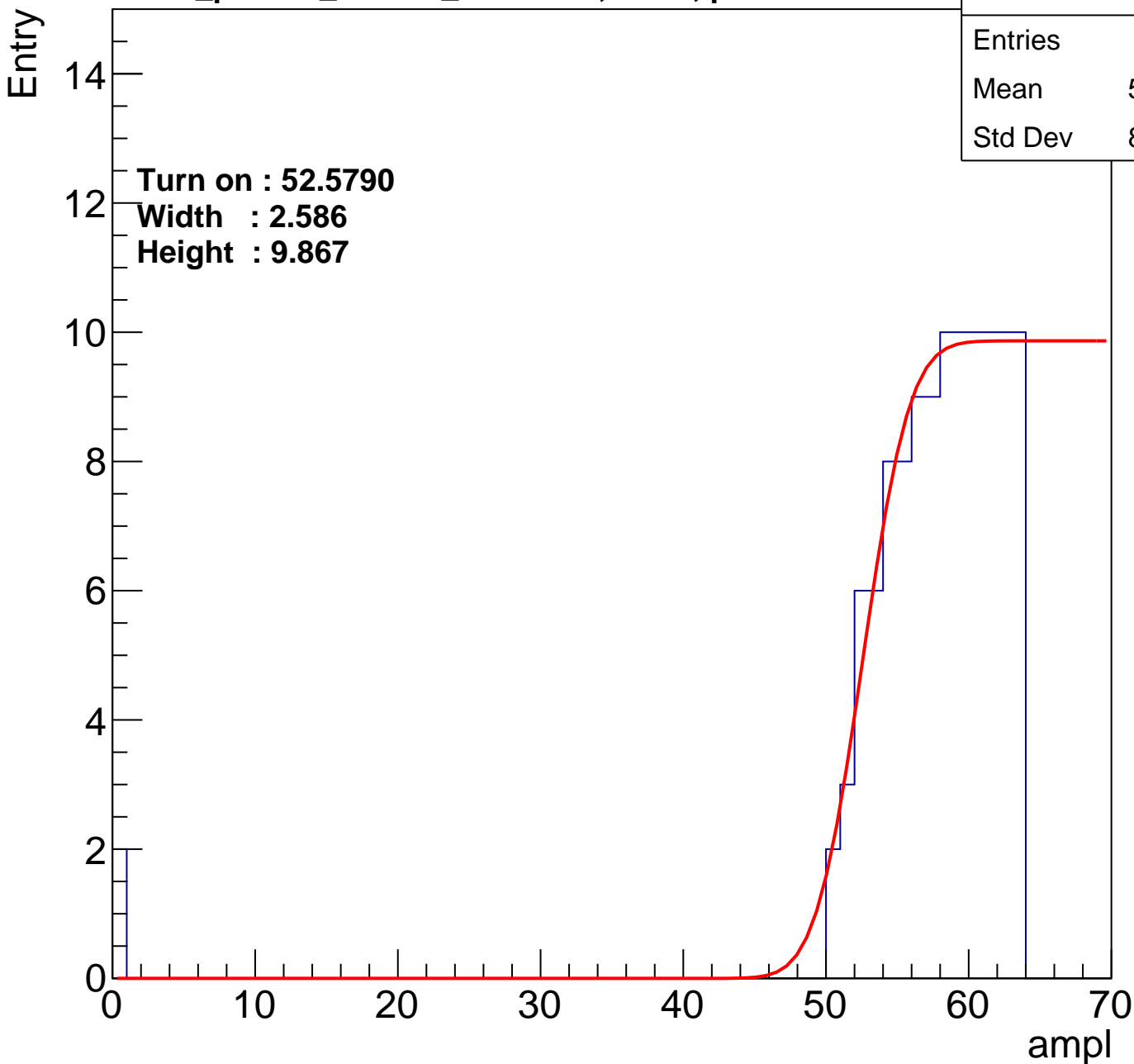
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	113
Mean	56.65
Std Dev	8.393

**Turn on : 52.5790**

**Width : 2.586**

**Height : 9.867**



# B0L103S, U25-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	115
Mean	55.17
Std Dev	12.29

Turn on : 52.9220

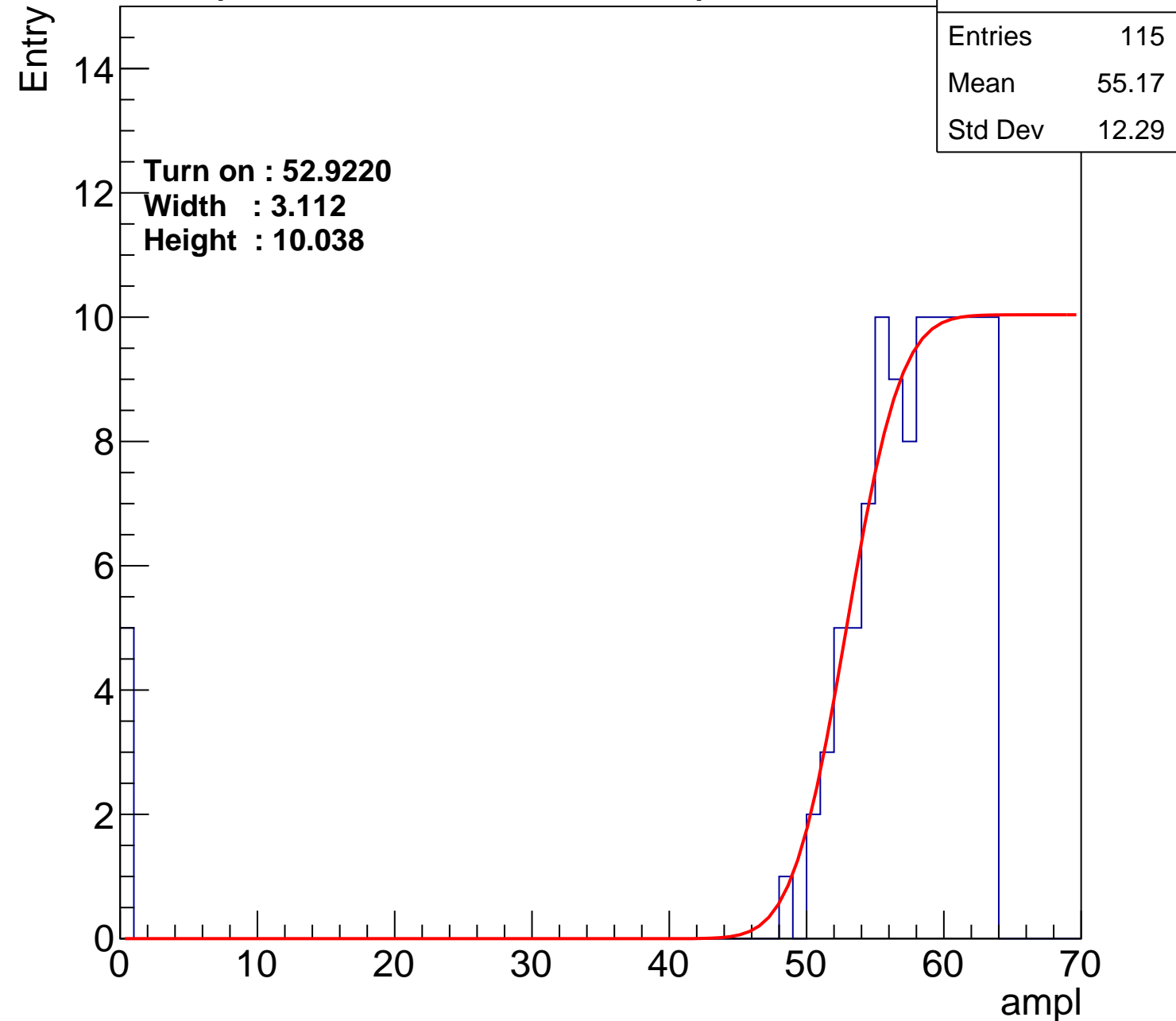
Width : 3.112

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.87
Std Dev	8.182

Turn on : 51.5487

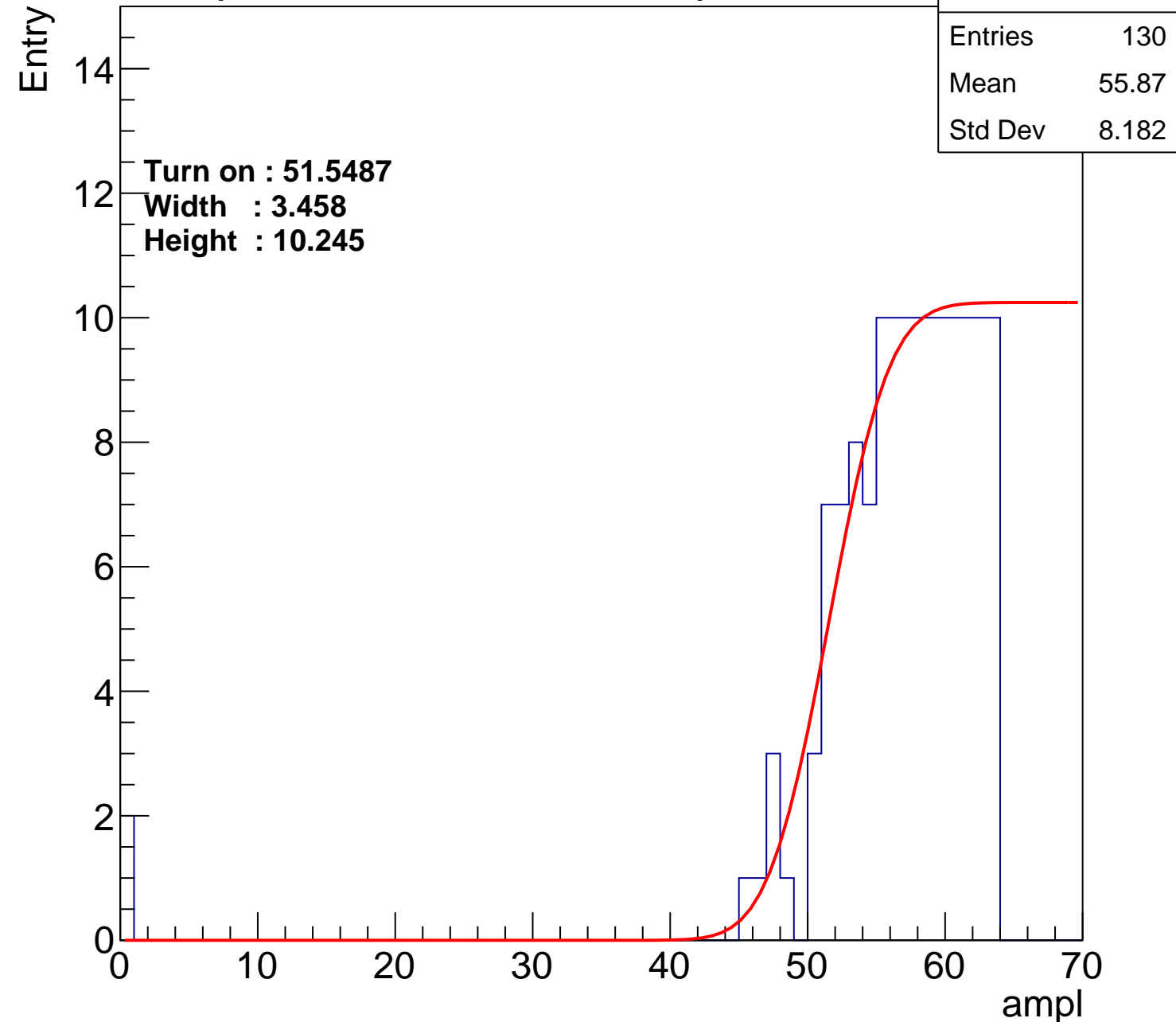
Width : 3.458

Height : 10.245

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

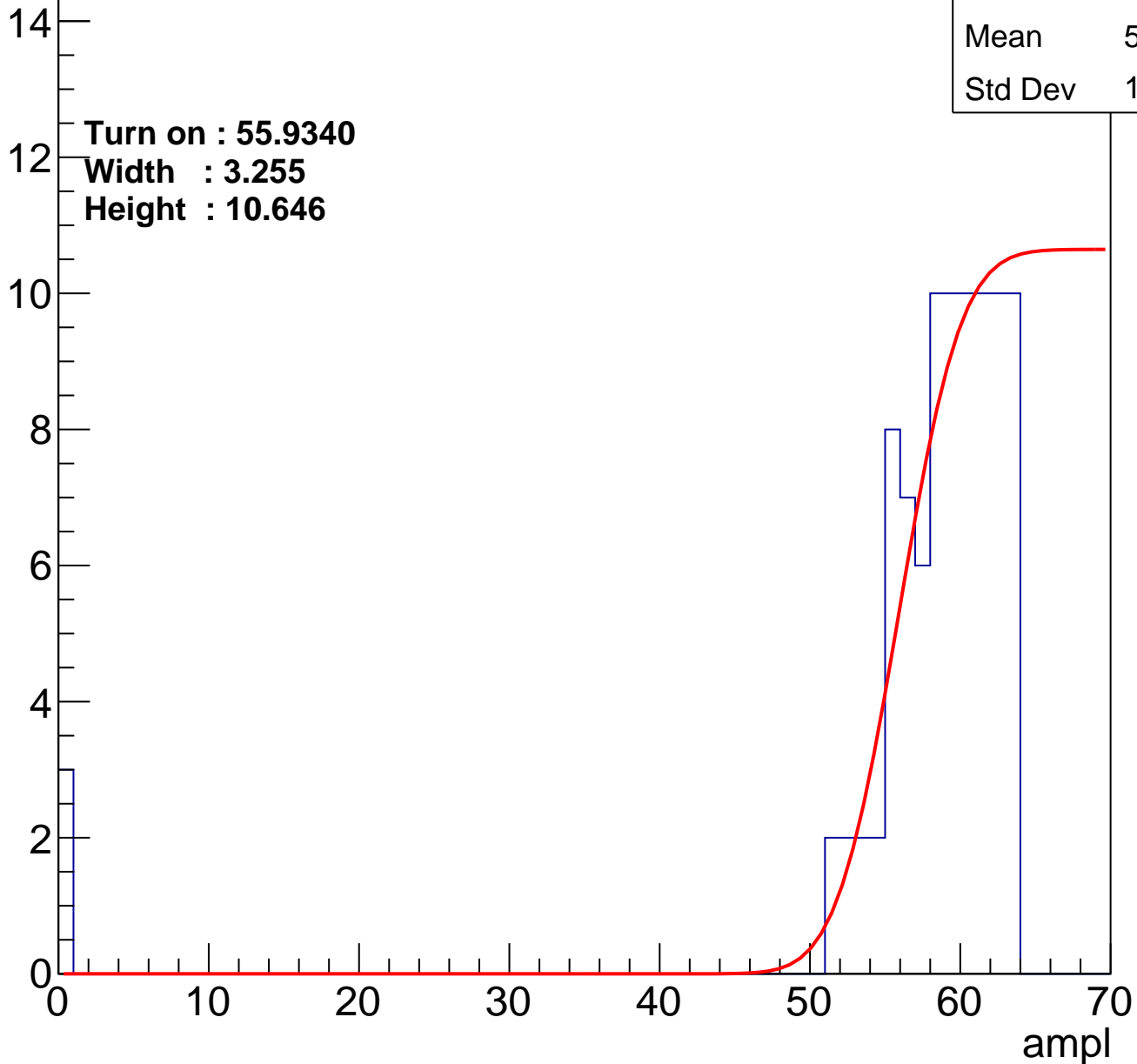
Entries	92
Mean	56.78
Std Dev	10.87

Turn on : 55.9340

Width : 3.255

Height : 10.646

Entry



# B0L103S, U25-ch22

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	99
Mean	56.66
Std Dev	10.49

Turn on : 54.8024

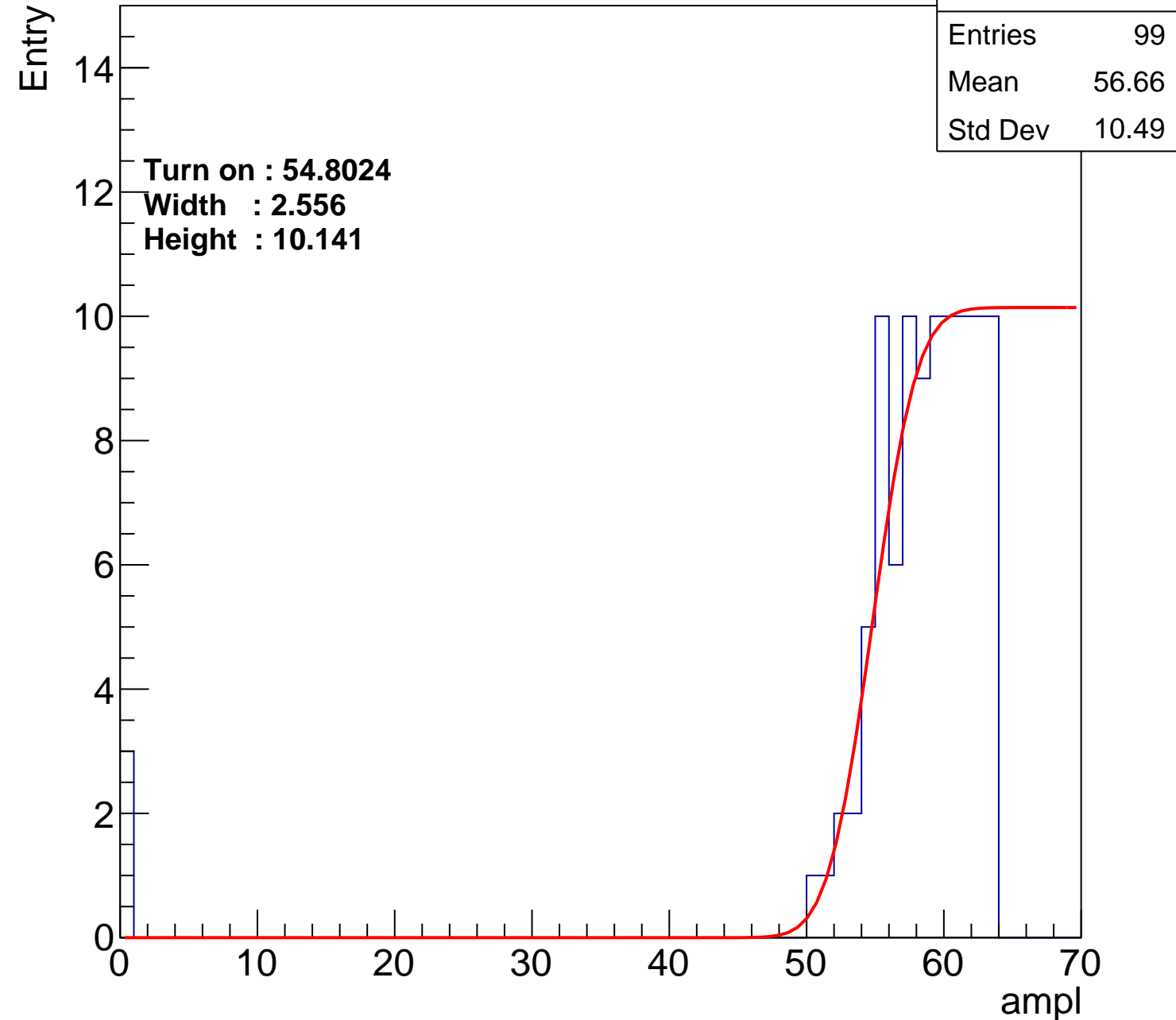
Width : 2.556

Height : 10.141

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

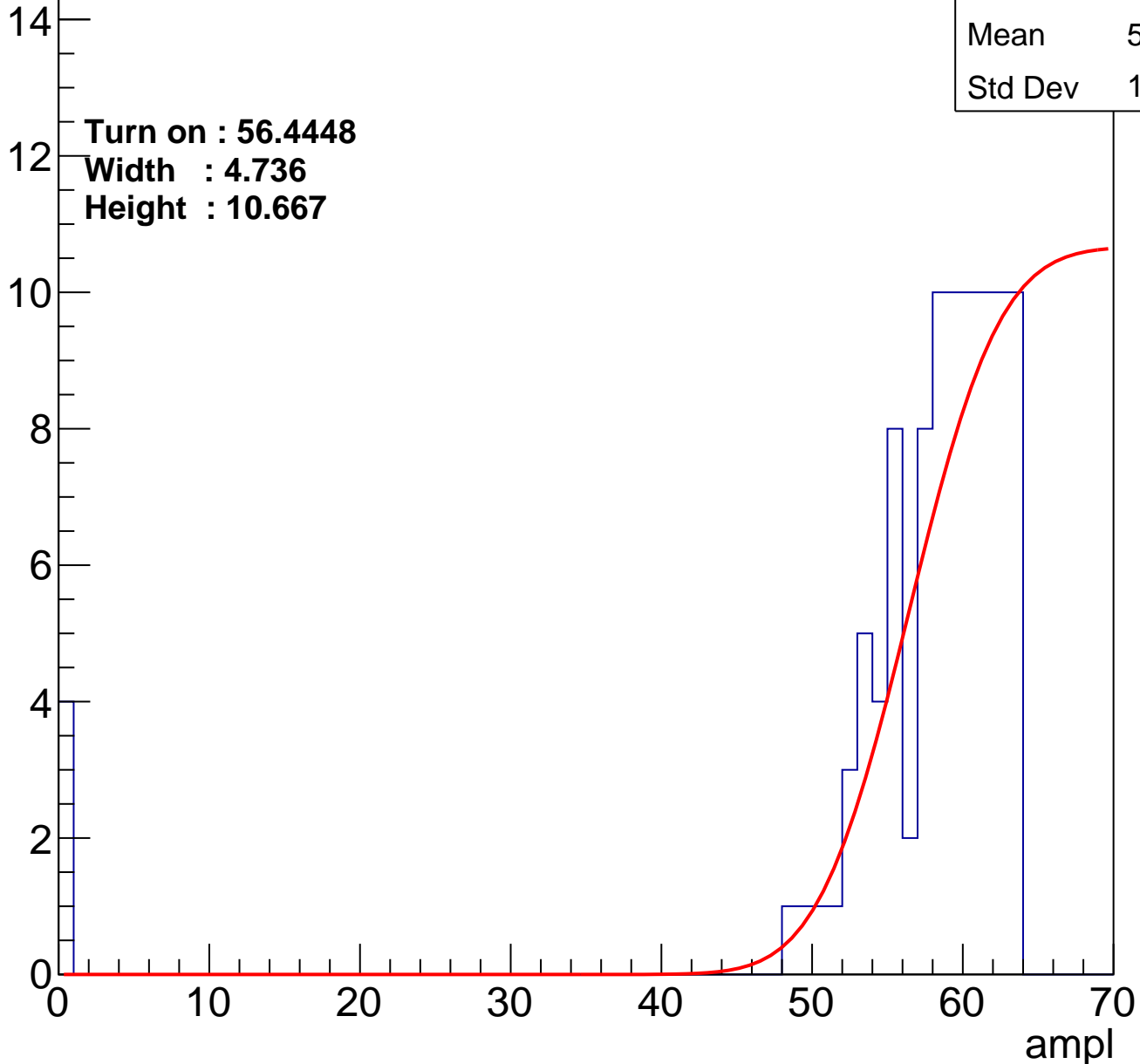
Entries	98
Mean	55.85
Std Dev	12.05

Turn on : 56.4448

Width : 4.736

Height : 10.667

Entry



# B0L103S, U25-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.48
Std Dev	10.97

Turn on : 52.7091

Width : 3.367

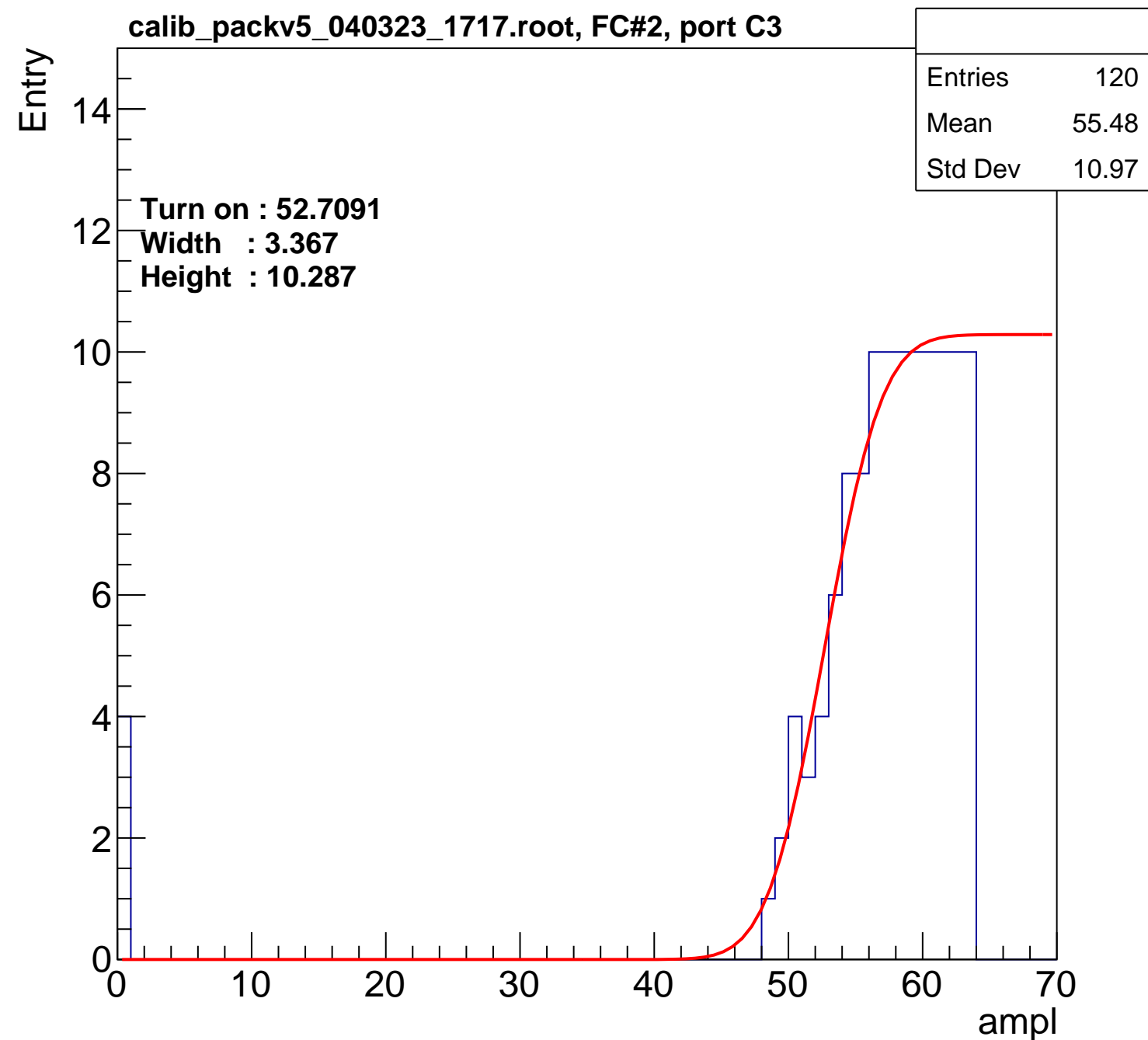
Height : 10.287

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U25-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

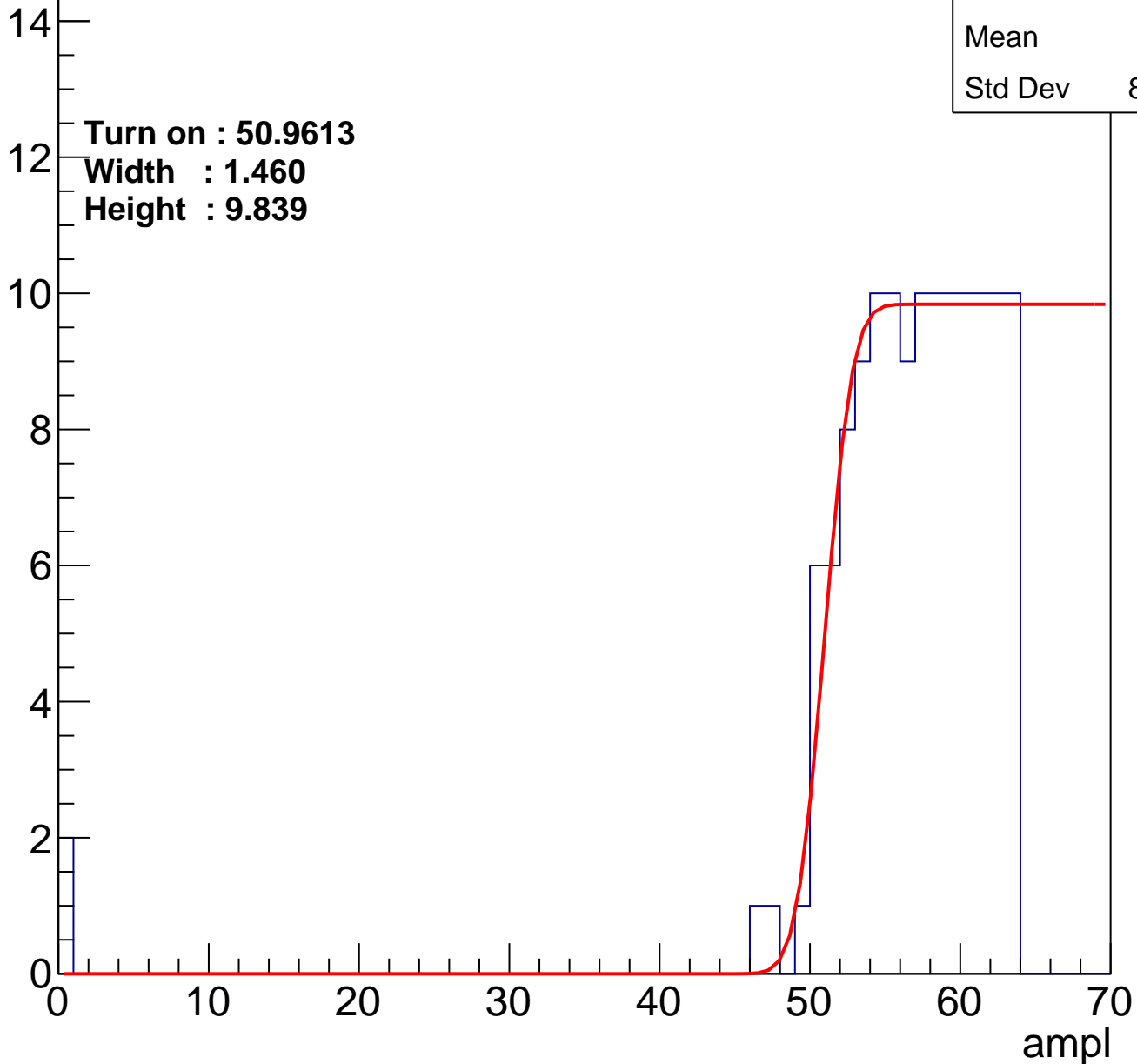
Entries	133
Mean	55.9
Std Dev	8.006

Turn on : 50.9613

Width : 1.460

Height : 9.839

Entry



# B0L103S, U25-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.67
Std Dev	8.01

Turn on : 50.6373

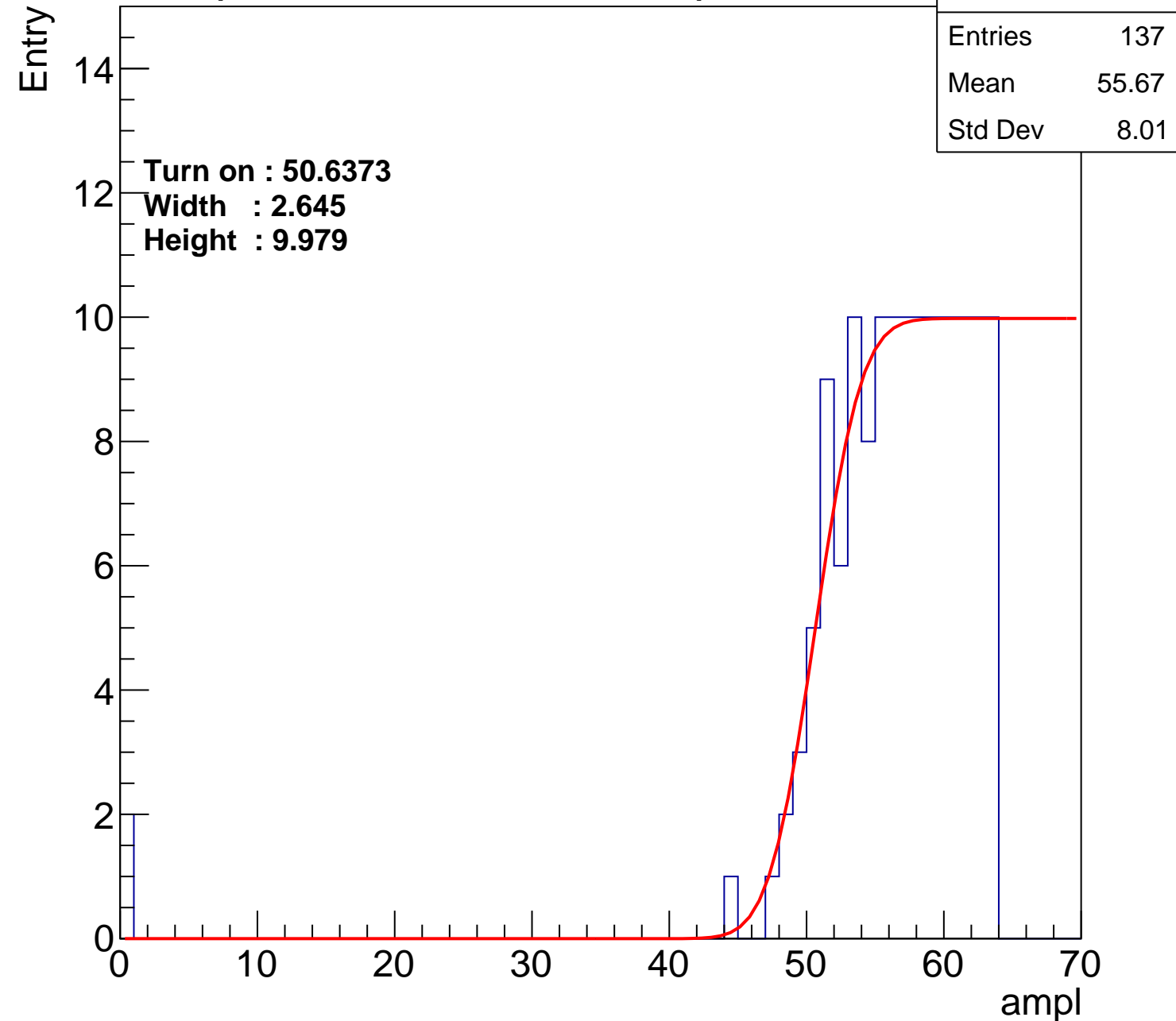
Width : 2.645

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	54.99
Std Dev	11.92

Turn on : 52.1557

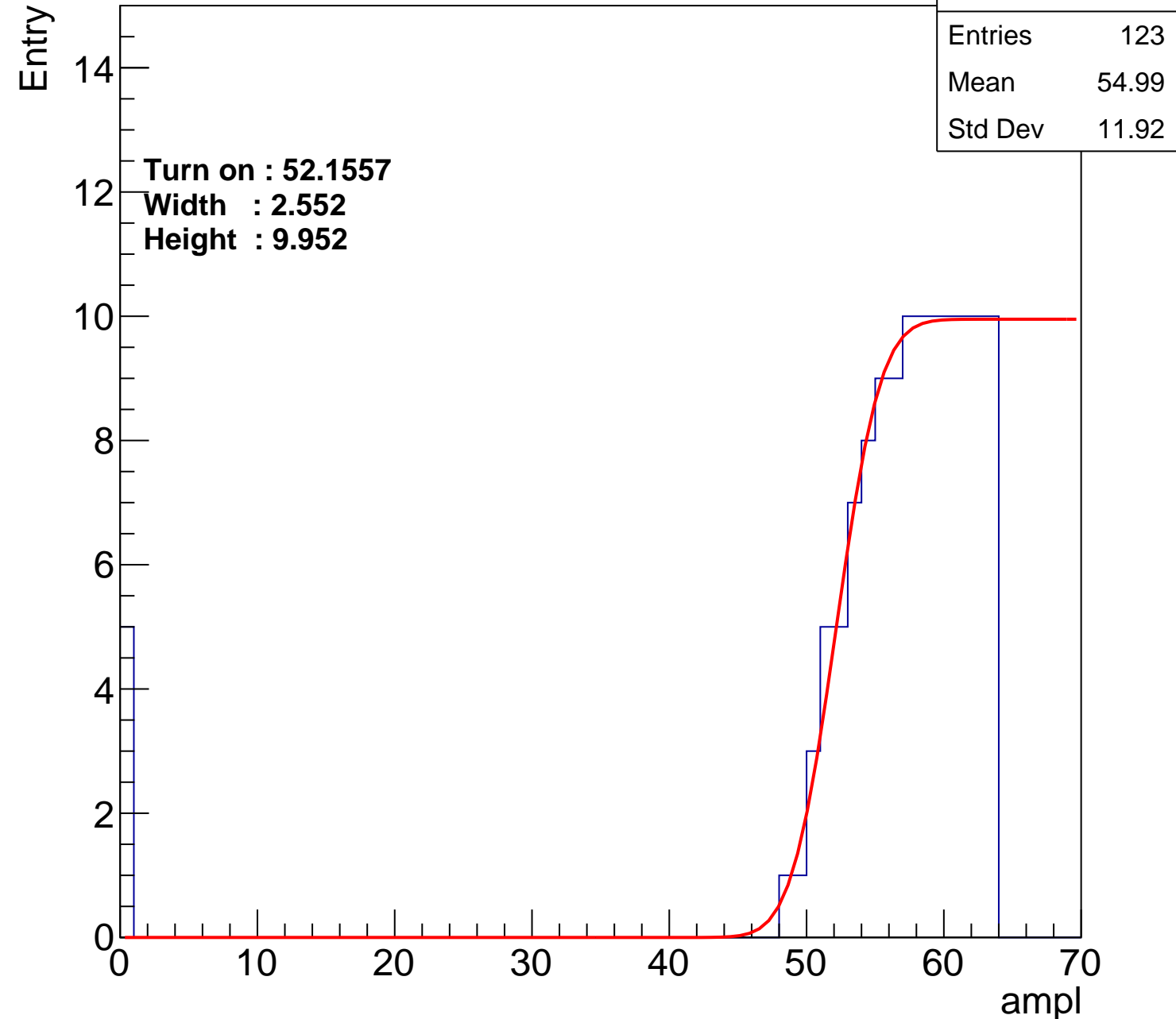
Width : 2.552

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.39
Std Dev	9.979

Turn on : 53.0601

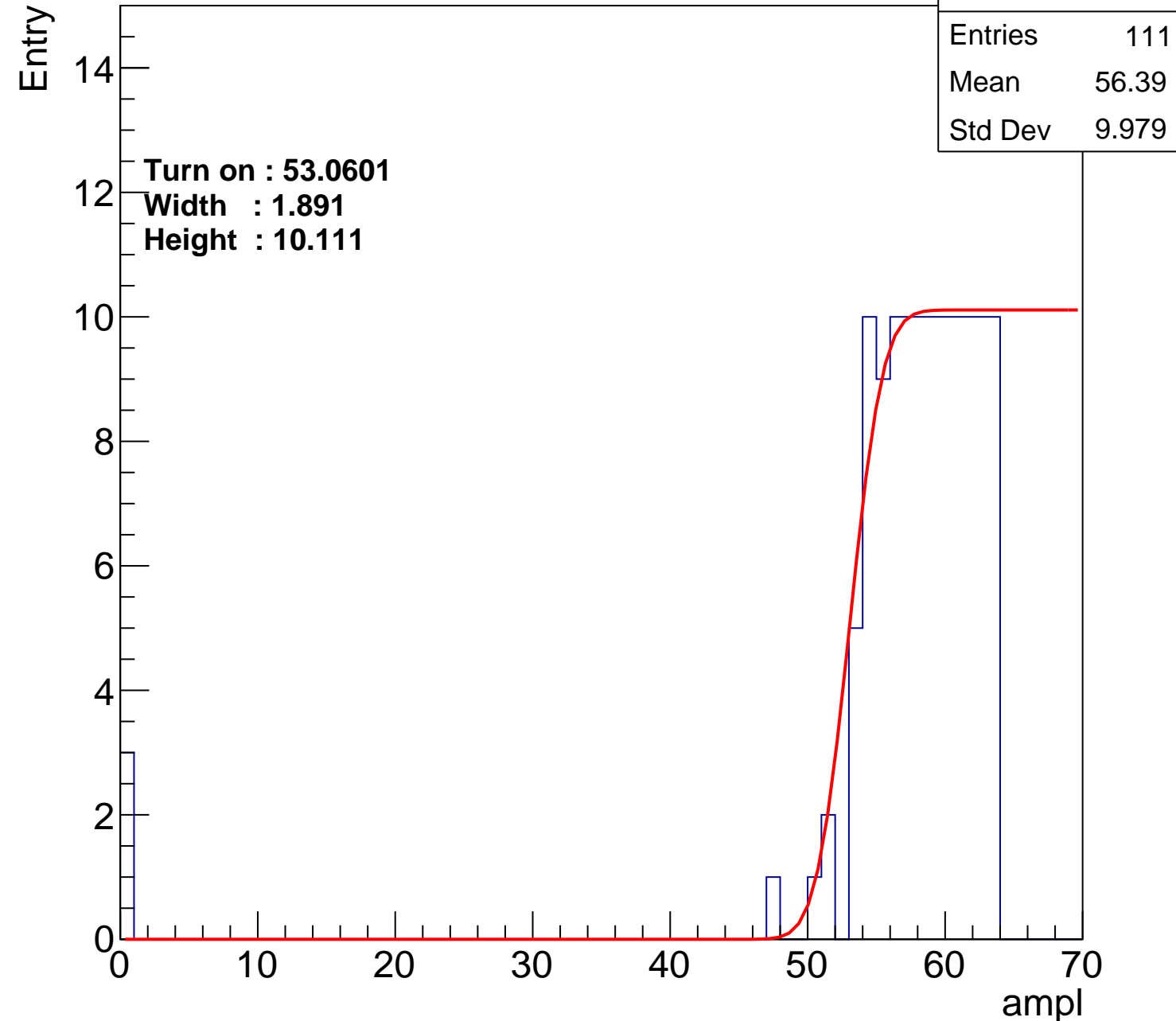
Width : 1.891

Height : 10.111

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.59
Std Dev	9.534

Turn on : 51.8172

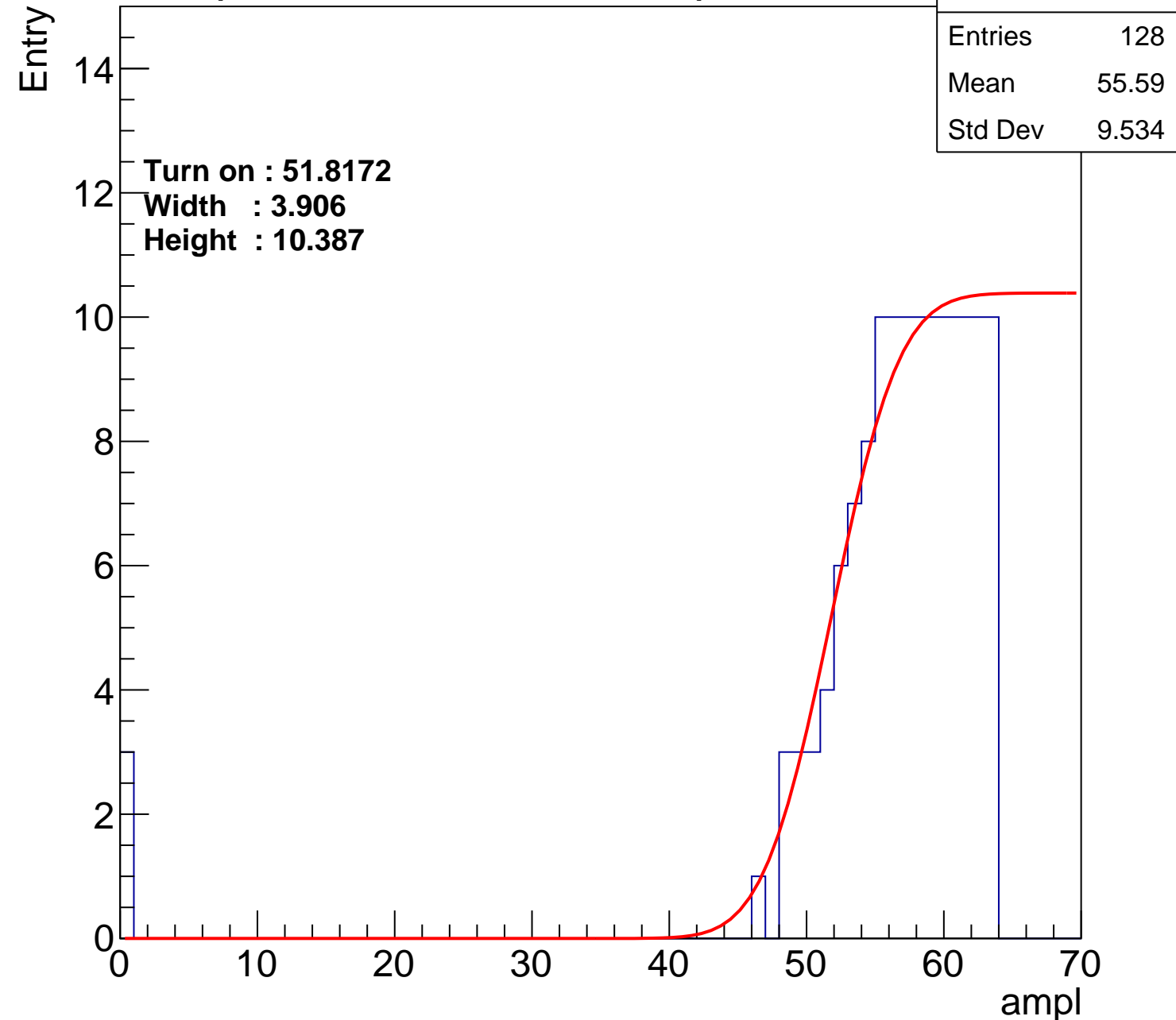
Width : 3.906

Height : 10.387

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

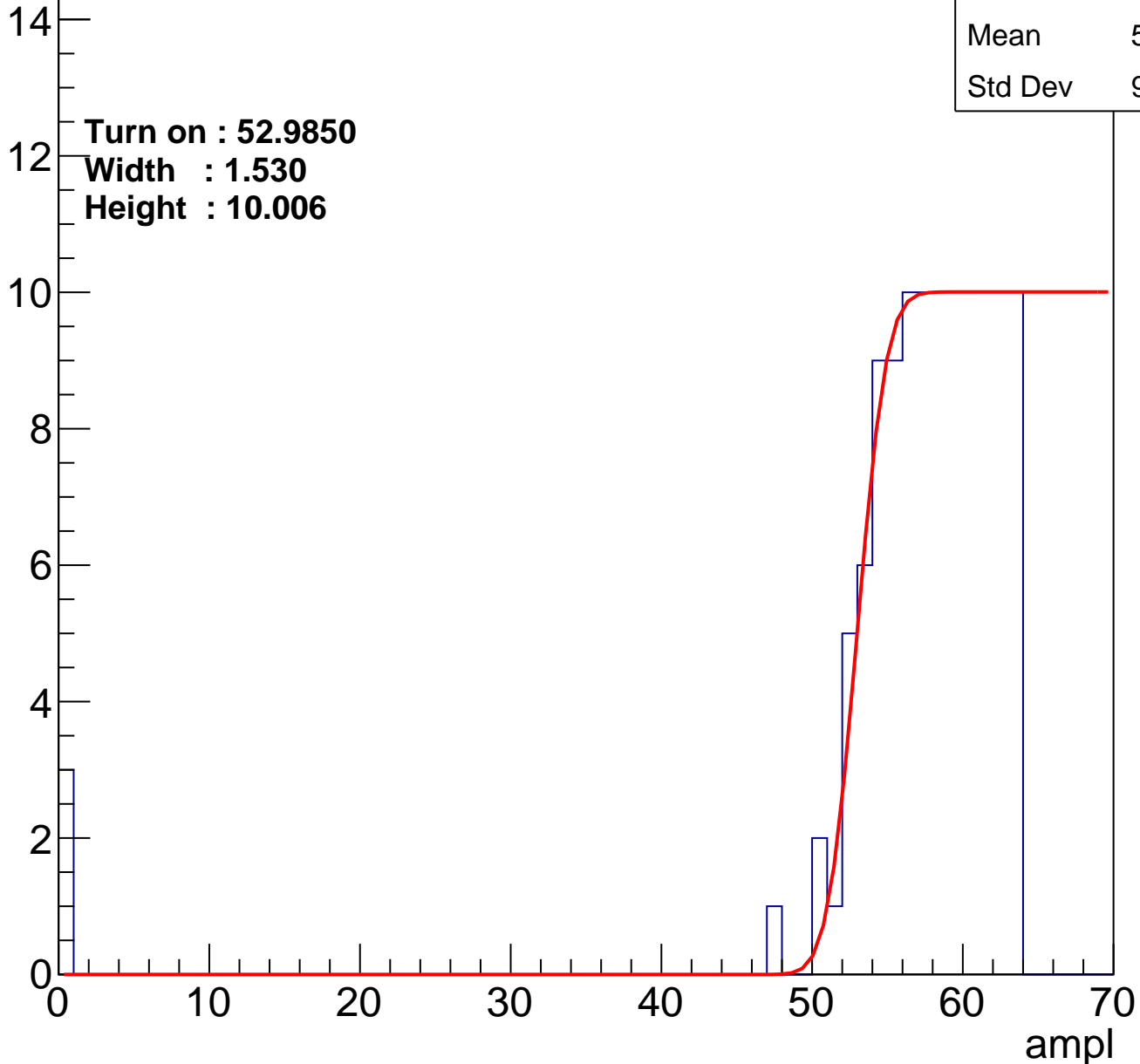
Entries	116
Mean	56.18
Std Dev	9.809

Turn on : 52.9850

Width : 1.530

Height : 10.006

Entry





# B0L103S, U25-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	54.79
Std Dev	10.38

Turn on : 50.6732

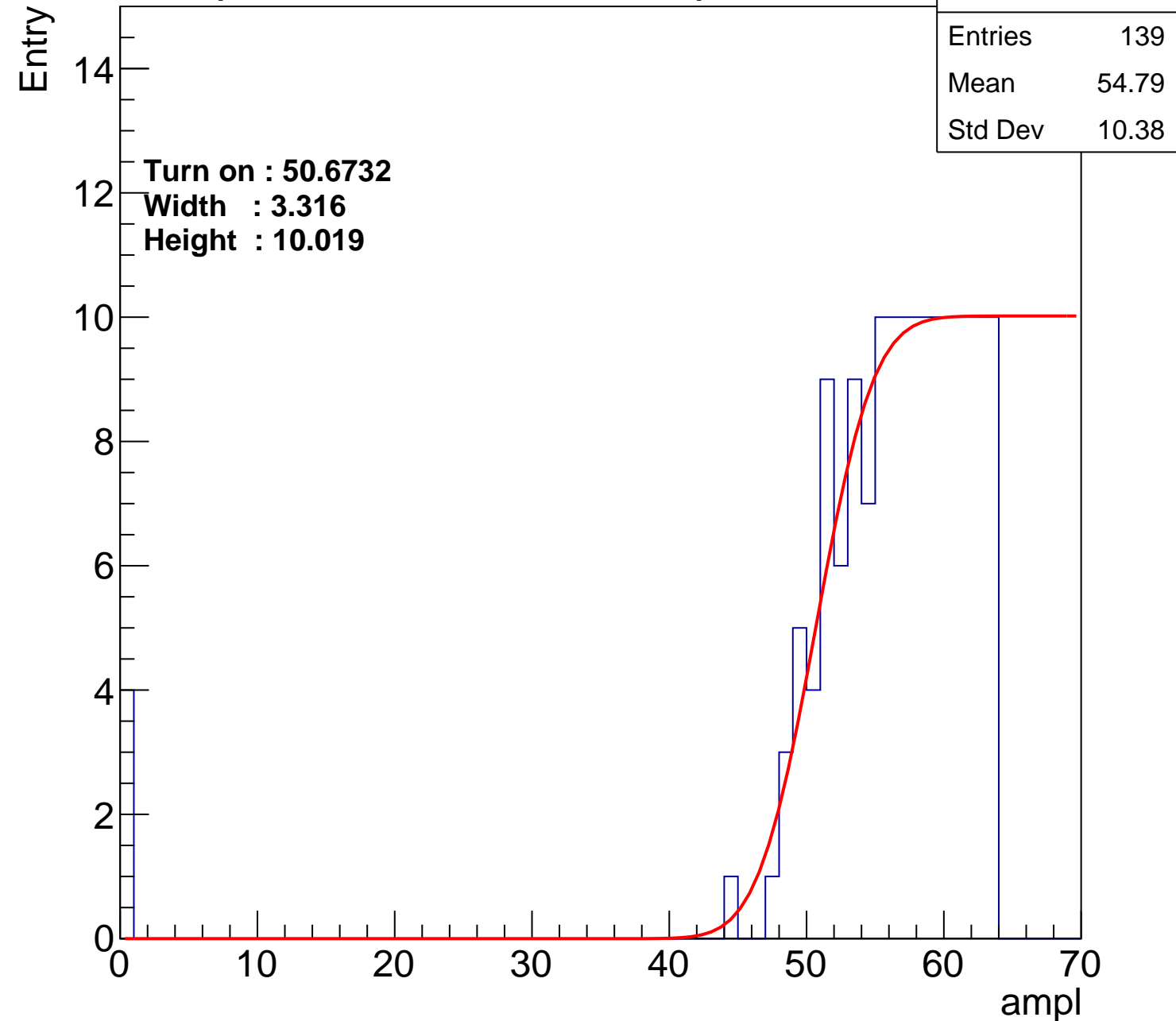
Width : 3.316

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.74
Std Dev	7.961

Turn on : 50.9961

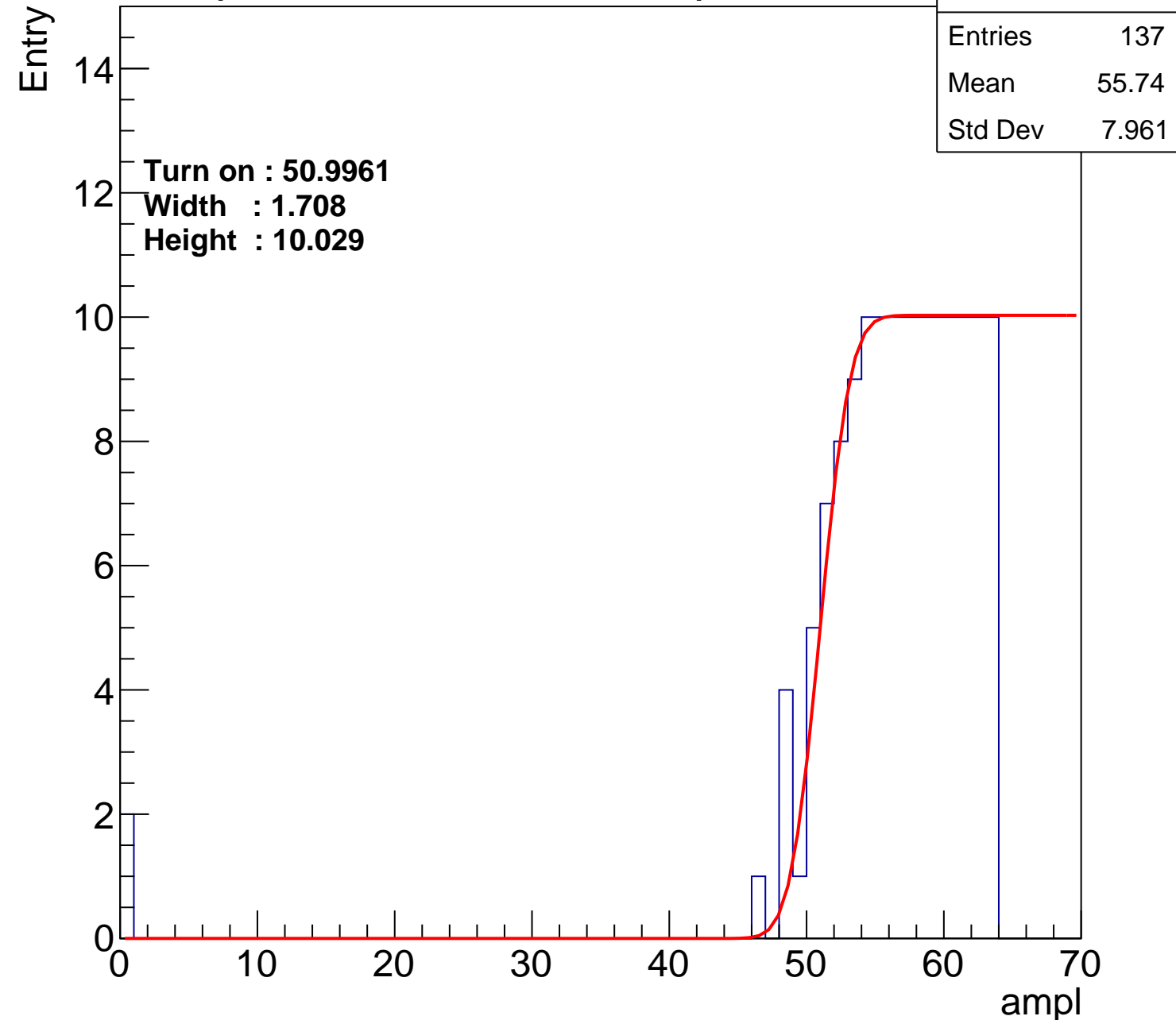
Width : 1.708

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch33

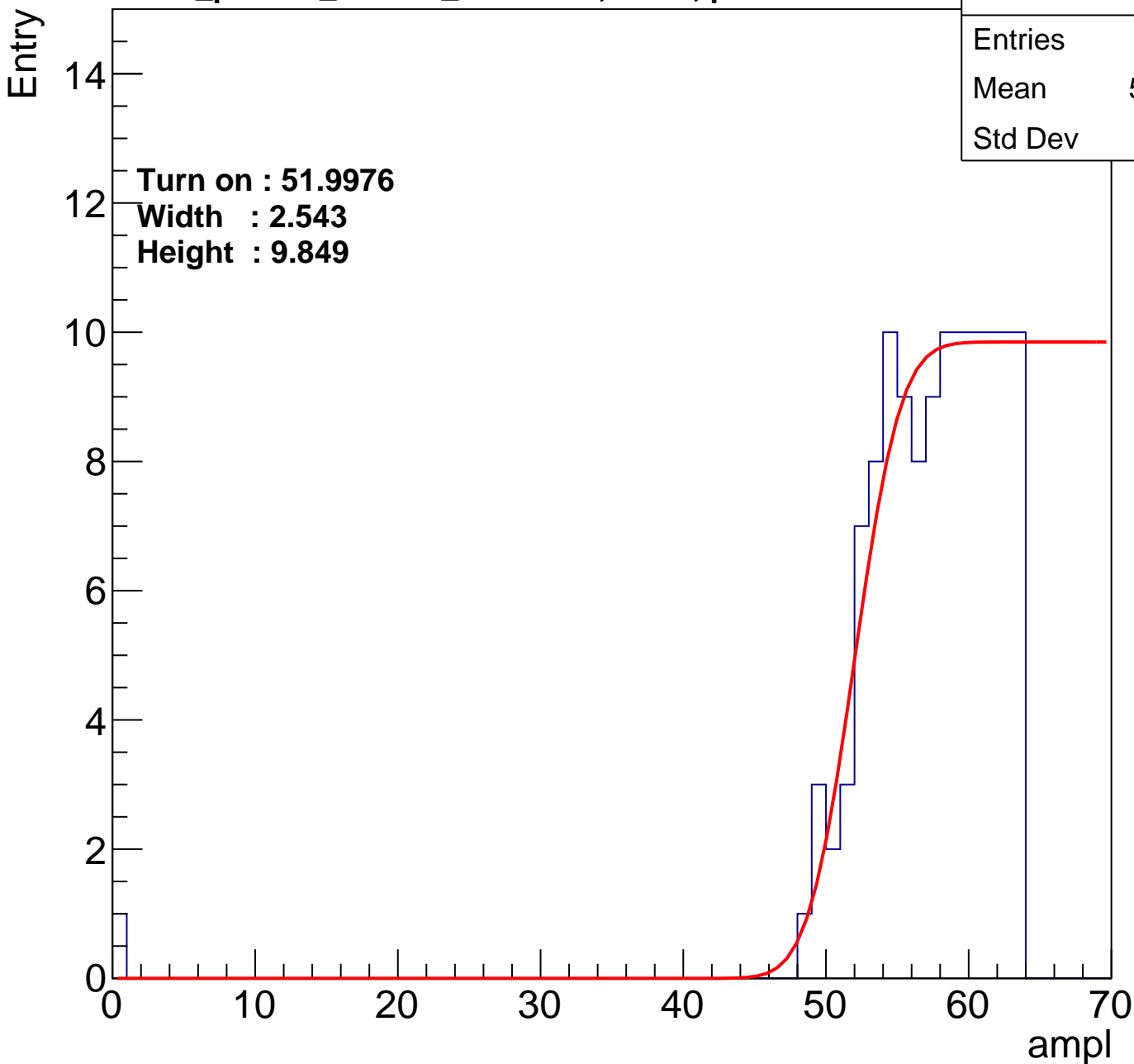
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	121
Mean	56.71
Std Dev	6.47

**Turn on : 51.9976**

**Width : 2.543**

**Height : 9.849**



# B0L103S, U25-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.67
Std Dev	8.067

Turn on : 51.3980

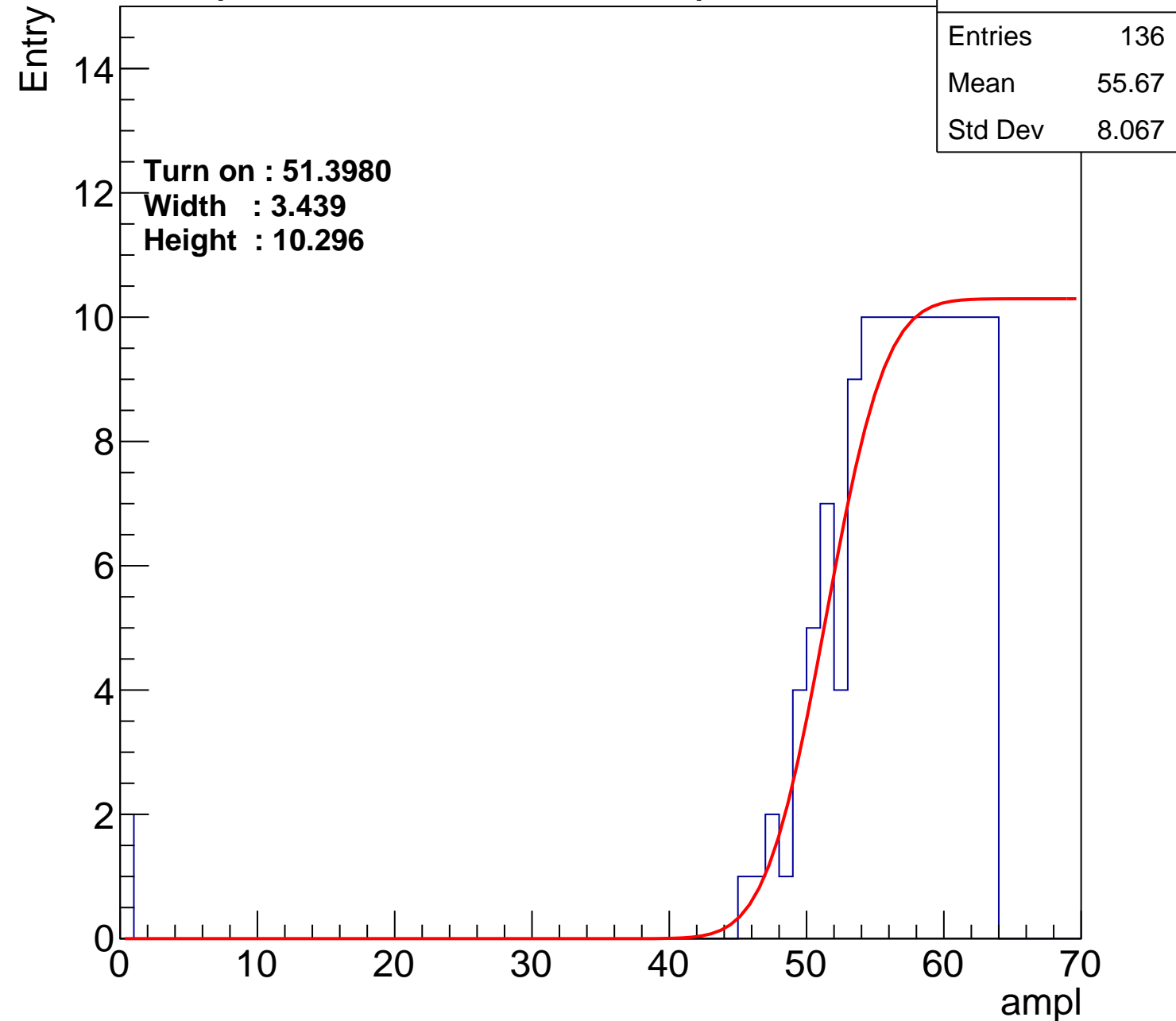
Width : 3.439

Height : 10.296

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	115
Mean	55.63
Std Dev	11.17

Turn on : 53.4228

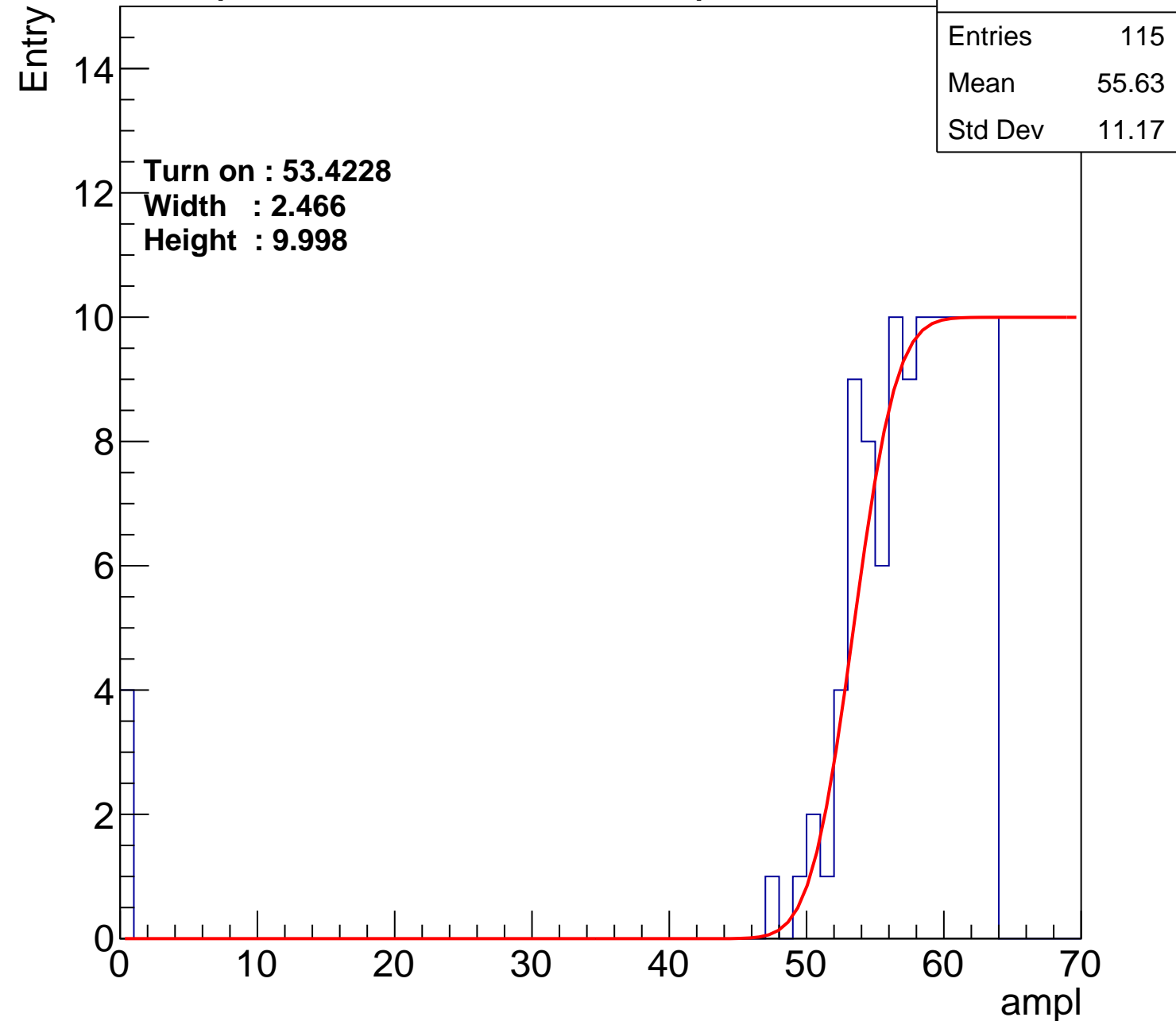
Width : 2.466

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.34
Std Dev	9.452

Turn on : 51.8779

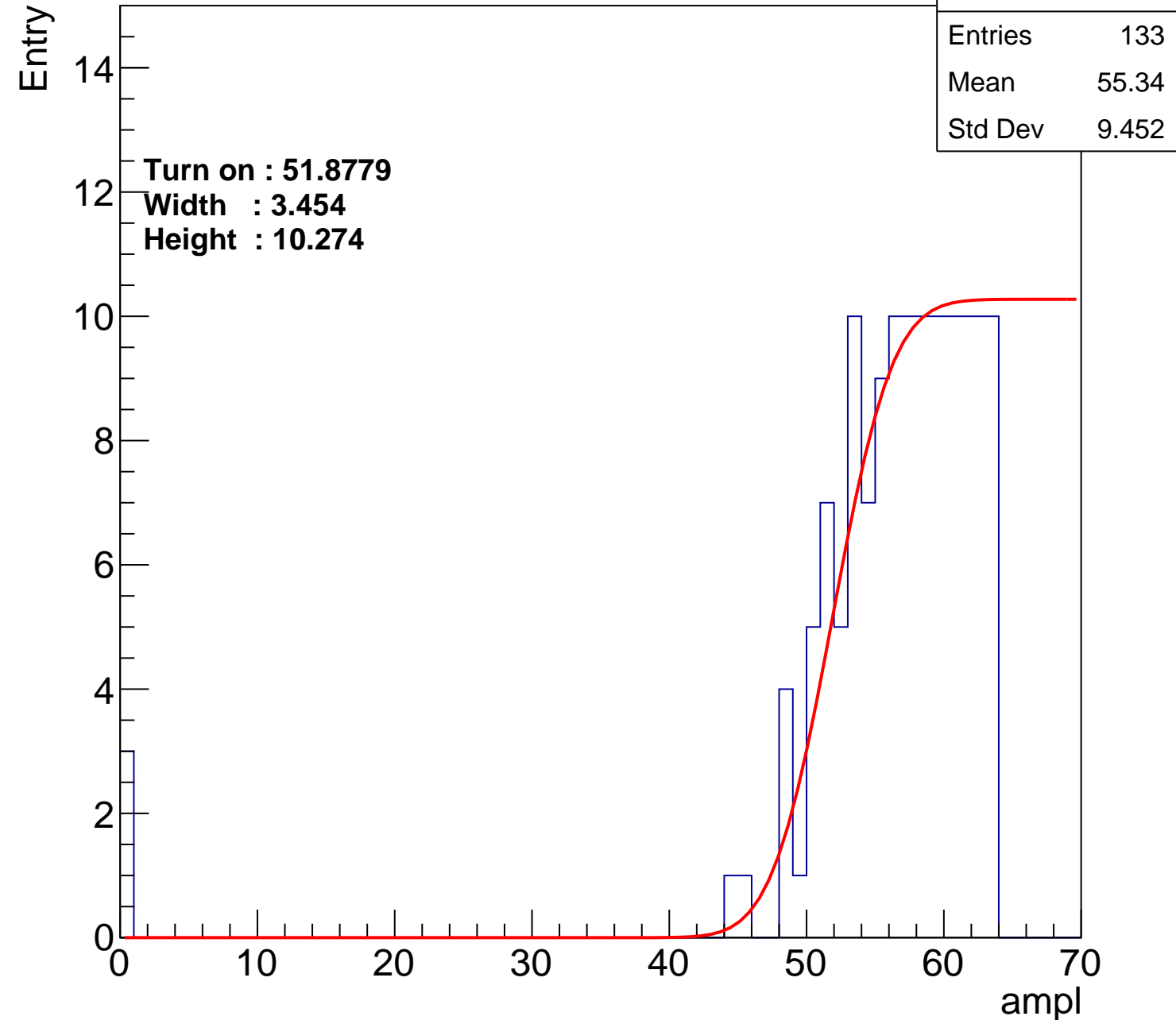
Width : 3.454

Height : 10.274

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

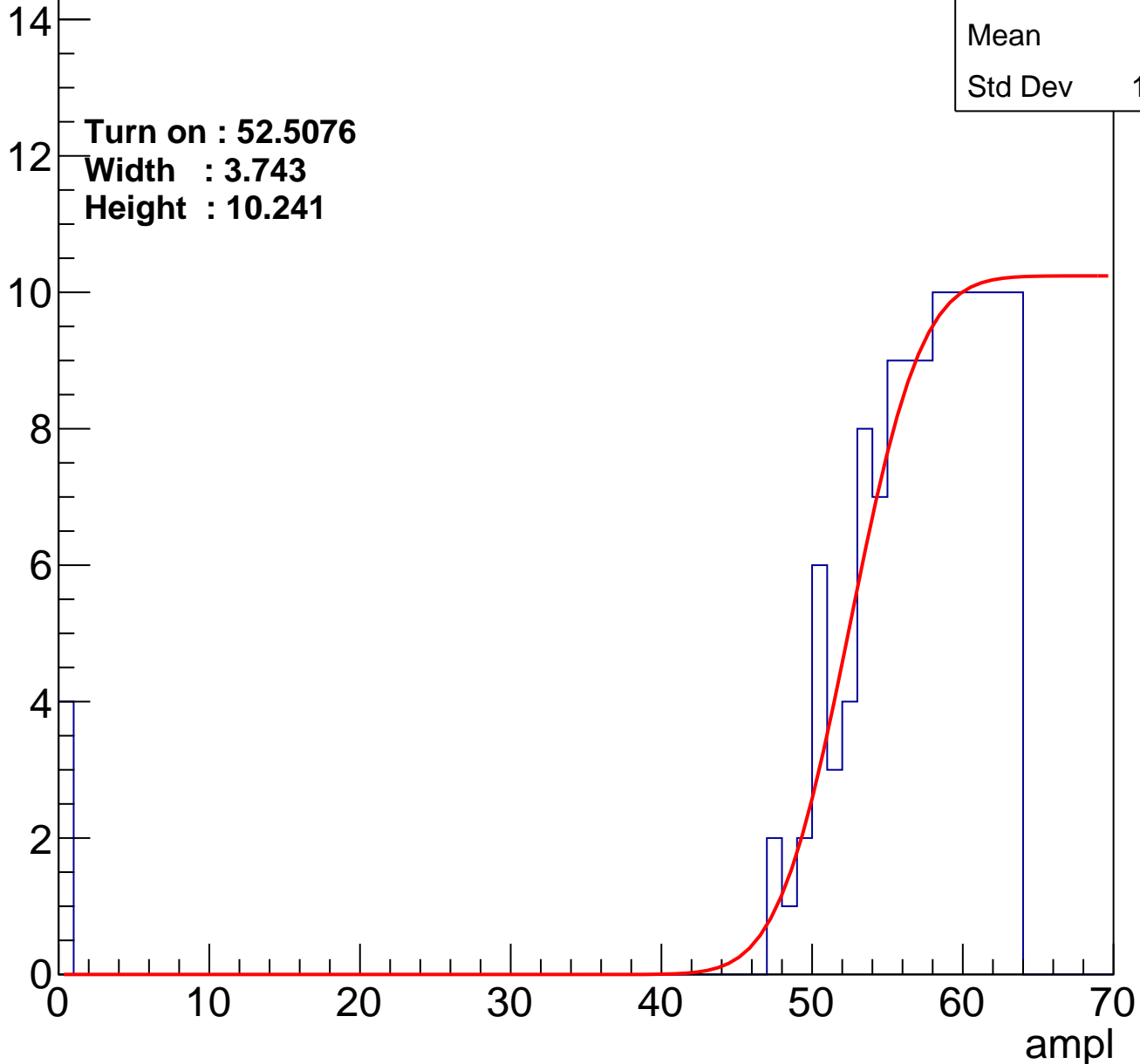
Entries	124
Mean	55.2
Std Dev	10.87

Turn on : 52.5076

Width : 3.743

Height : 10.241

Entry



# B0L103S, U25-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	103
Mean	56.54
Std Dev	10.31

Turn on : 54.4055

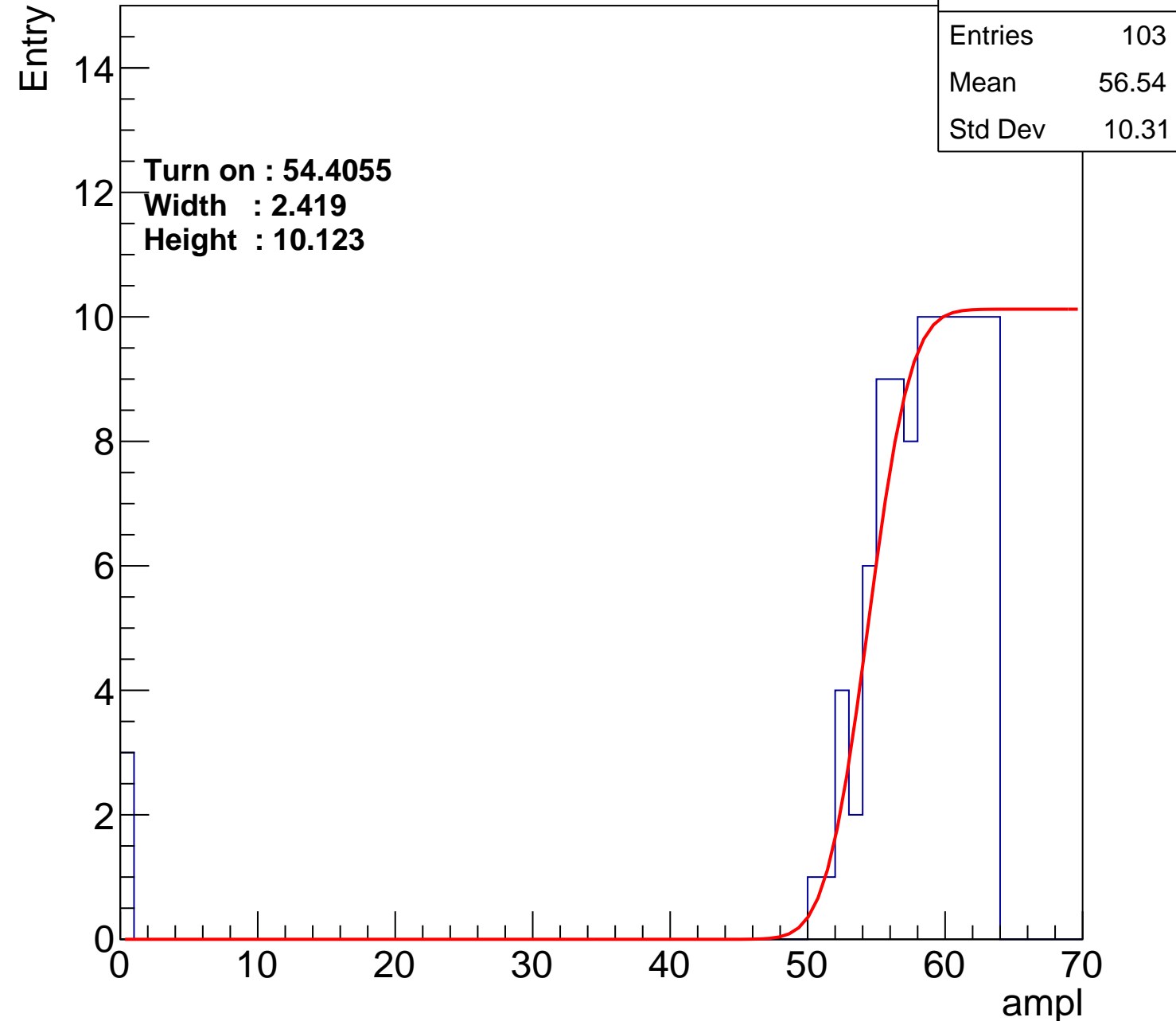
Width : 2.419

Height : 10.123

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	54.92
Std Dev	11.93

Turn on : 52.1248

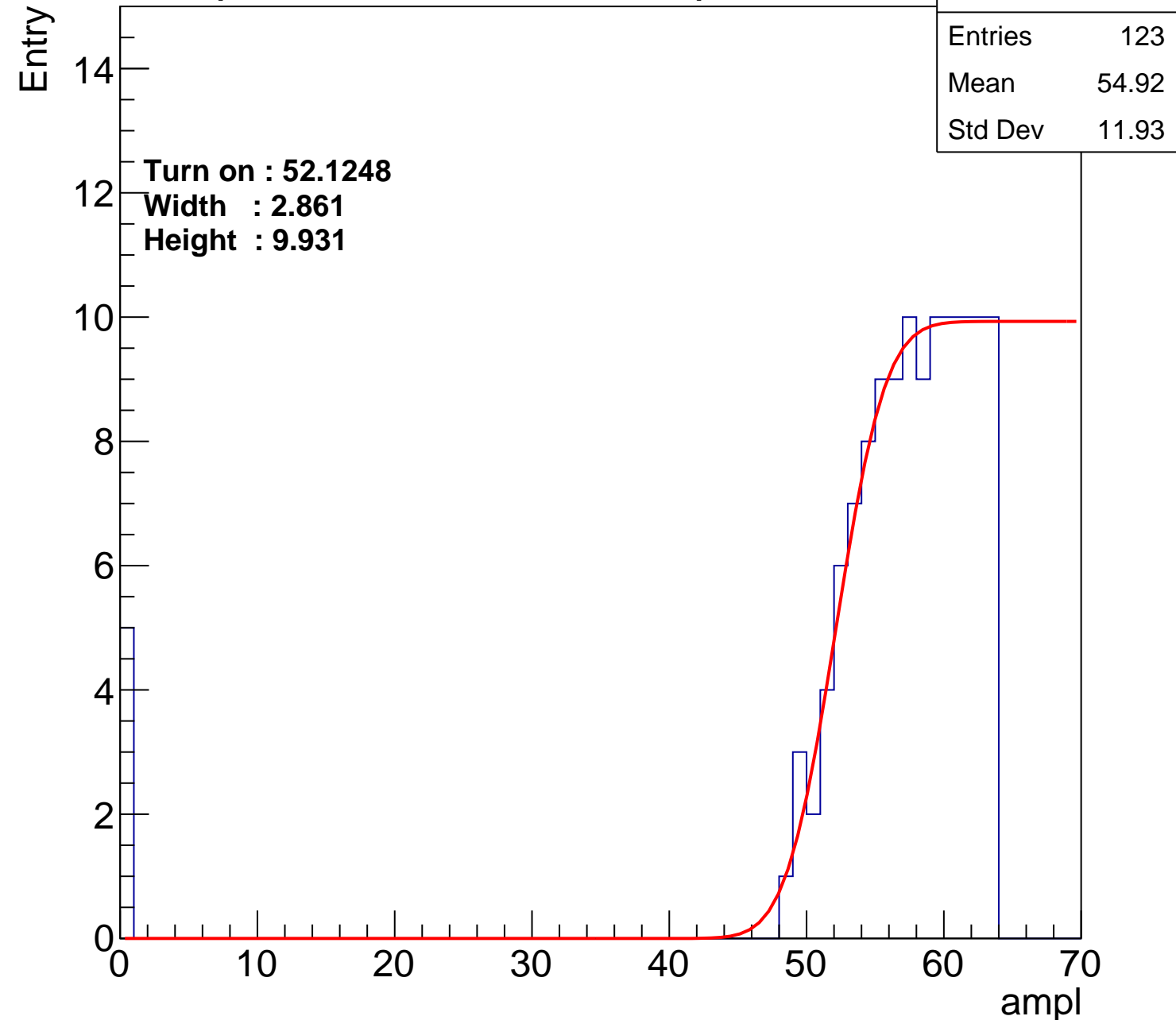
Width : 2.861

Height : 9.931

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.1
Std Dev	9.864

Turn on : 52.2086

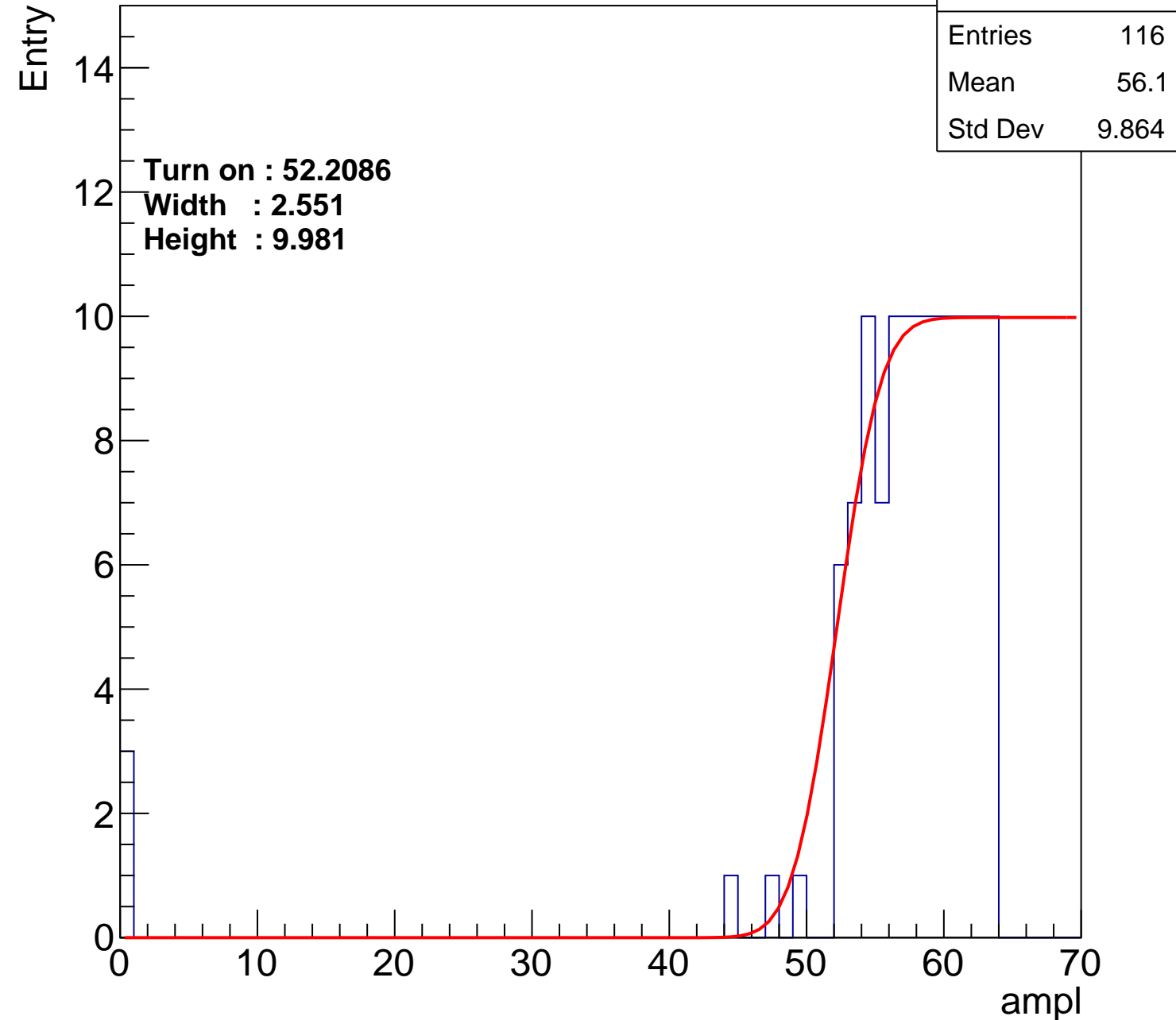
Width : 2.551

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

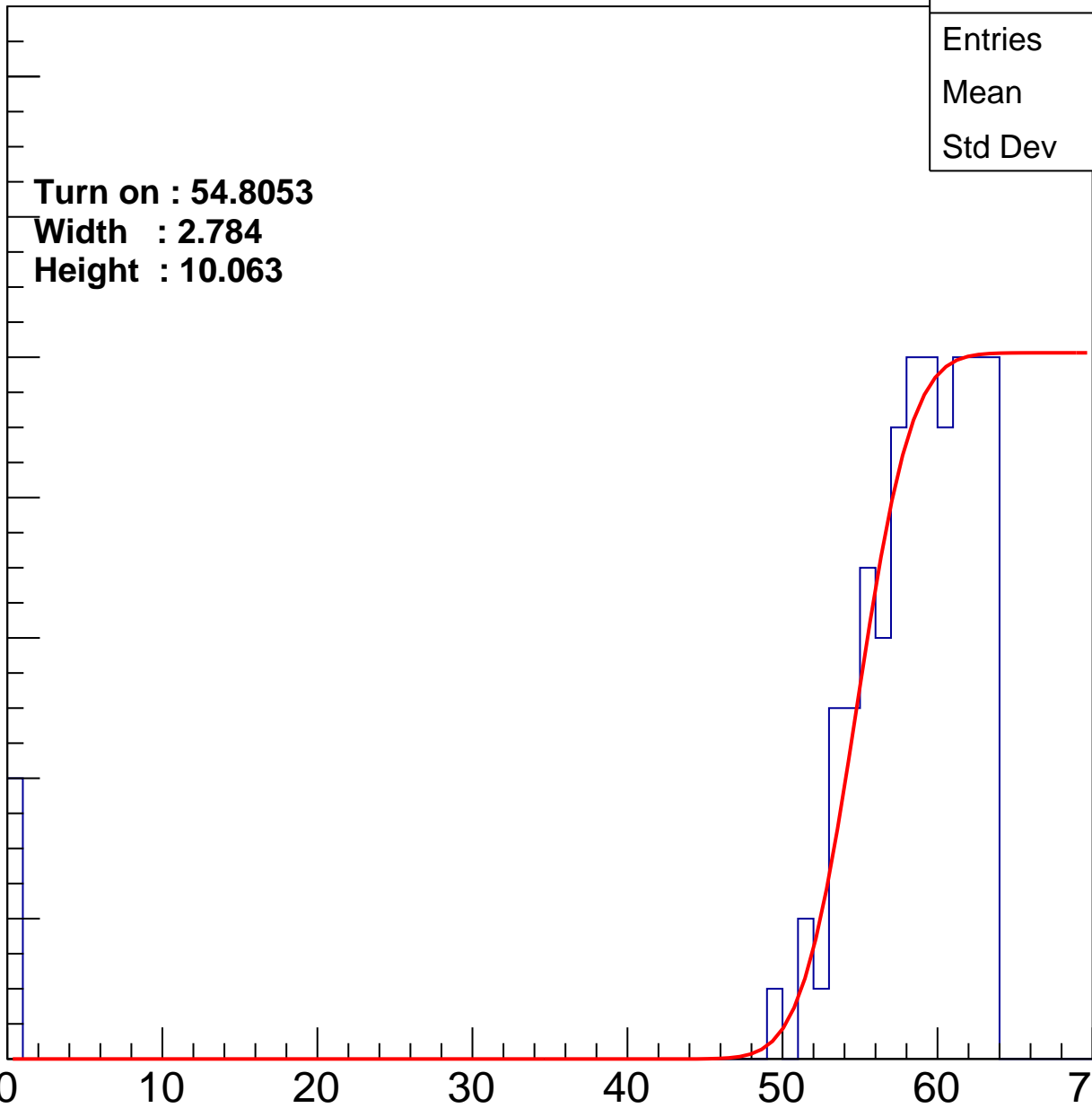
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 54.8053  
Width : 2.784  
Height : 10.063

Entries	99
Mean	55.98
Std Dev	11.94

ampl



# B0L103S, U25-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

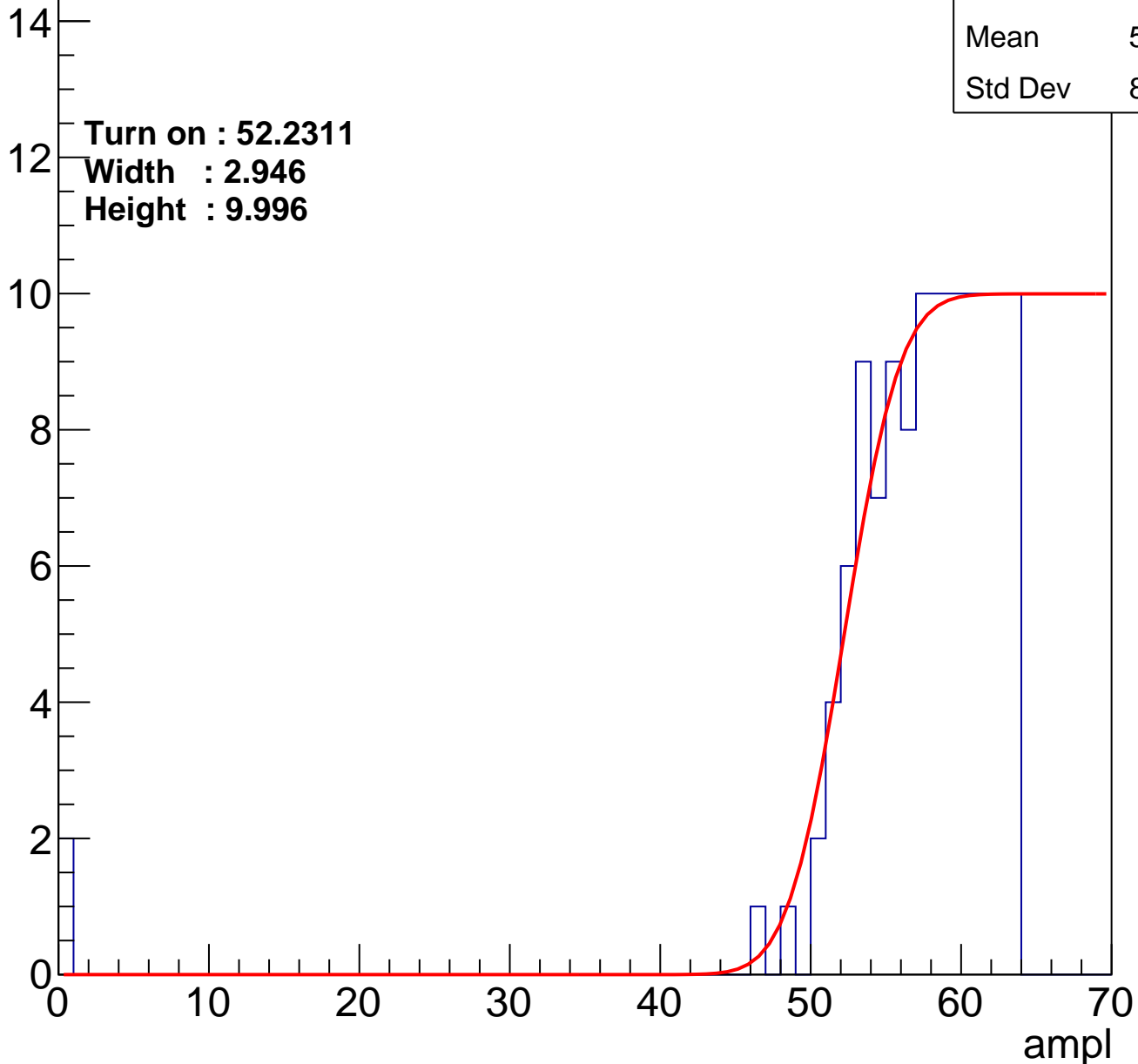
Entries	119
Mean	56.37
Std Dev	8.304

Turn on : 52.2311

Width : 2.946

Height : 9.996

Entry



# B0L103S, U25-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.99
Std Dev	9.078

Turn on : 49.8686

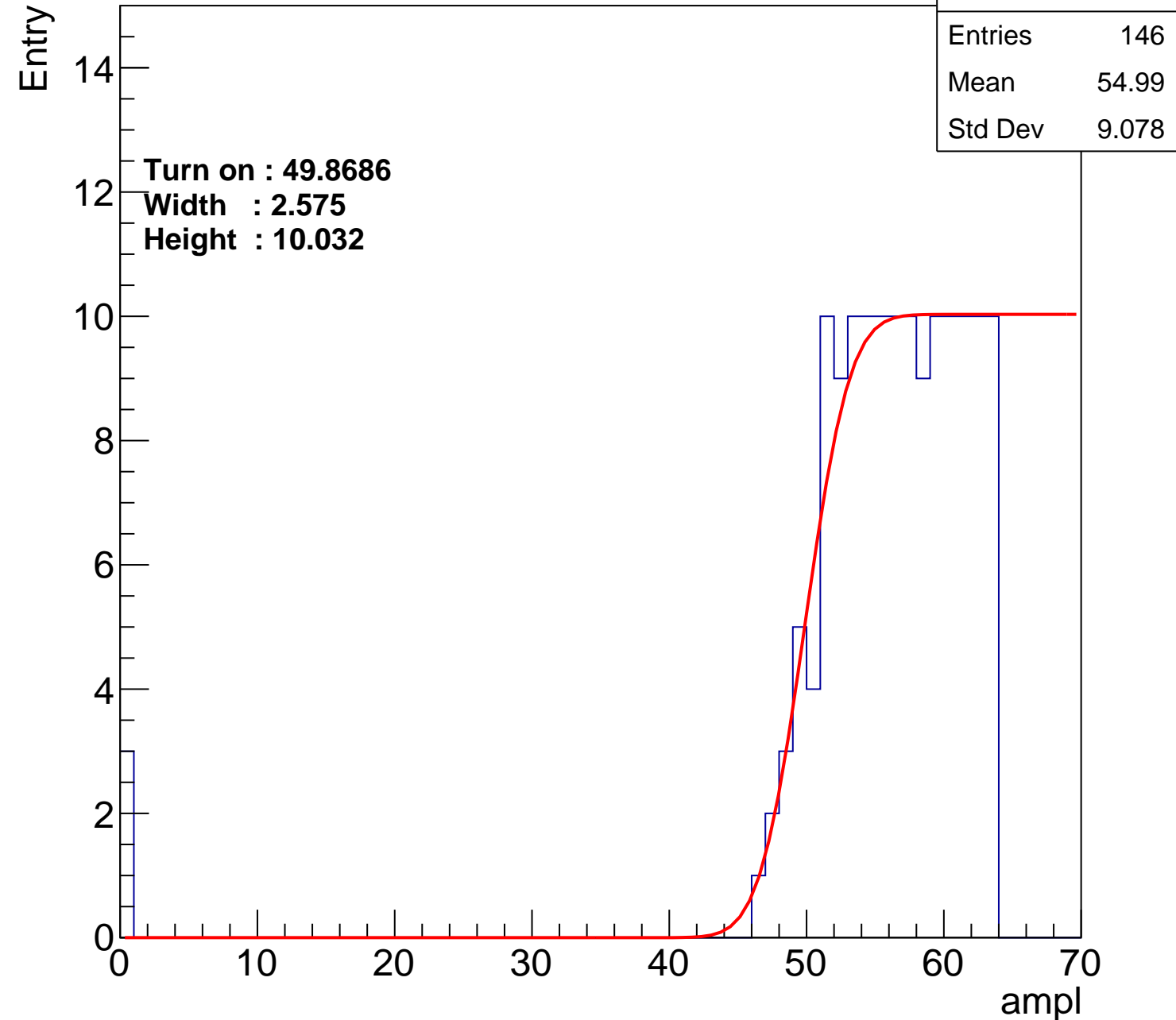
Width : 2.575

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch44

calib\_packv5\_040323\_1717.root, FC#2, port C3

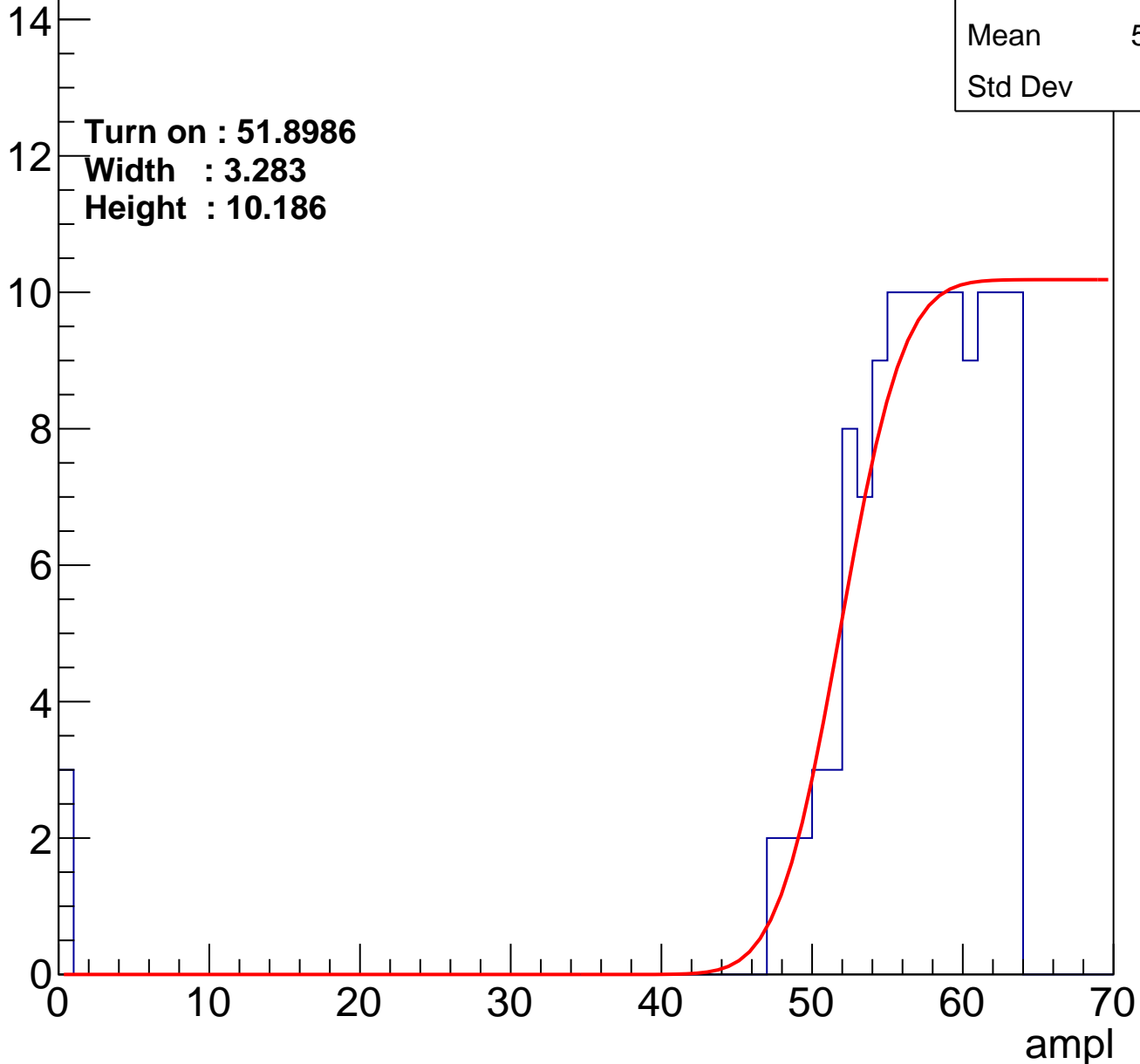
Entries	128
Mean	55.58
Std Dev	9.51

Turn on : 51.8986

Width : 3.283

Height : 10.186

Entry



# B0L103S, U25-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	103
Mean	55.95
Std Dev	11.72

Turn on : 54.1651

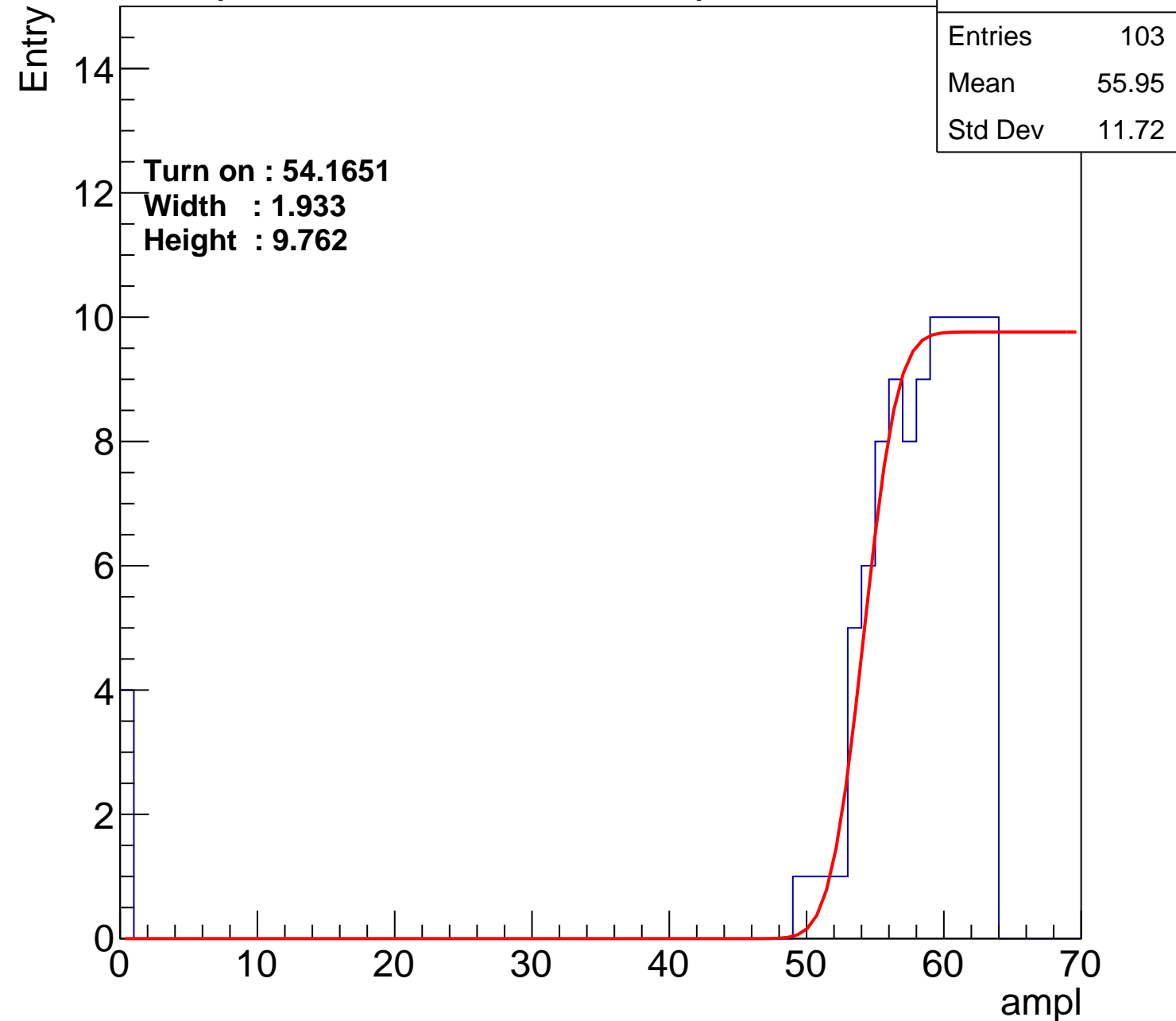
Width : 1.933

Height : 9.762

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.98
Std Dev	8.052

**Turn on : 51.4285**

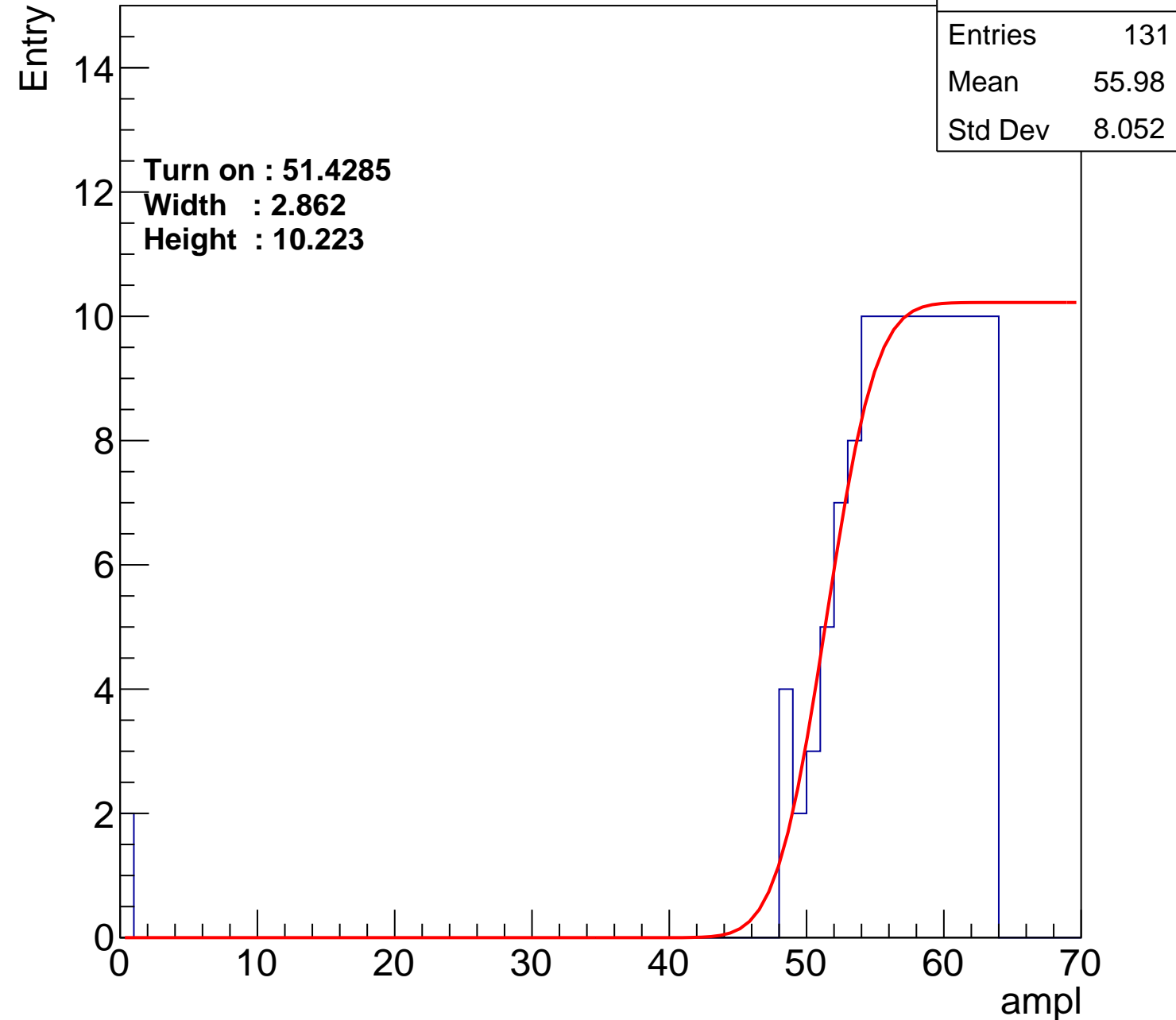
**Width : 2.862**

**Height : 10.223**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

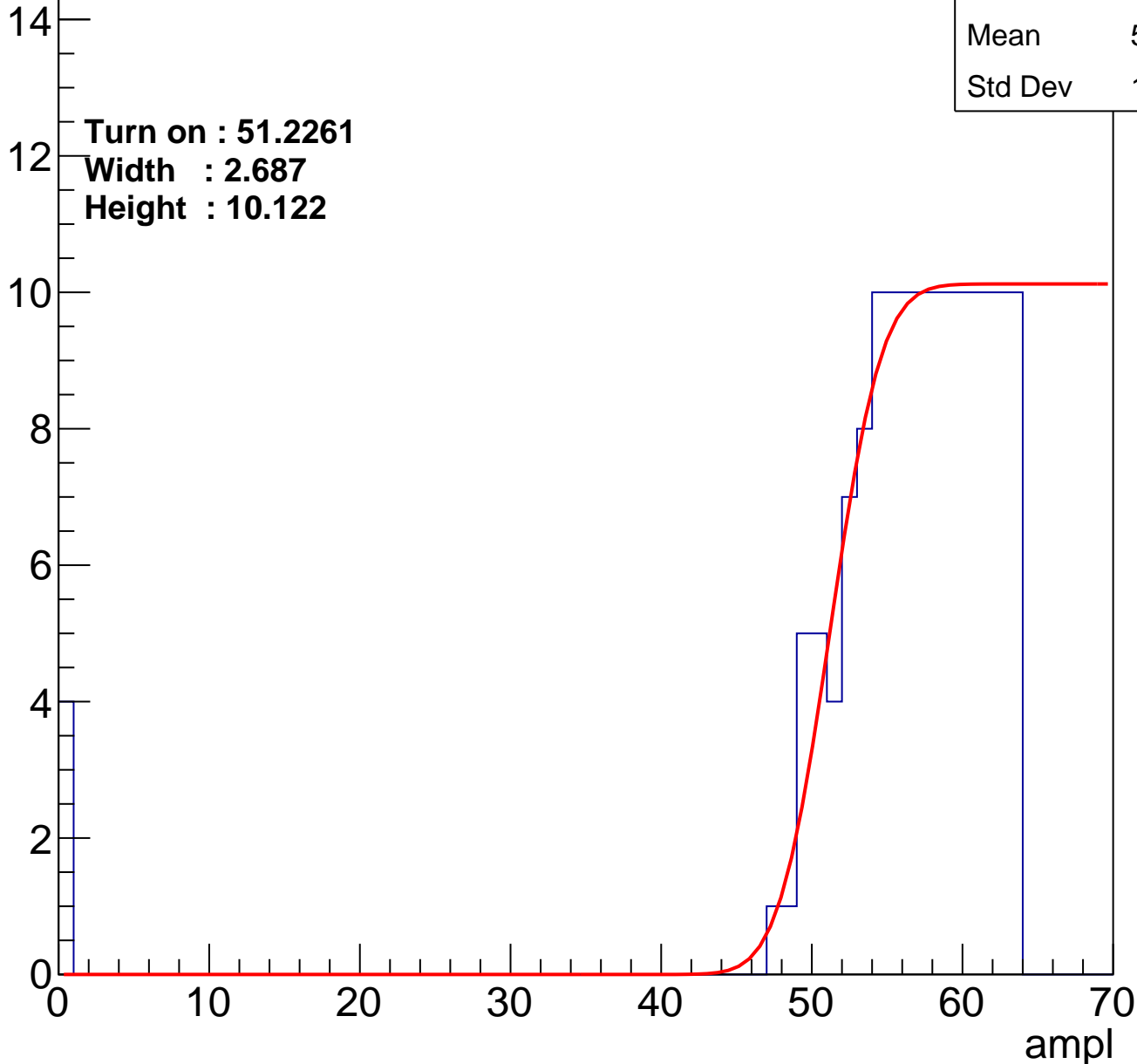
Entries	135
Mean	55.05
Std Dev	10.44

Turn on : 51.2261

Width : 2.687

Height : 10.122

Entry



# B0L103S, U25-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	56.3
Std Dev	8.328

Turn on : 52.8879

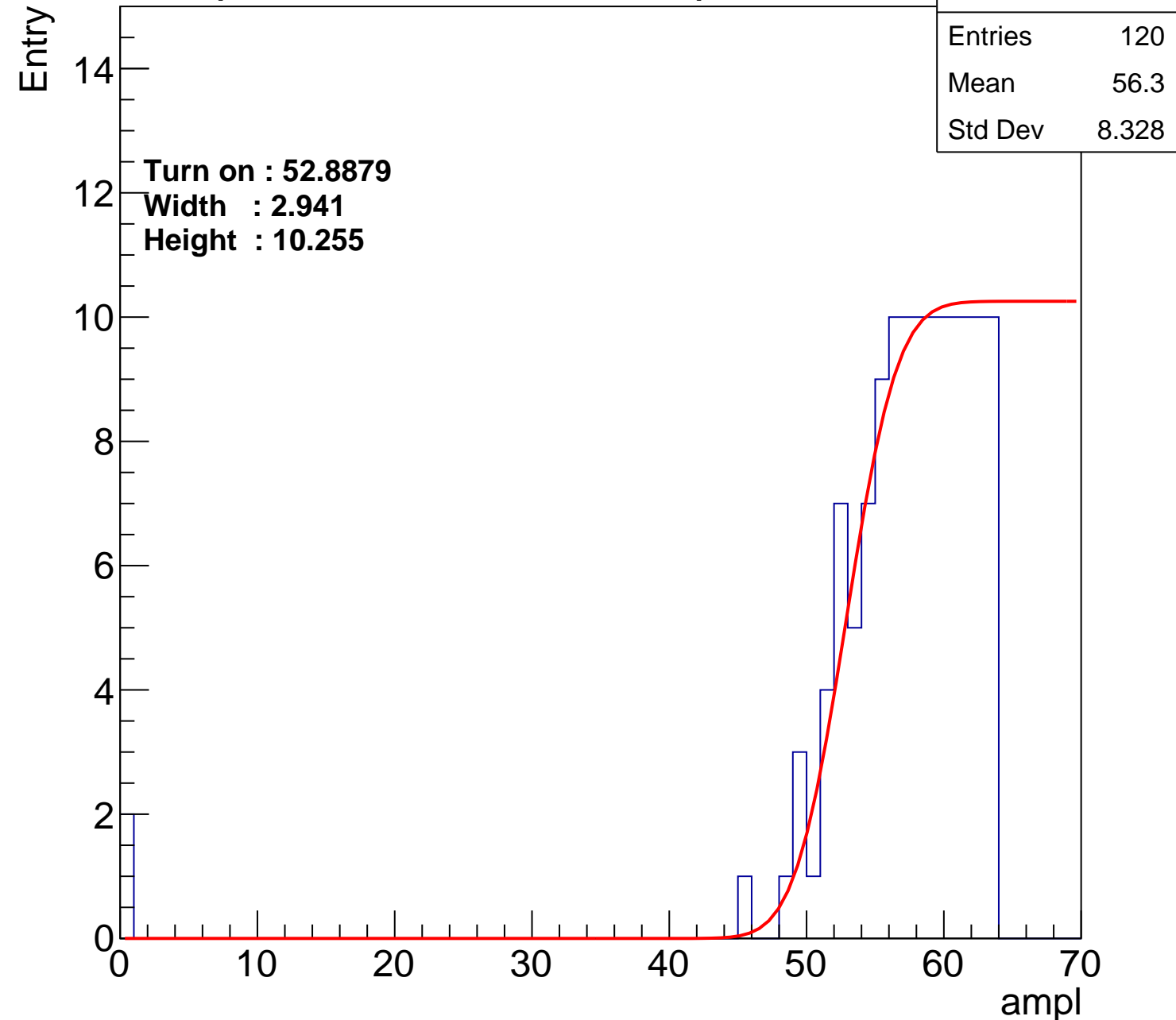
Width : 2.941

Height : 10.255

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	56.7
Std Dev	8.613

**Turn on : 53.7697**

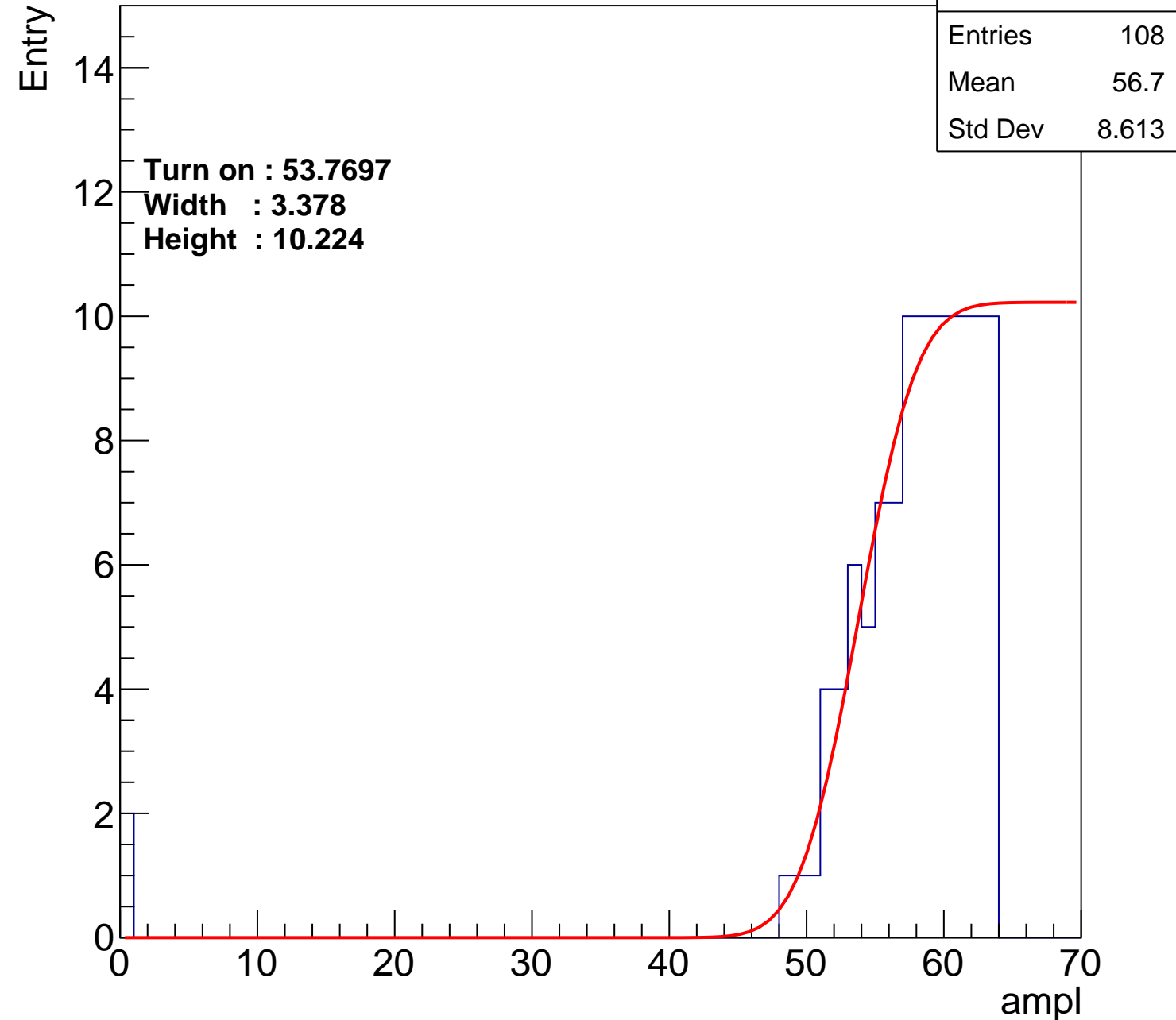
**Width : 3.378**

**Height : 10.224**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.11
Std Dev	12.16

**Turn on : 52.8975**

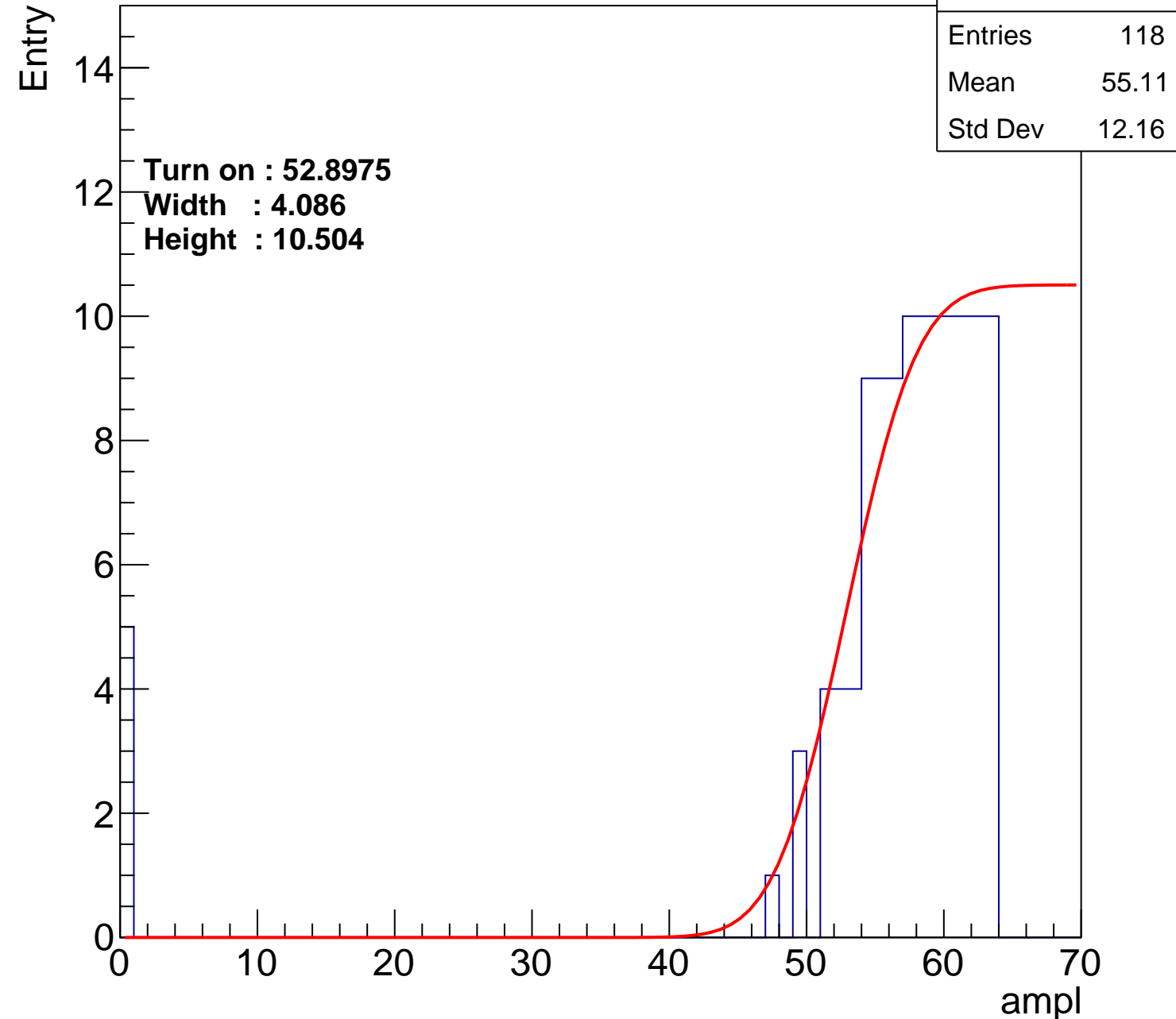
**Width : 4.086**

**Height : 10.504**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch51

calib\_packv5\_040323\_1717.root, FC#2, port C3

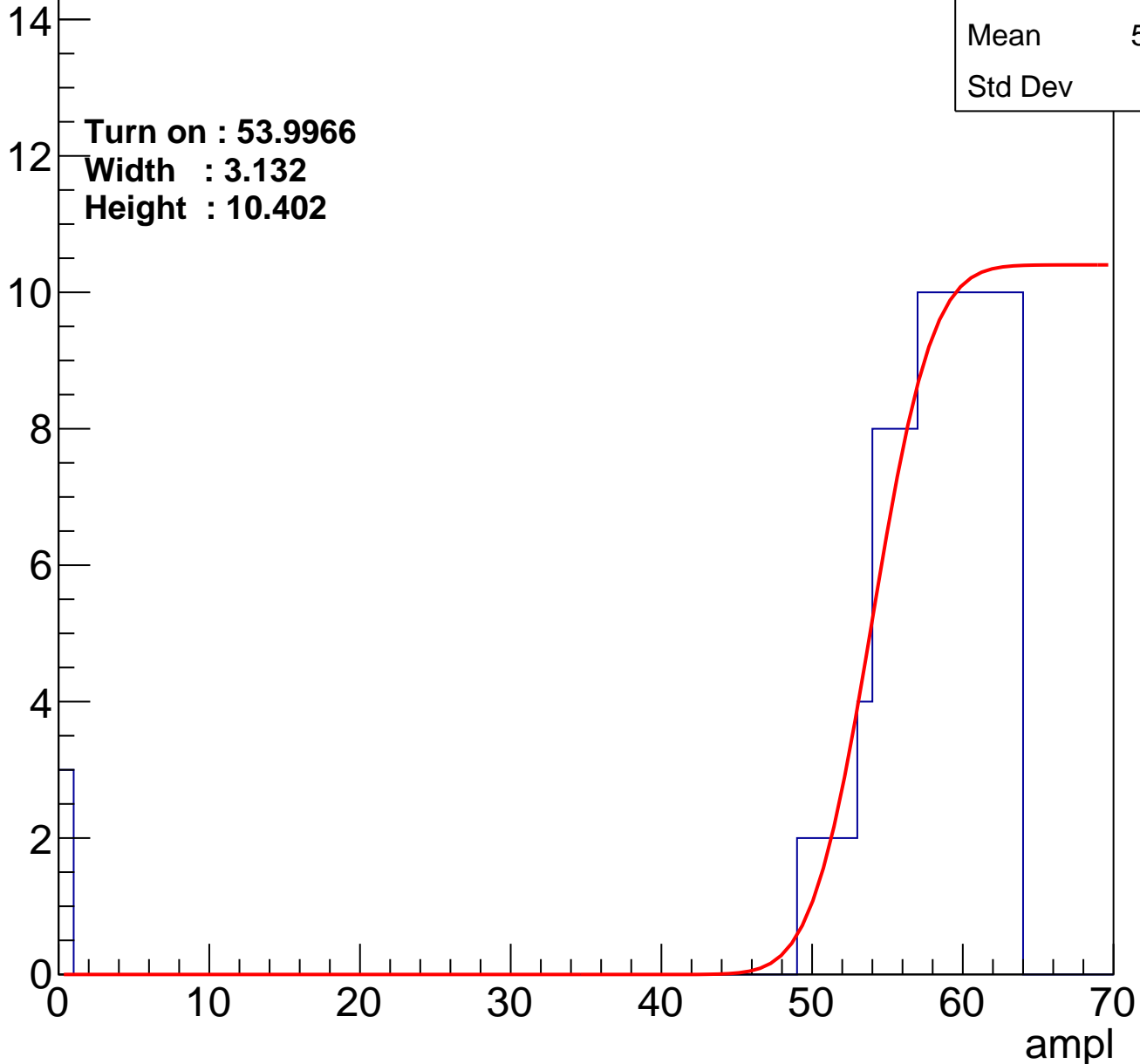
Entries	109
Mean	56.29
Std Dev	10.1

Turn on : 53.9966

Width : 3.132

Height : 10.402

Entry



# B0L103S, U25-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.05
Std Dev	9.719

Turn on : 52.7683

Width : 1.424

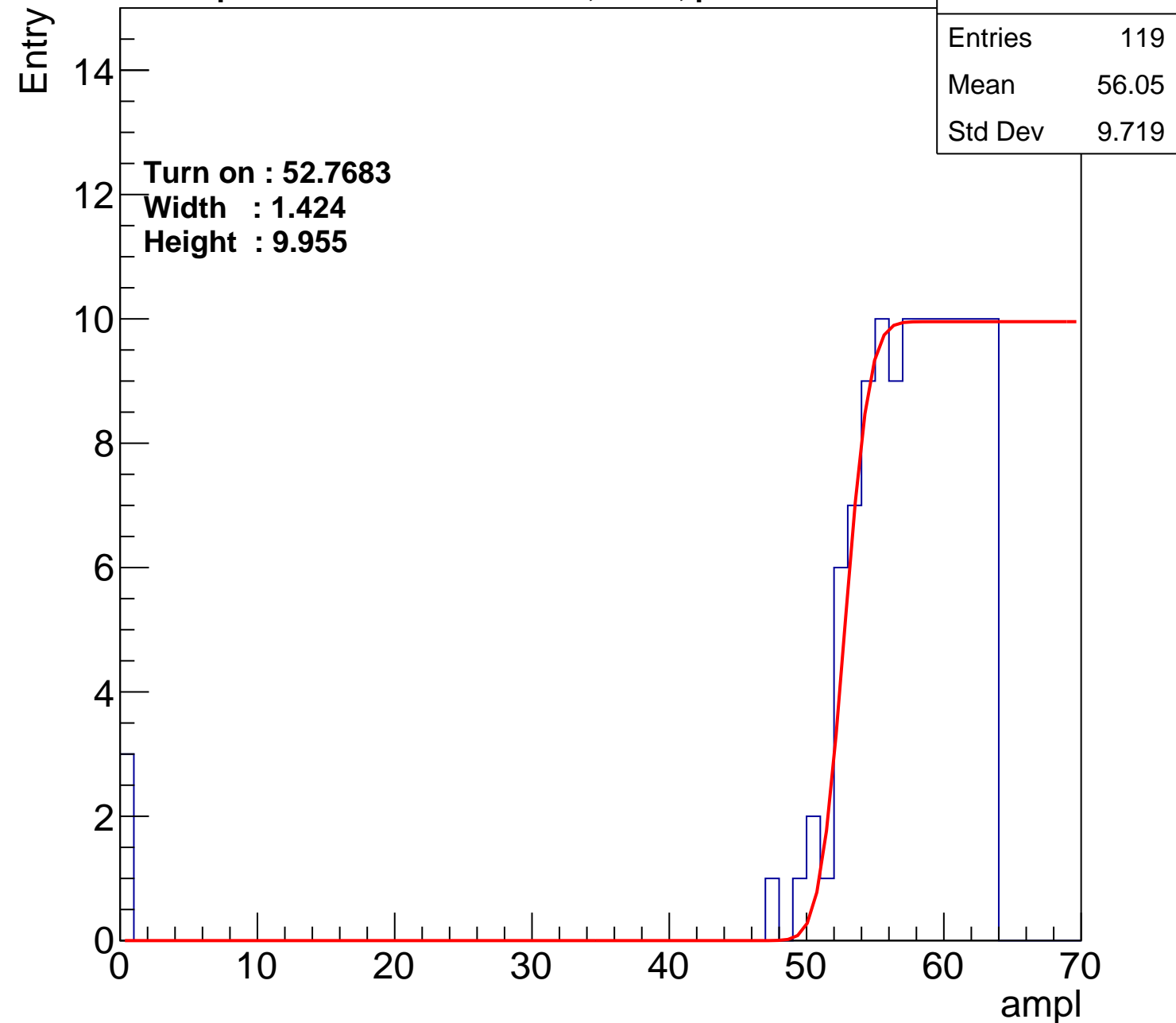
Height : 9.955

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U25-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	55.95
Std Dev	11.28

Turn on : 53.2849

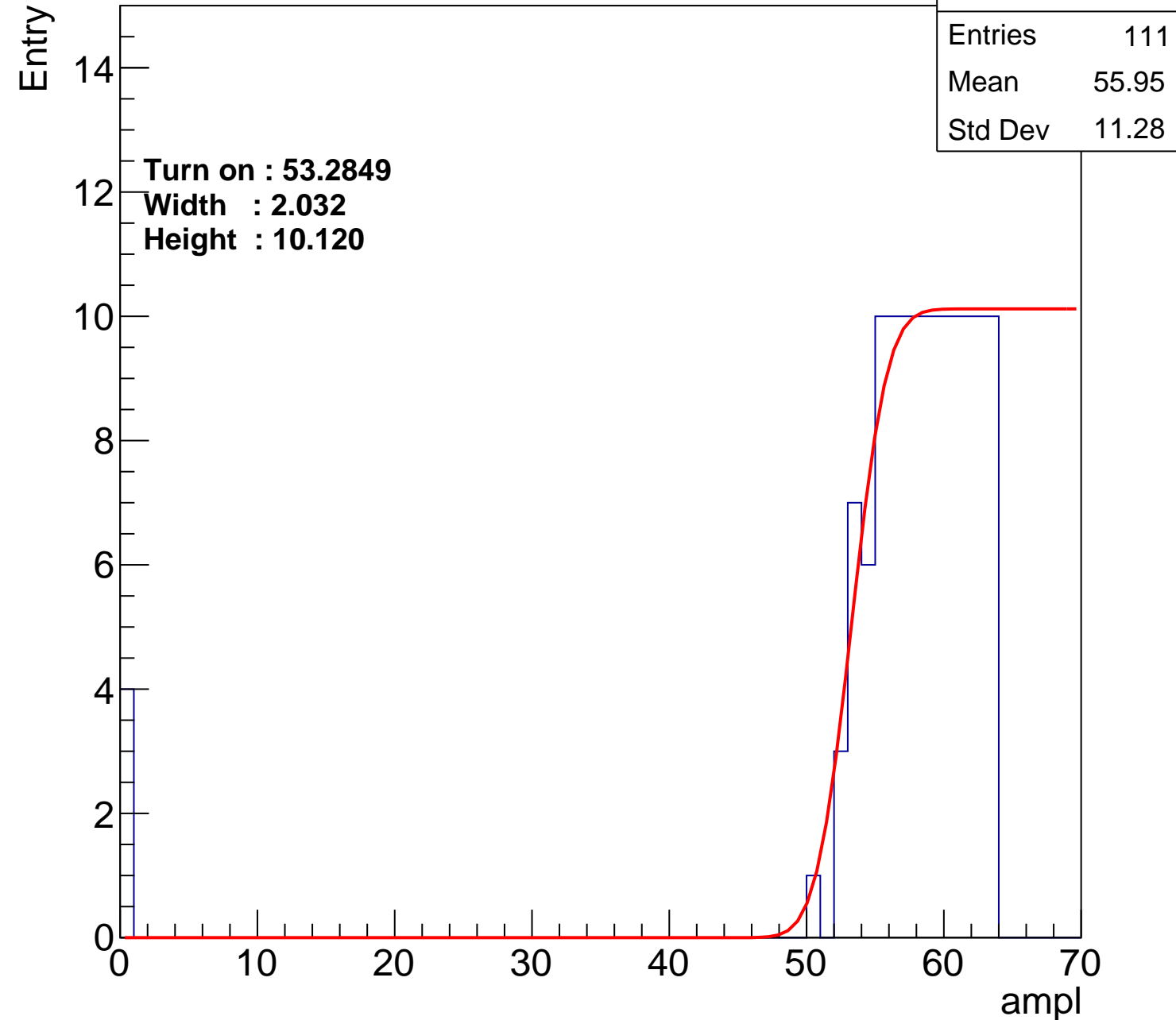
Width : 2.032

Height : 10.120

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.57
Std Dev	9.436

Turn on : 51.1228

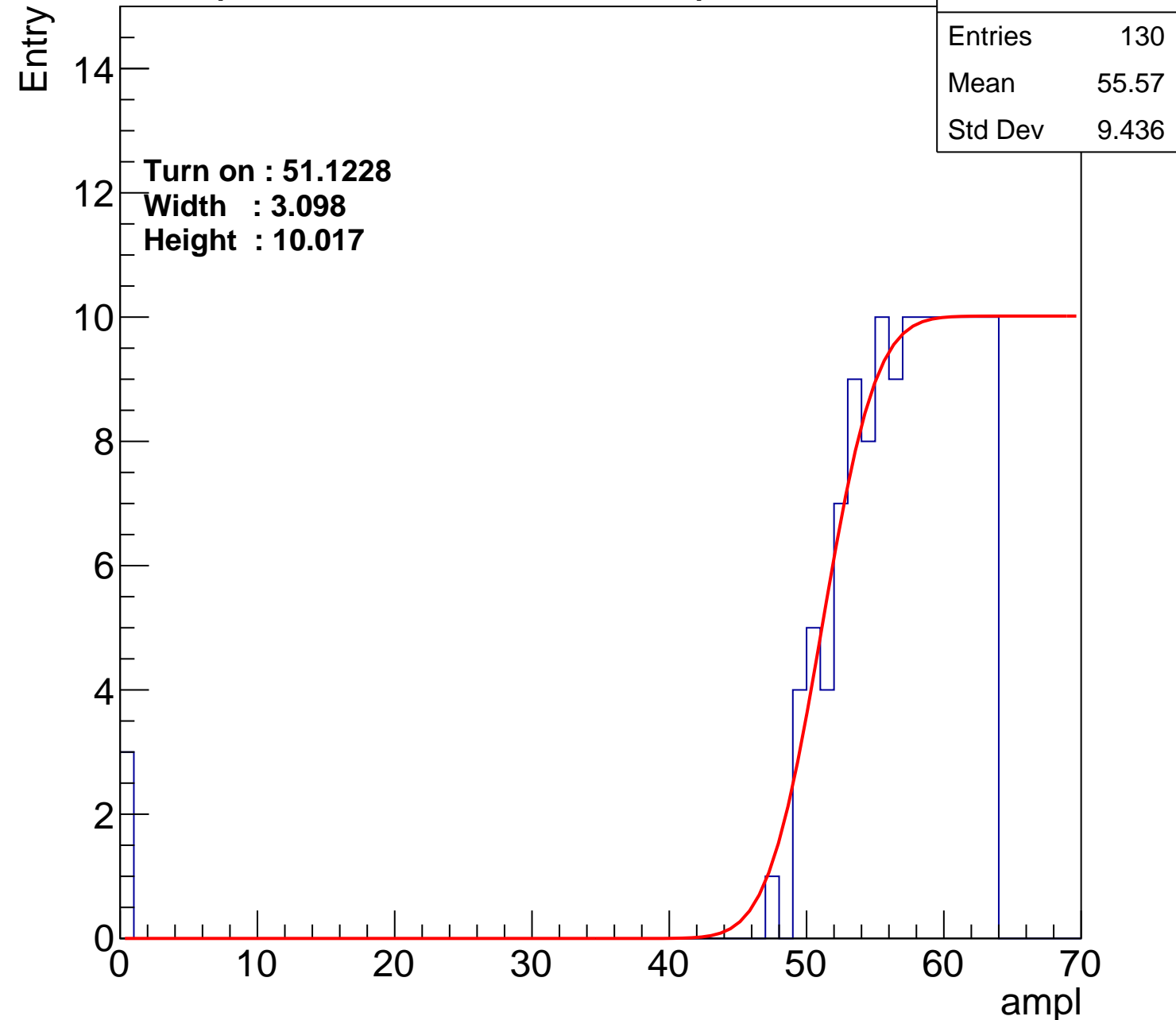
Width : 3.098

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

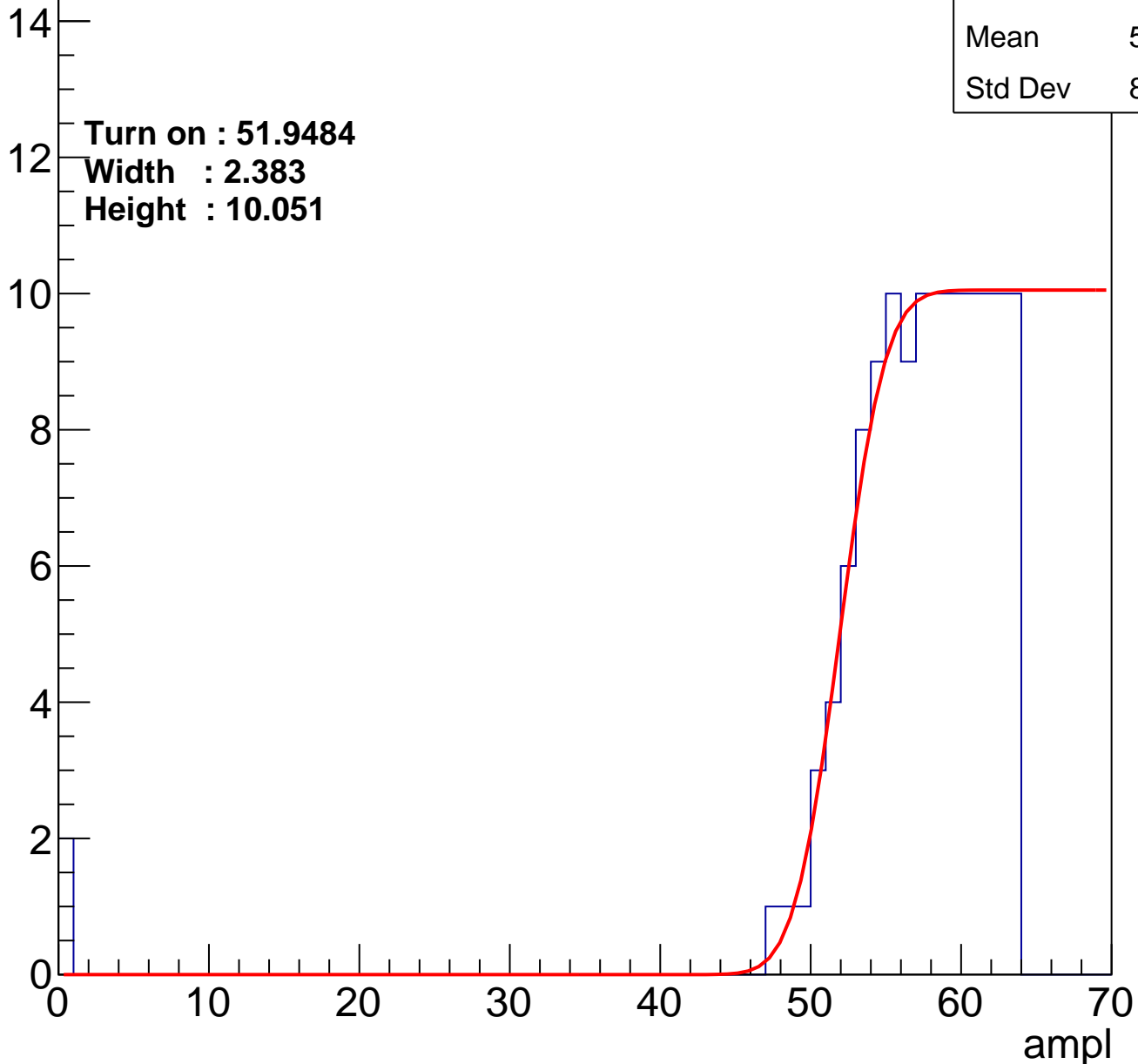
Entries	124
Mean	56.24
Std Dev	8.172

Turn on : 51.9484

Width : 2.383

Height : 10.051

Entry



# B0L103S, U25-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.62
Std Dev	10.17

Turn on : 49.7264

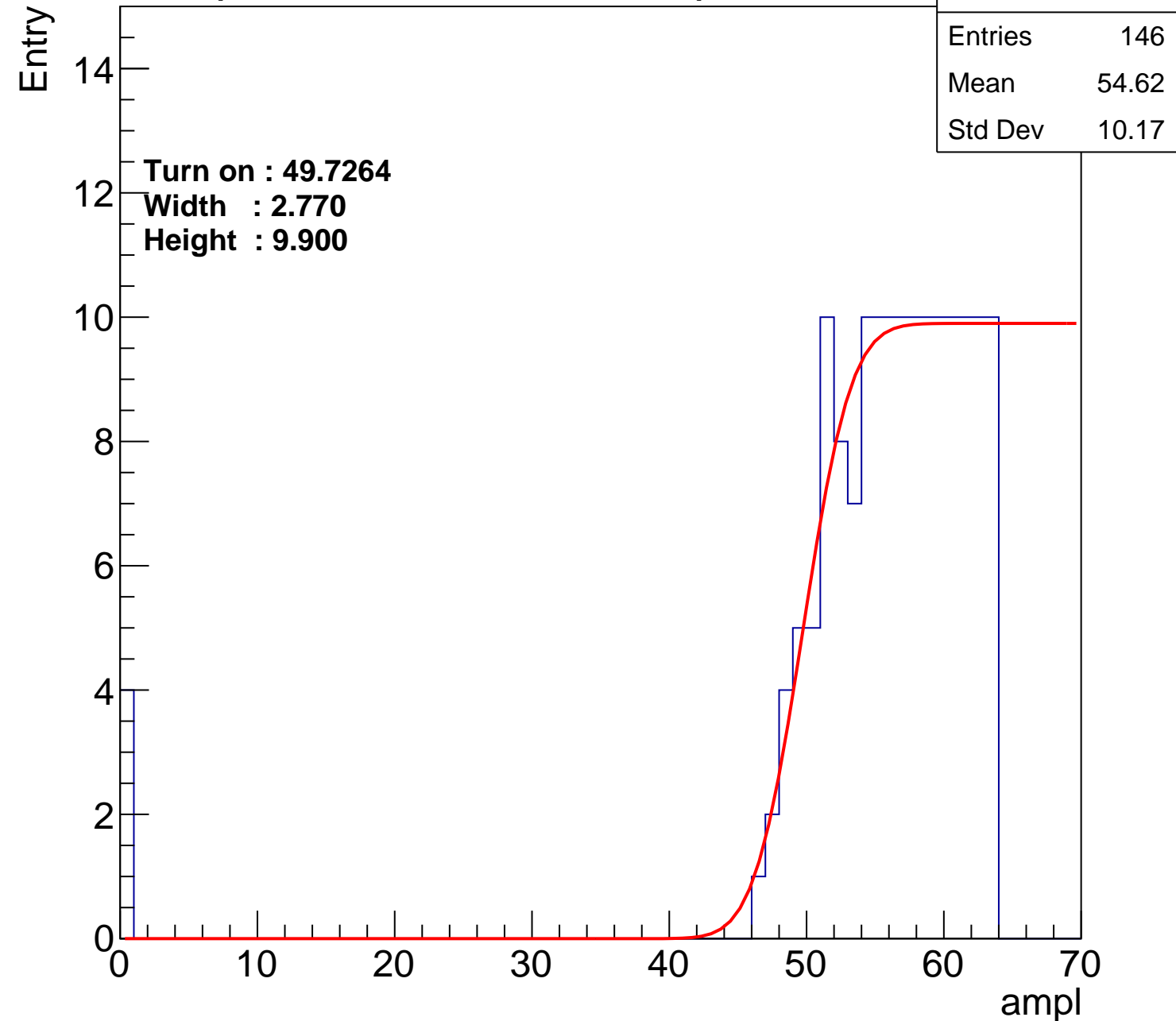
Width : 2.770

Height : 9.900

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	56.25
Std Dev	6.561

Turn on : 51.7209

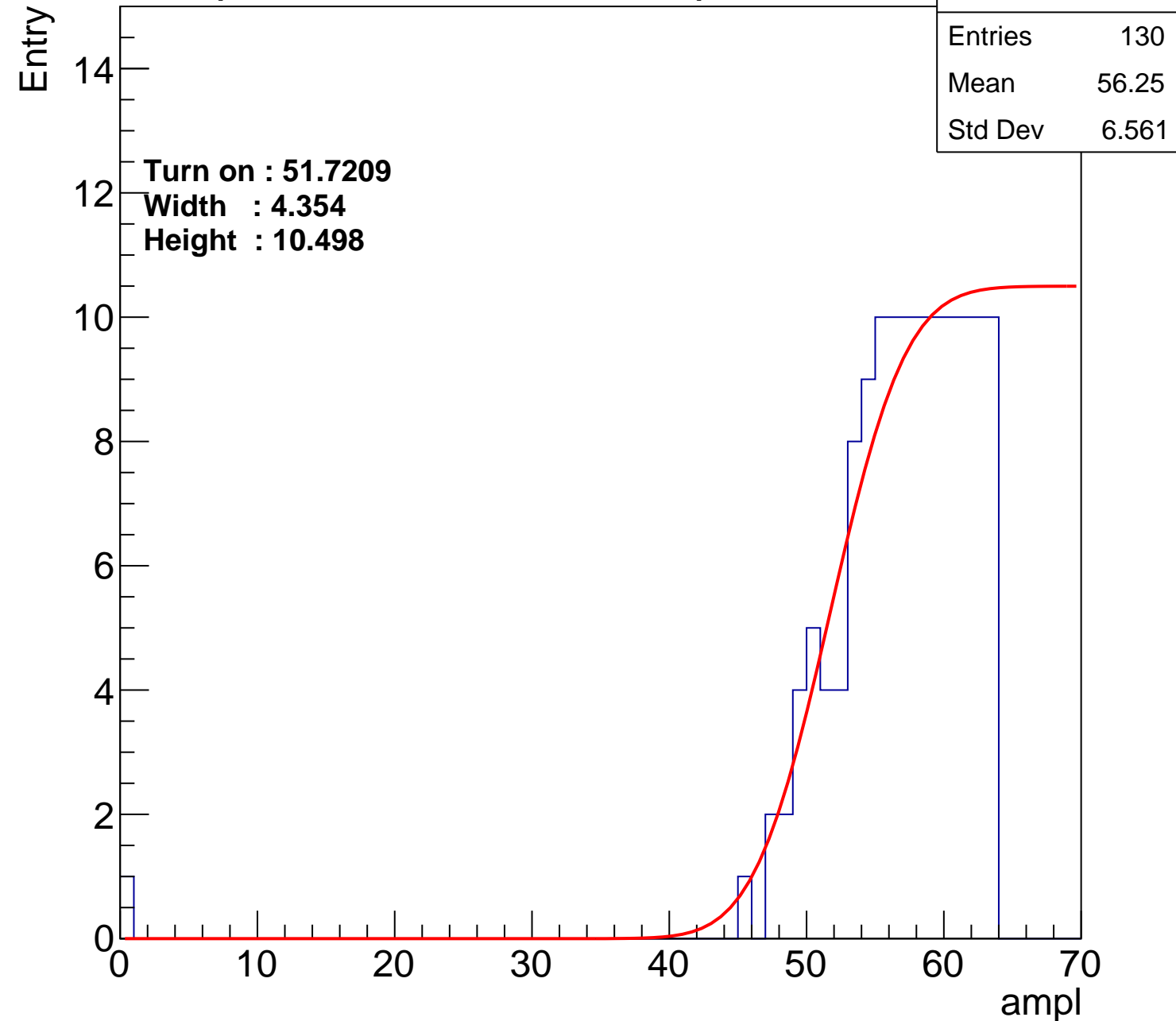
Width : 4.354

Height : 10.498

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

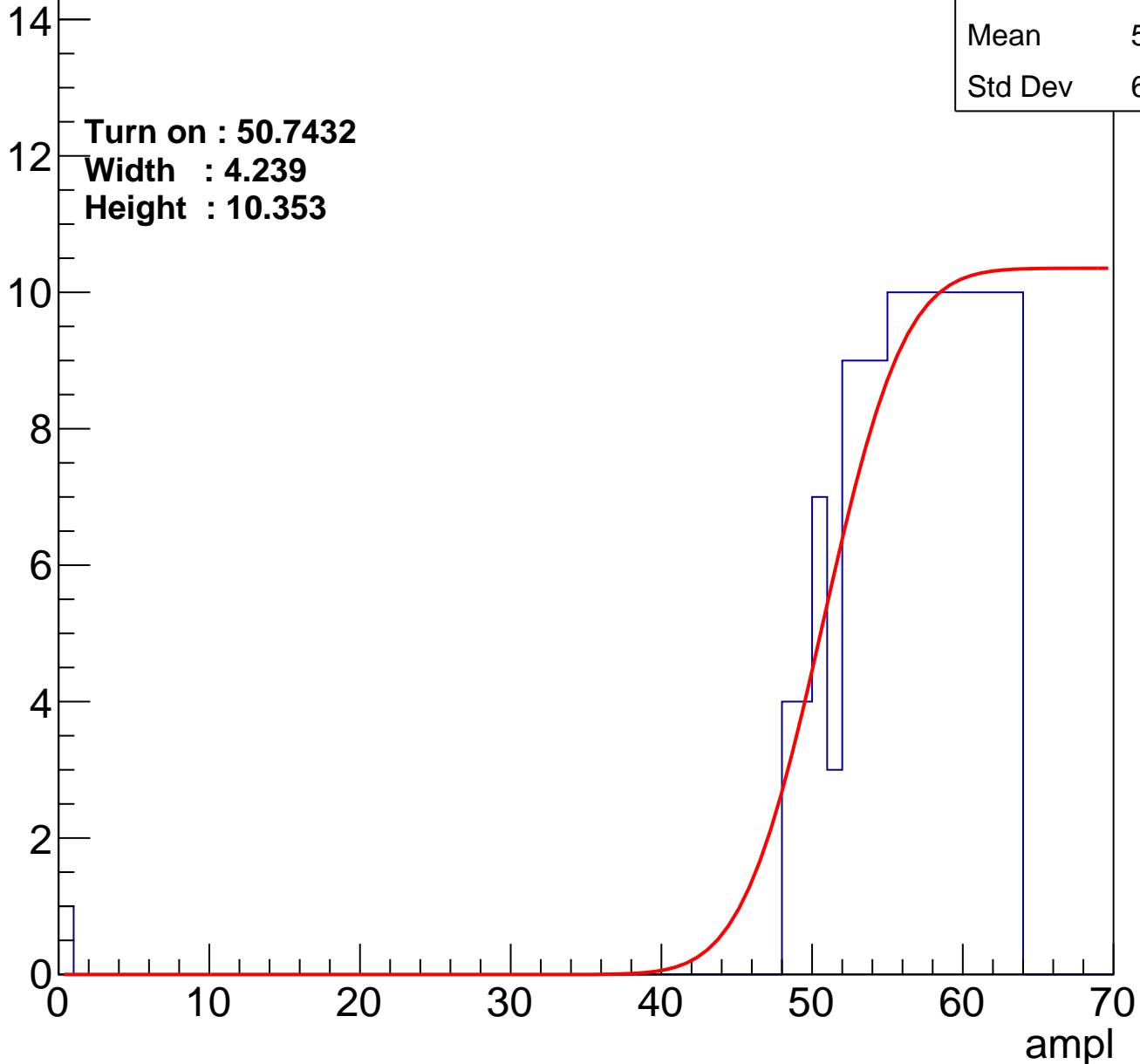
Entries	136
Mean	56.12
Std Dev	6.408

Turn on : 50.7432

Width : 4.239

Height : 10.353

Entry



# B0L103S, U25-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.29
Std Dev	8.354

Turn on : 52.8590

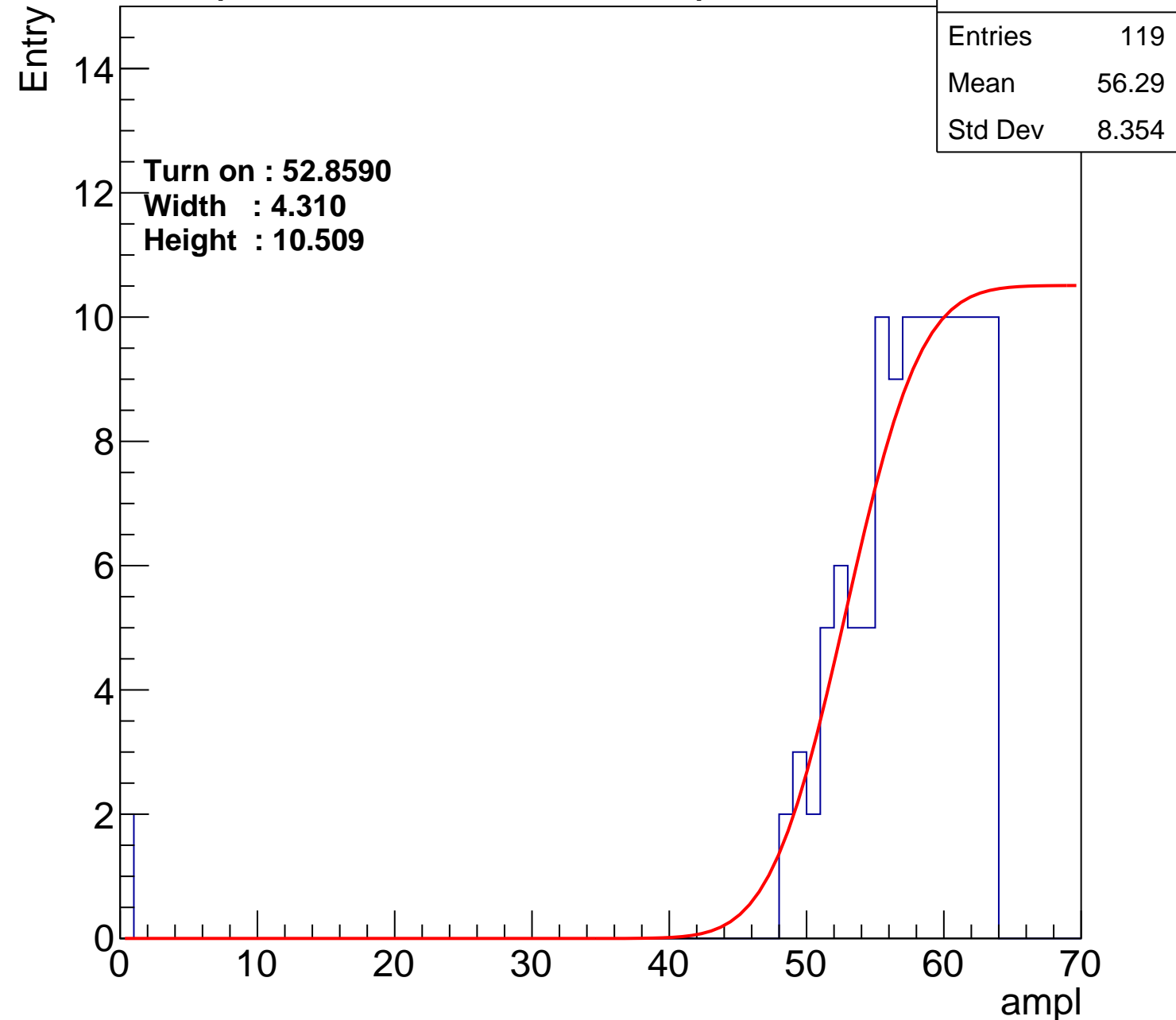
Width : 4.310

Height : 10.509

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch60

calib\_packv5\_040323\_1717.root, FC#2, port C3

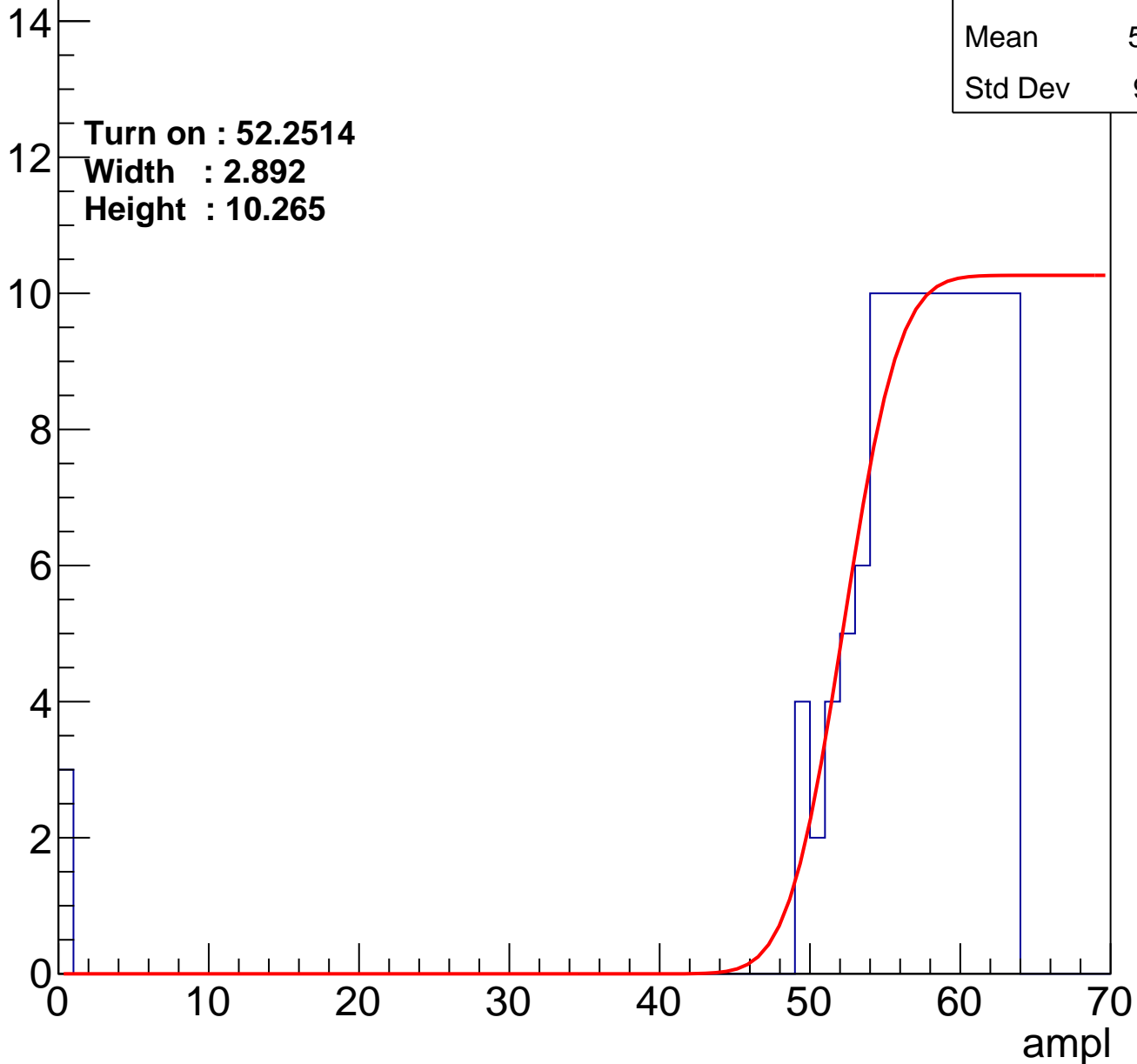
Entries	124
Mean	55.87
Std Dev	9.571

Turn on : 52.2514

Width : 2.892

Height : 10.265

Entry



# B0L103S, U25-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

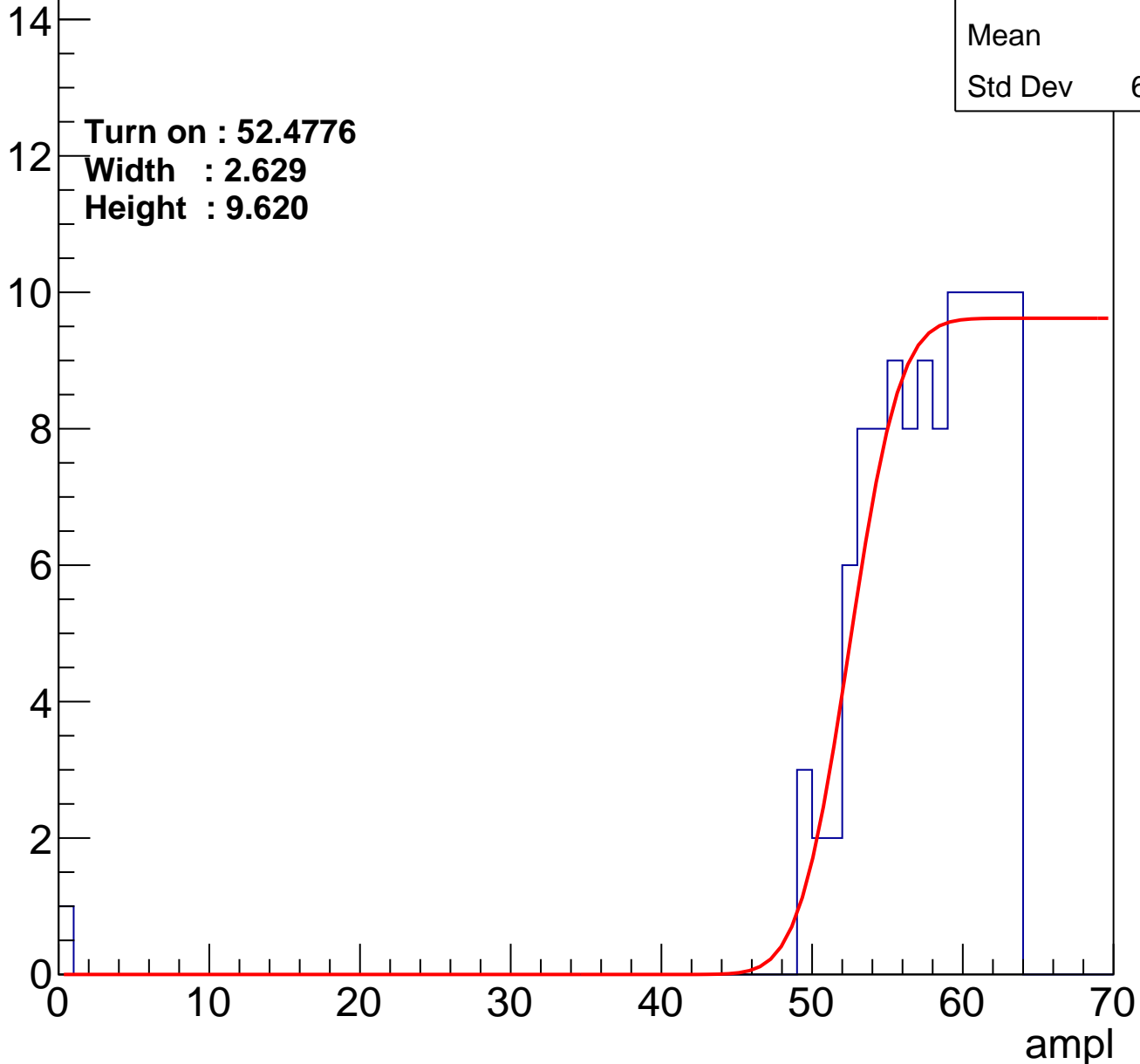
Entries	114
Mean	56.9
Std Dev	6.564

Turn on : 52.4776

Width : 2.629

Height : 9.620

Entry



# B0L103S, U25-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

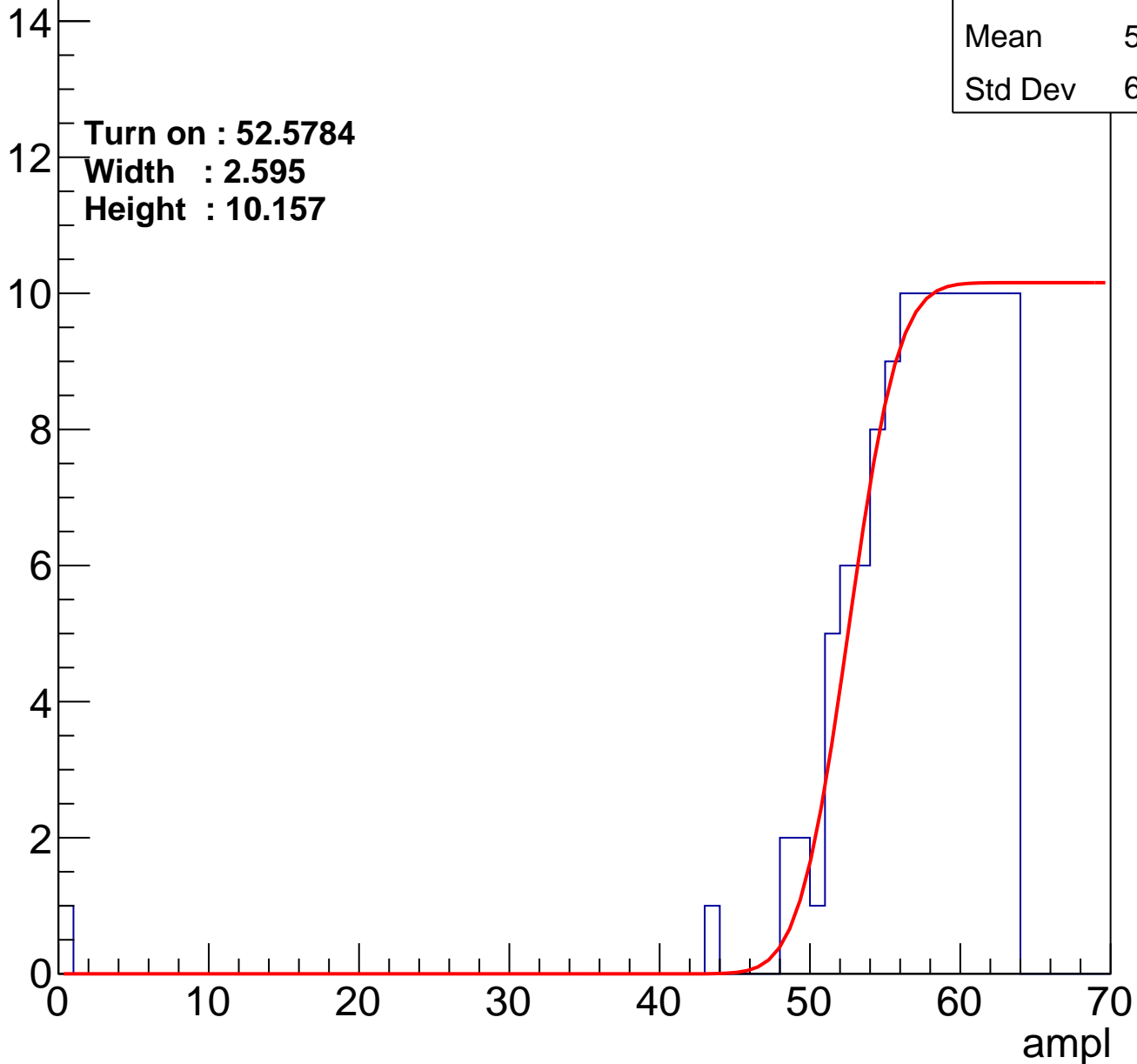
Entries	121
Mean	56.69
Std Dev	6.572

Turn on : 52.5784

Width : 2.595

Height : 10.157

Entry





# B0L103S, U25-ch63

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.41
Std Dev	9.307

Turn on : 50.7889

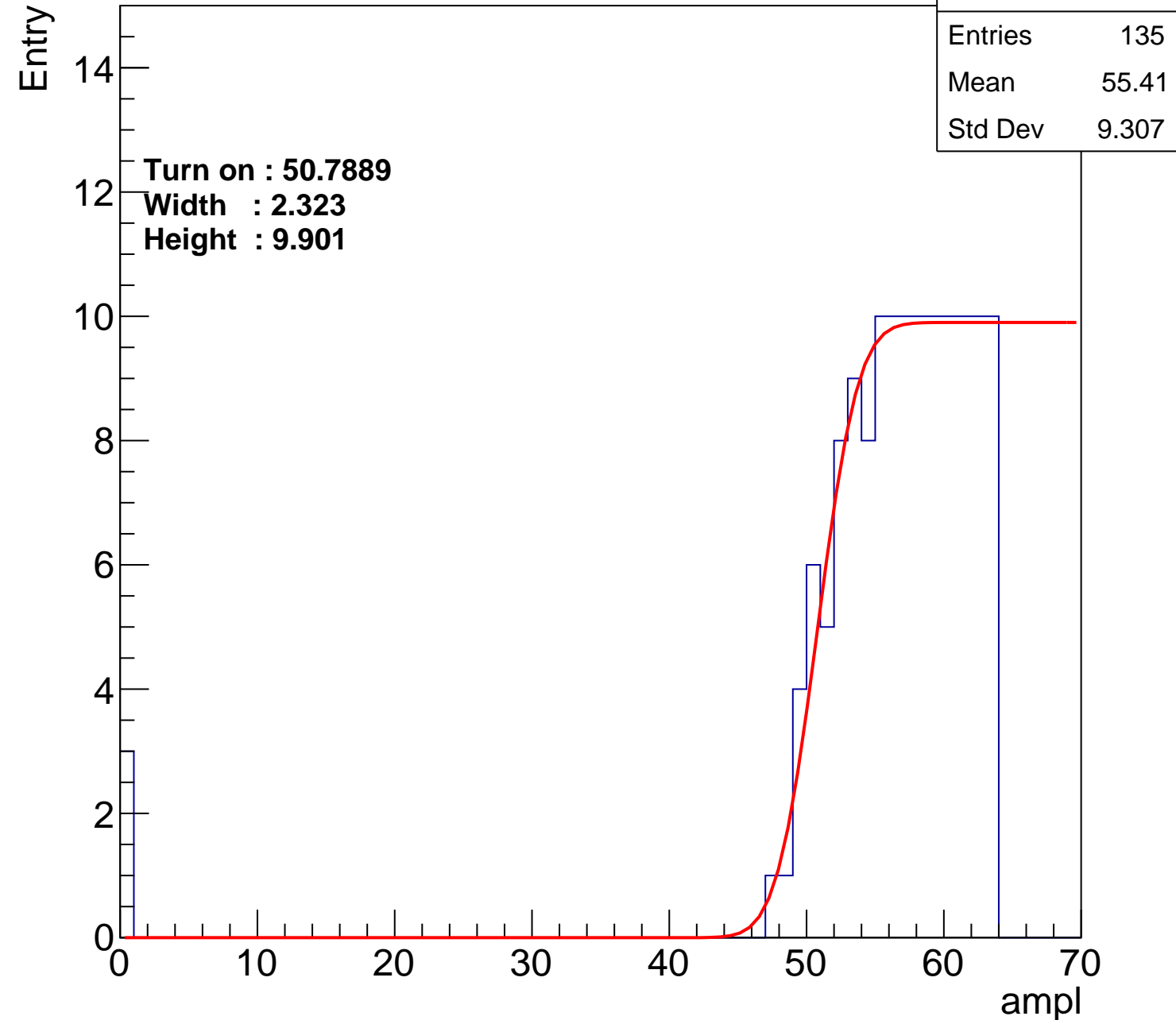
Width : 2.323

Height : 9.901

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	56.61
Std Dev	6.372

Turn on : 50.9765

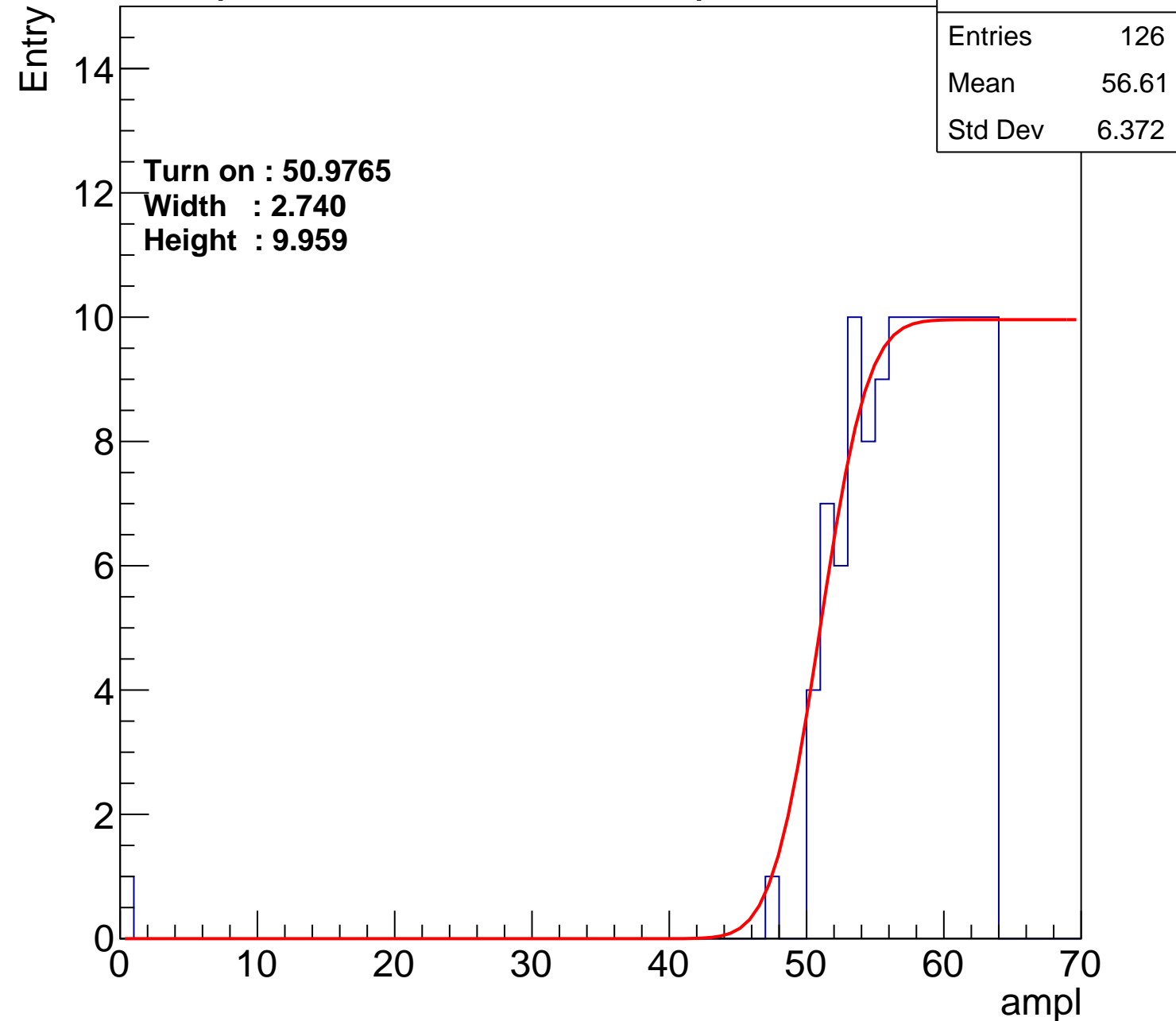
Width : 2.740

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

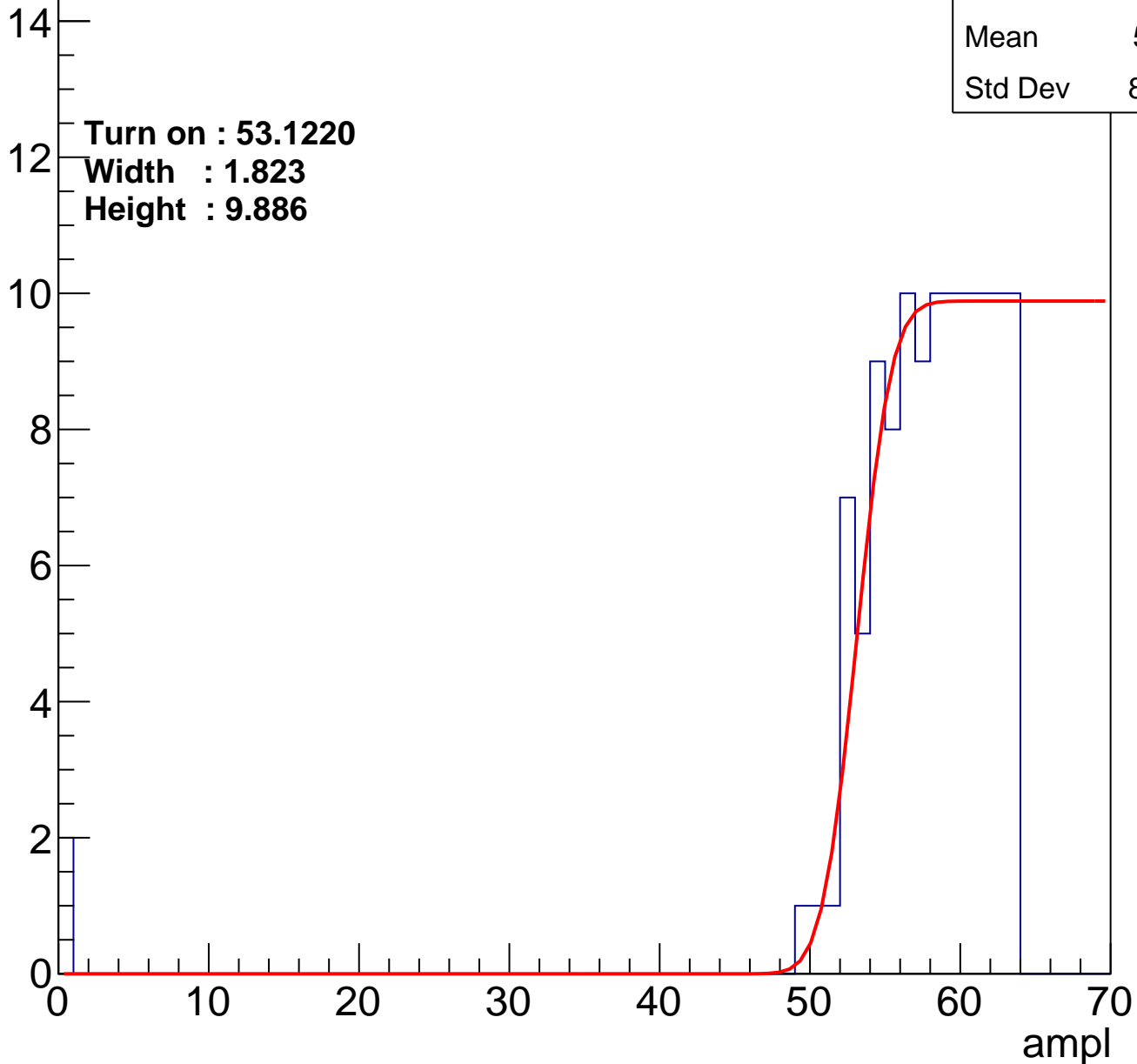
Entries	113
Mean	56.71
Std Dev	8.375

Turn on : 53.1220

Width : 1.823

Height : 9.886

Entry



# B0L103S, U25-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	56.33
Std Dev	6.476

Turn on : 51.7293

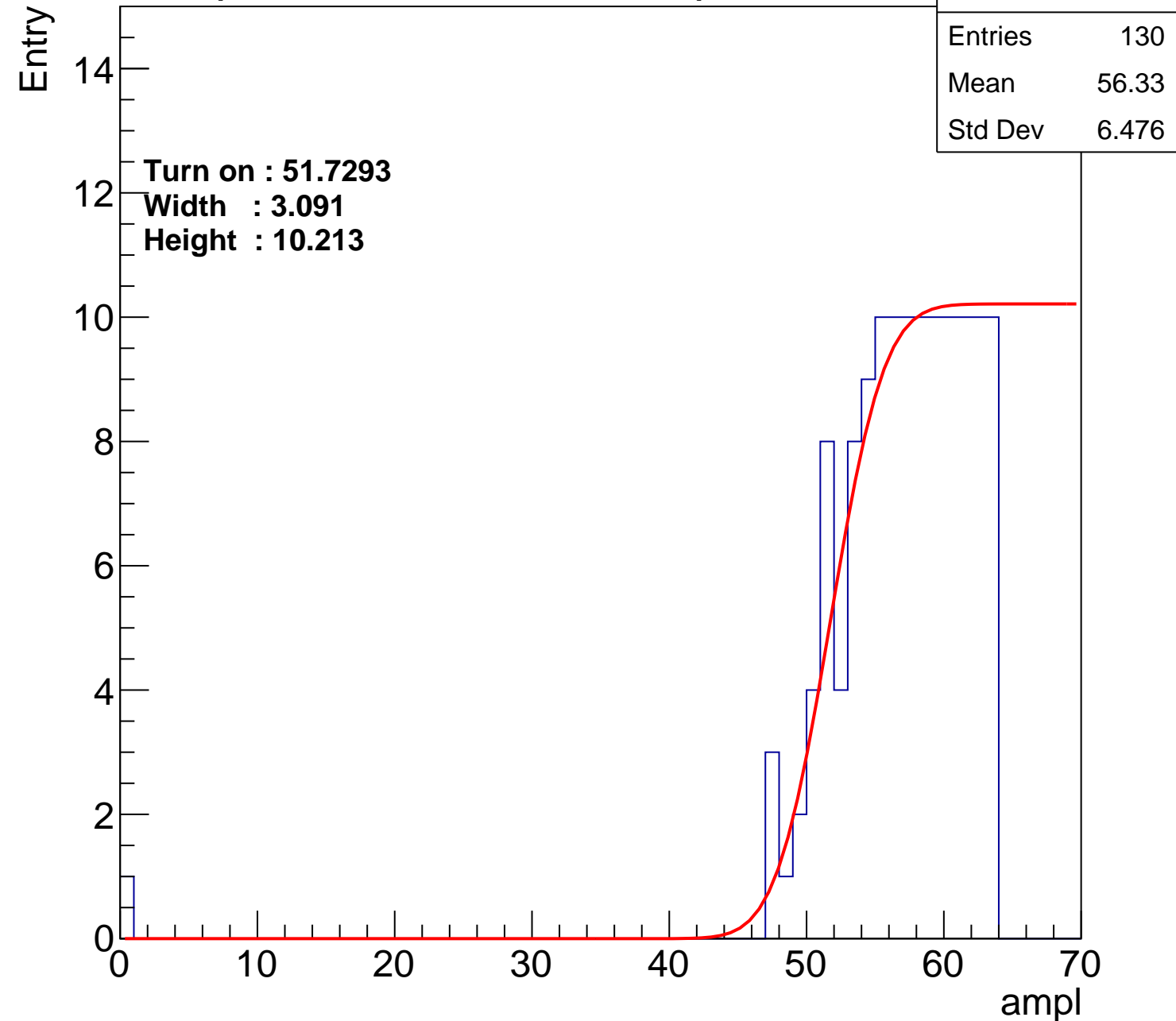
Width : 3.091

Height : 10.213

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	101
Mean	55.92
Std Dev	11.85

Turn on : 55.2781

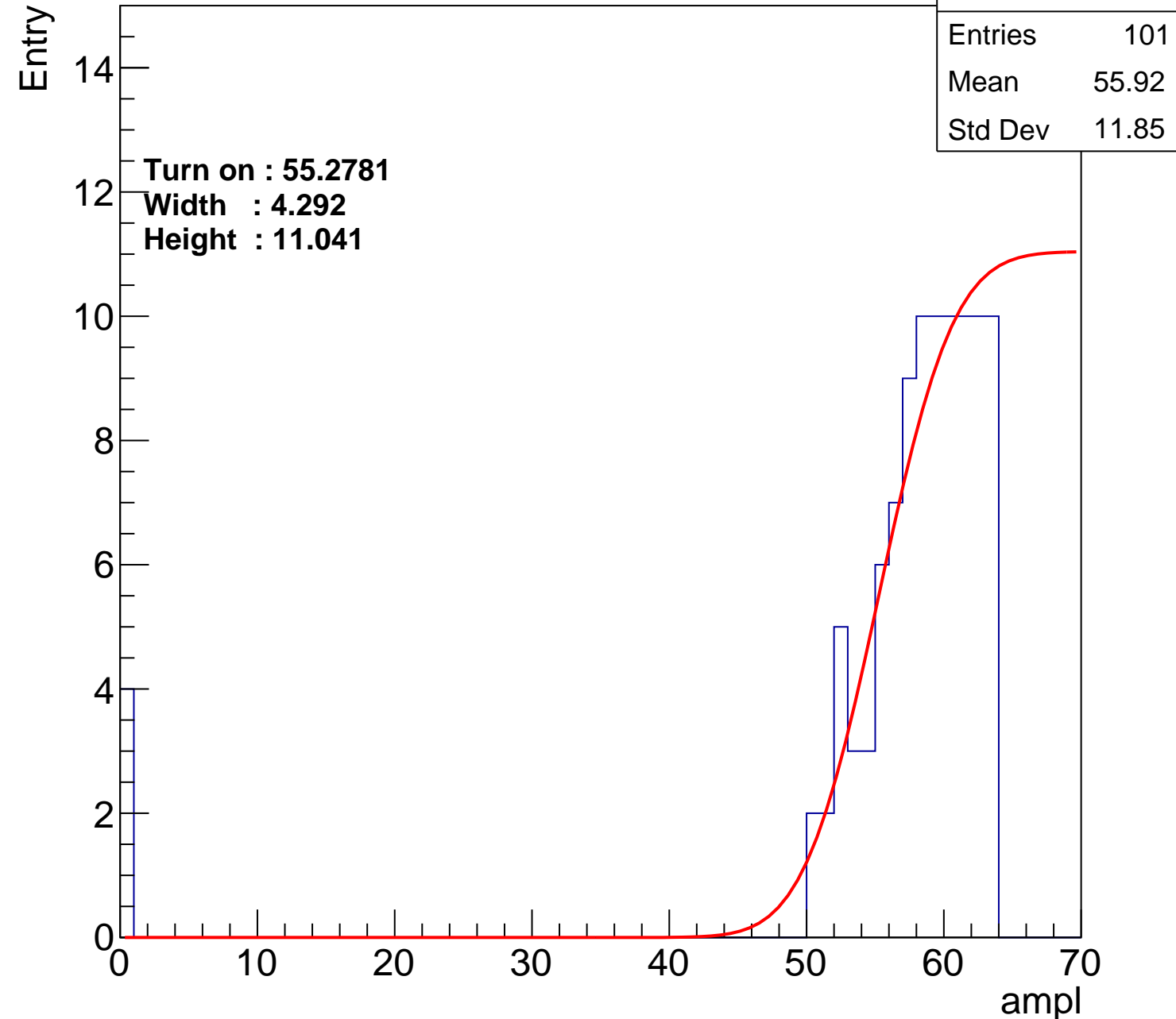
Width : 4.292

Height : 11.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

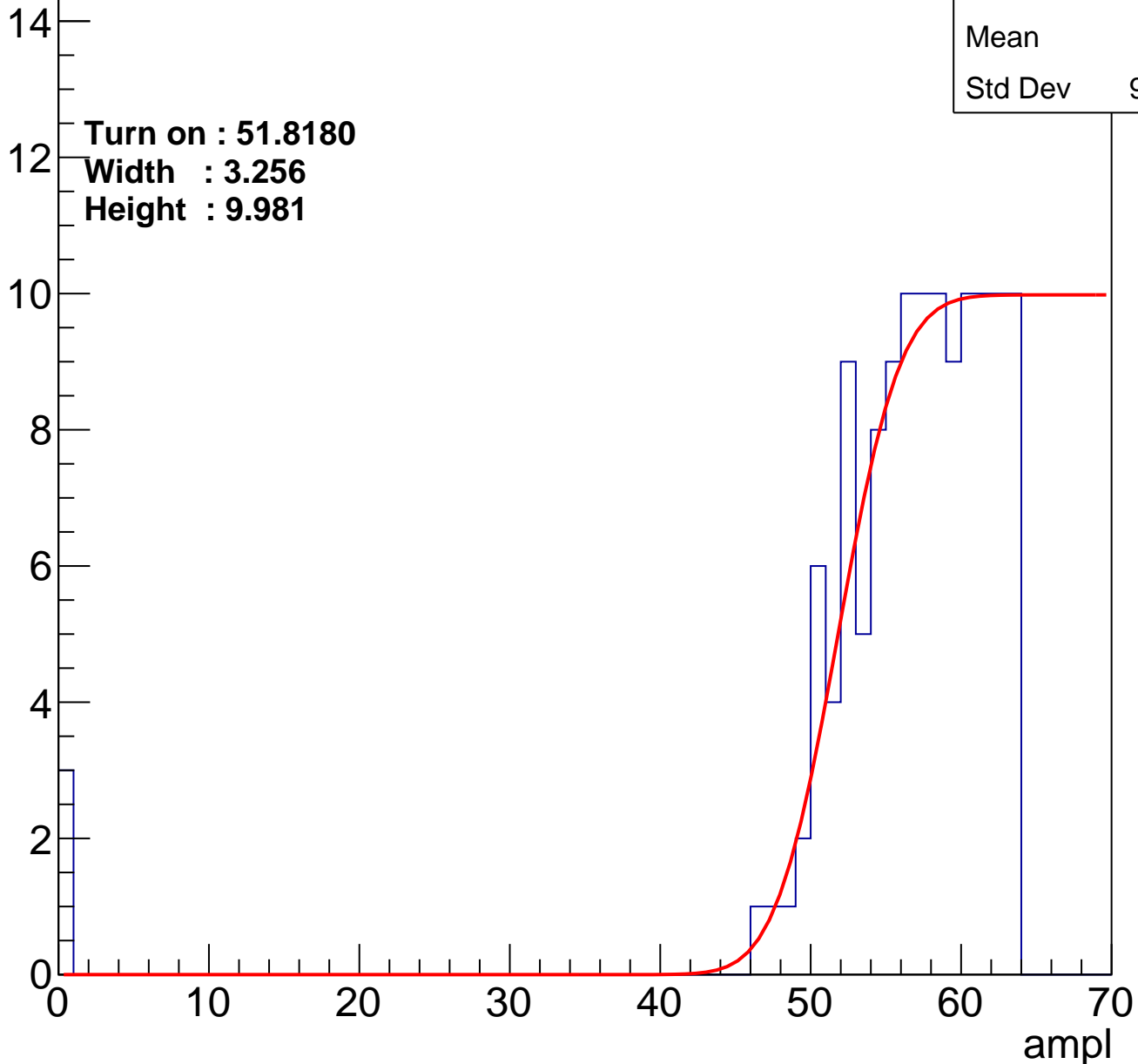
Entries	128
Mean	55.5
Std Dev	9.543

Turn on : 51.8180

Width : 3.256

Height : 9.981

Entry



# B0L103S, U25-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	56.22
Std Dev	6.417

Turn on : 50.7445

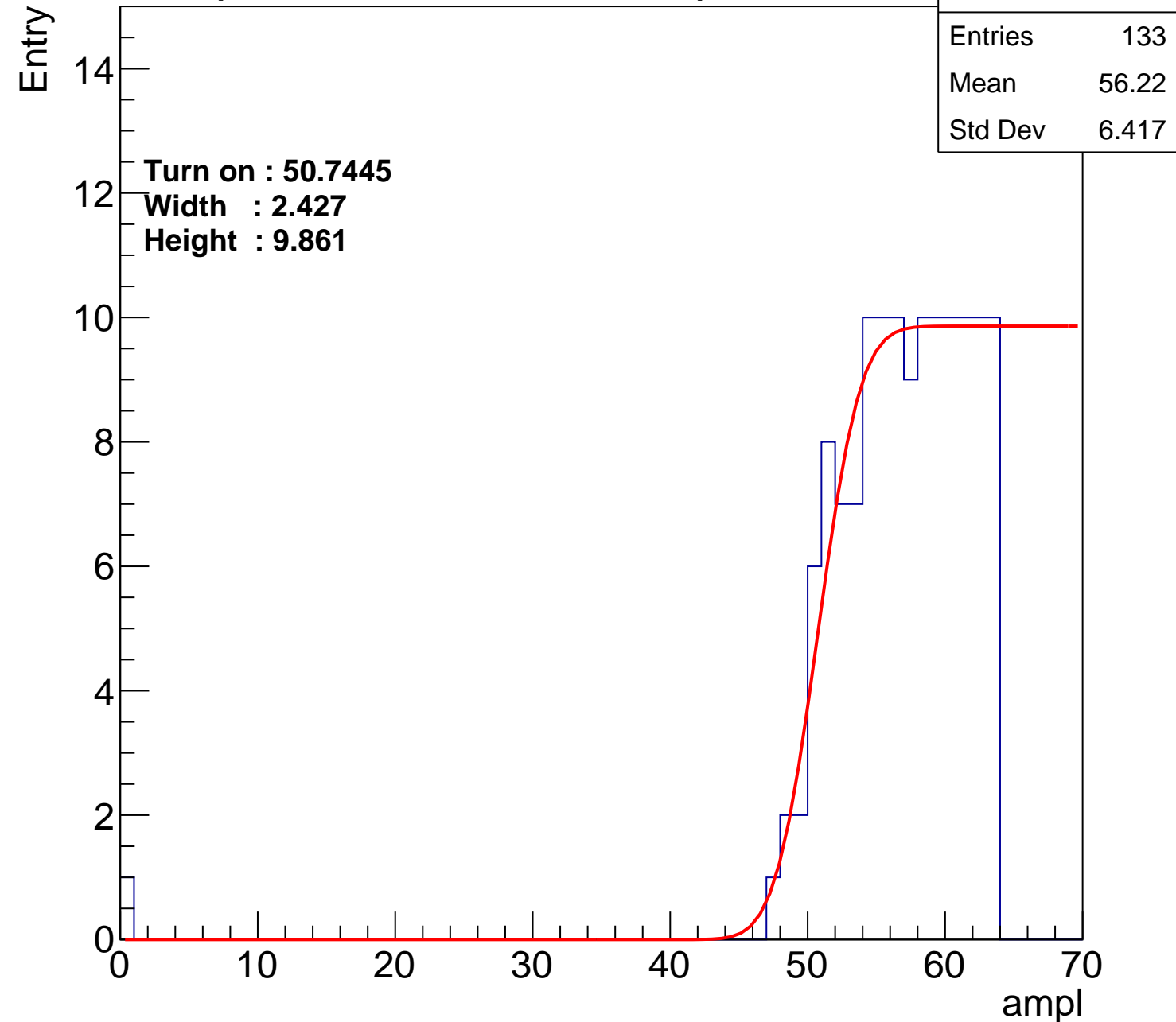
Width : 2.427

Height : 9.861

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.06
Std Dev	9.952

Turn on : 52.9180

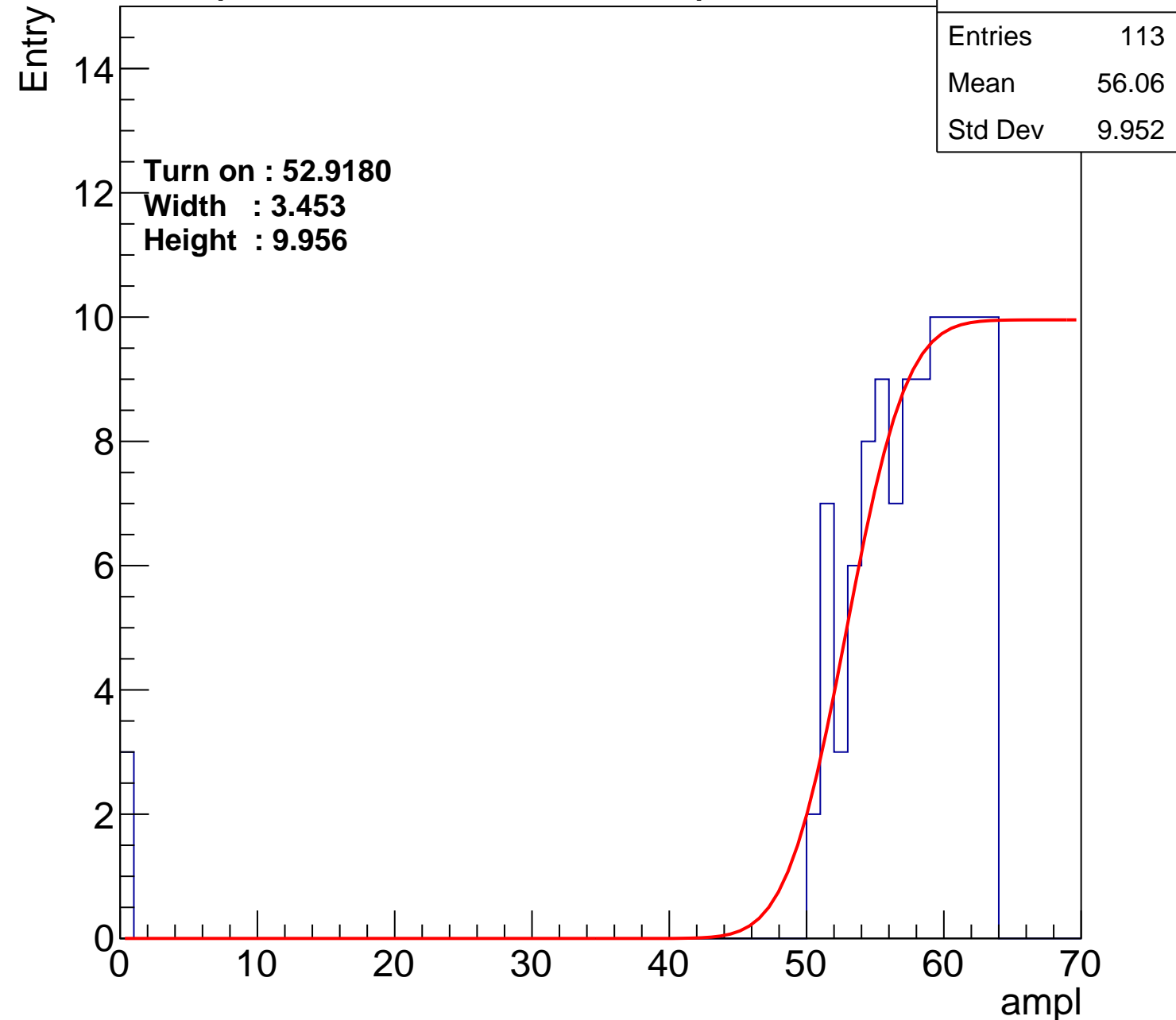
Width : 3.453

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.47
Std Dev	11.06

Turn on : 52.7198

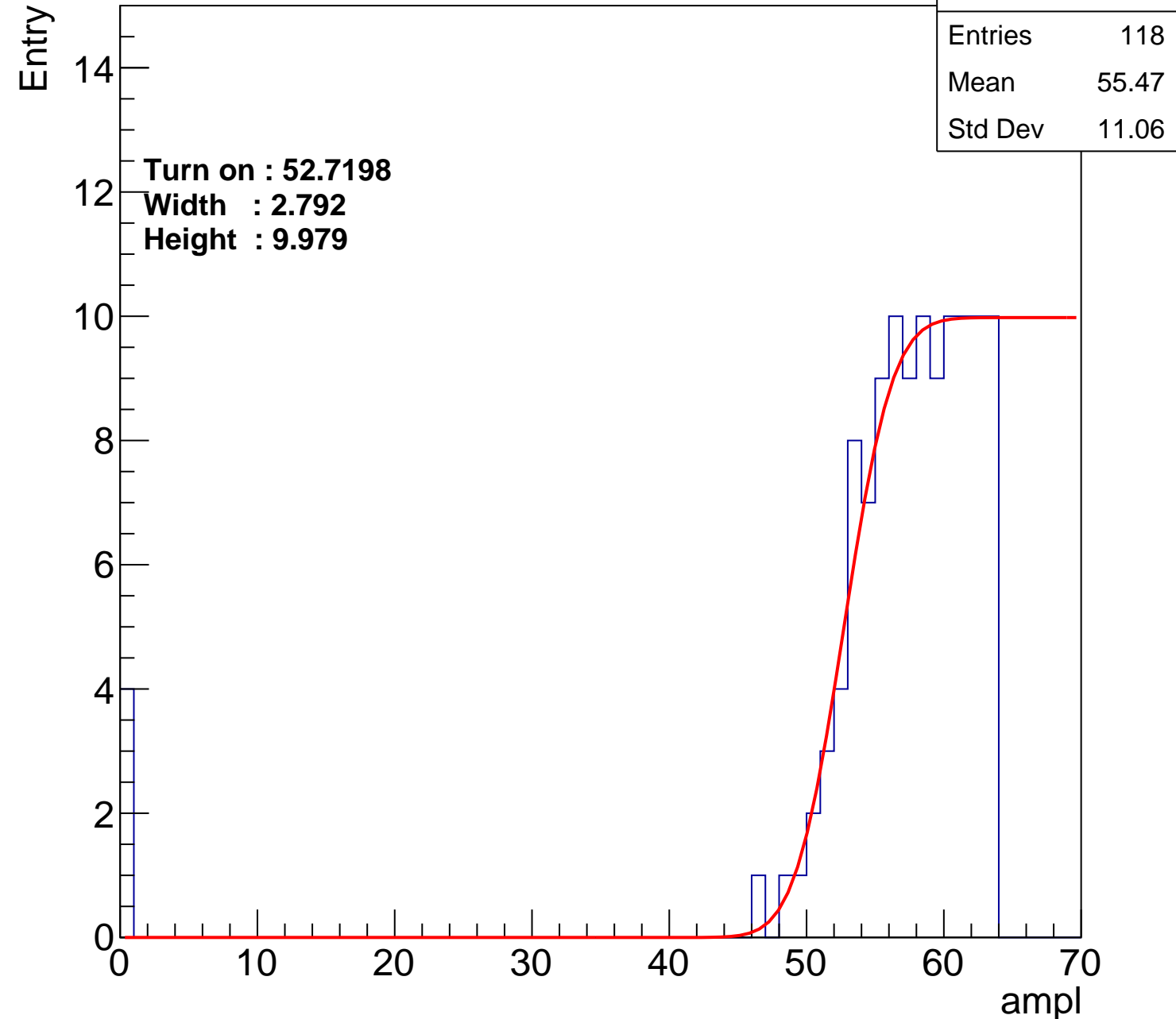
Width : 2.792

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	56.54
Std Dev	6.447

Turn on : 51.4622

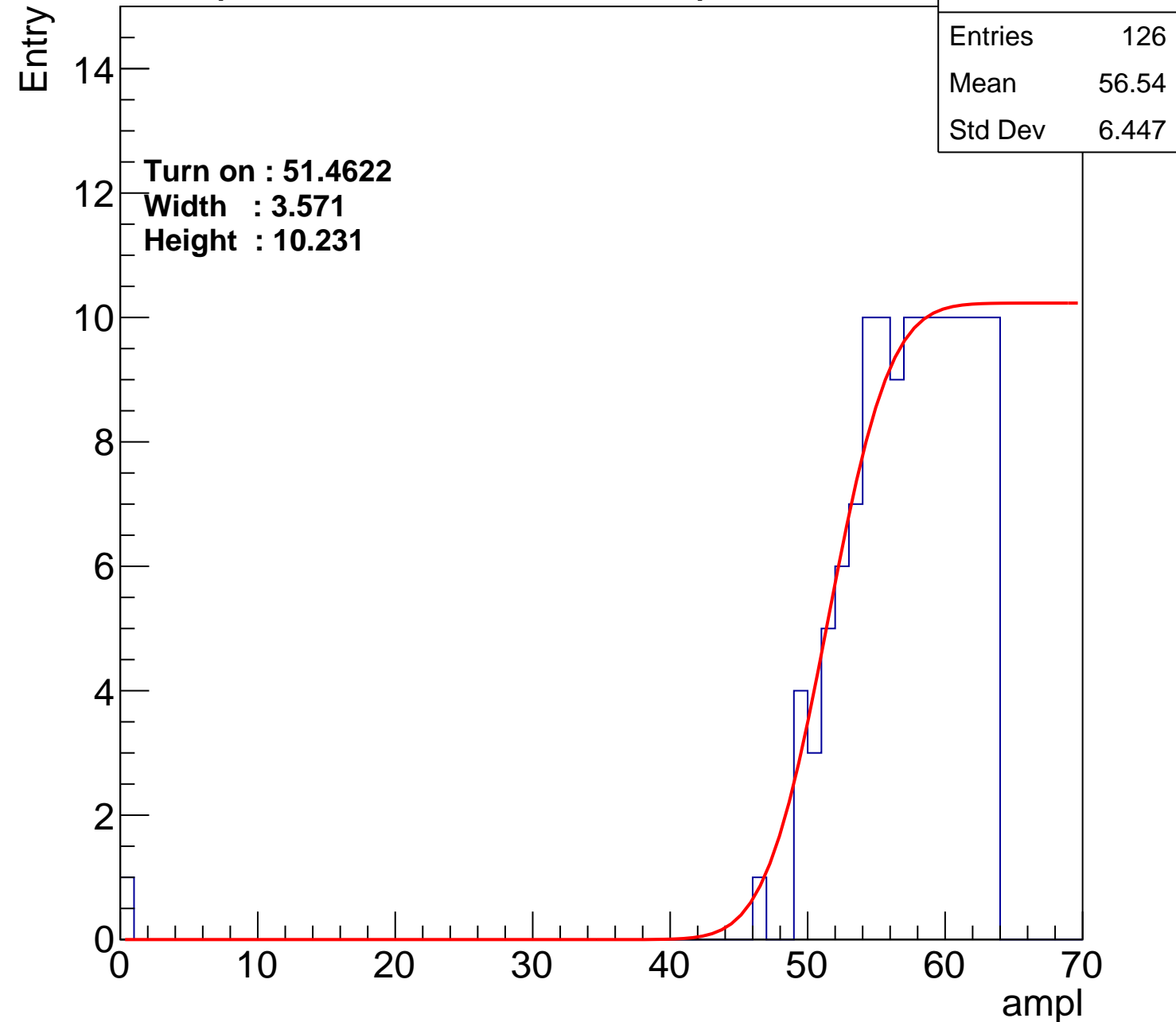
Width : 3.571

Height : 10.231

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	99
Mean	57.85
Std Dev	6.616

**Turn on : 54.0613**

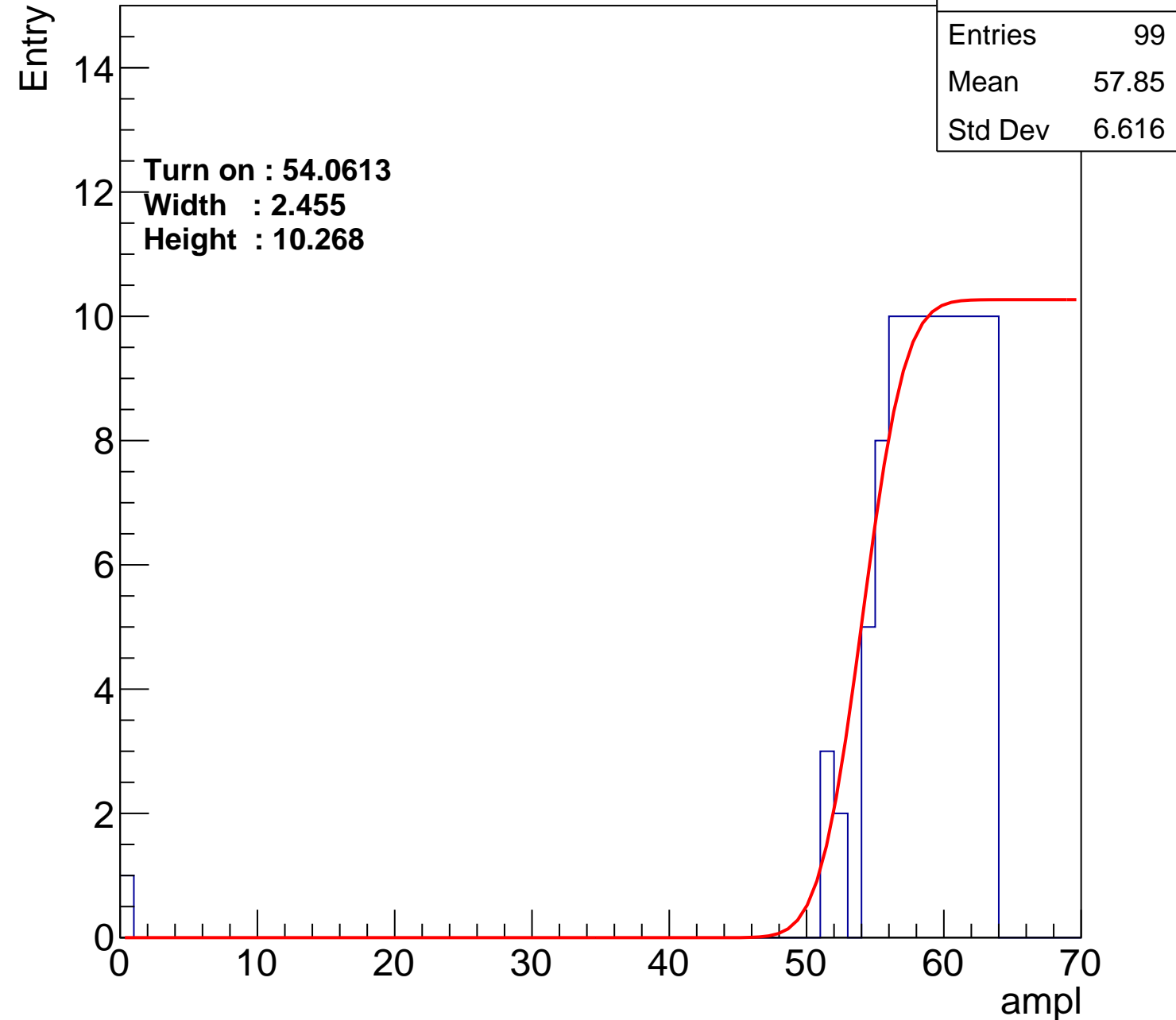
**Width : 2.455**

**Height : 10.268**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

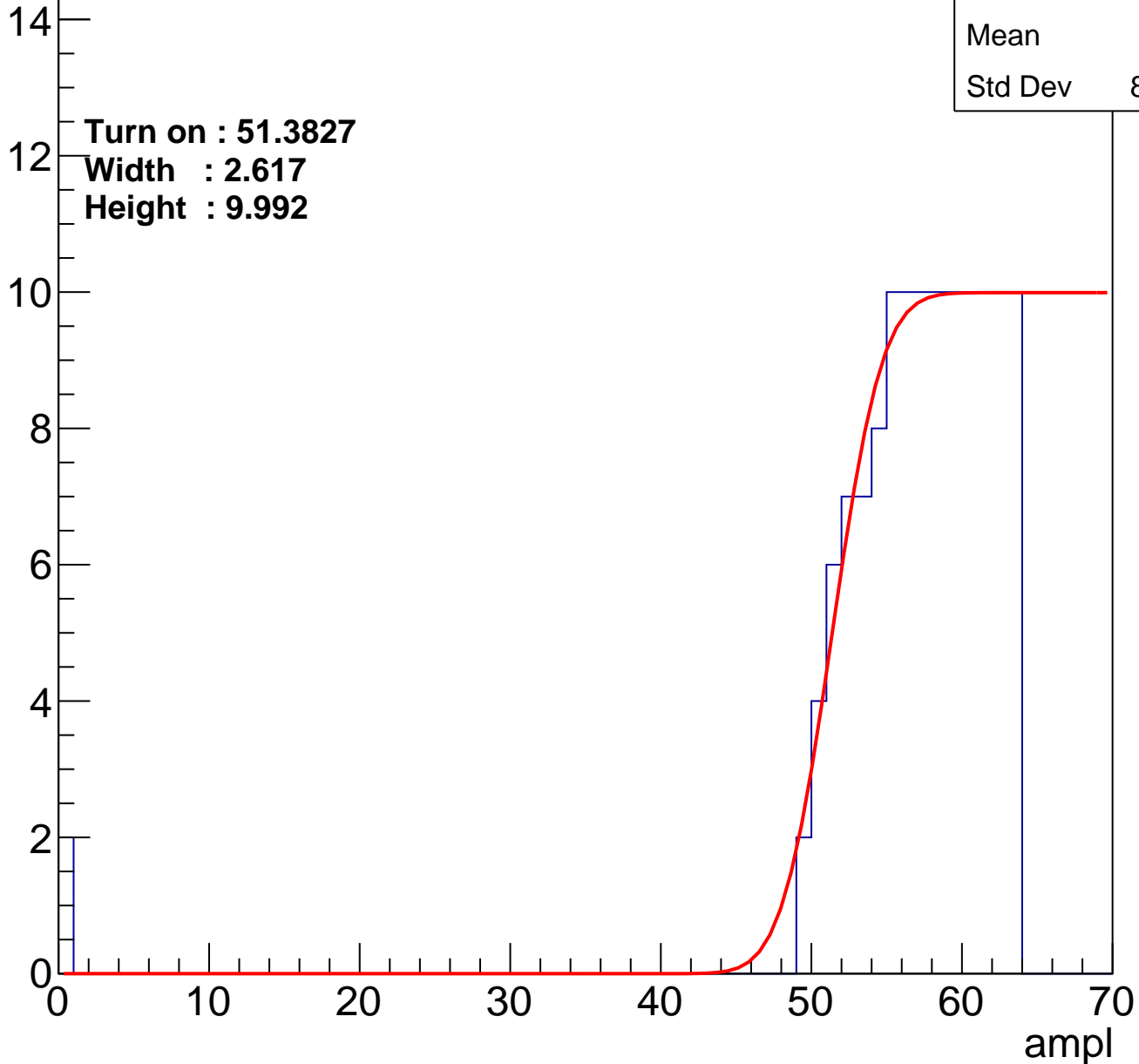
Entries	126
Mean	56.2
Std Dev	8.104

**Turn on : 51.3827**

**Width : 2.617**

**Height : 9.992**

Entry



# B0L103S, U25-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	55.23
Std Dev	12.44

Turn on : 54.1846

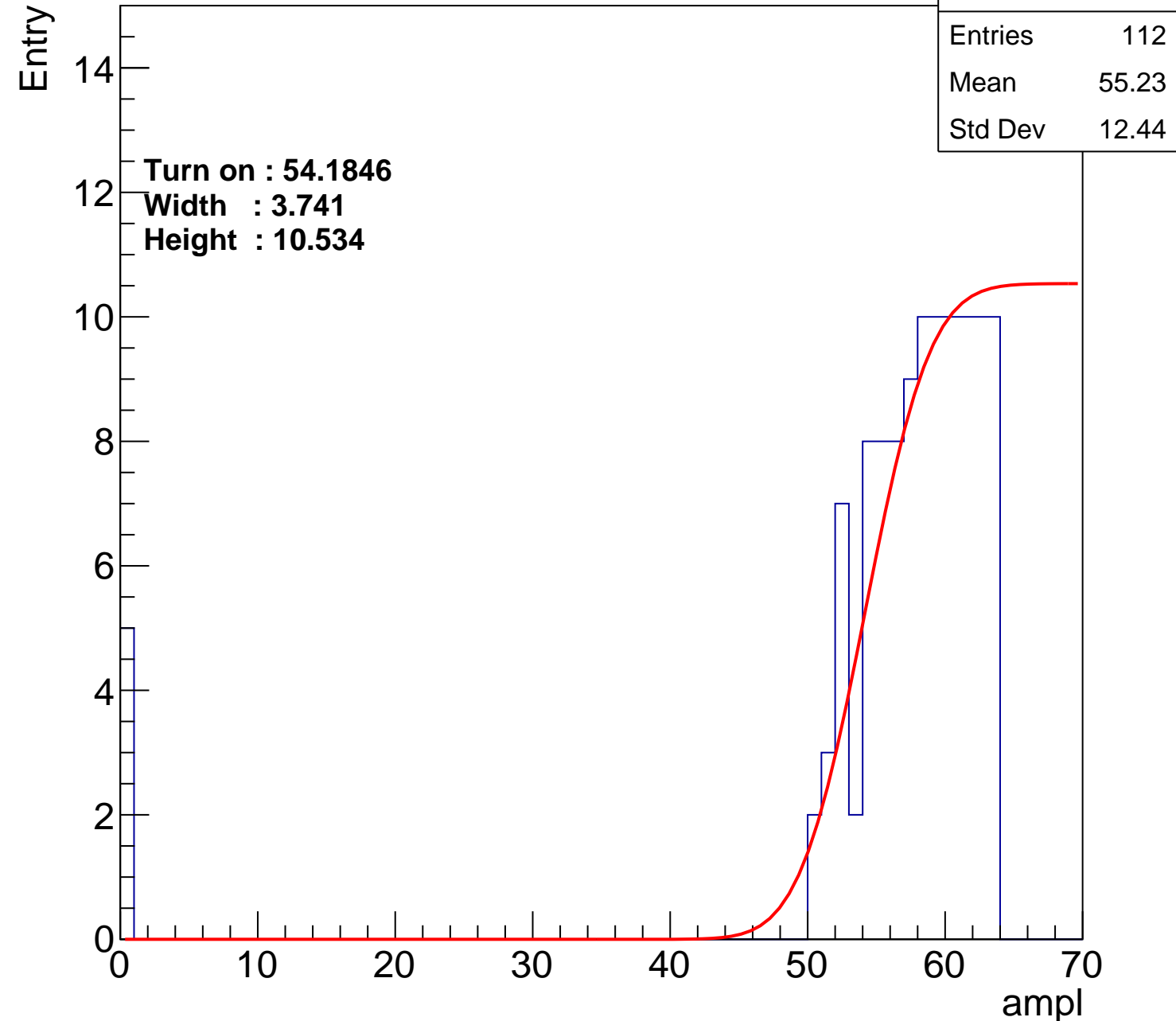
Width : 3.741

Height : 10.534

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

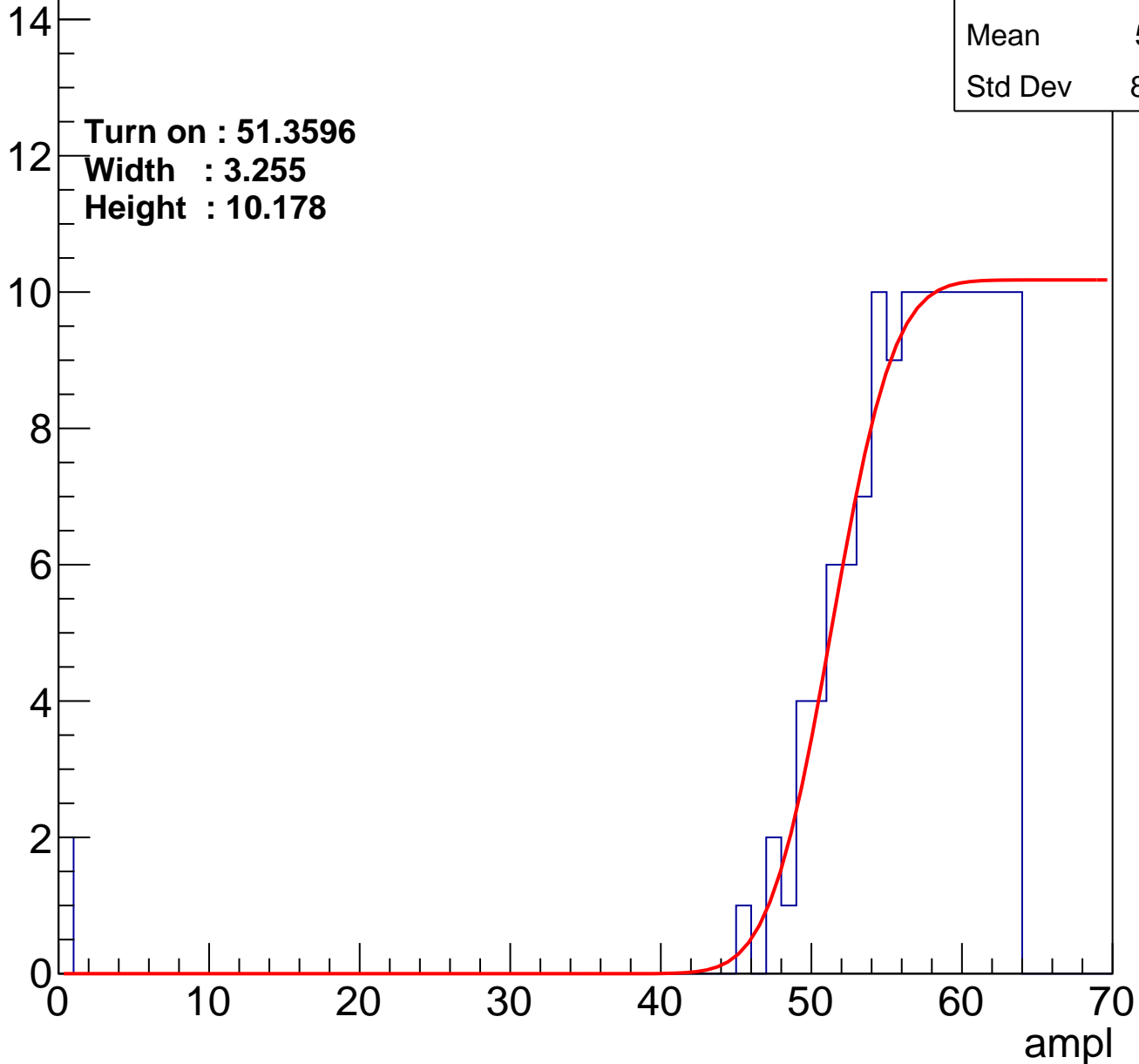
Entries	132
Mean	55.81
Std Dev	8.124

Turn on : 51.3596

Width : 3.255

Height : 10.178

Entry



# B0L103S, U25-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.99
Std Dev	10.49

Turn on : 51.4502

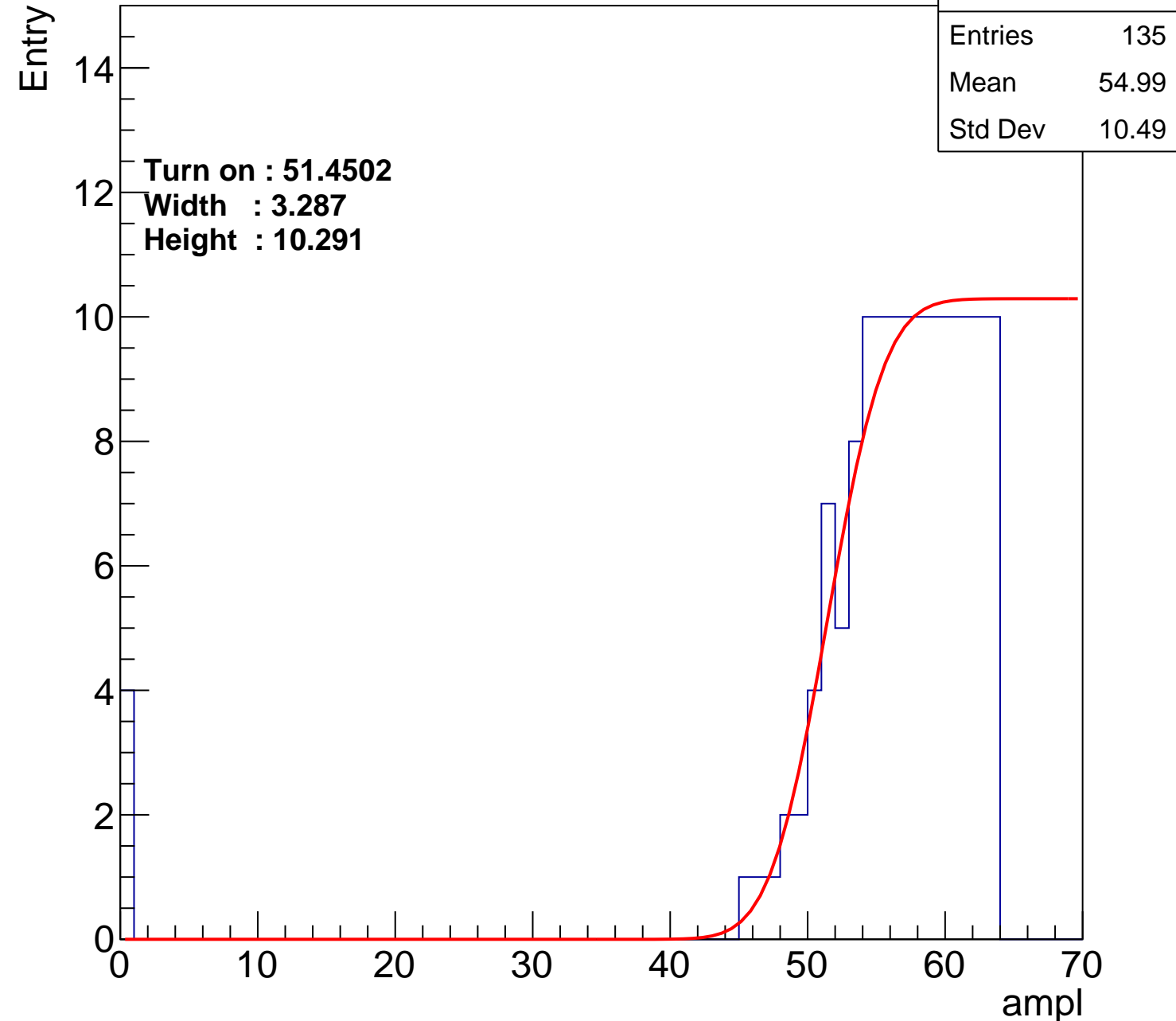
Width : 3.287

Height : 10.291

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	53.58
Std Dev	14.43

Turn on : 53.1760

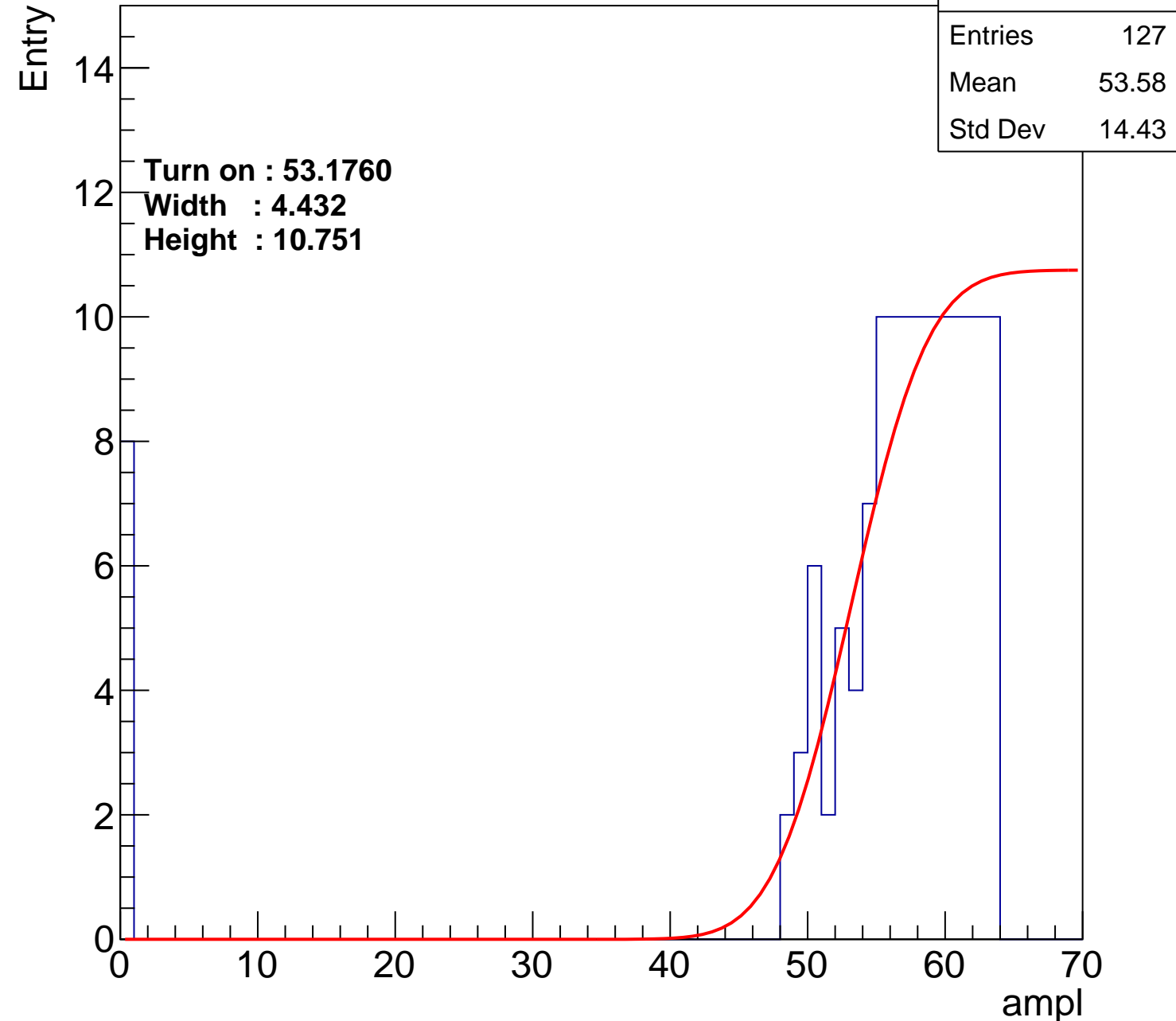
Width : 4.432

Height : 10.751

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	55.84
Std Dev	11.31

Turn on : 53.3881

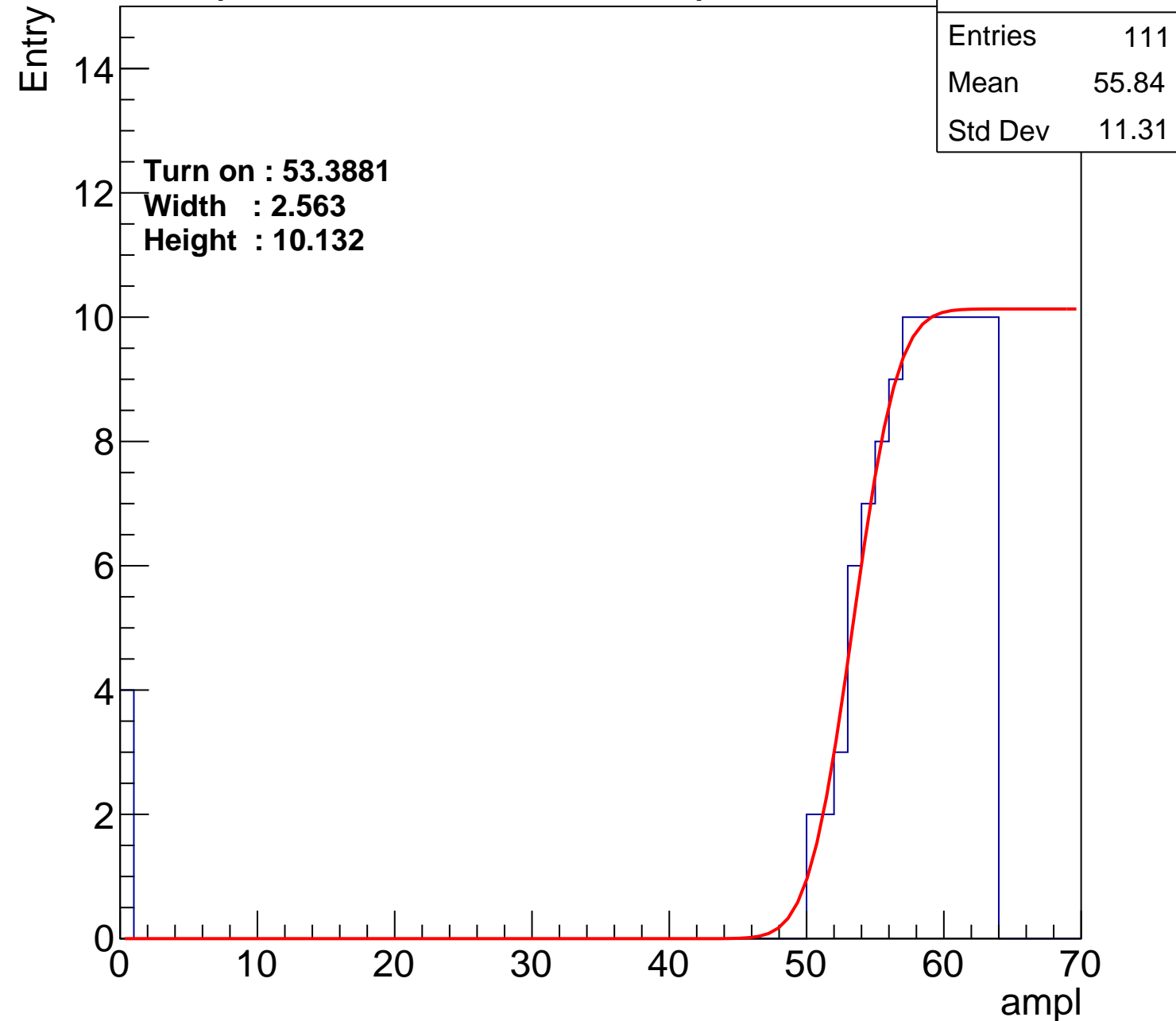
Width : 2.563

Height : 10.132

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.43
Std Dev	8.257

Turn on : 52.1096

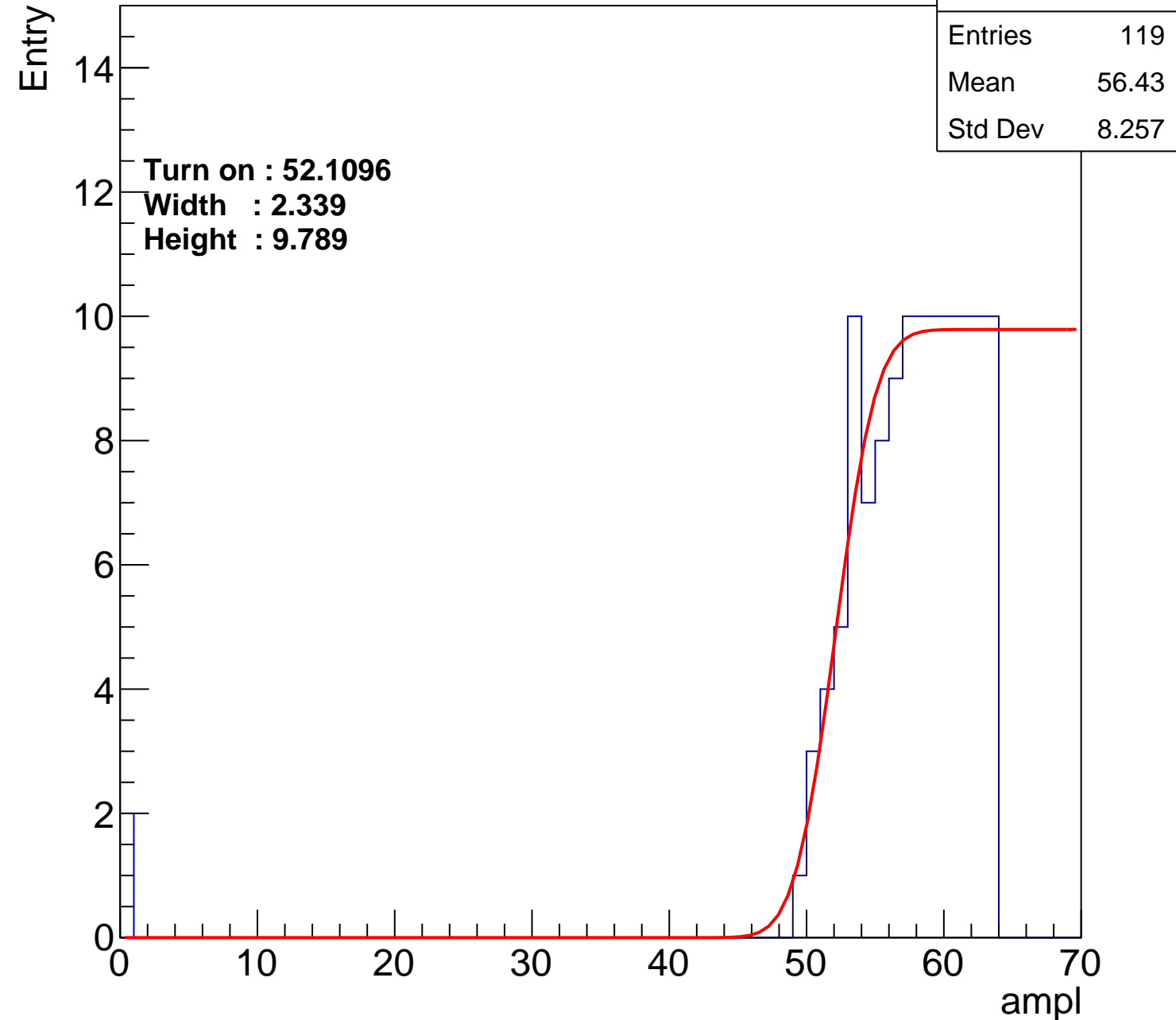
Width : 2.339

Height : 9.789

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch81

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.03
Std Dev	12

Turn on : 51.0499

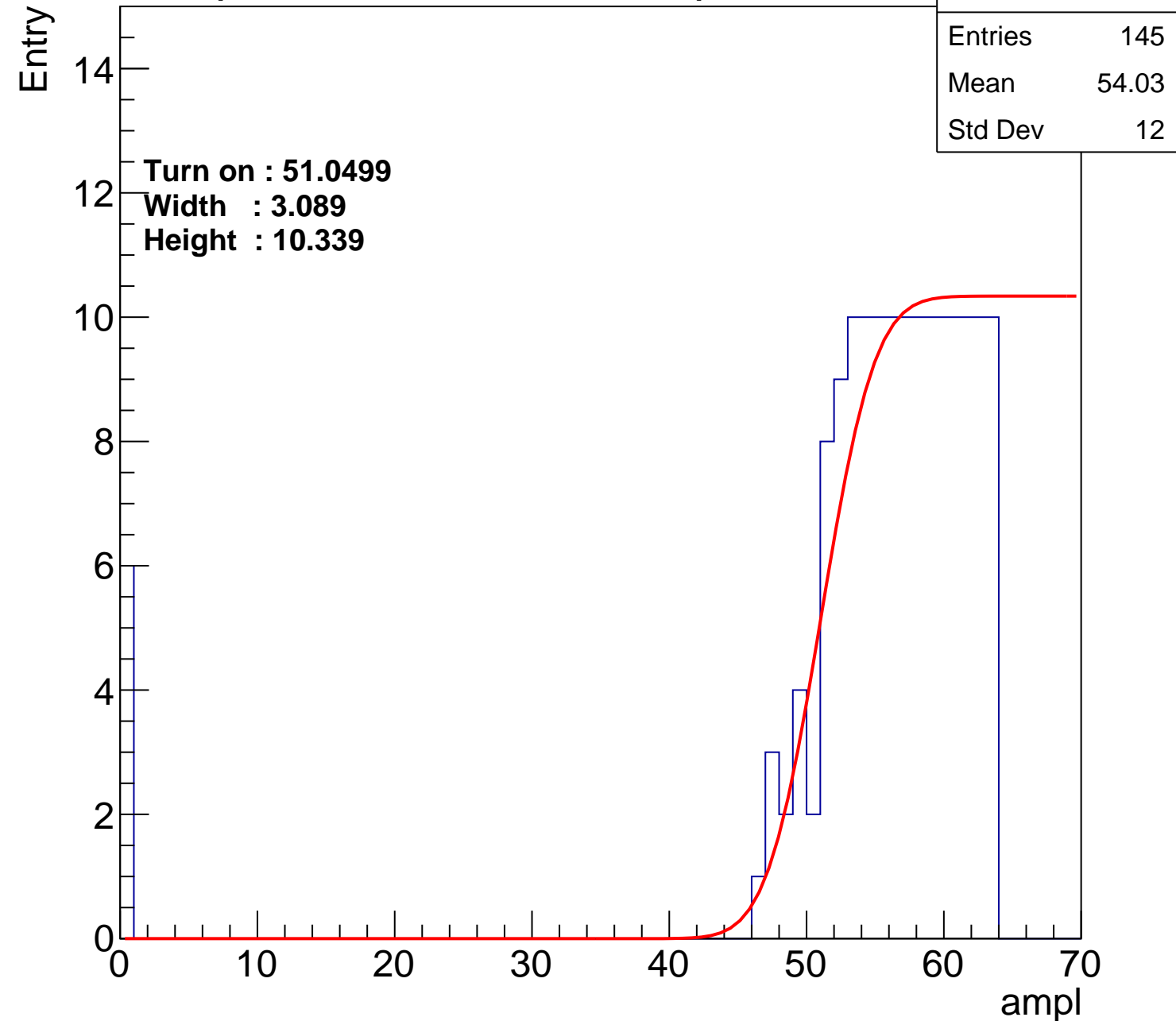
Width : 3.089

Height : 10.339

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.16
Std Dev	8.287

Turn on : 51.9731

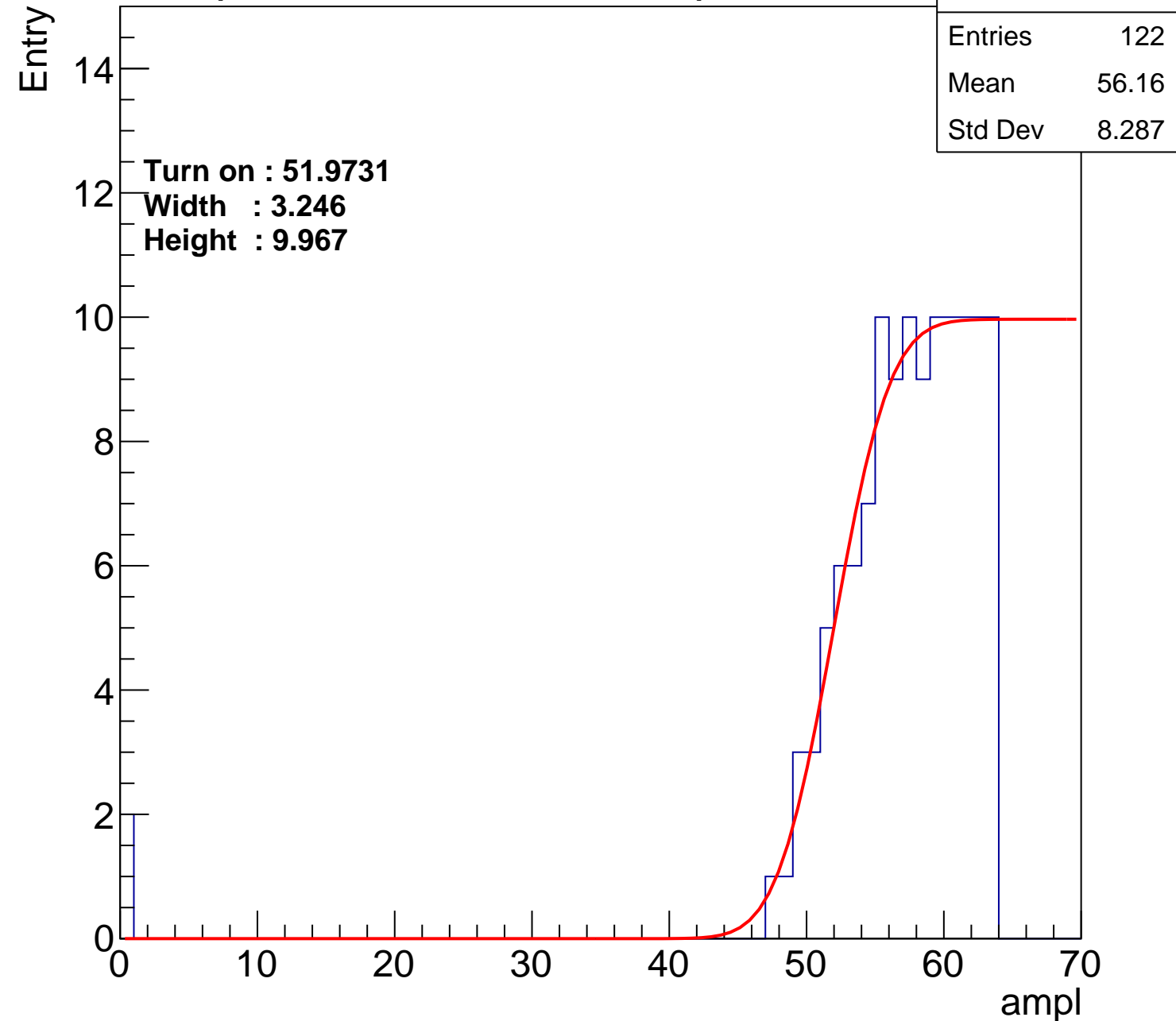
Width : 3.246

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.53
Std Dev	11

Turn on : 52.7156

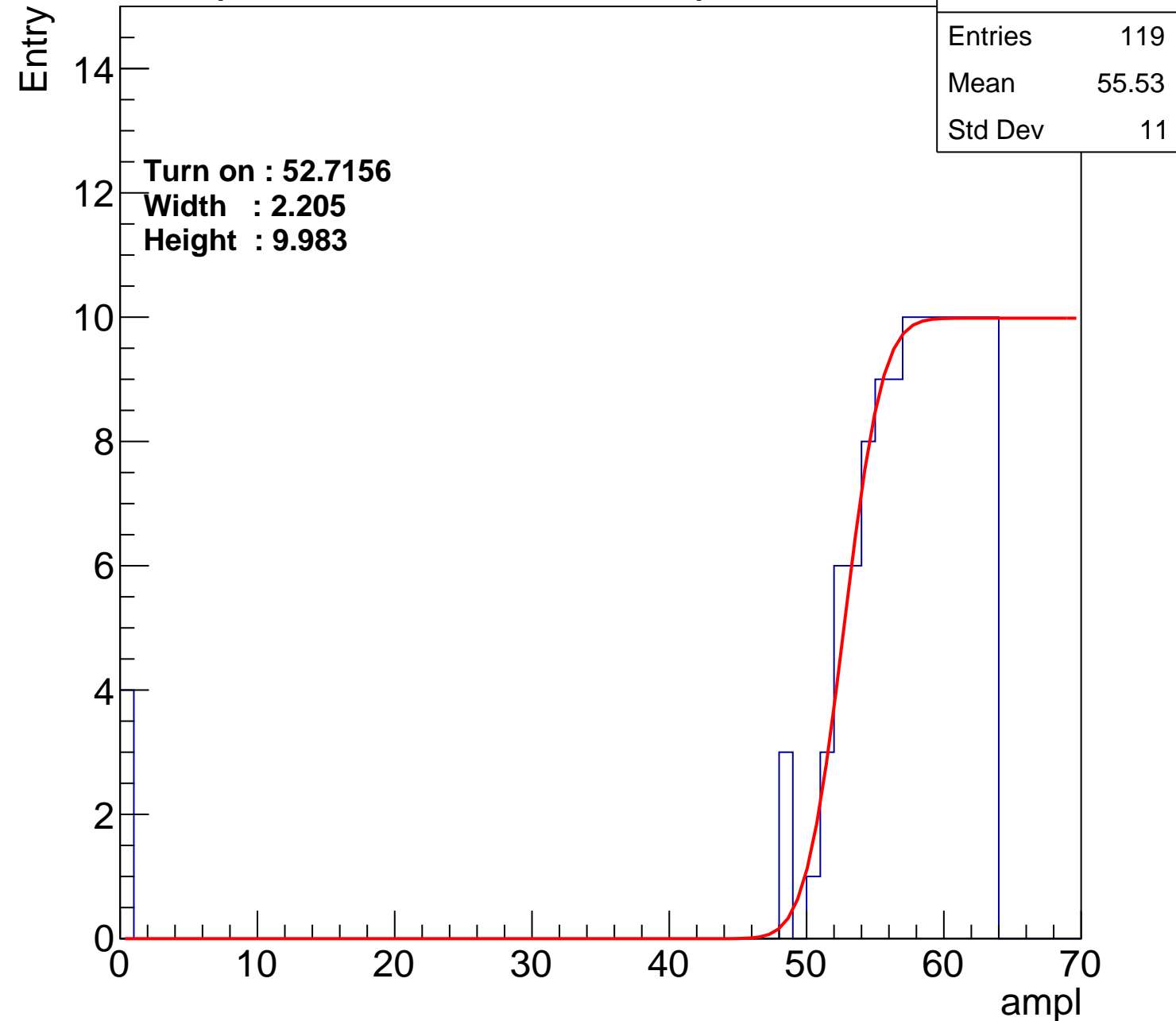
Width : 2.205

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	55.83
Std Dev	11.25

Turn on : 52.9769

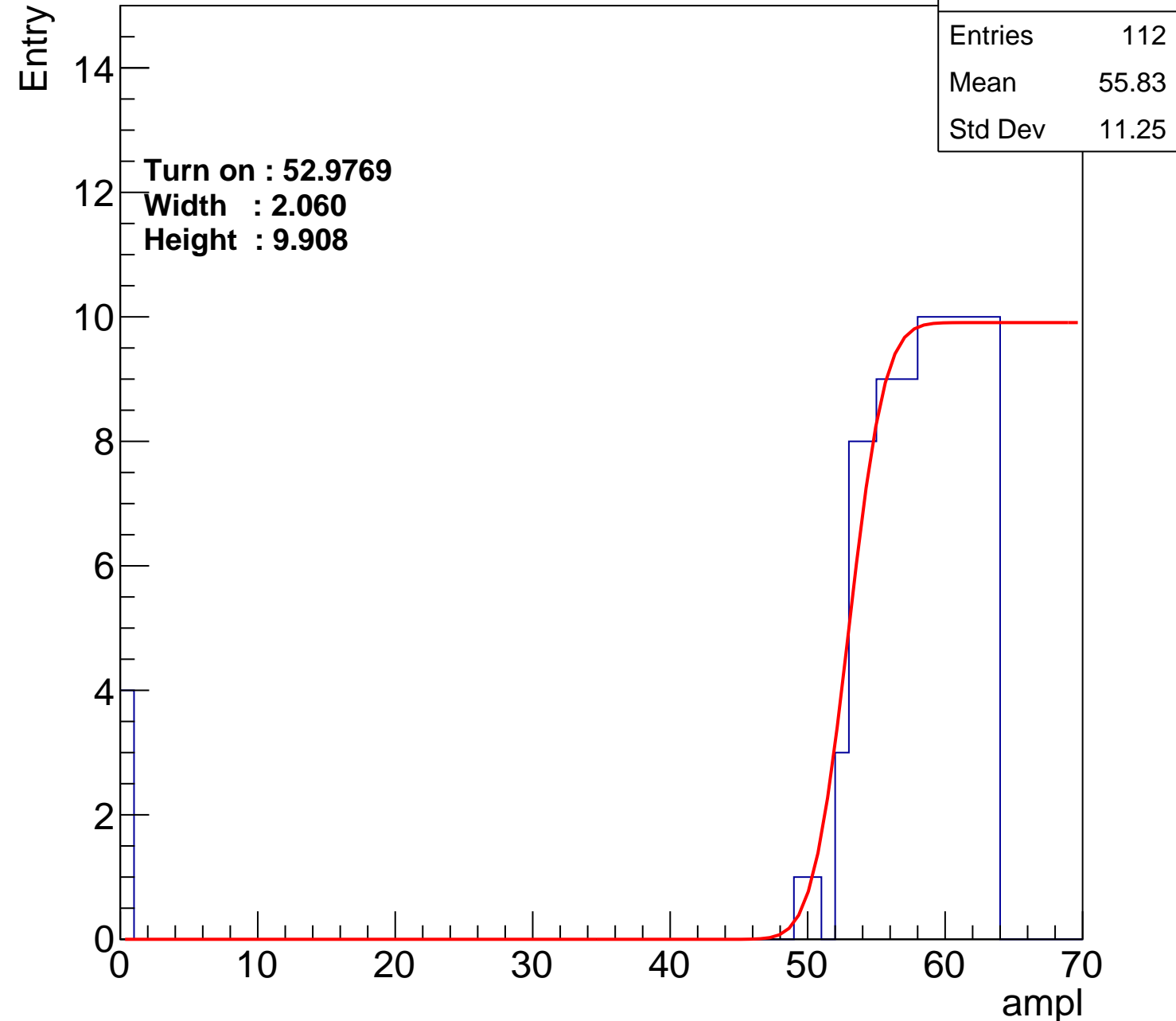
Width : 2.060

Height : 9.908

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.68
Std Dev	8.517

Turn on : 53.9338

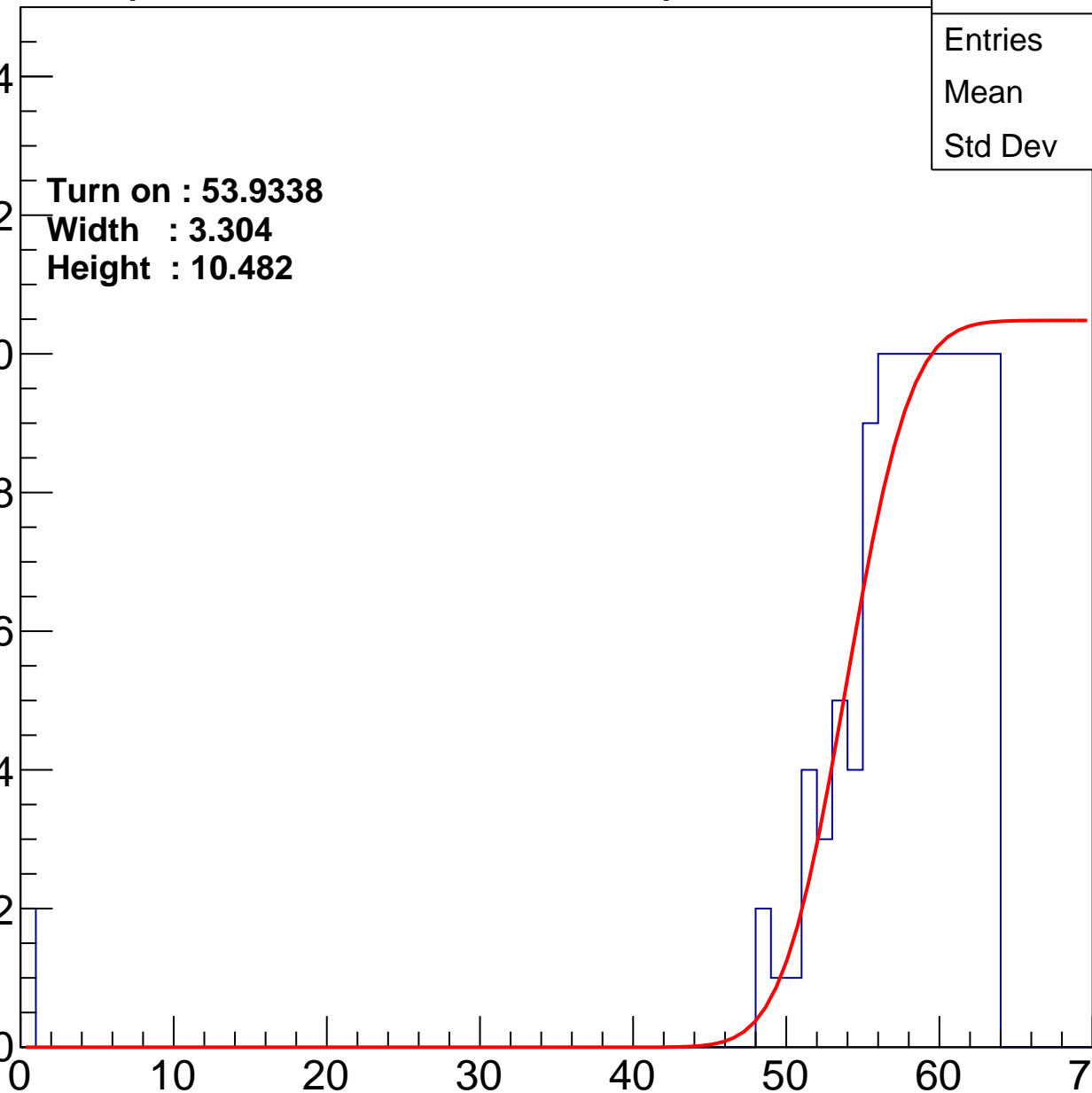
Width : 3.304

Height : 10.482

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.92
Std Dev	9.821

Turn on : 52.9373

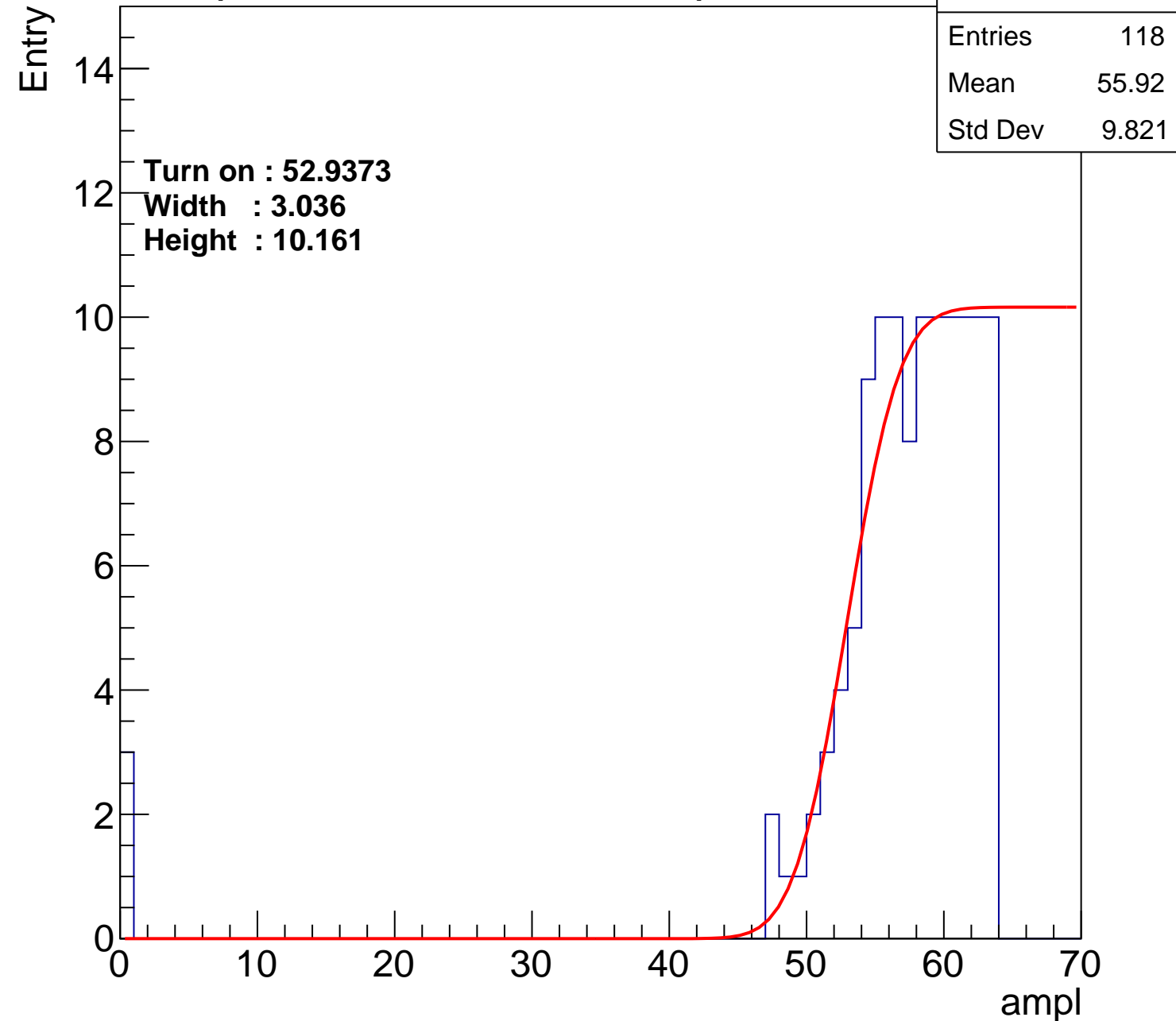
Width : 3.036

Height : 10.161

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.8
Std Dev	9.611

Turn on : 51.4394

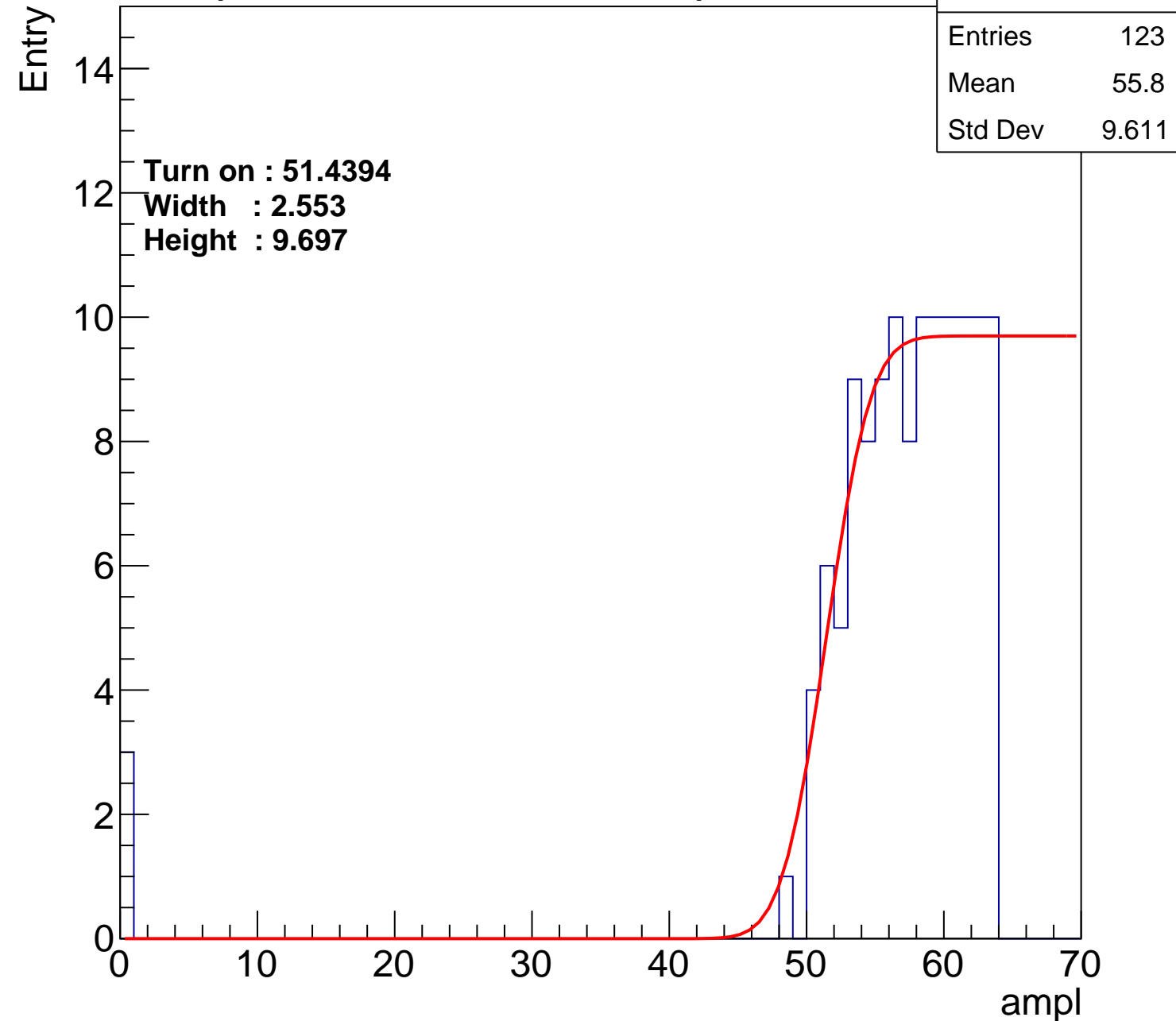
Width : 2.553

Height : 9.697

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.71
Std Dev	8.253

Turn on : 52.1217

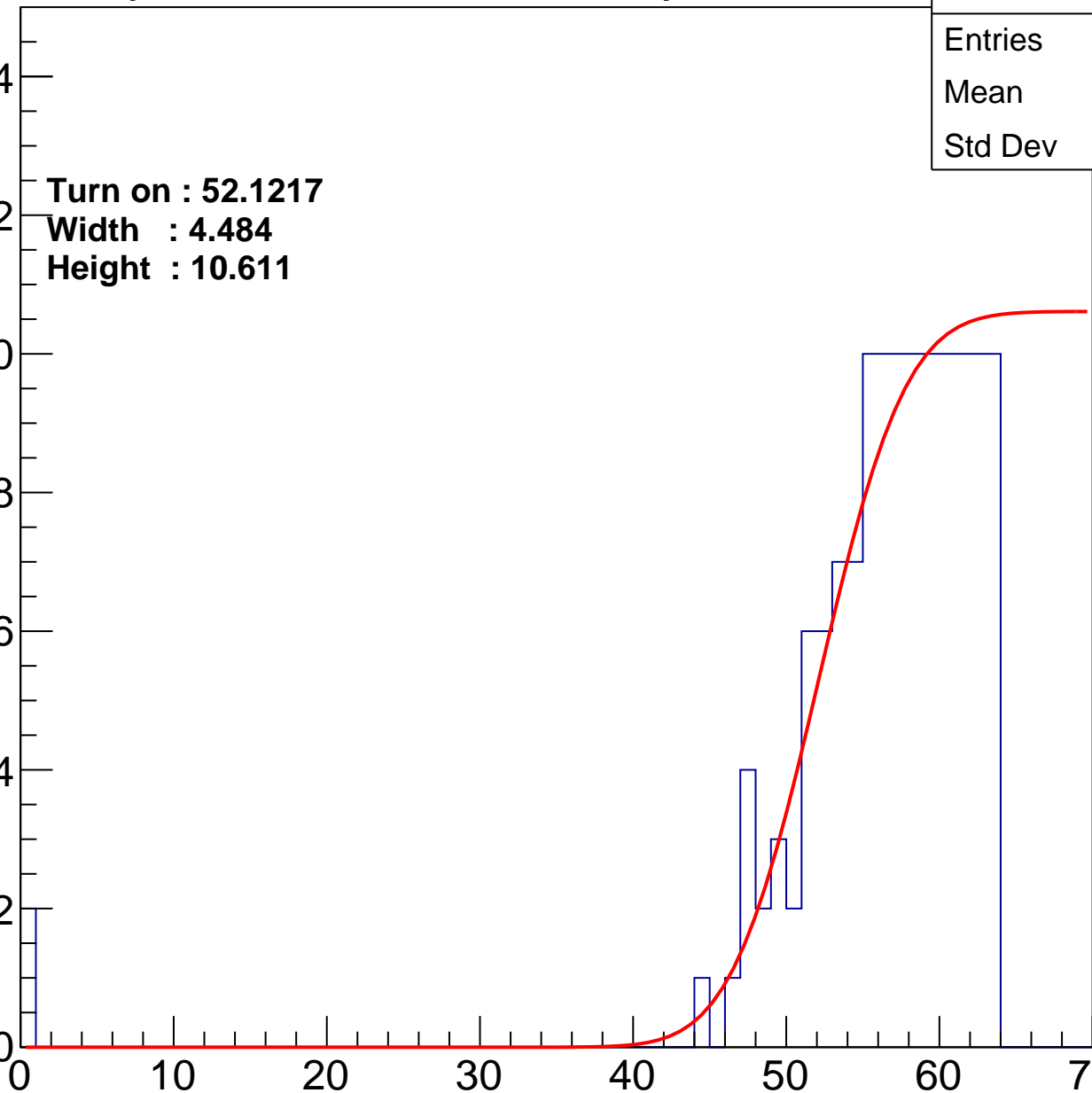
Width : 4.484

Height : 10.611

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

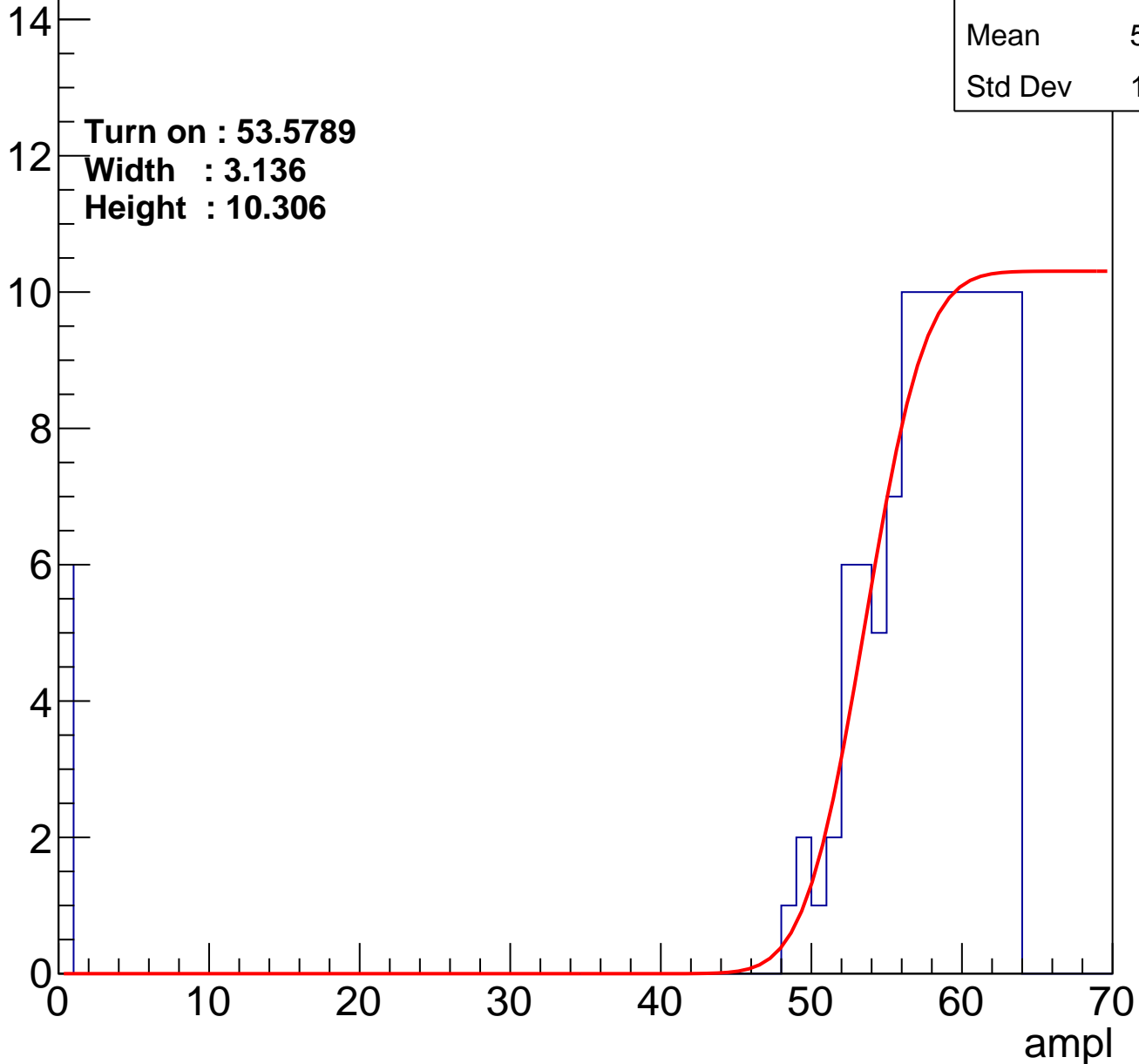
Entries	116
Mean	54.68
Std Dev	13.27

**Turn on : 53.5789**

**Width : 3.136**

**Height : 10.306**

Entry



# B0L103S, U25-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	54.01
Std Dev	13.67

Turn on : 51.9345

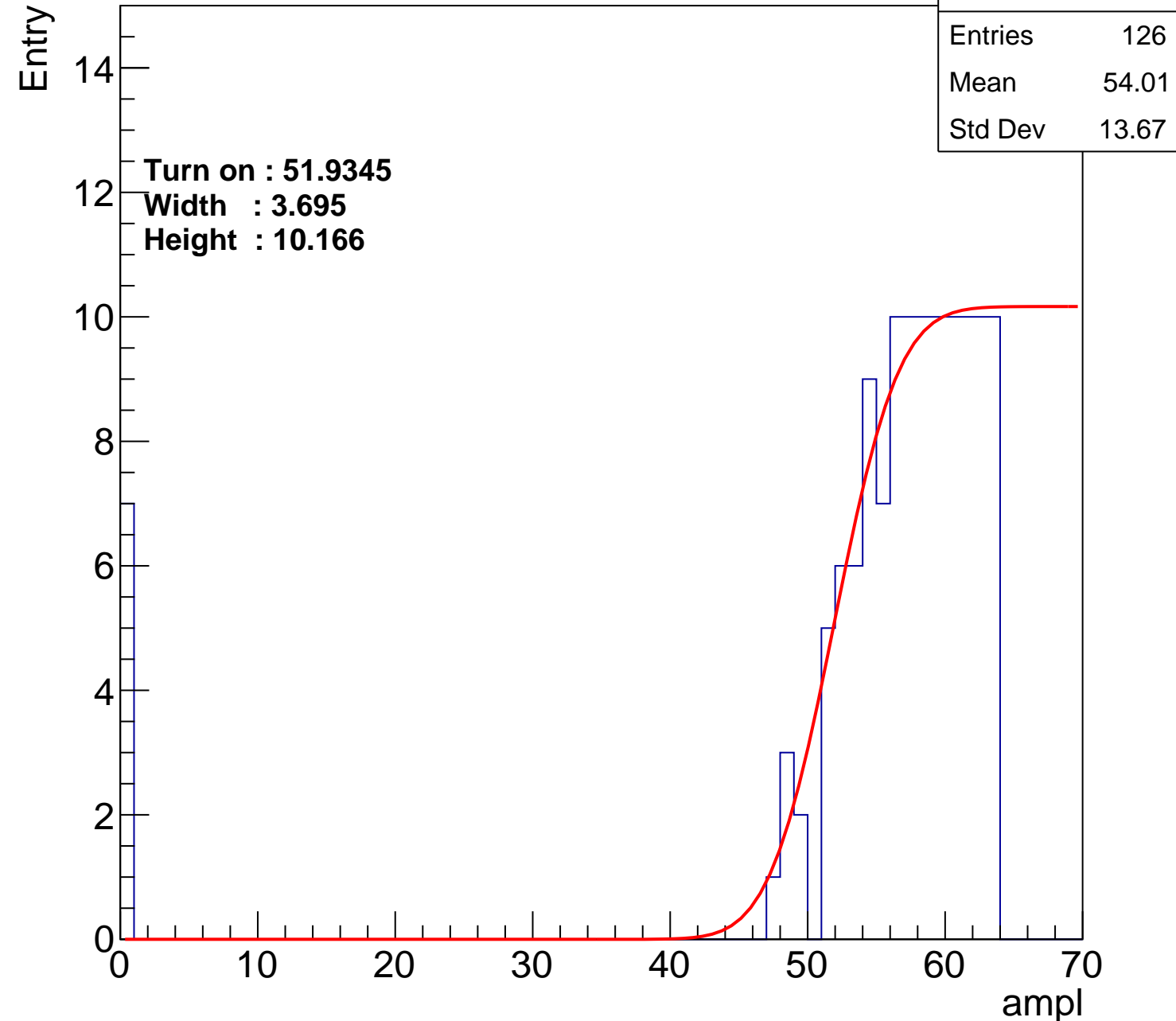
Width : 3.695

Height : 10.166

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	54.15
Std Dev	14.39

Turn on : 53.8332

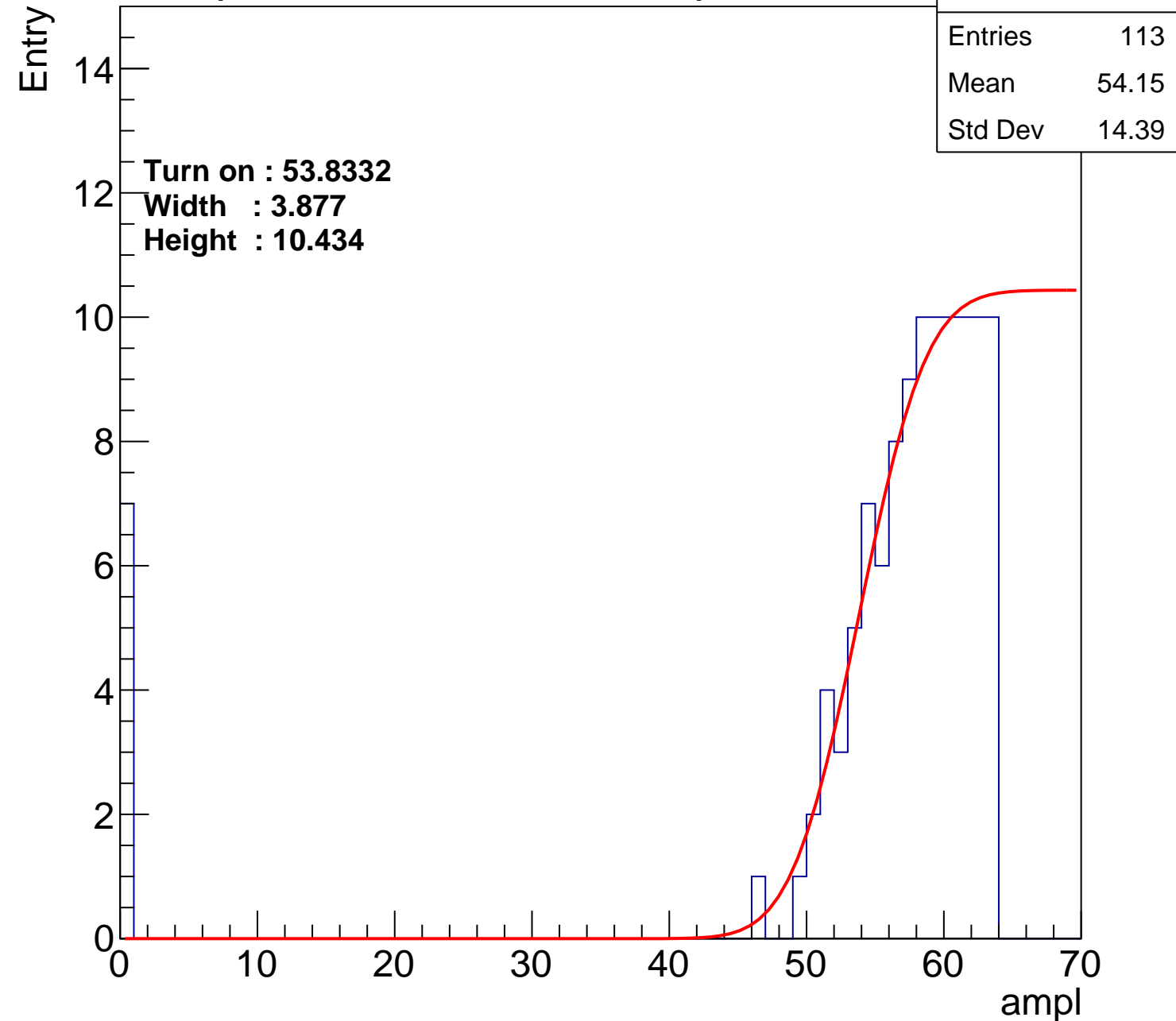
Width : 3.877

Height : 10.434

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	54.44
Std Dev	12.92

Turn on : 51.9717

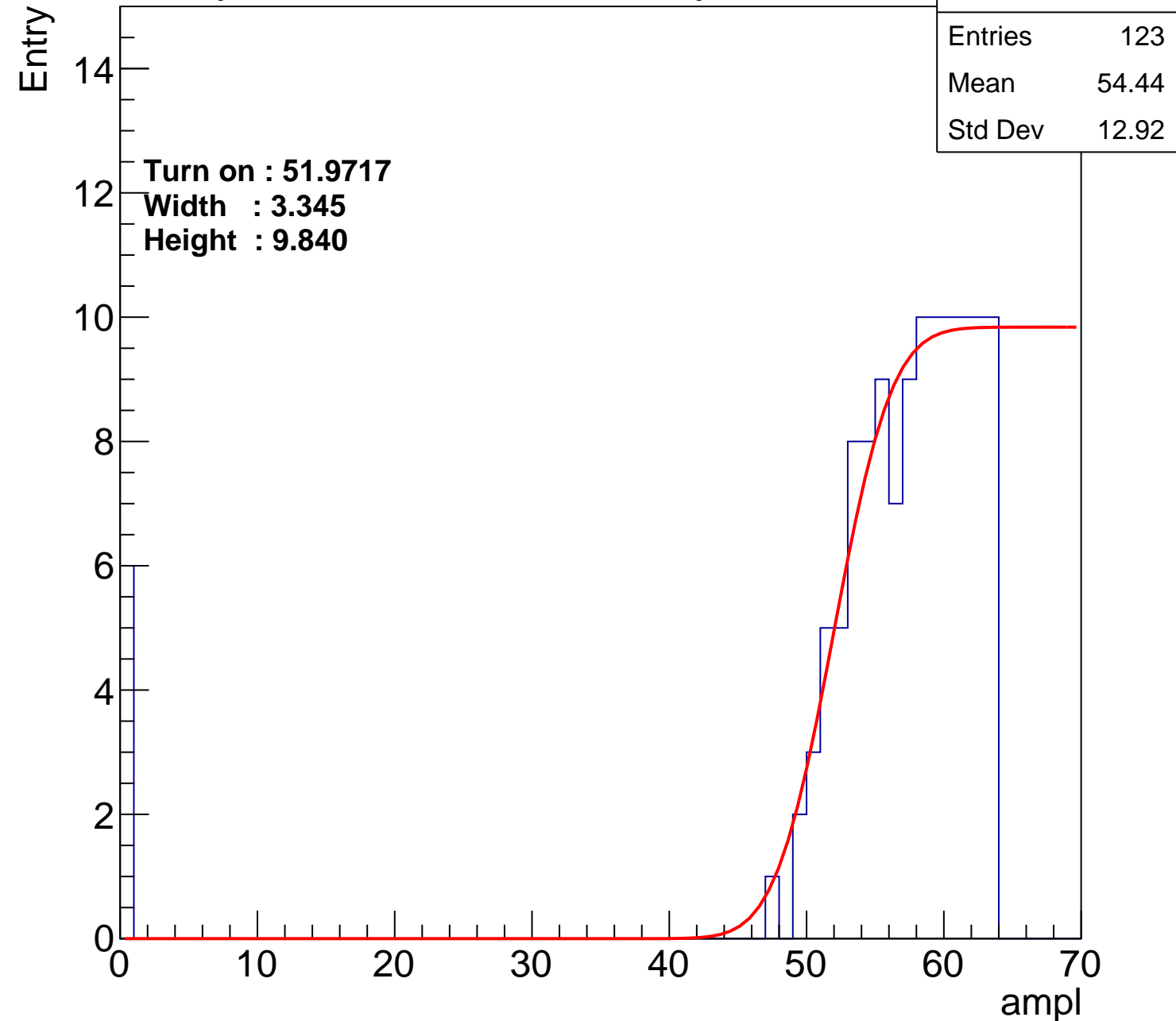
Width : 3.345

Height : 9.840

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.41
Std Dev	9.415

Turn on : 51.4671

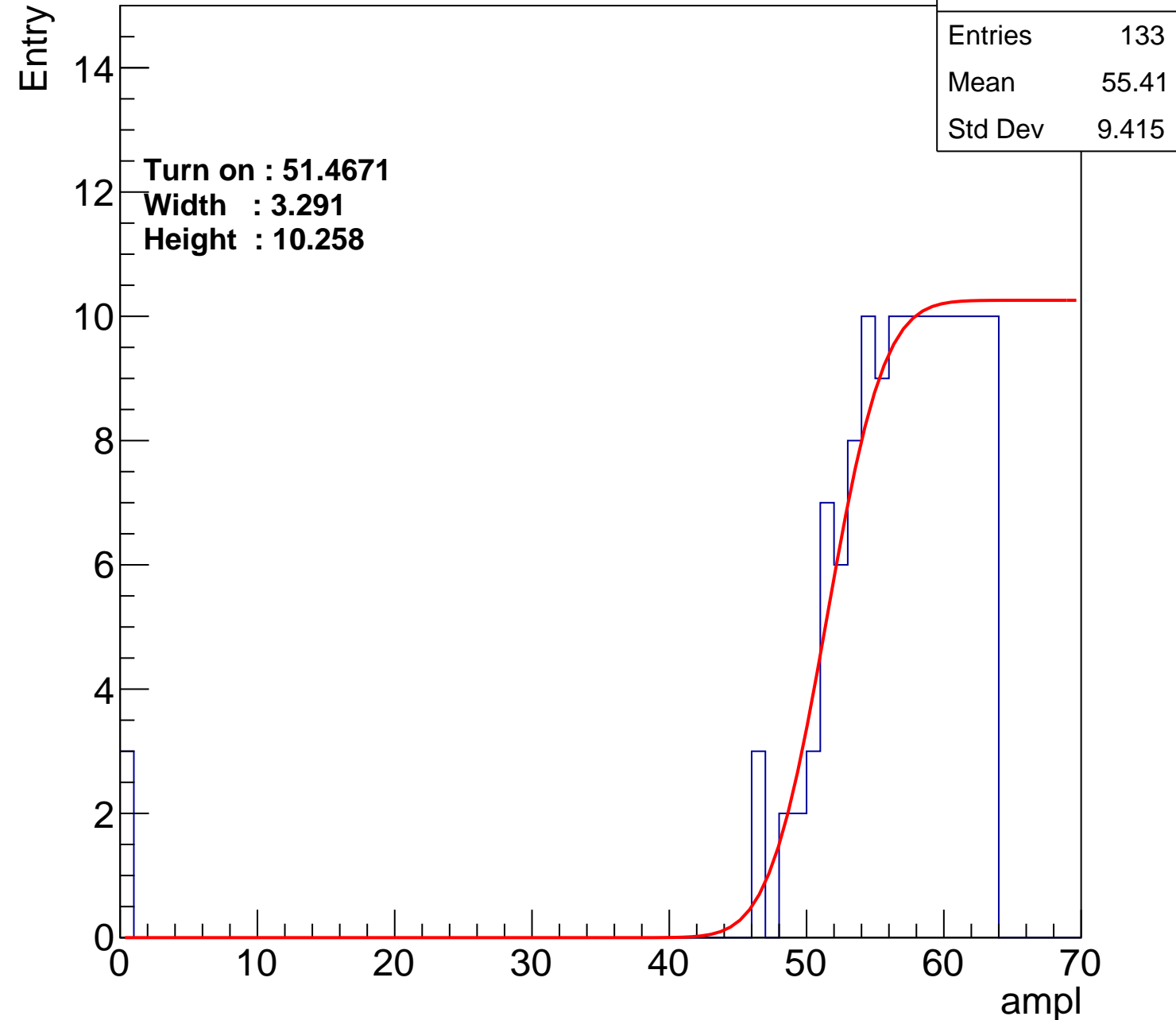
Width : 3.291

Height : 10.258

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.36
Std Dev	9.473

Turn on : 52.0628

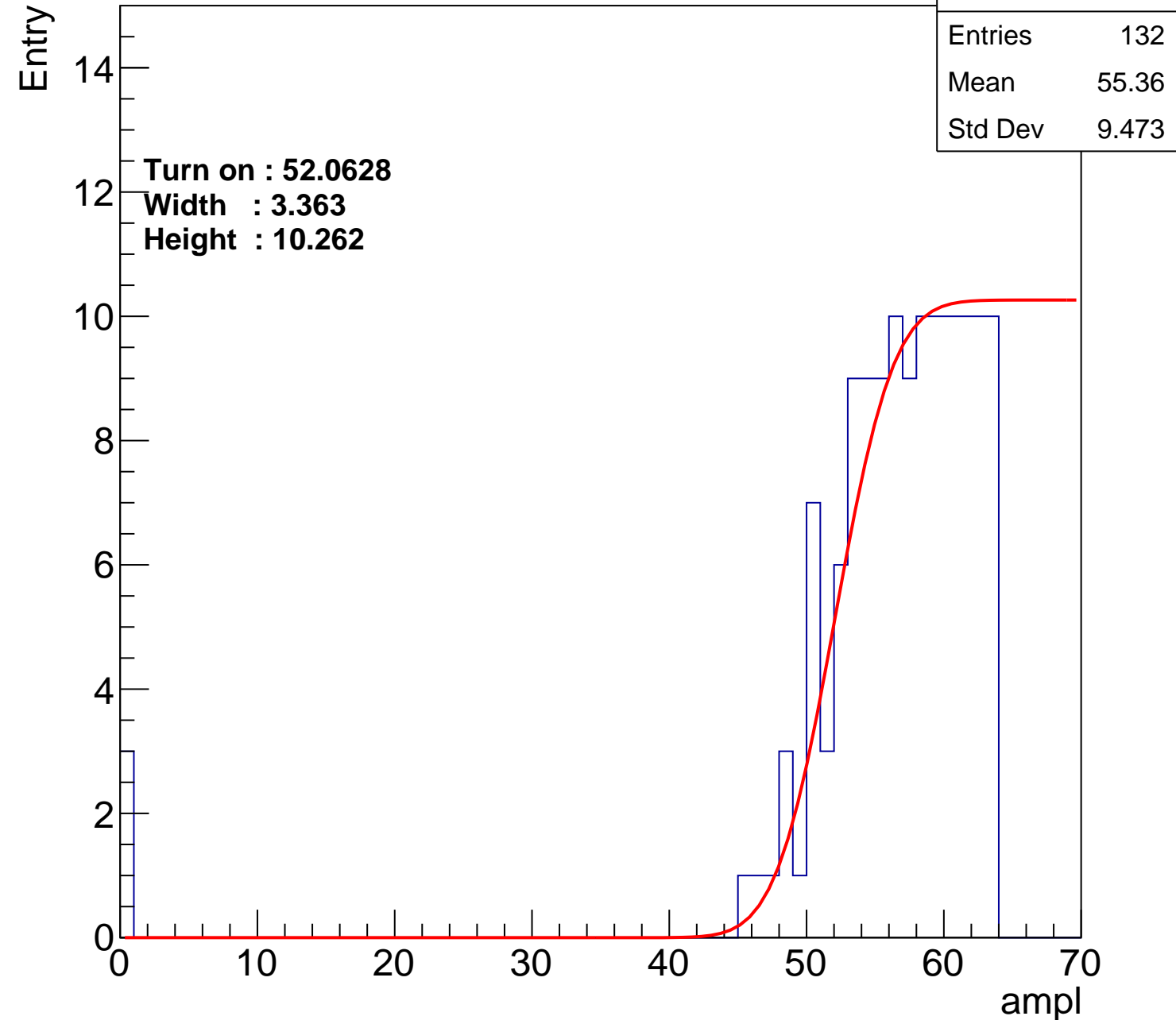
Width : 3.363

Height : 10.262

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.49
Std Dev	10.95

Turn on : 52.4763

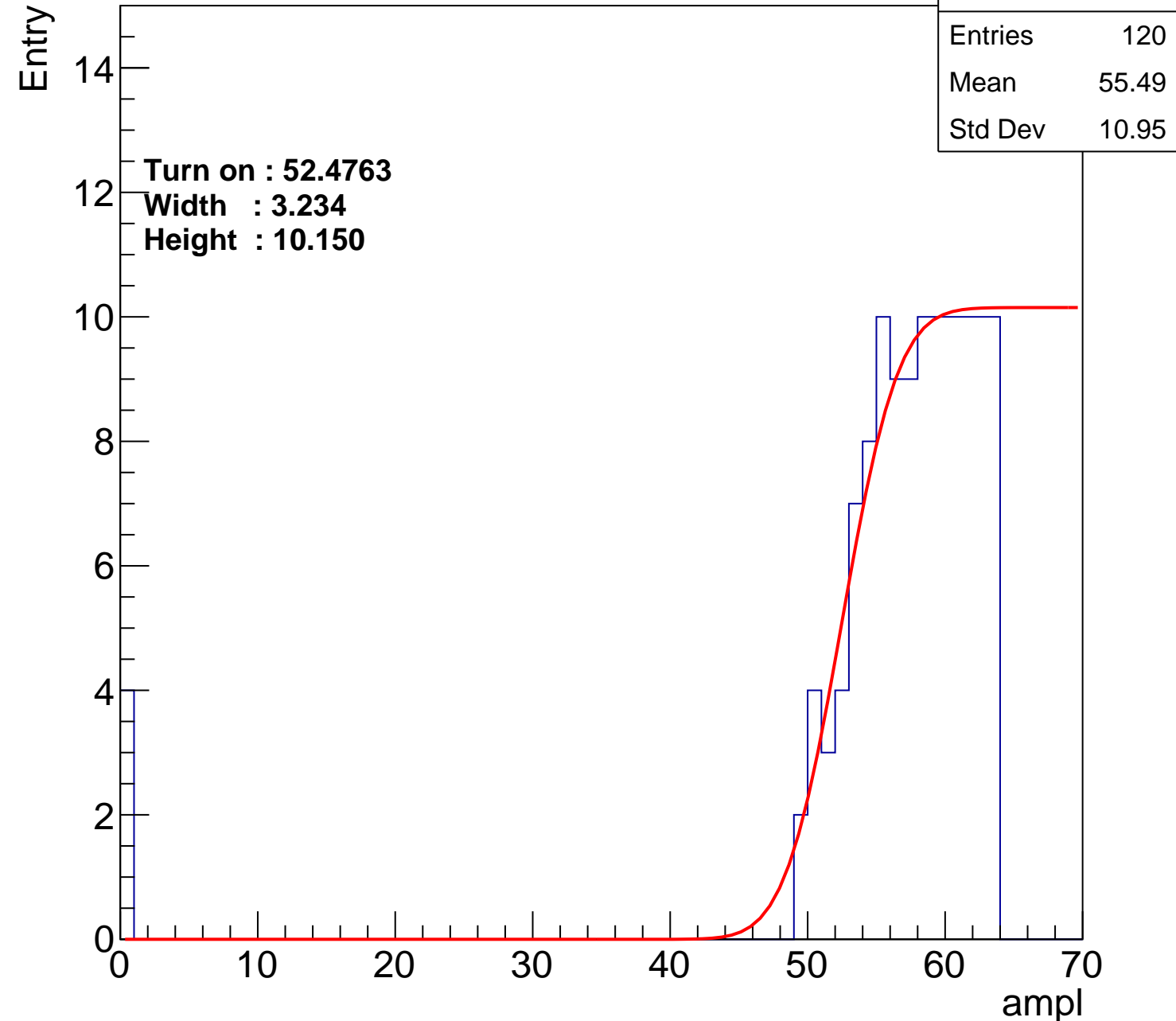
Width : 3.234

Height : 10.150

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	54.44
Std Dev	12.63

Turn on : 52.3505

Width : 2.310

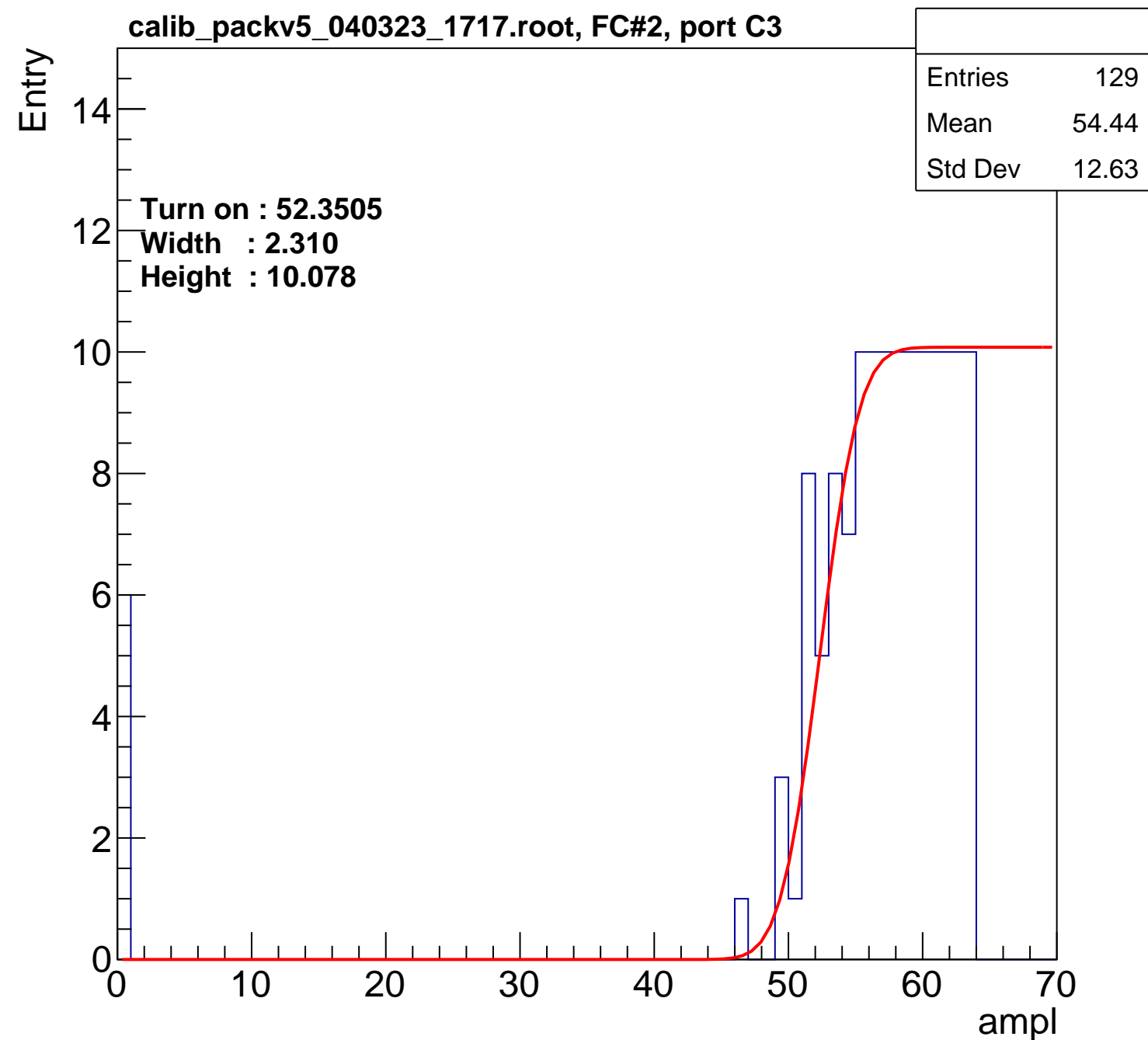
Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U25-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

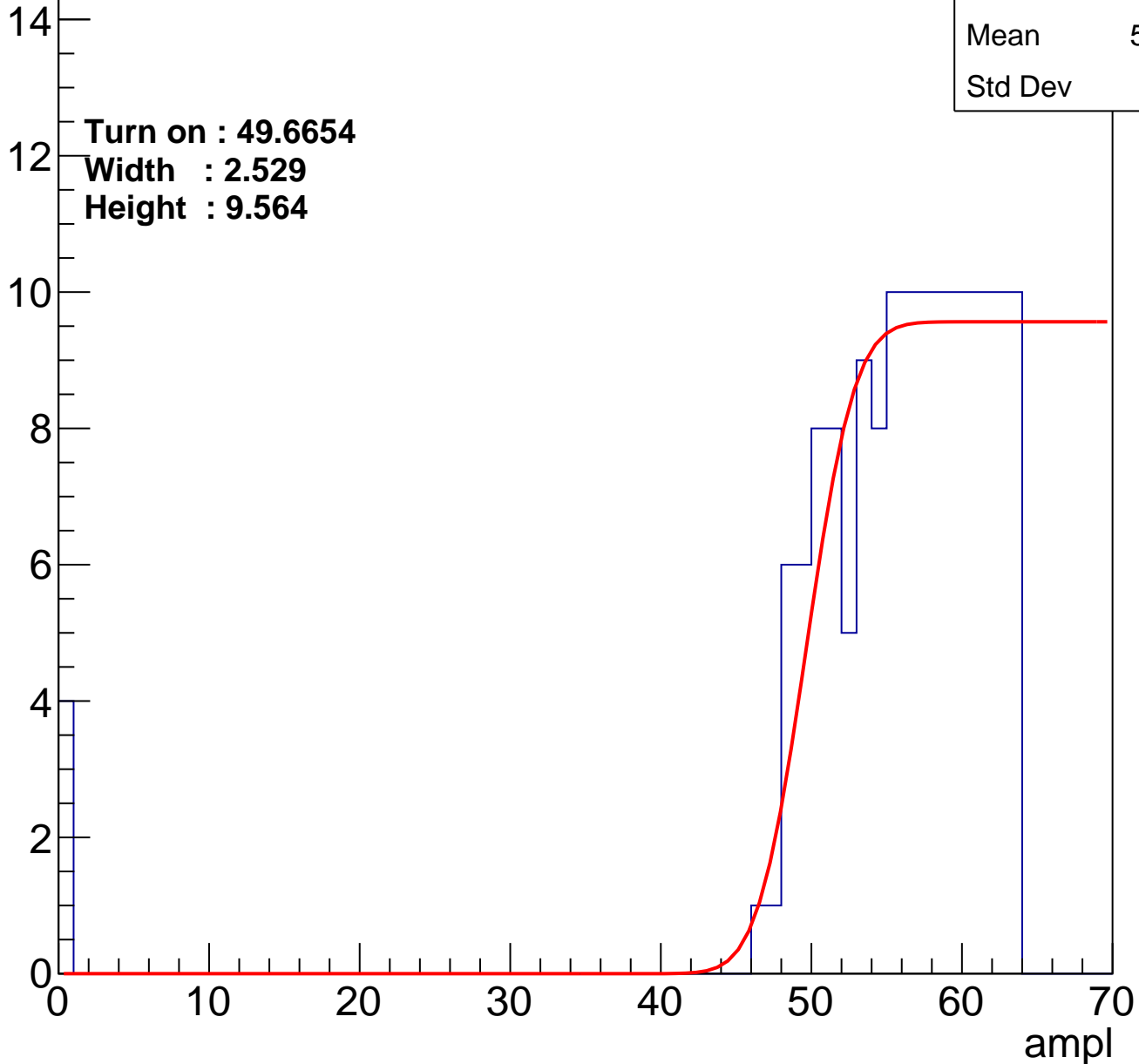
Entries	146
Mean	54.53
Std Dev	10.2

Turn on : 49.6654

Width : 2.529

Height : 9.564

Entry



# B0L103S, U25-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	54.94
Std Dev	11.83

Turn on : 51.9015

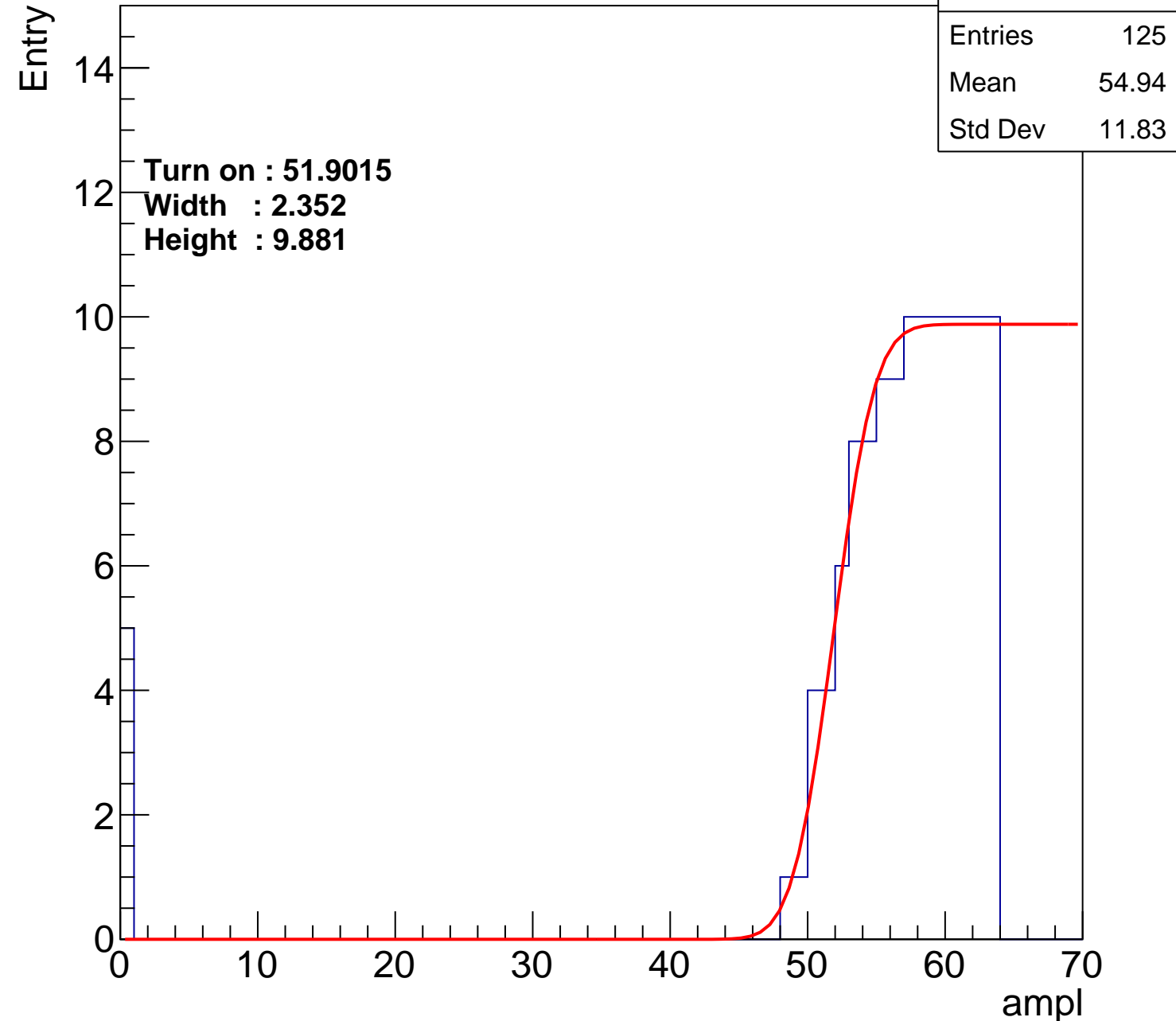
Width : 2.352

Height : 9.881

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

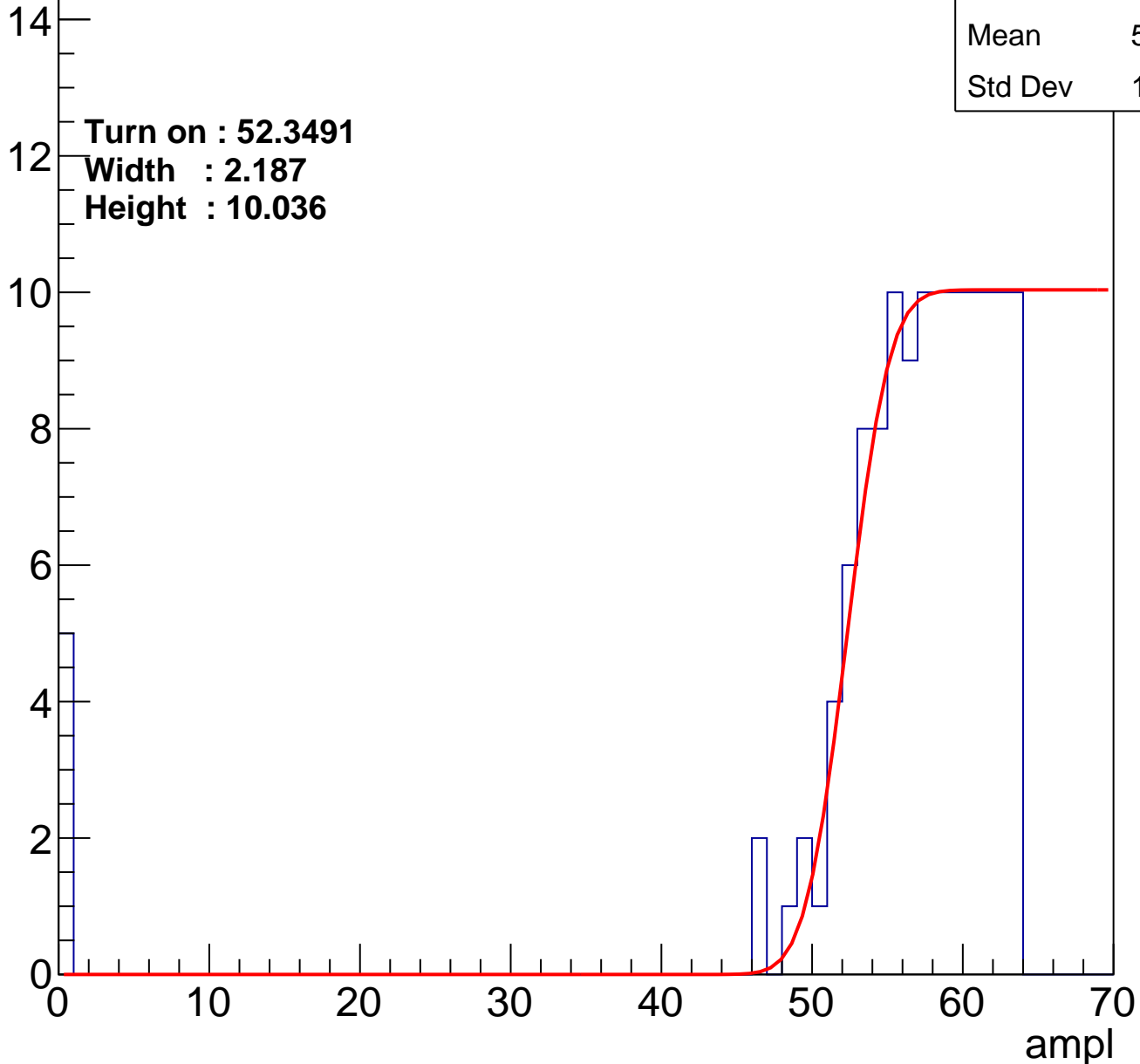
Entries	126
Mean	54.87
Std Dev	11.83

Turn on : 52.3491

Width : 2.187

Height : 10.036

Entry



# B0L103S, U25-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	56.46
Std Dev	8.647

Turn on : 53.5540

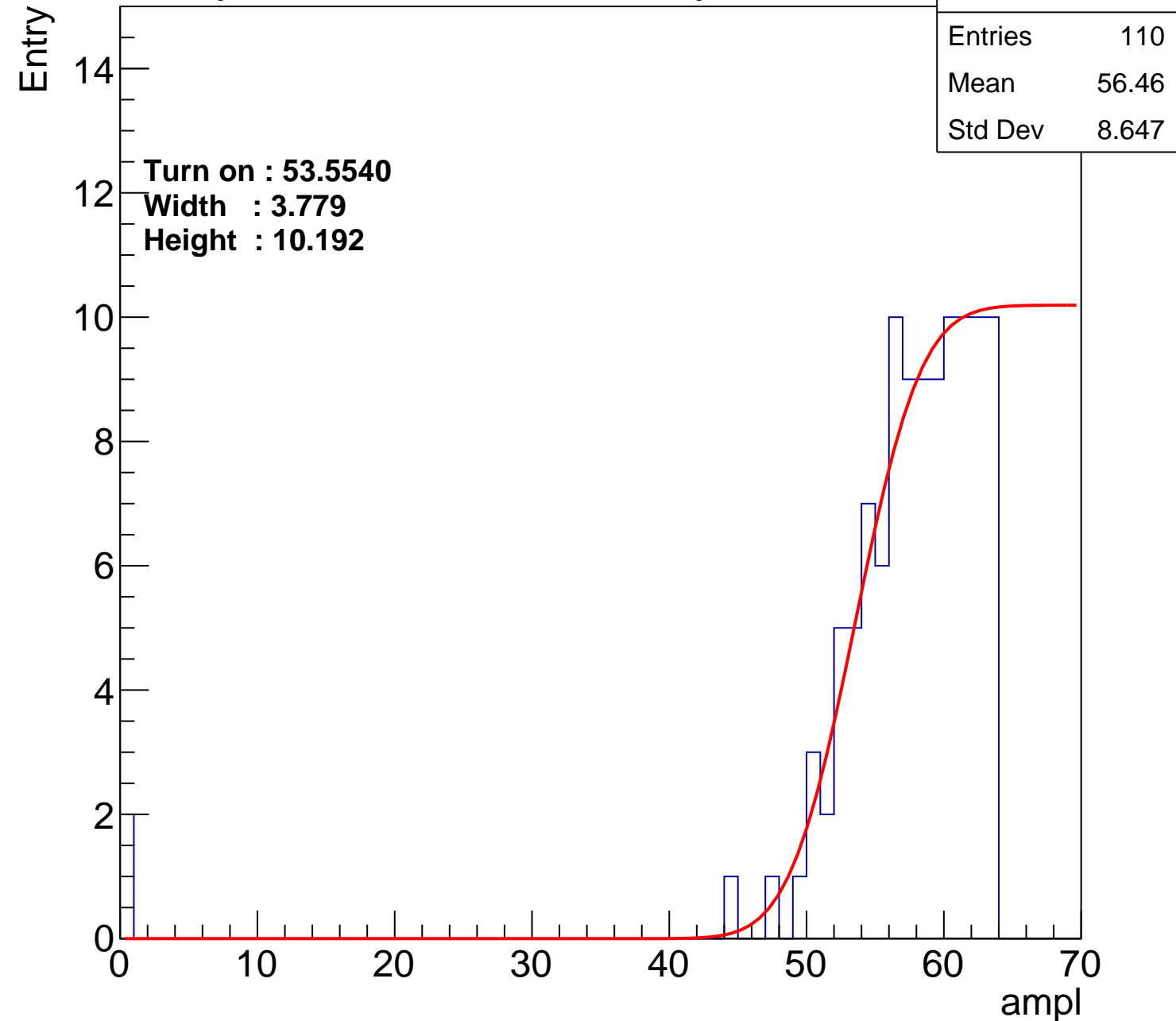
Width : 3.779

Height : 10.192

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.92
Std Dev	8.091

Turn on : 51.4702

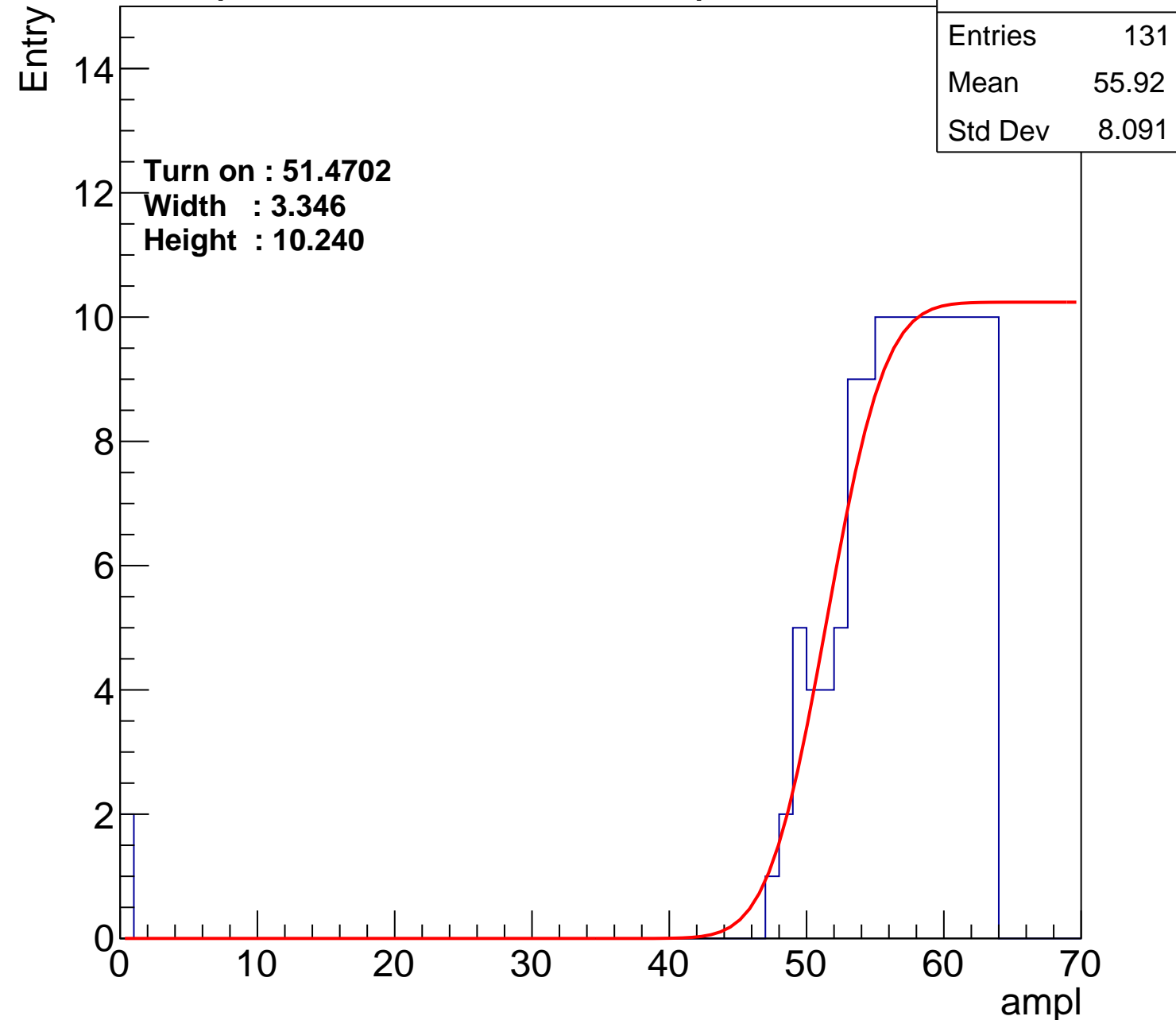
Width : 3.346

Height : 10.240

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.19
Std Dev	10.56

Turn on : 51.4303

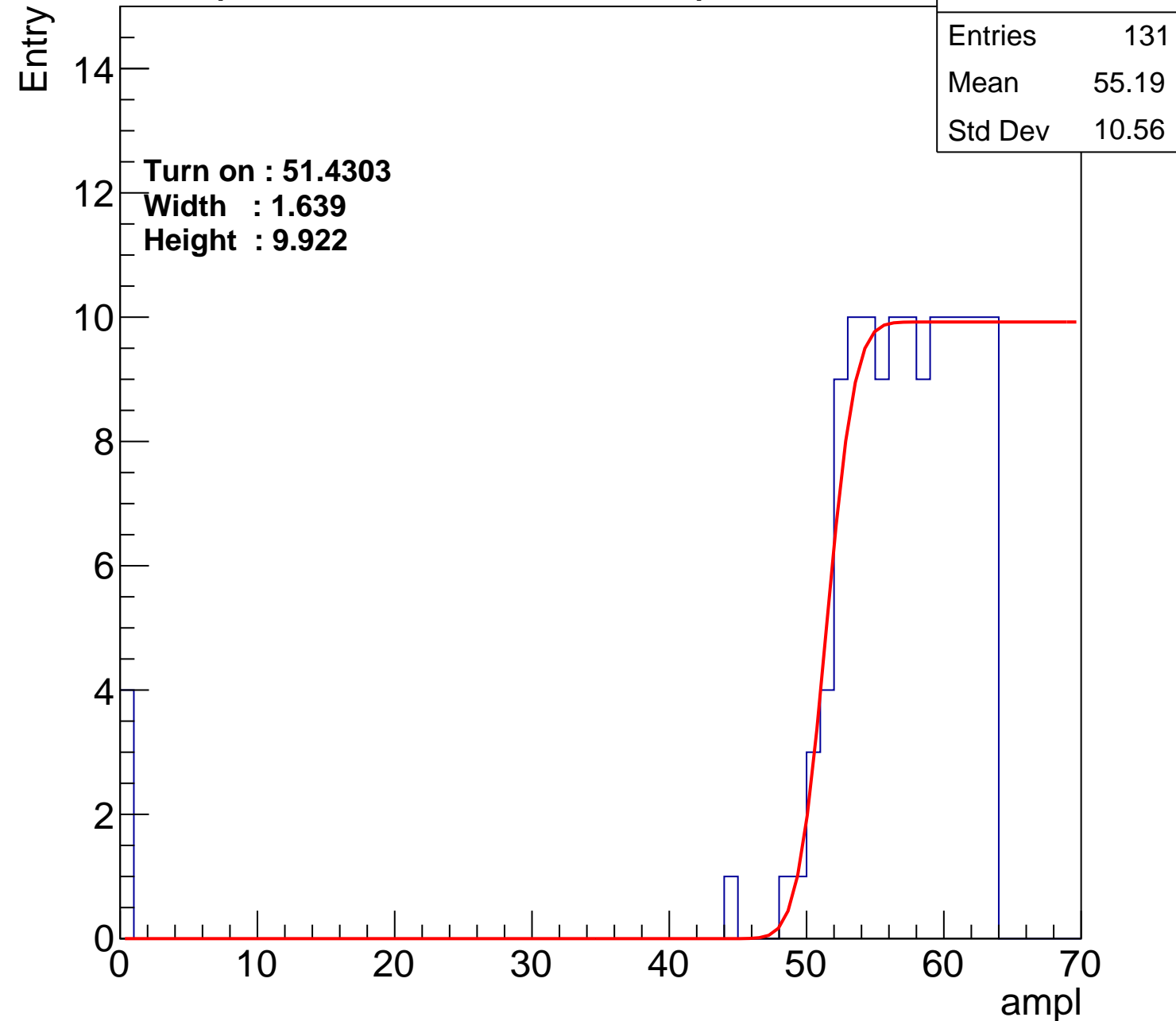
Width : 1.639

Height : 9.922

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	101
Mean	55.98
Std Dev	11.84

Turn on : 54.6569

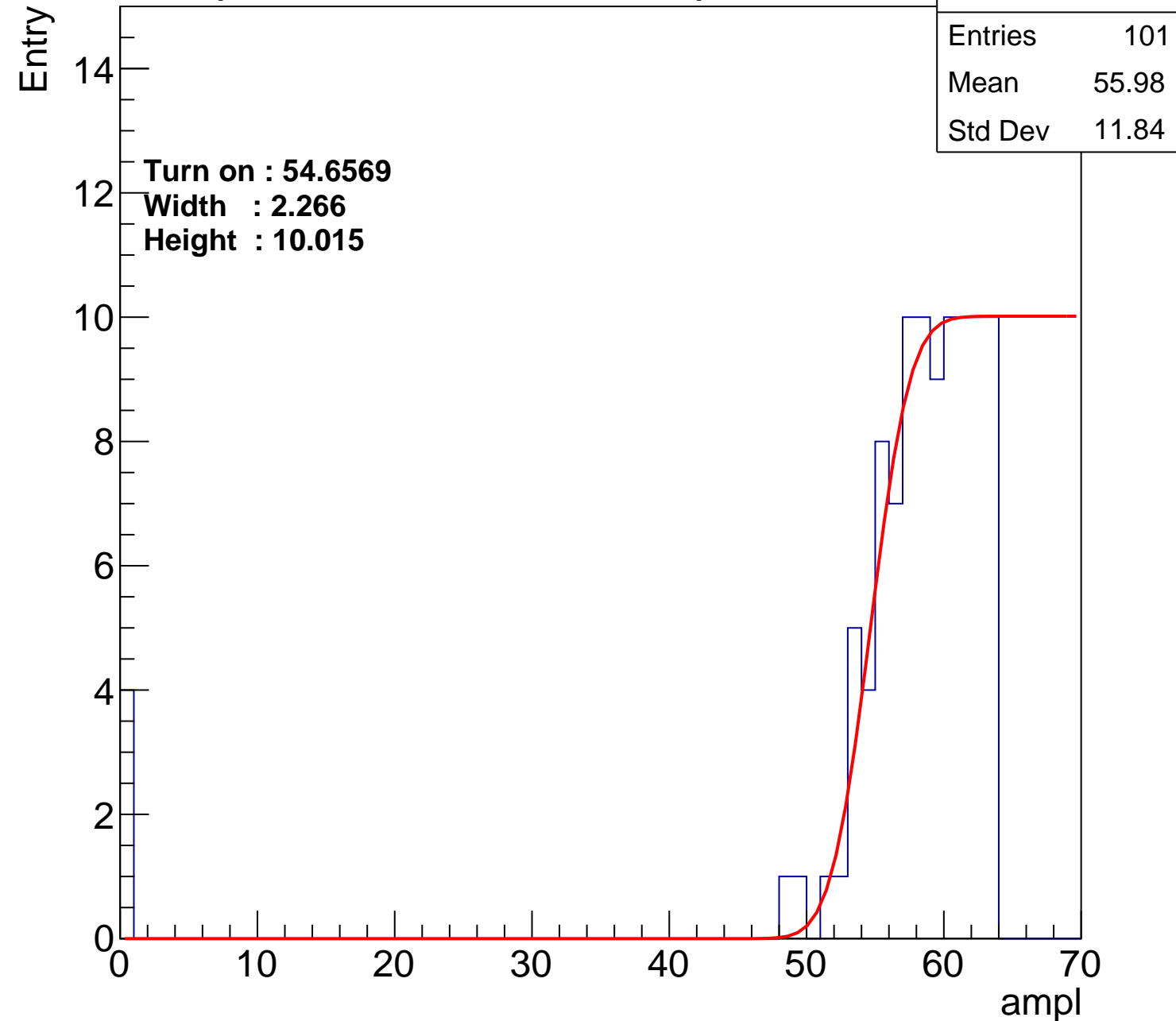
Width : 2.266

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	56.03
Std Dev	9.819

**Turn on : 52.7528**

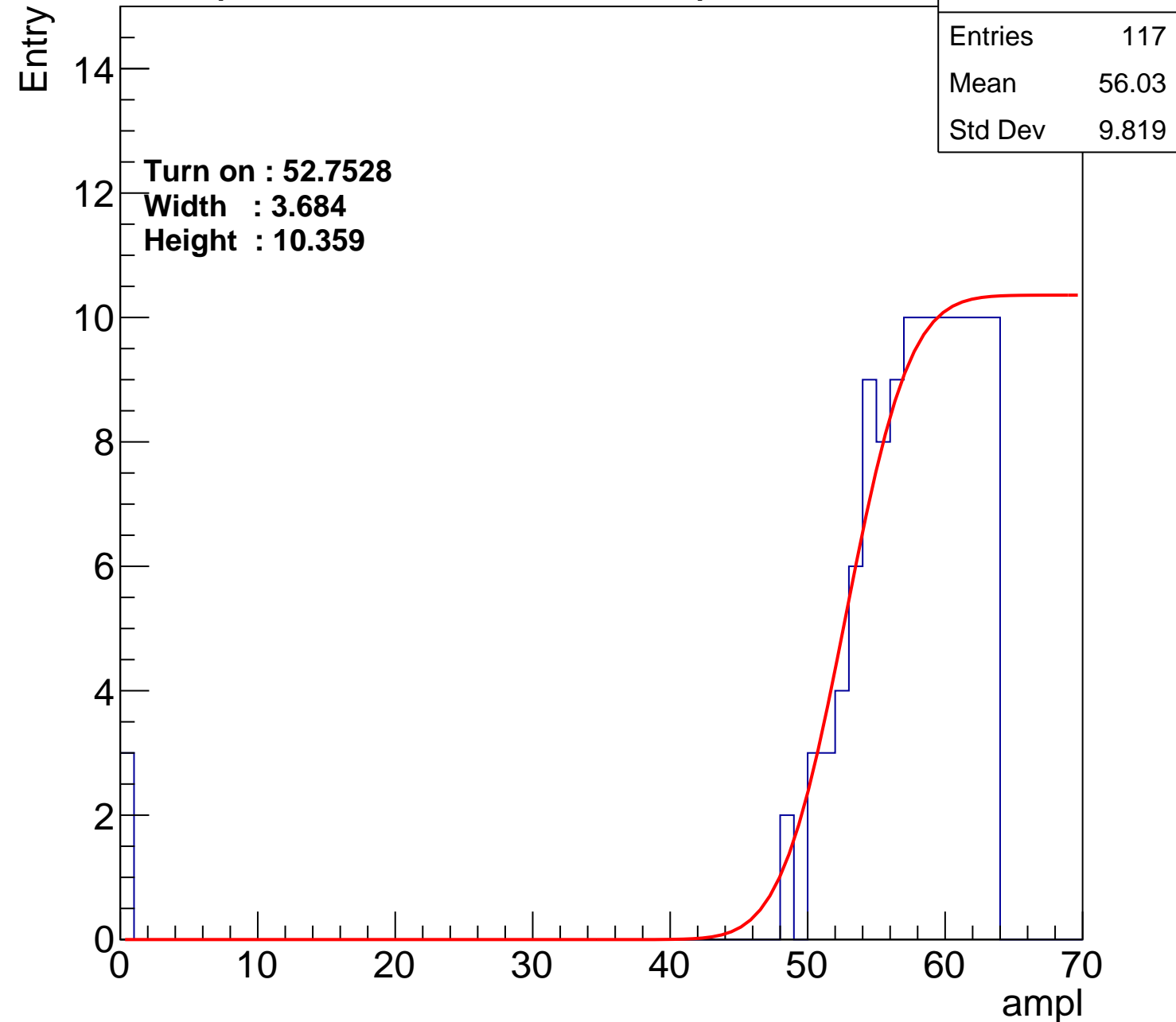
**Width : 3.684**

**Height : 10.359**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.13
Std Dev	10.04

Turn on : 53.2993

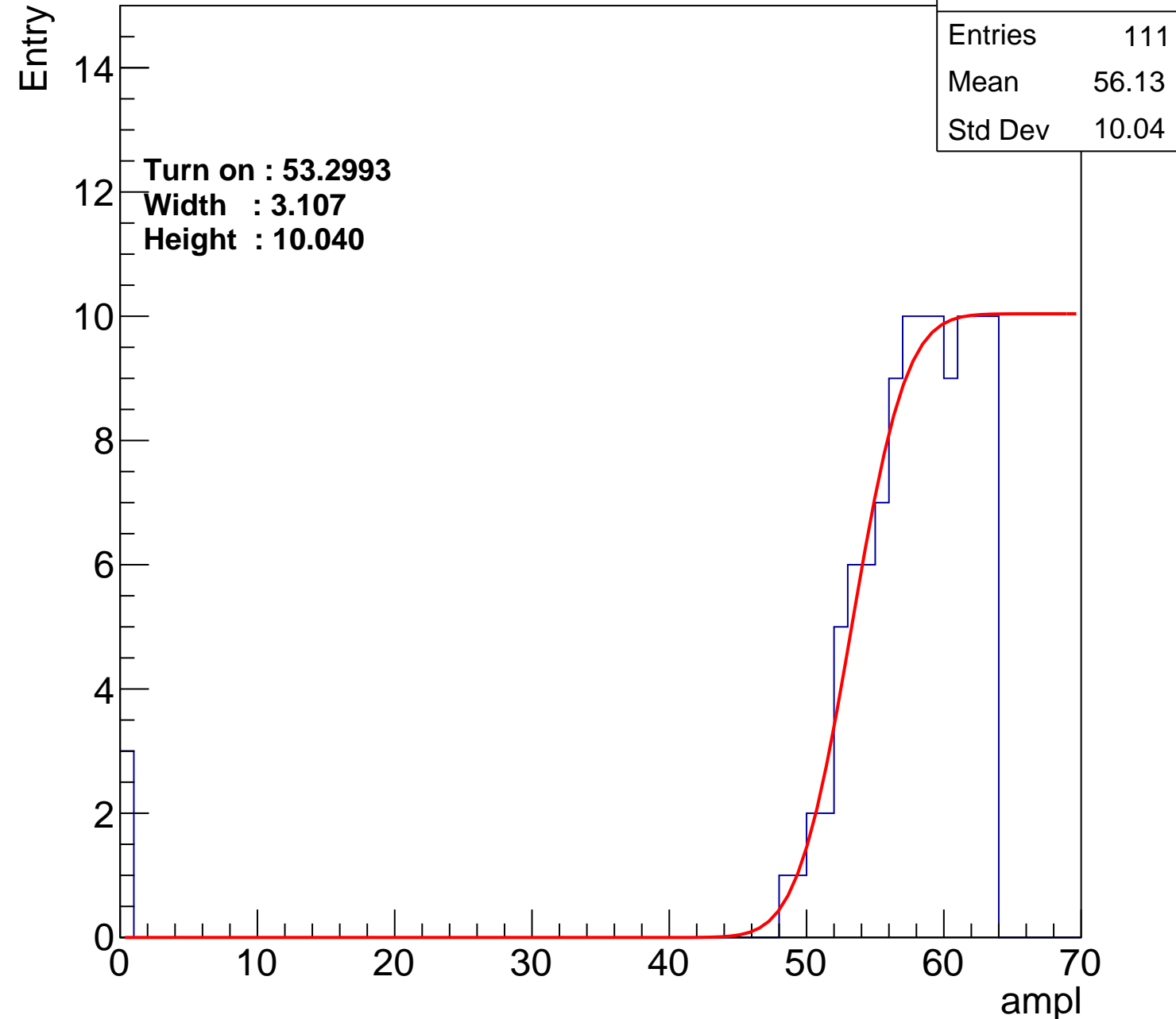
Width : 3.107

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.65
Std Dev	6.557

Turn on : 52.2743

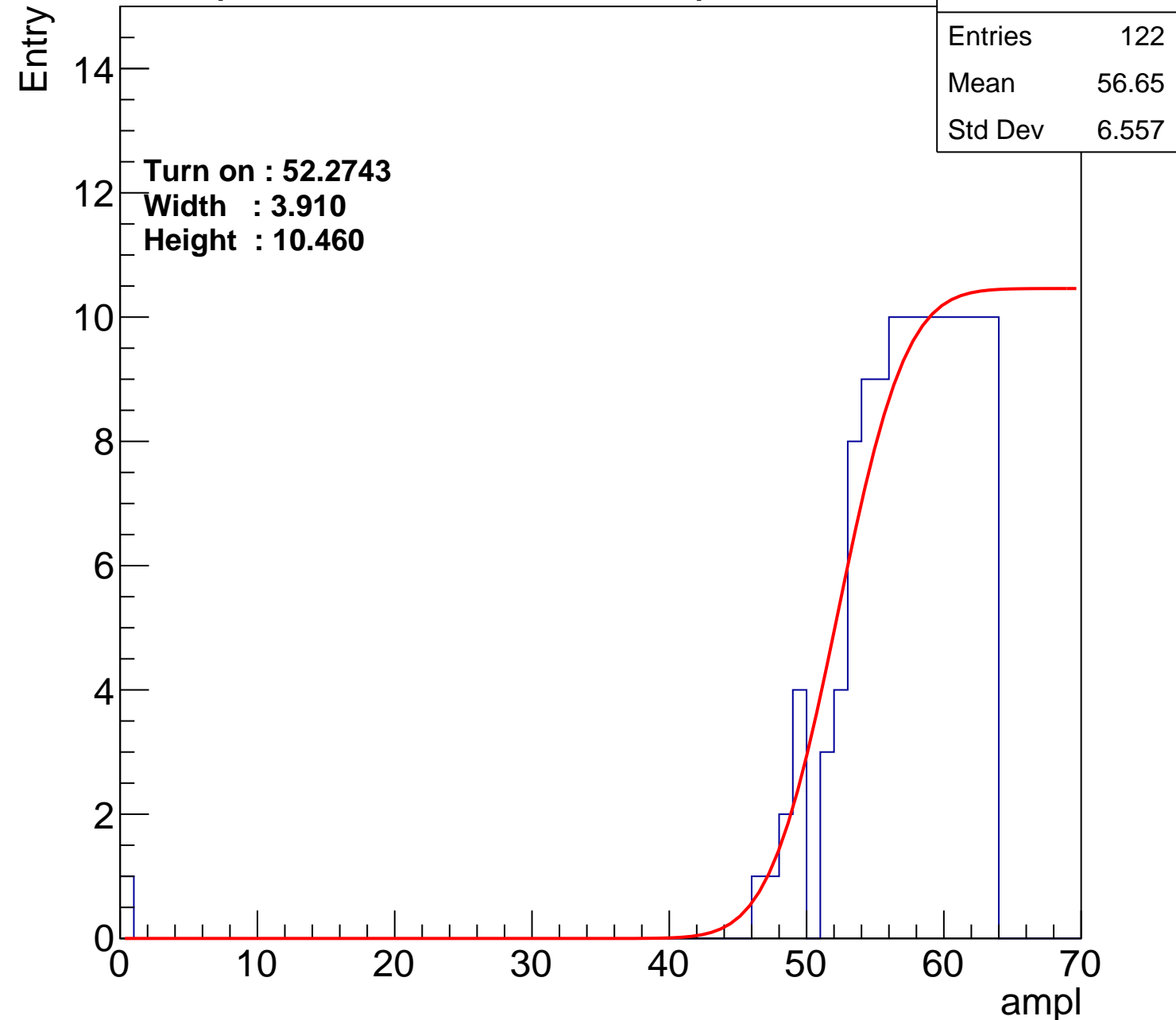
Width : 3.910

Height : 10.460

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

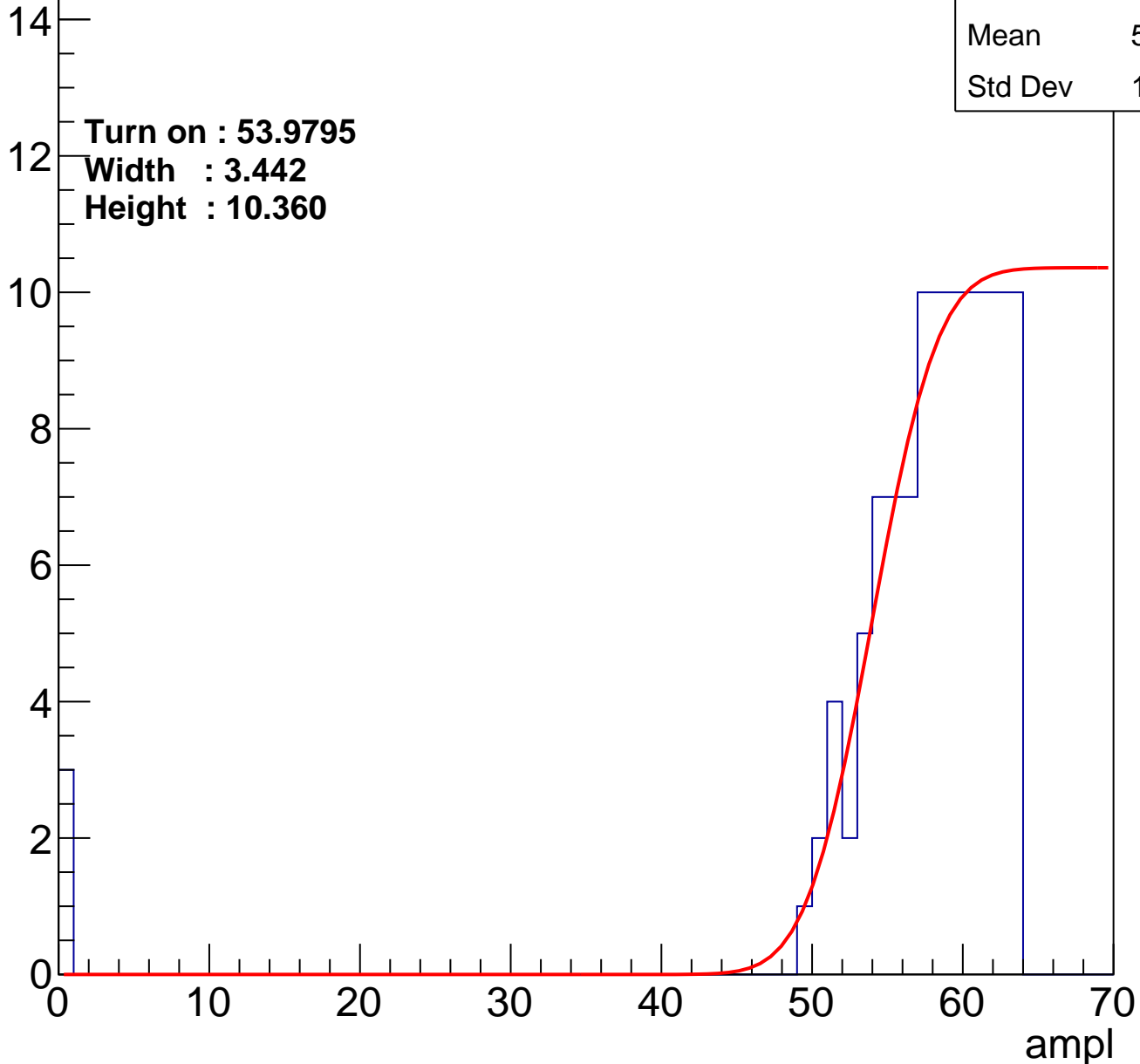
Entries	108
Mean	56.27
Std Dev	10.15

Turn on : 53.9795

Width : 3.442

Height : 10.360

Entry



# B0L103S, U25-ch108

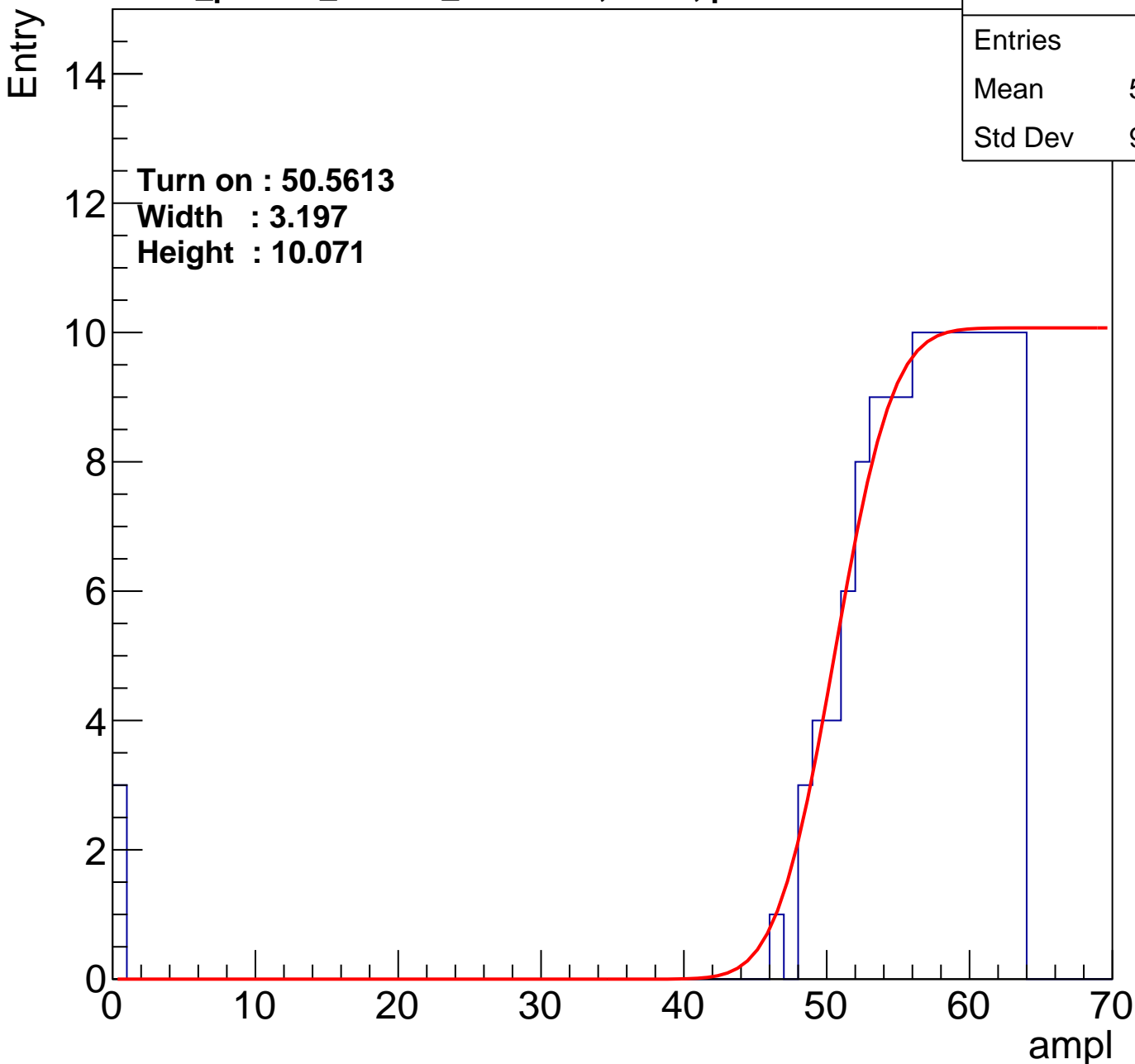
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

**Turn on : 50.5613**

**Width : 3.197**

**Height : 10.071**

Entries	136
Mean	55.34
Std Dev	9.309



# B0L103S, U25-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

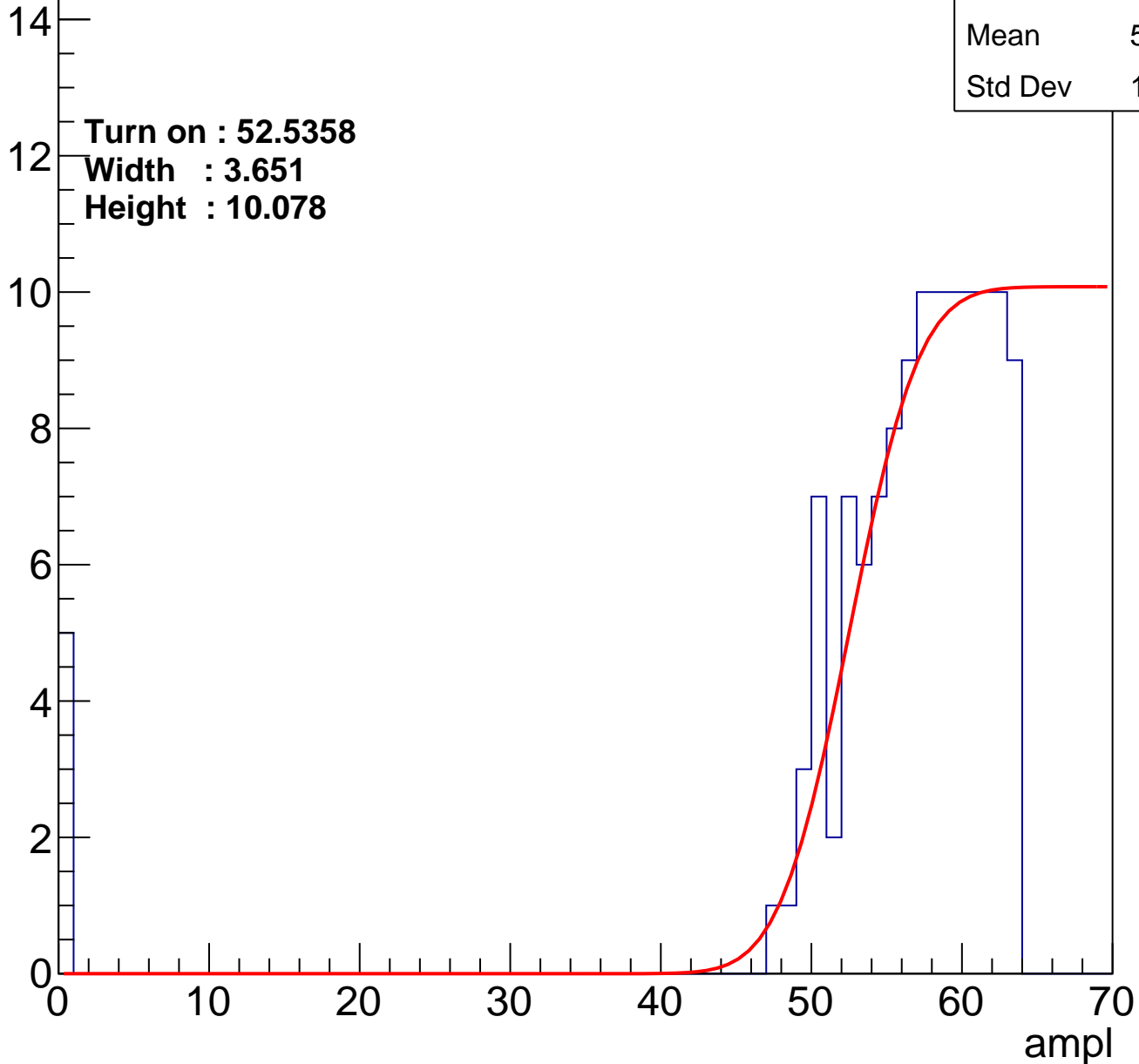
Entries	125
Mean	54.68
Std Dev	11.87

Turn on : 52.5358

Width : 3.651

Height : 10.078

Entry



# B0L103S, U25-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	55.2
Std Dev	9.188

Turn on : 50.5946

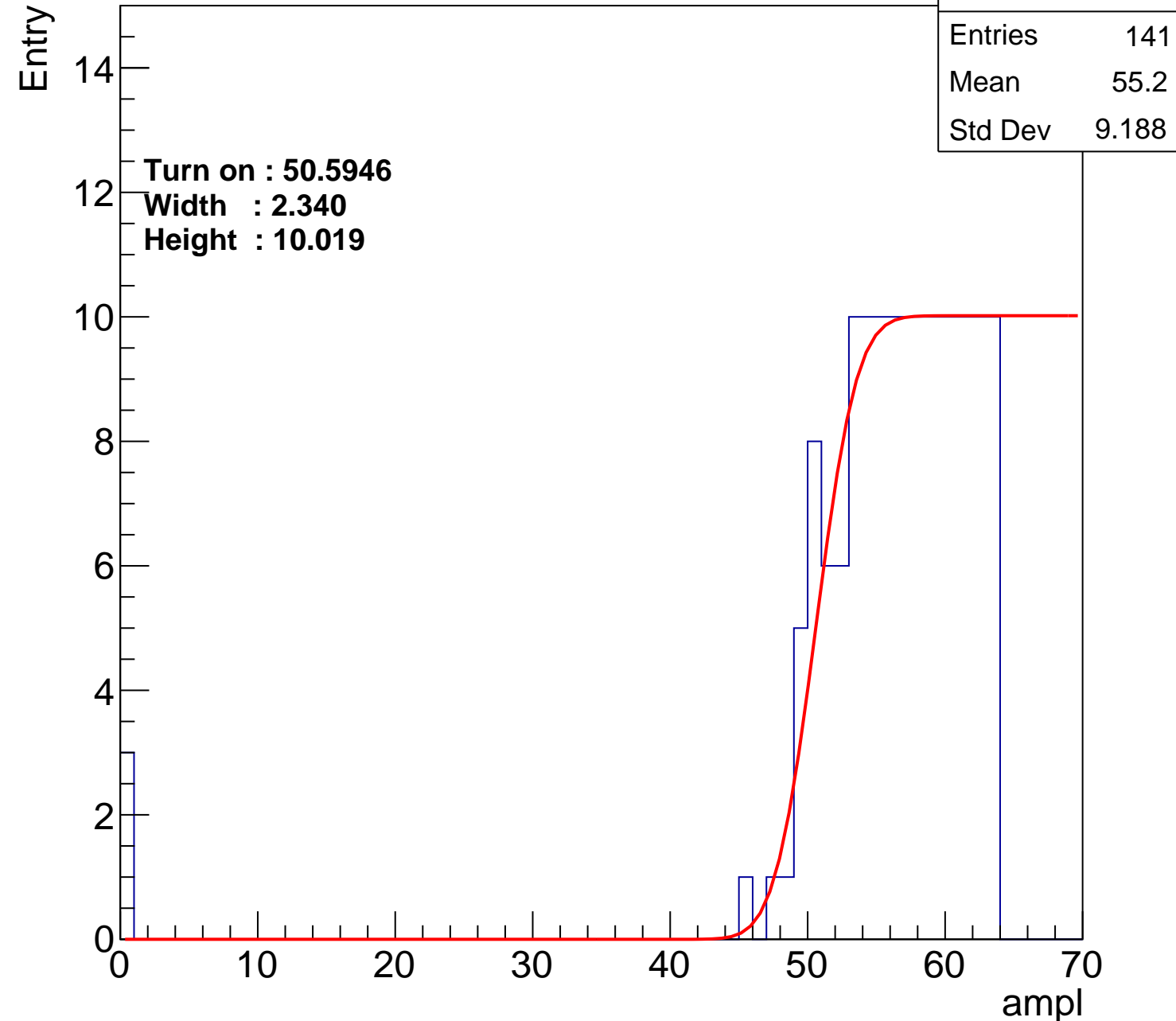
Width : 2.340

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch111

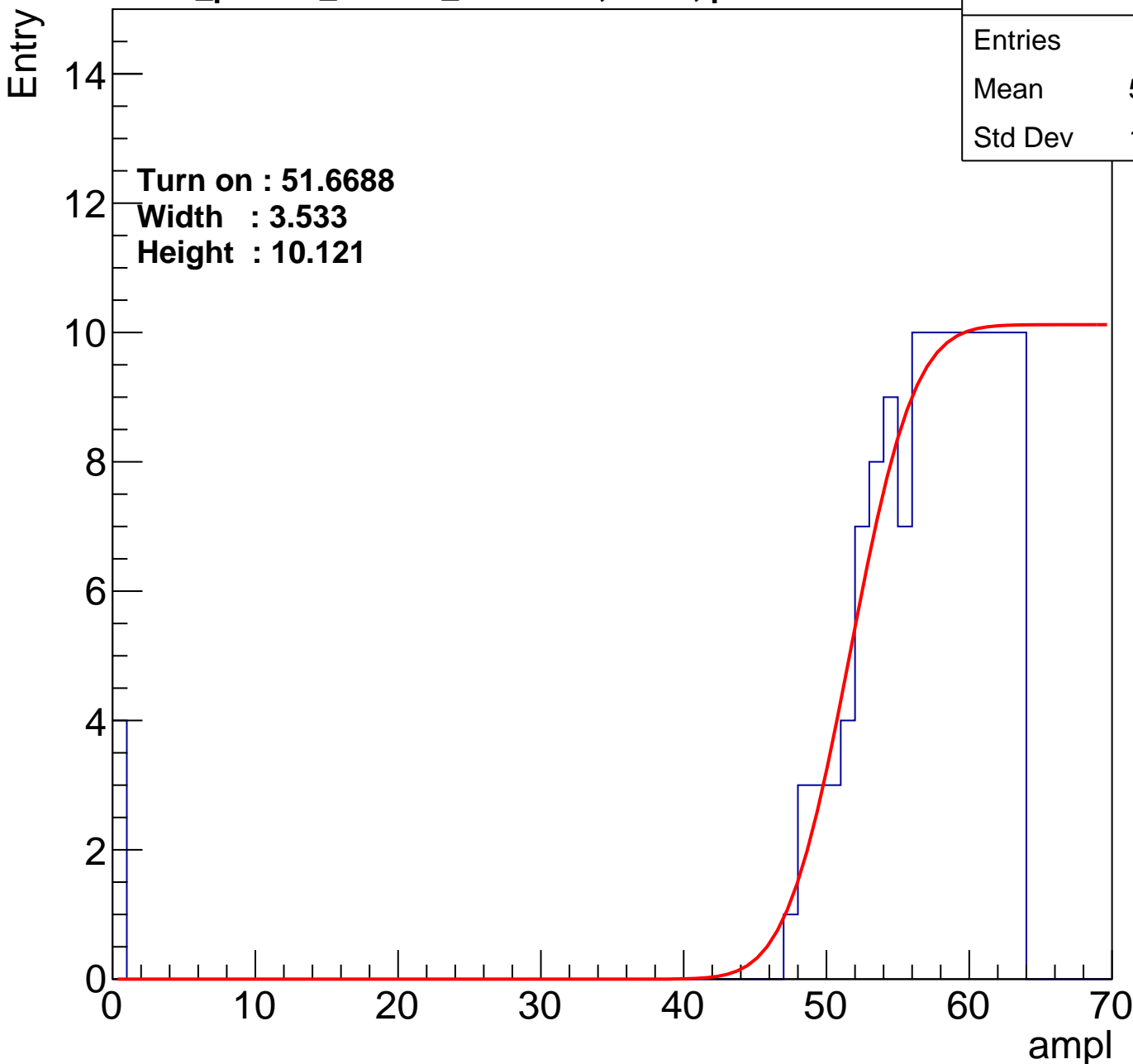
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	129
Mean	55.12
Std Dev	10.67

**Turn on : 51.6688**

**Width : 3.533**

**Height : 10.121**



# B0L103S, U25-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

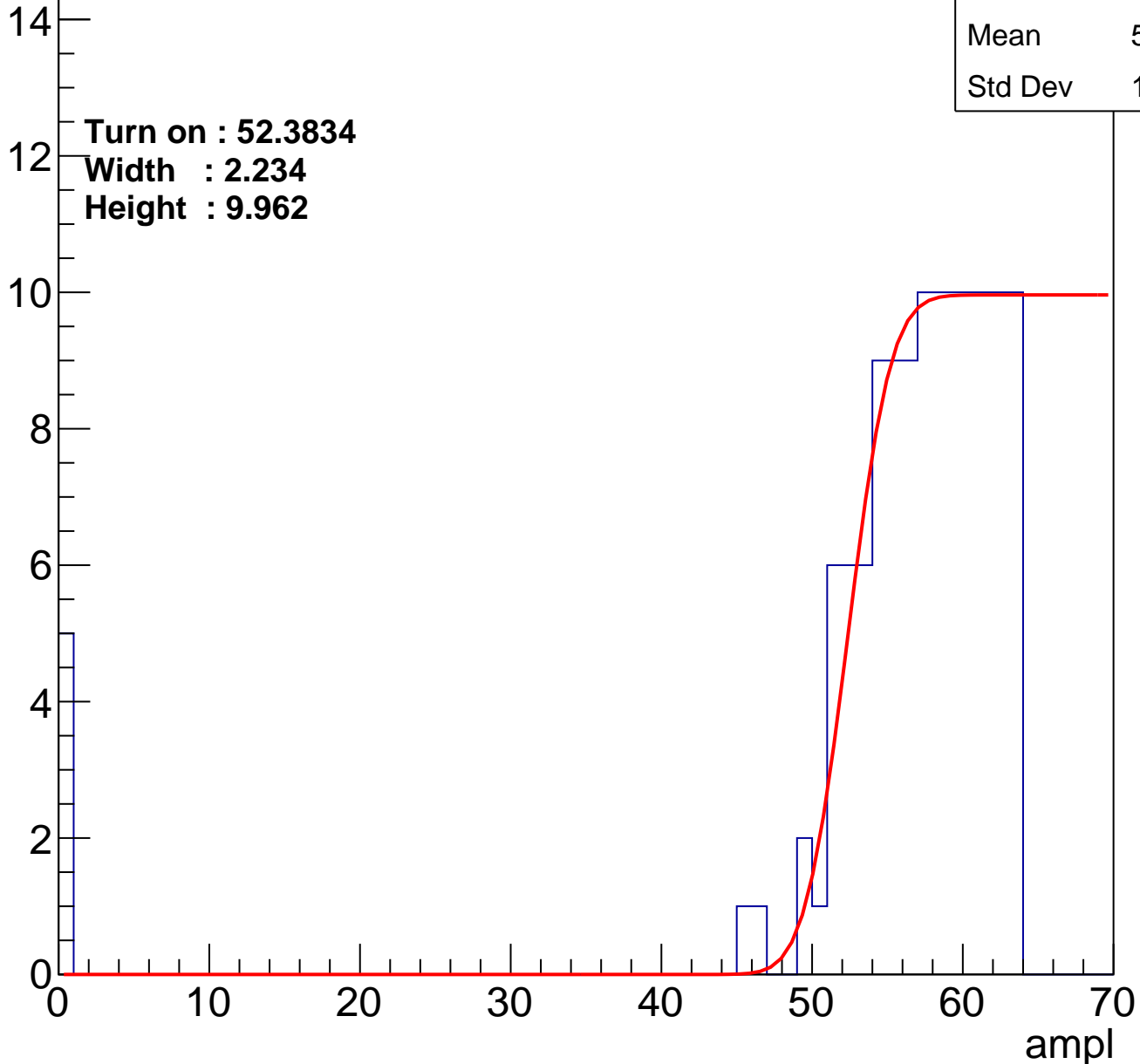
Entries	125
Mean	54.88
Std Dev	11.87

Turn on : 52.3834

Width : 2.234

Height : 9.962

Entry



# B0L103S, U25-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	98
Mean	57.2
Std Dev	8.841

Turn on : 54.3743

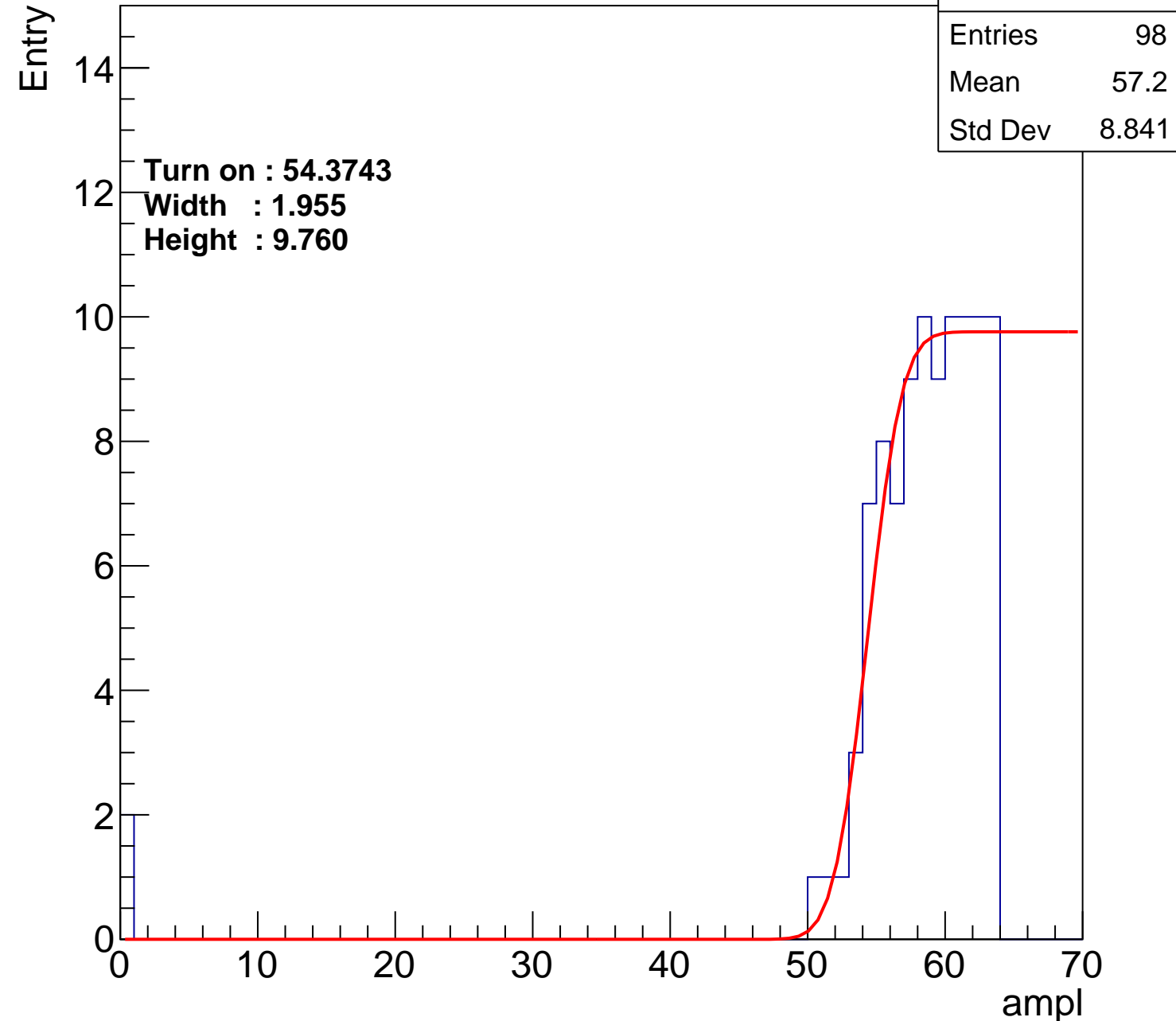
Width : 1.955

Height : 9.760

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.15
Std Dev	9.24

Turn on : 50.4208

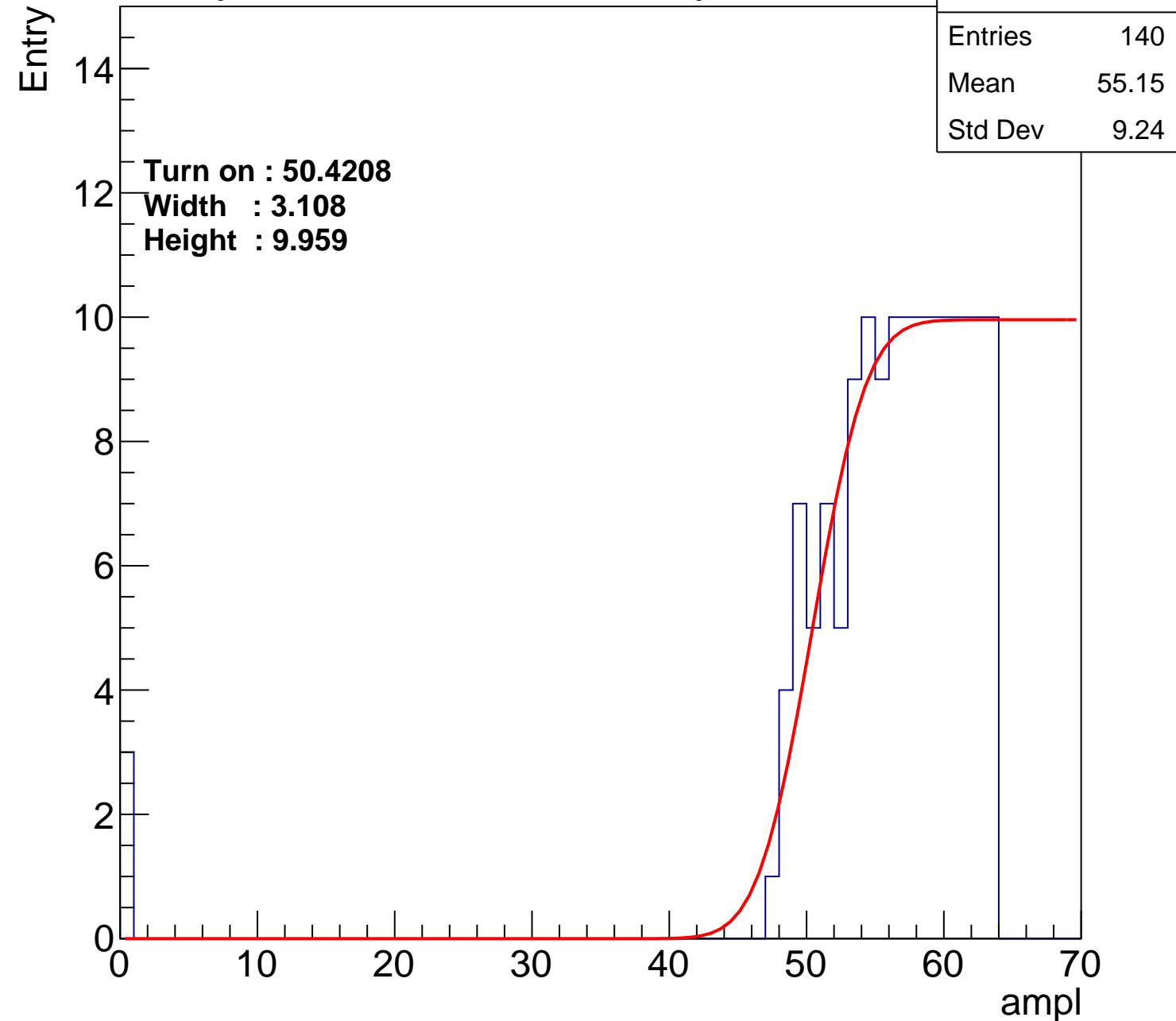
Width : 3.108

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.01
Std Dev	10.61

Turn on : 51.7442

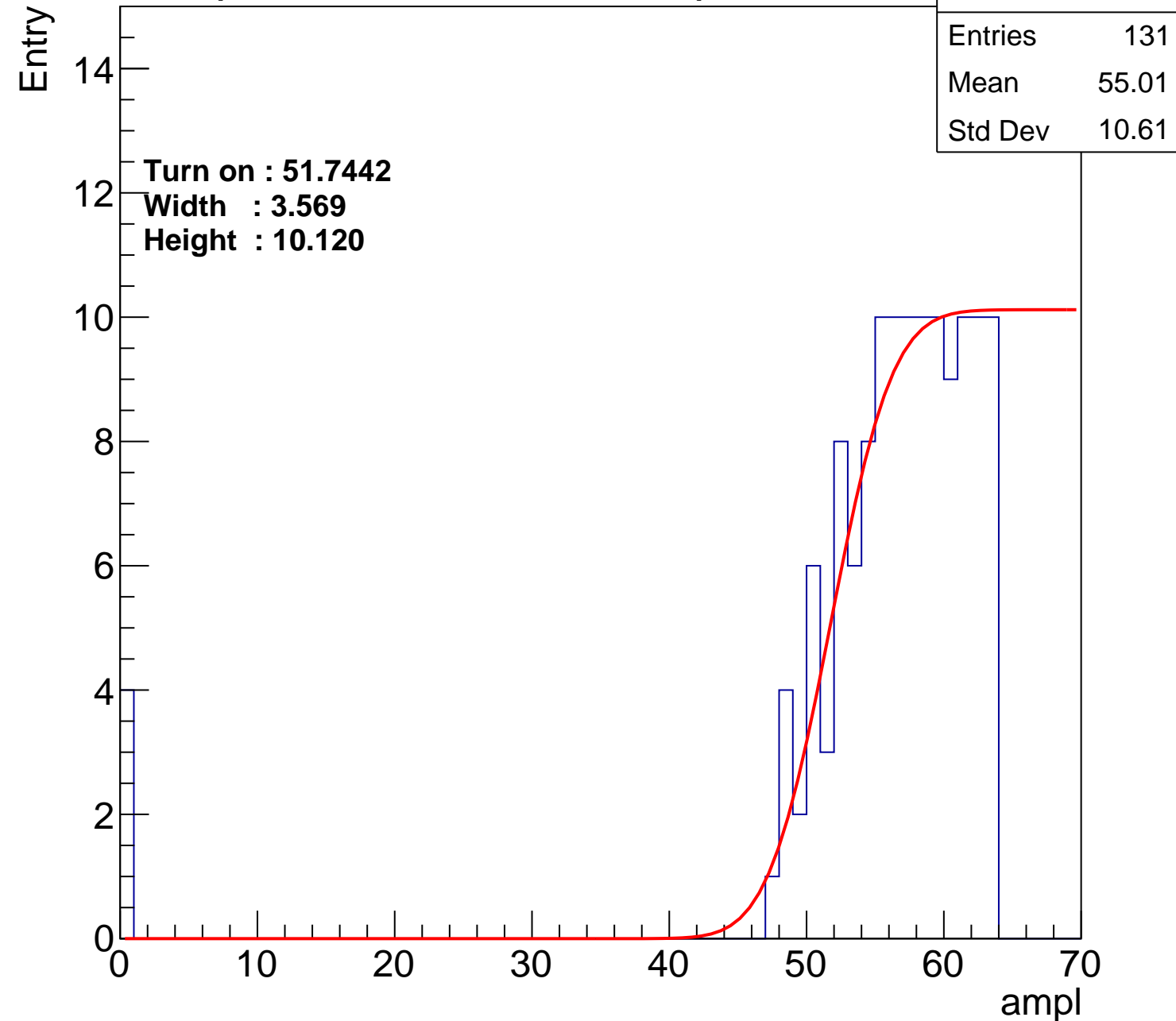
Width : 3.569

Height : 10.120

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.58
Std Dev	11.06

Turn on : 52.8521

Width : 2.260

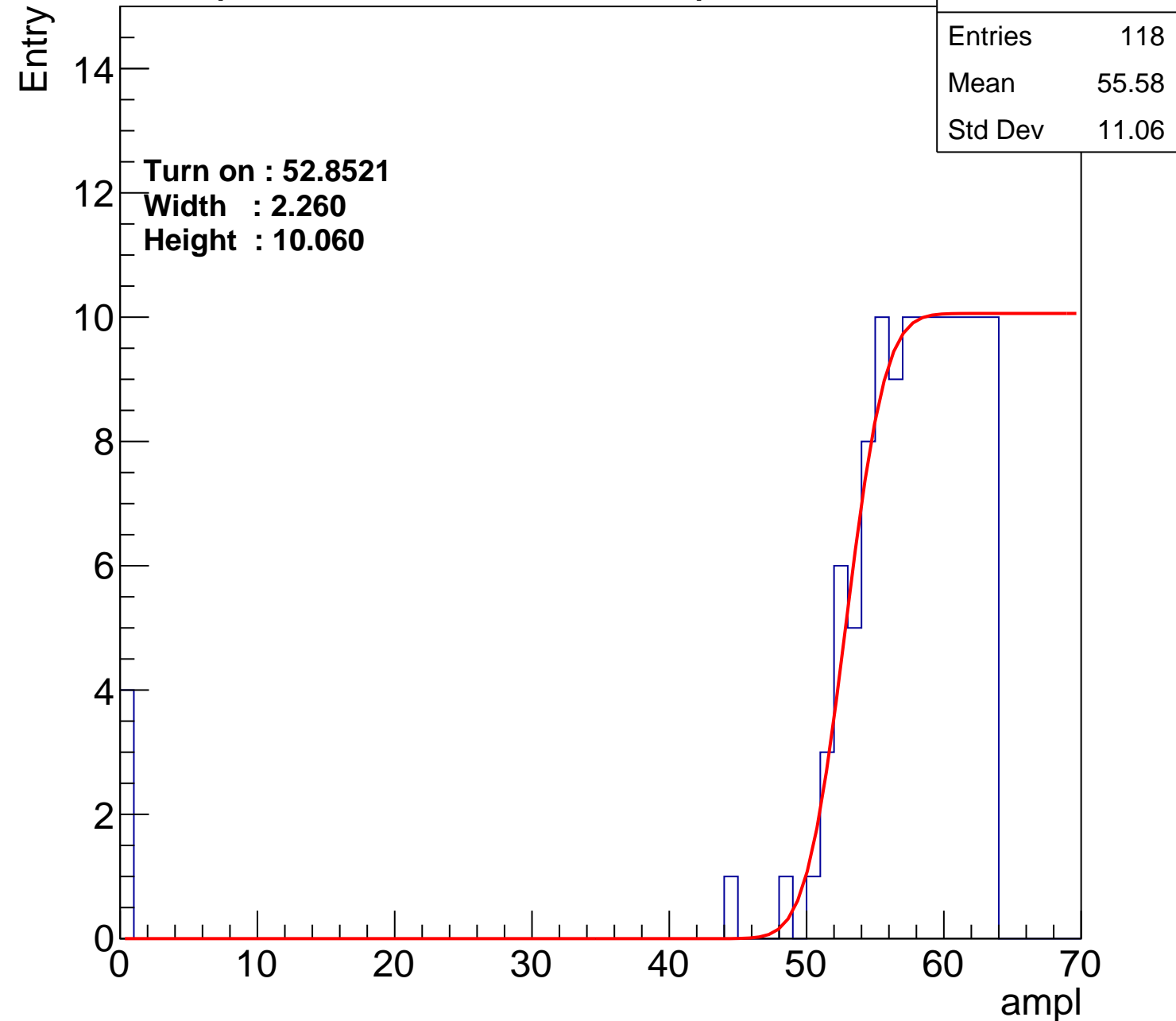
Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U25-ch117

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	54.99
Std Dev	11.98

Turn on : 52.6265

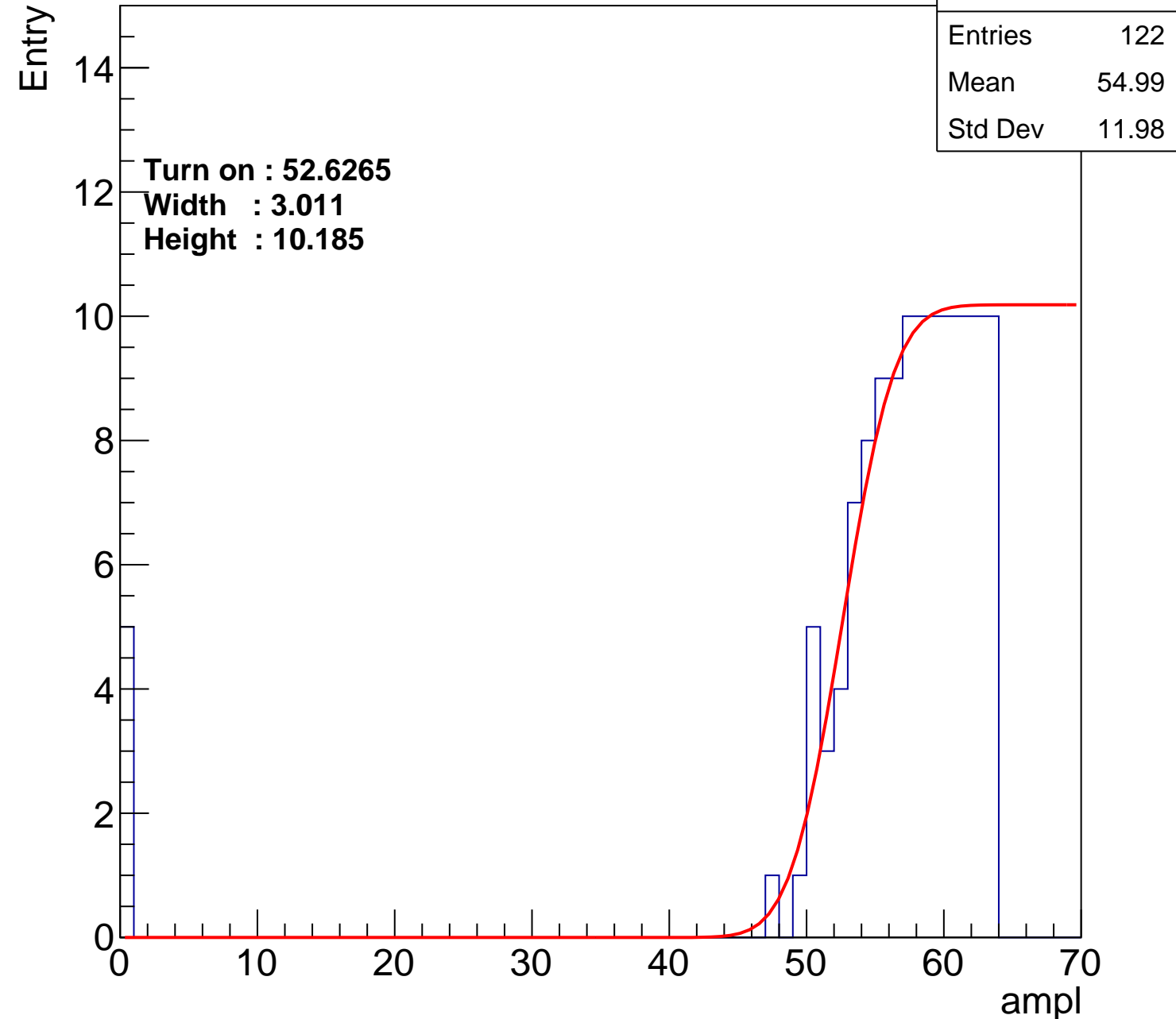
Width : 3.011

Height : 10.185

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.87
Std Dev	9.766

Turn on : 52.6758

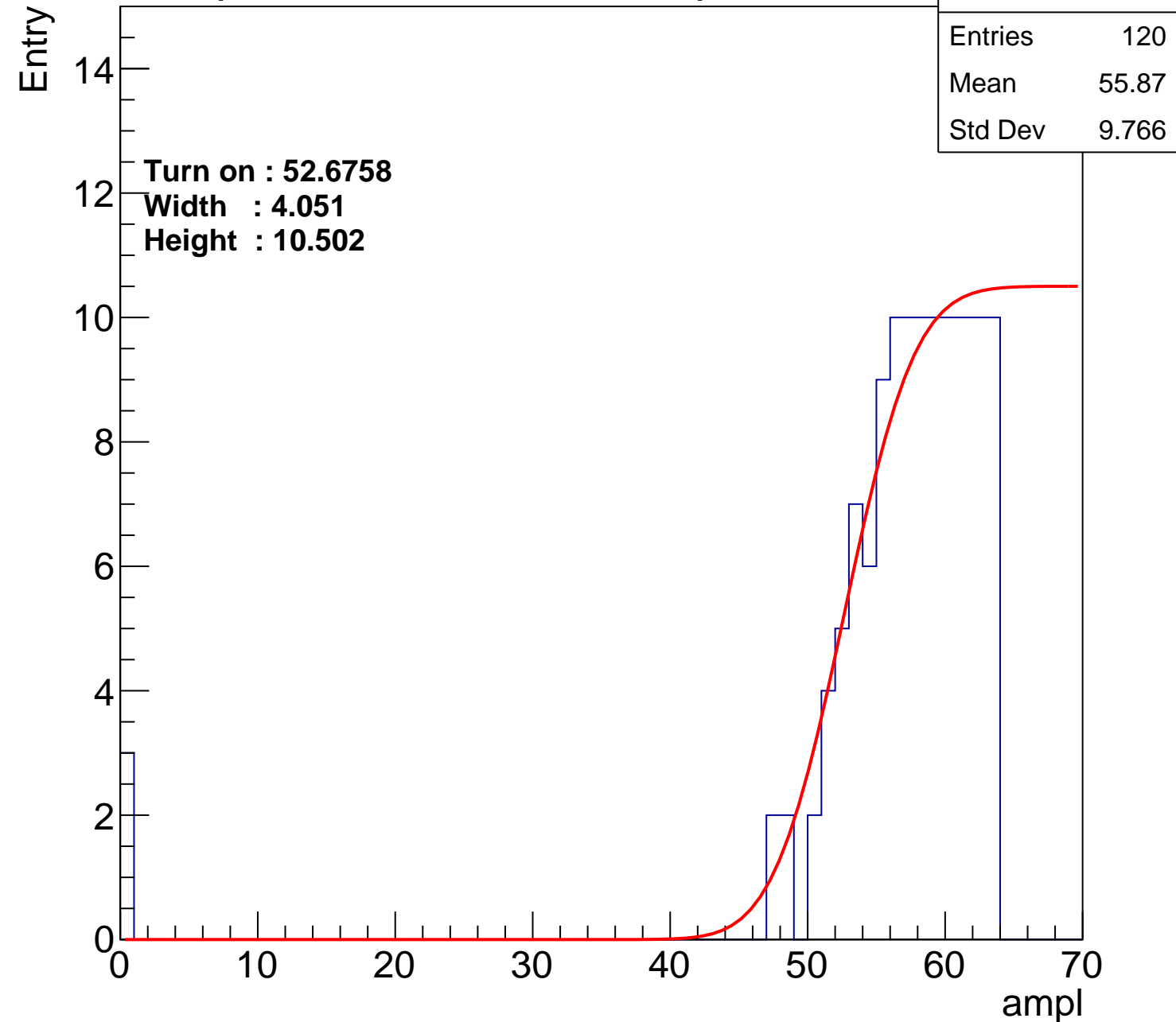
Width : 4.051

Height : 10.502

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55
Std Dev	10.42

Turn on : 51.1168

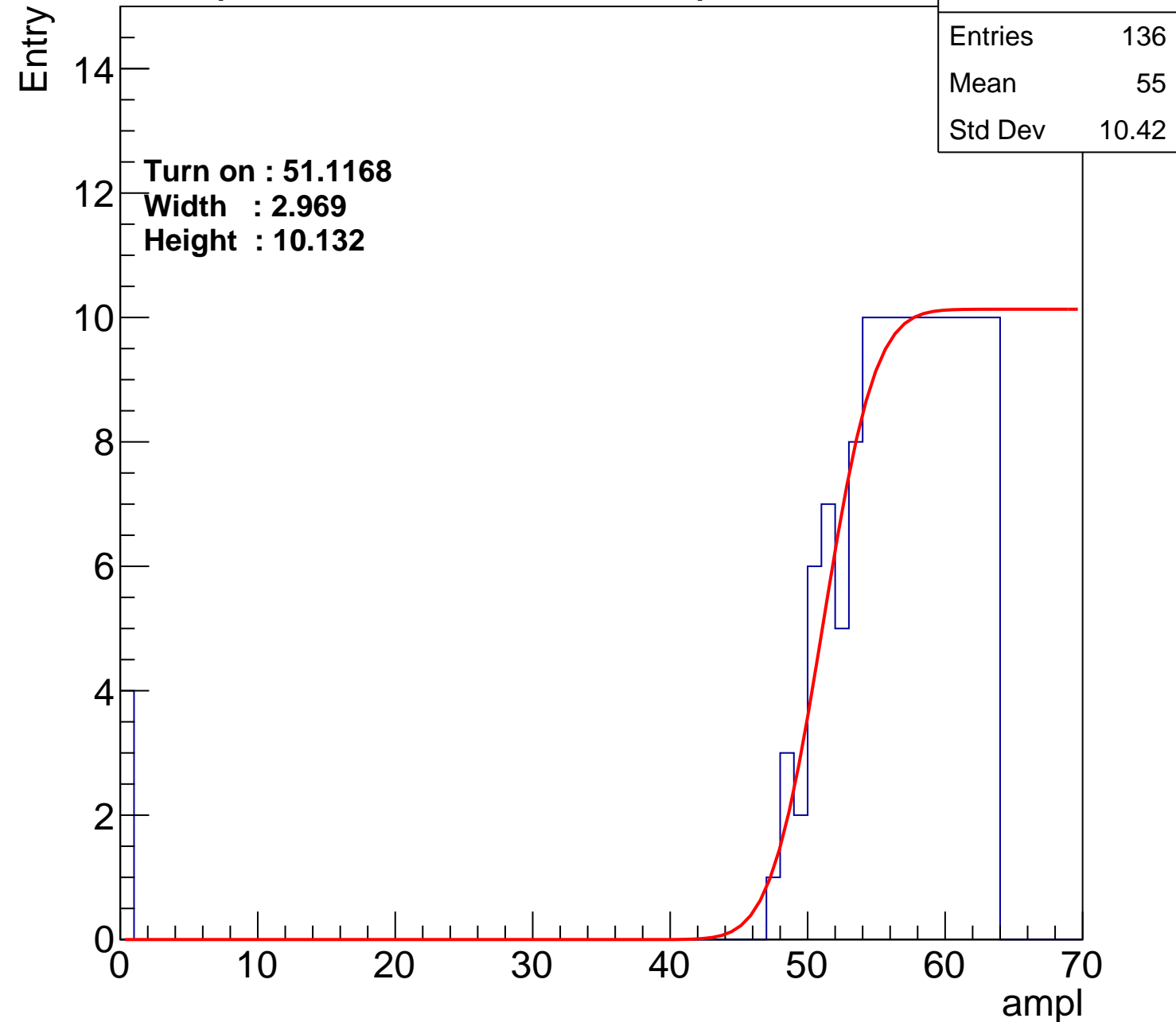
Width : 2.969

Height : 10.132

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

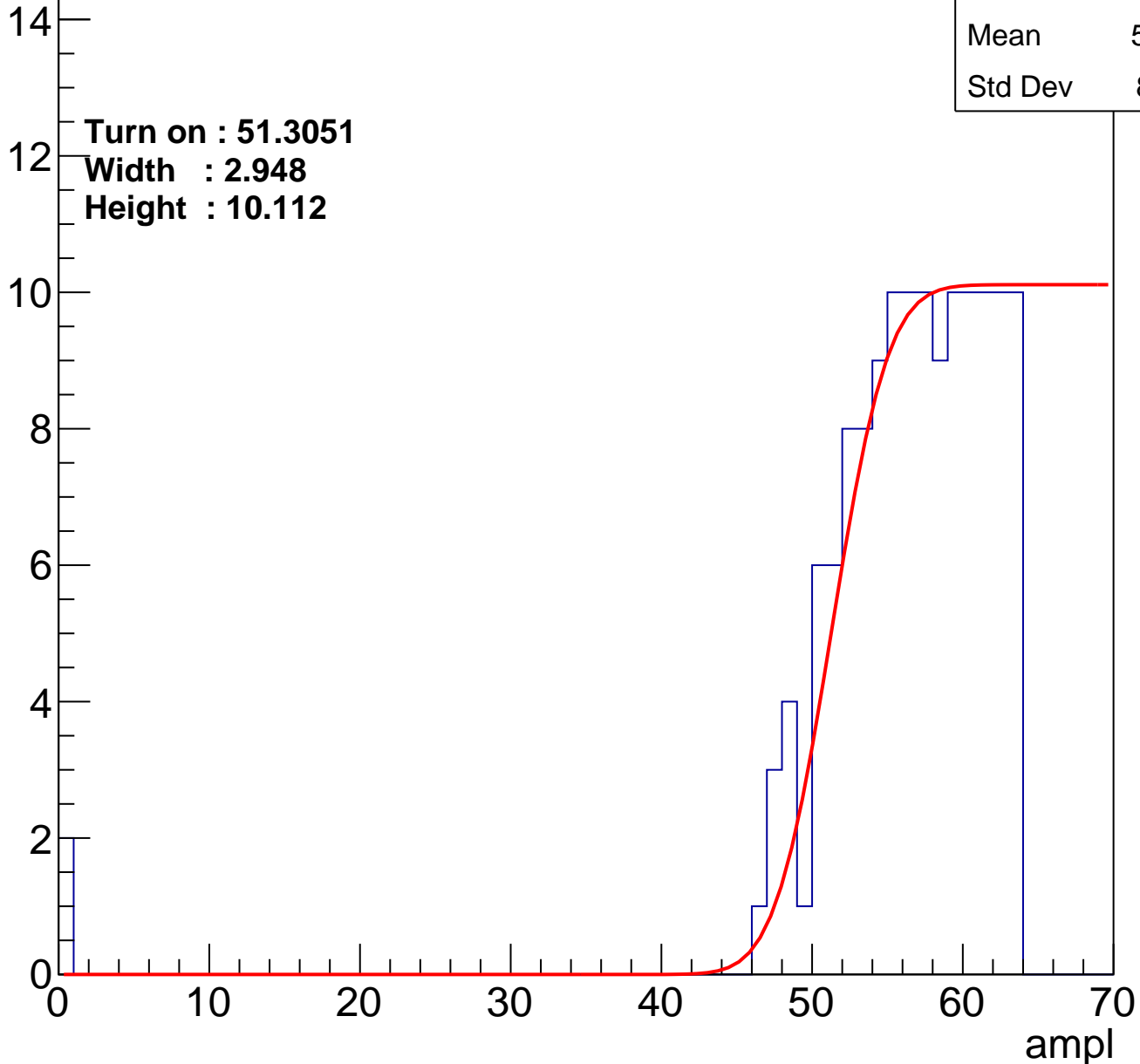
Entries	137
Mean	55.56
Std Dev	8.061

Turn on : 51.3051

Width : 2.948

Height : 10.112

Entry



# B0L103S, U25-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.19
Std Dev	12.38

Turn on : 50.8684

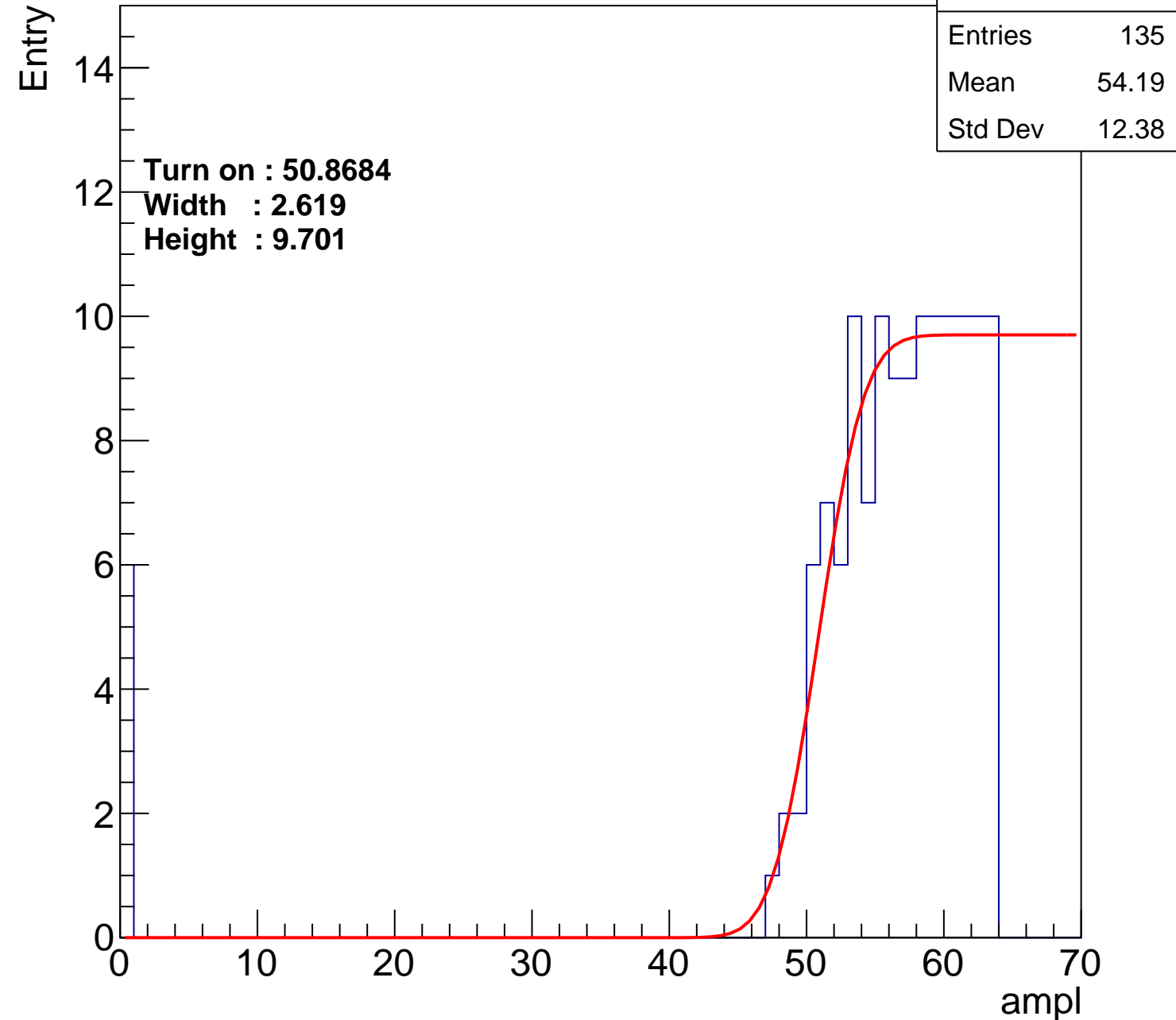
Width : 2.619

Height : 9.701

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	55.42
Std Dev	7.932

Turn on : 49.9088

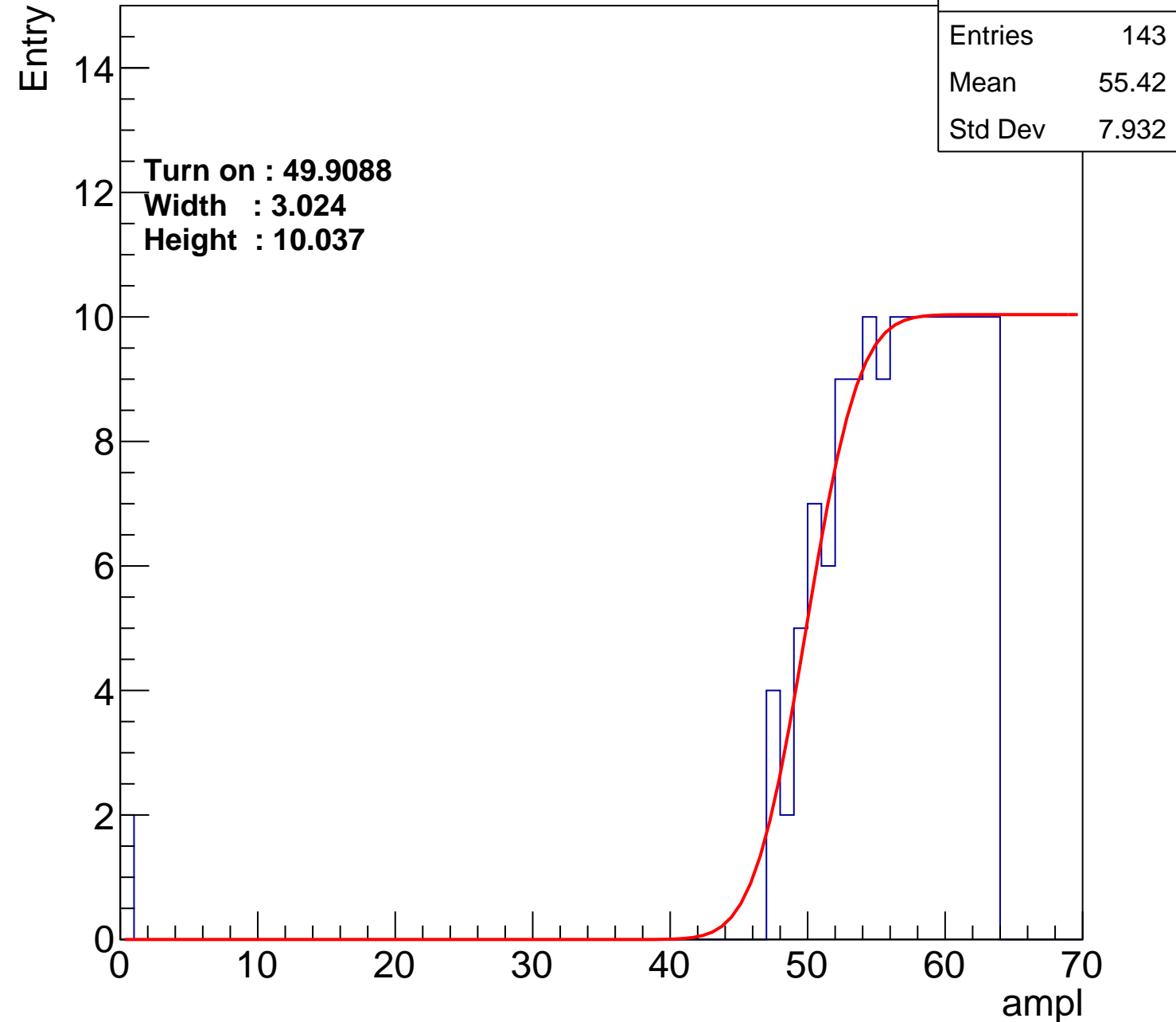
Width : 3.024

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch123

calib\_packv5\_040323\_1717.root, FC#2, port C3

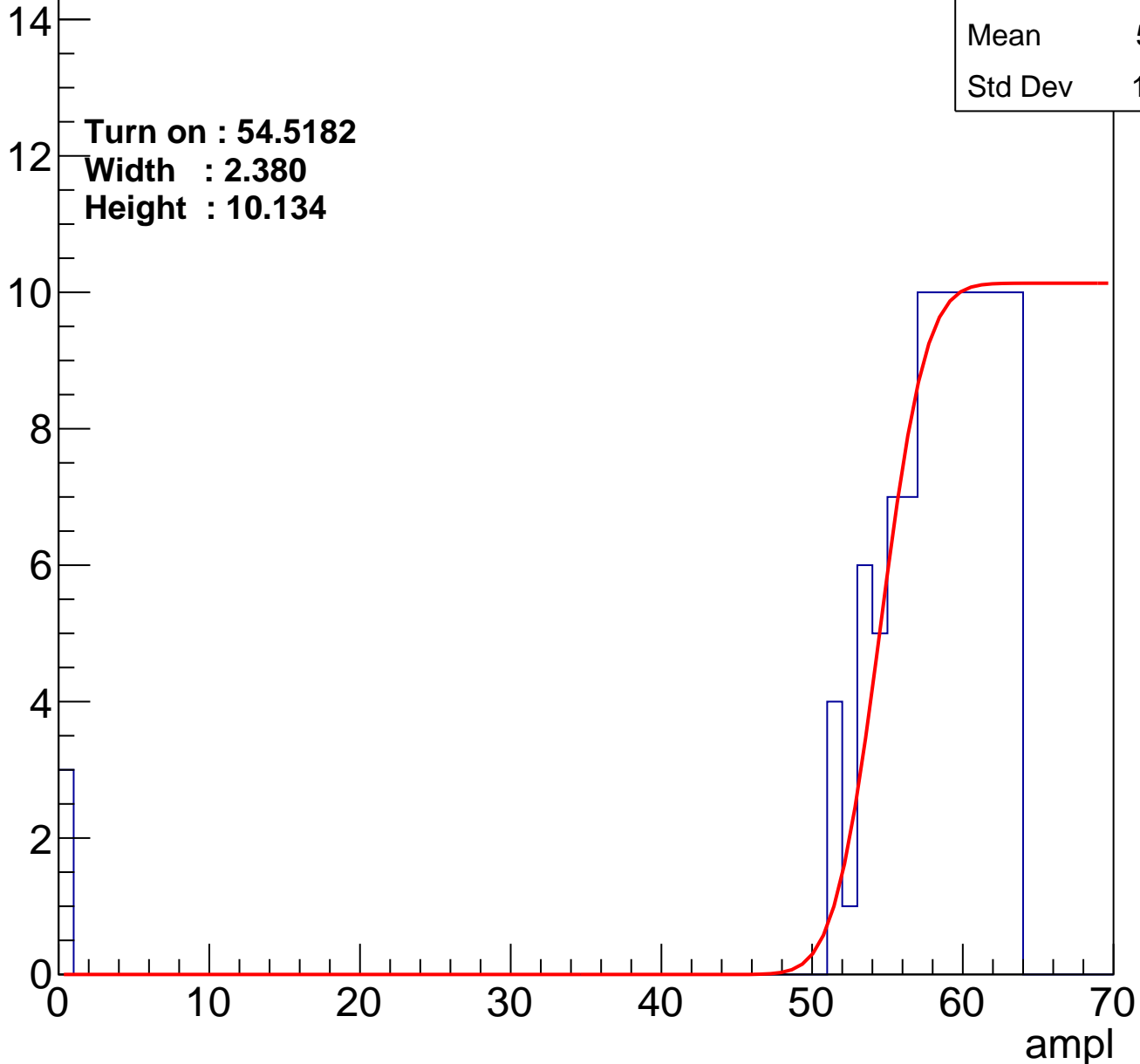
Entries	103
Mean	56.51
Std Dev	10.32

Turn on : 54.5182

Width : 2.380

Height : 10.134

Entry



# B0L103S, U25-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	53.96
Std Dev	12.45

Turn on : 50.7917

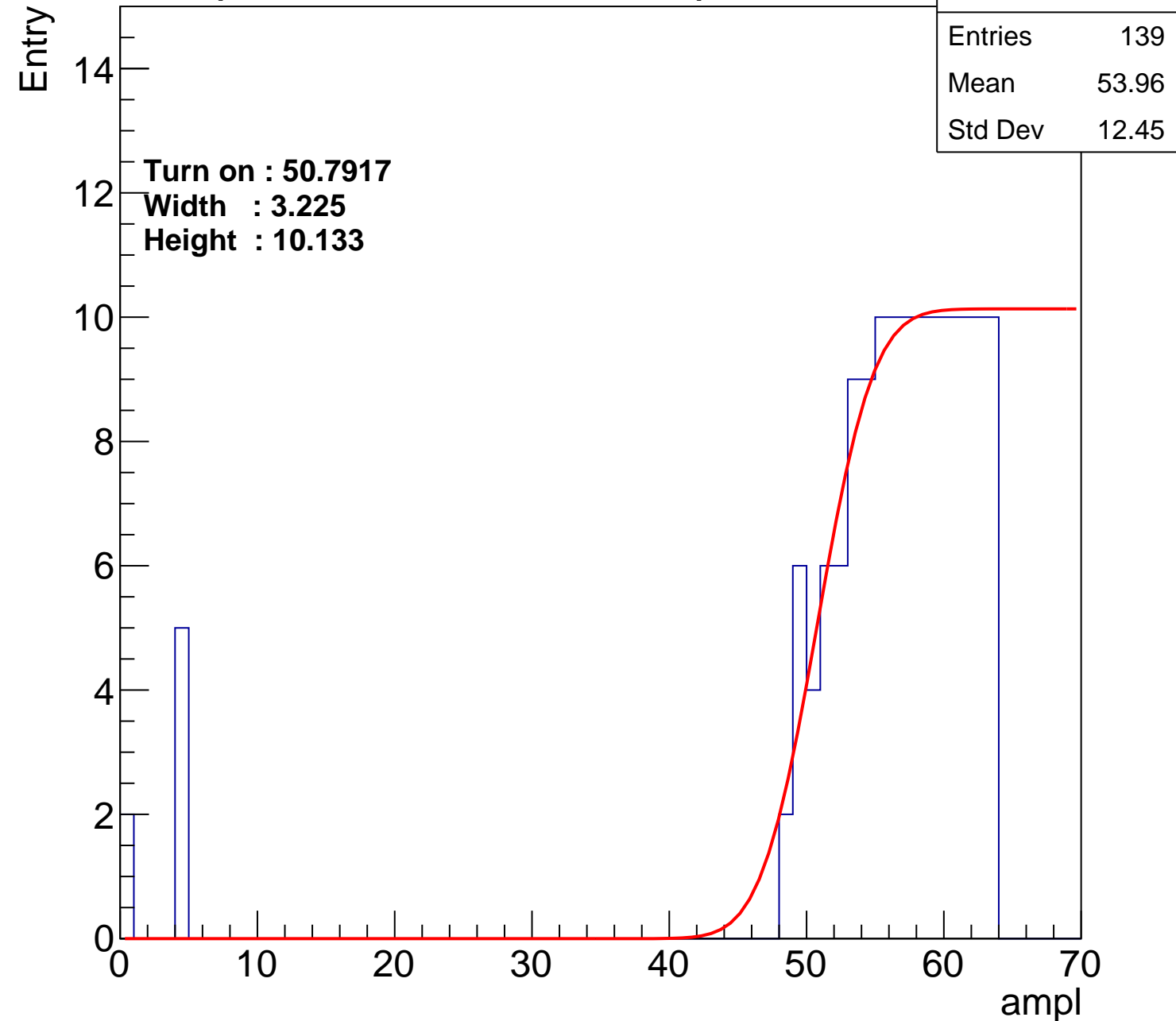
Width : 3.225

Height : 10.133

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch125

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	109
Mean	54.83
Std Dev	13.67

Turn on : 47.7334

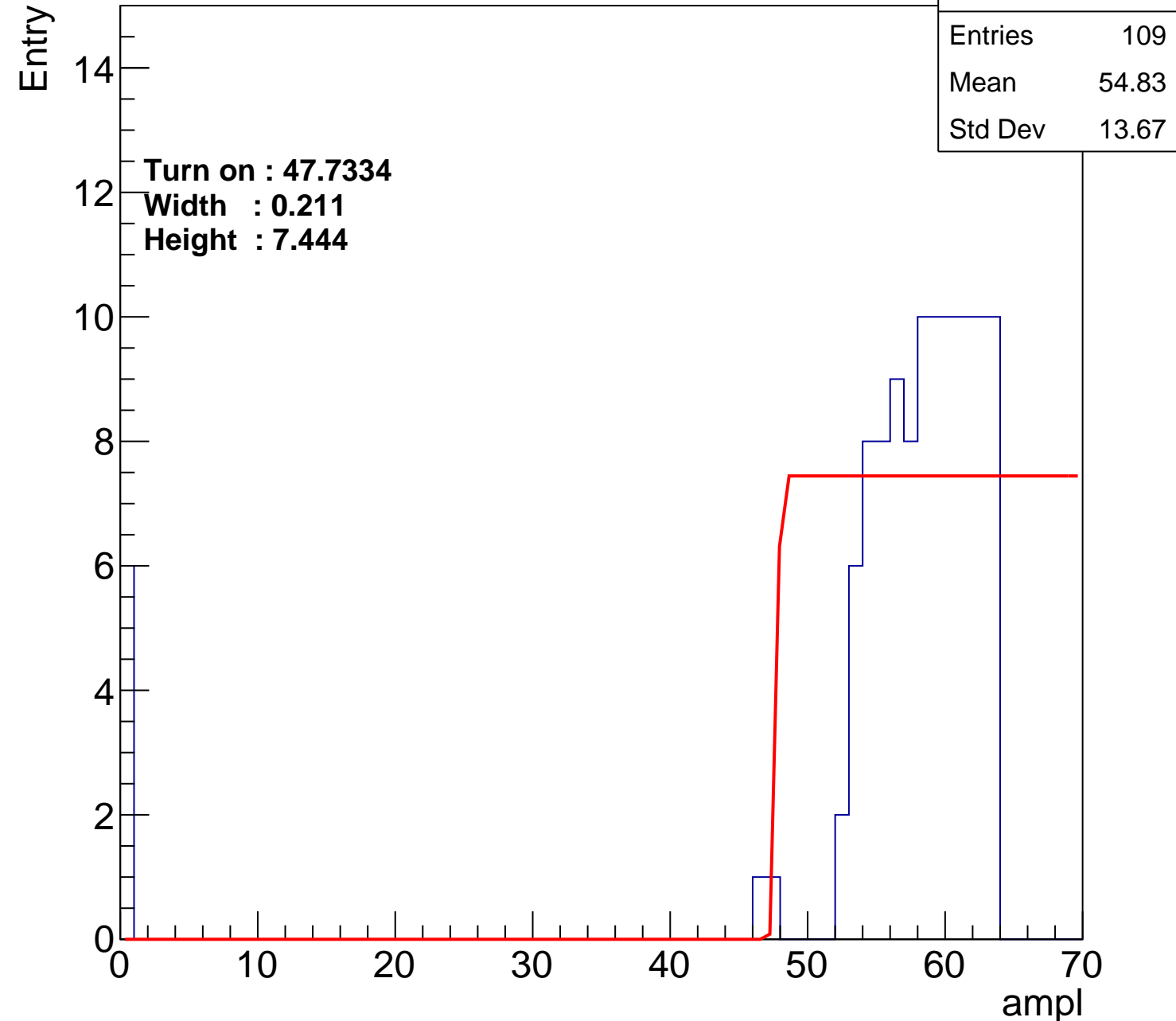
Width : 0.211

Height : 7.444

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	54.12
Std Dev	10.03

Turn on : 49.3918

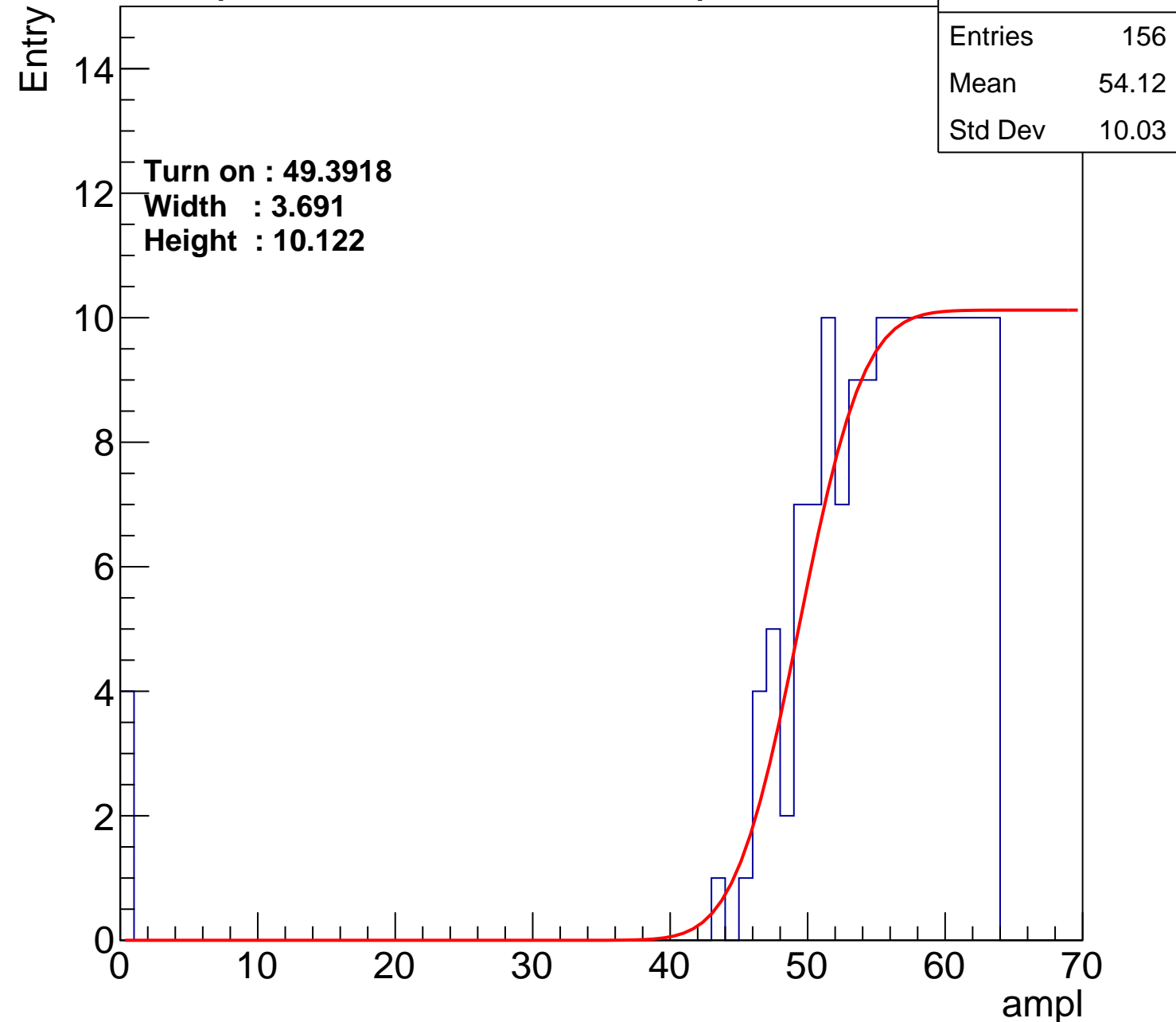
Width : 3.691

Height : 10.122

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U25-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	150
Mean	54.37
Std Dev	10.12

Turn on : 49.3869

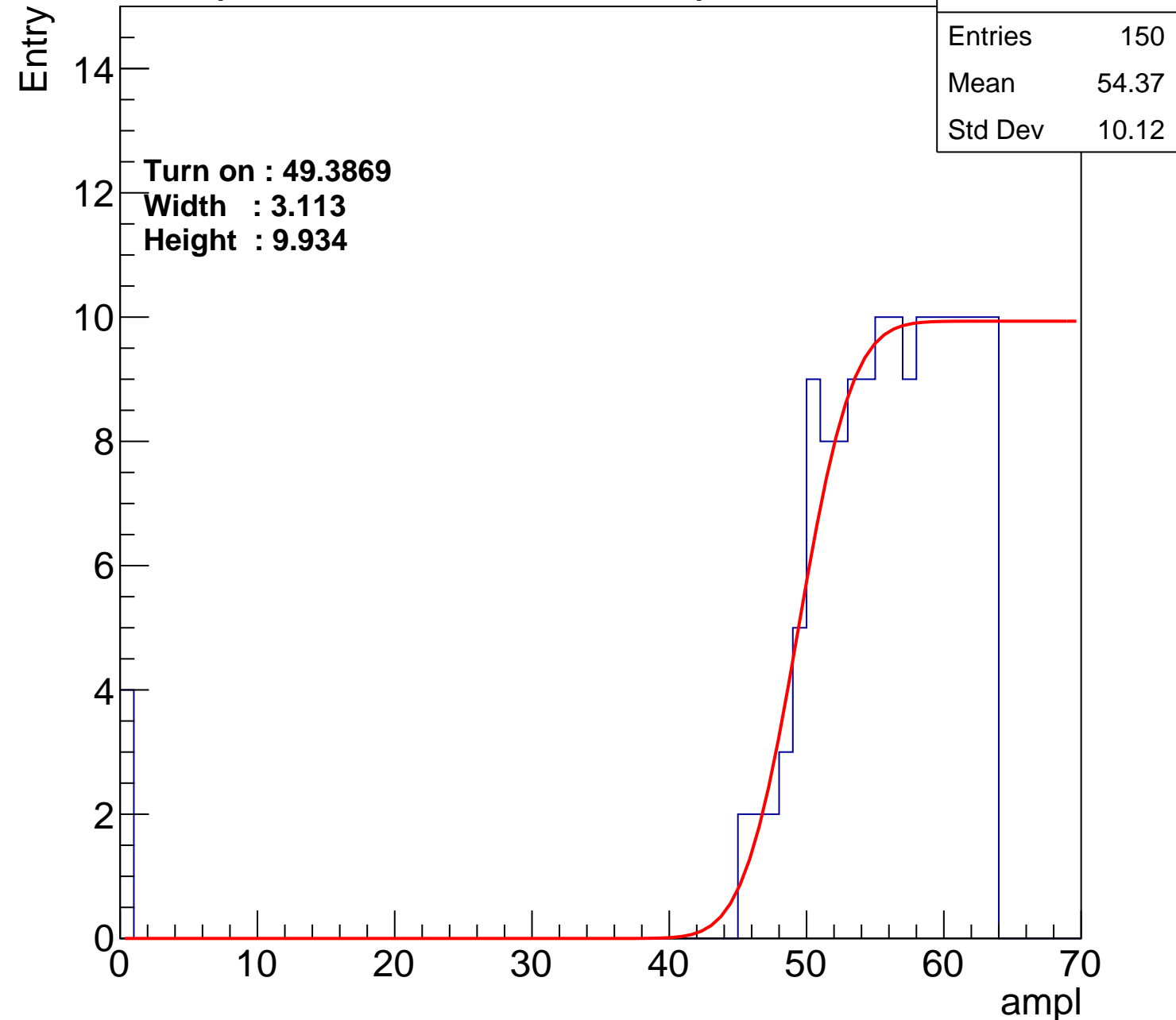
Width : 3.113

Height : 9.934

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U25-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	150
Mean	54.37
Std Dev	10.12

Turn on : 49.3869

Width : 3.113

Height : 9.934

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

