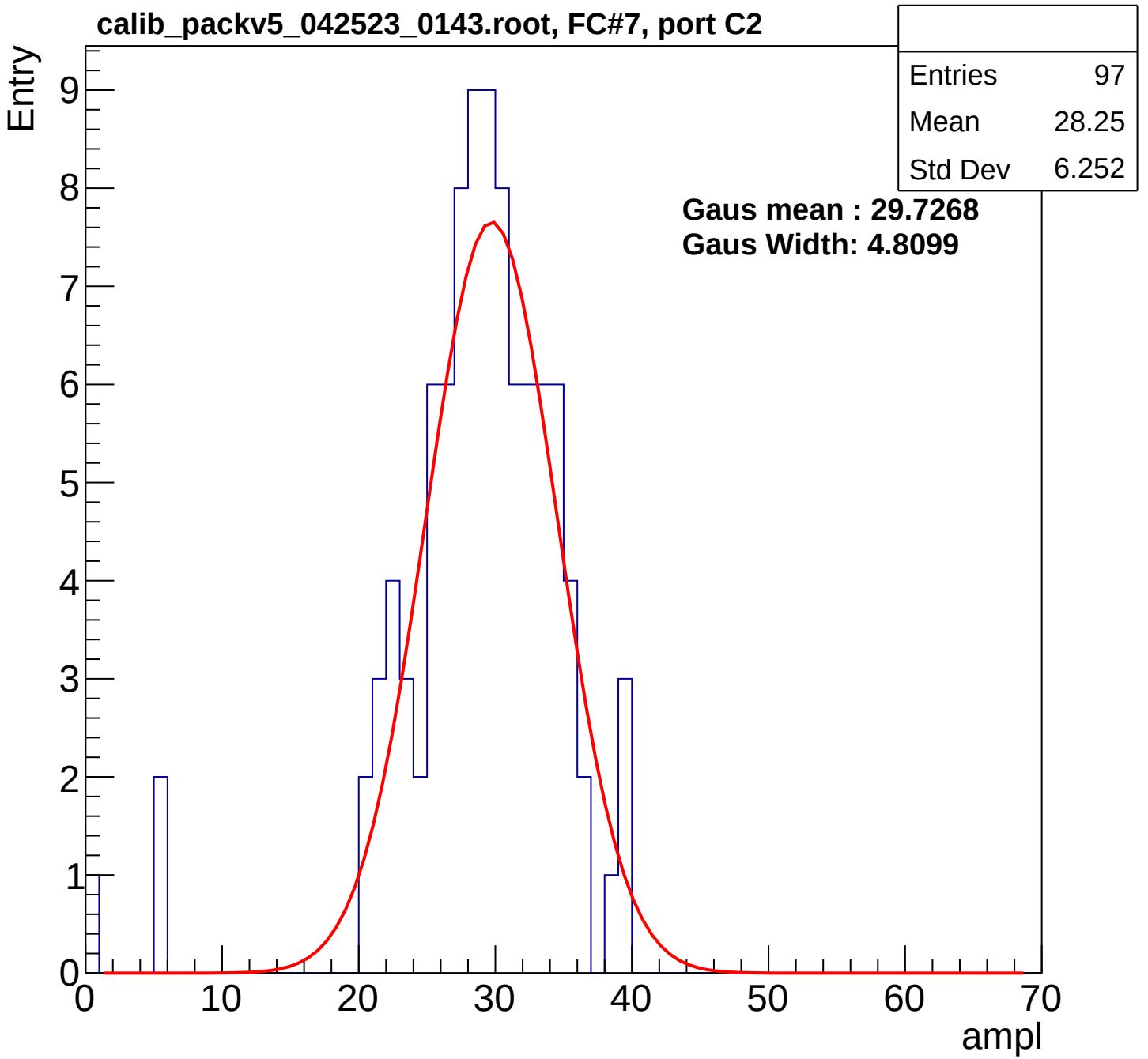
B1L103S, U13-ch0, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 97
Mean 28.25
Std Dev 6.252
Gaus mean : 29.7268
Gaus Width: 4.8099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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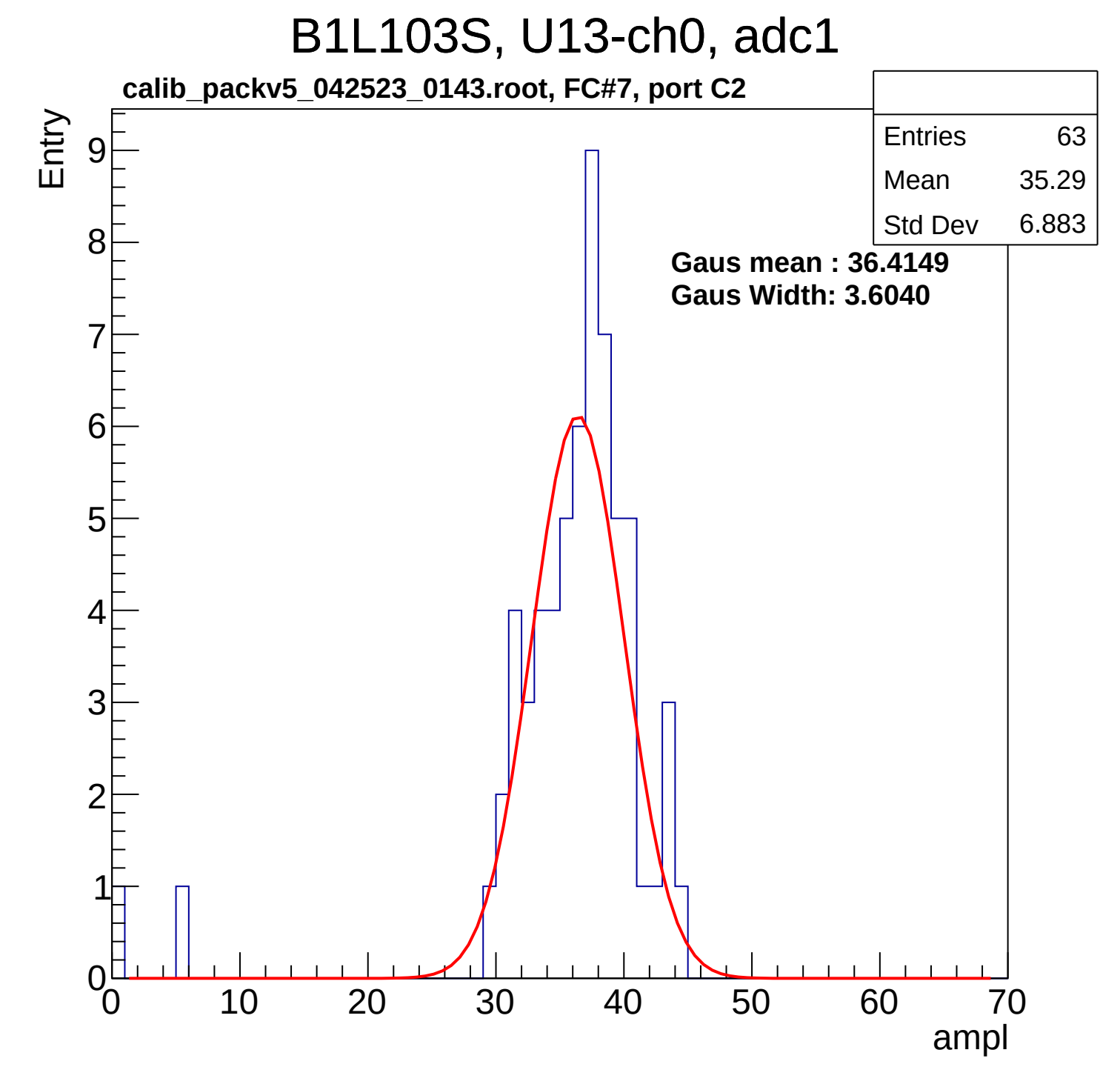
B1L103S, U13-ch0, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.29
Std Dev 6.883
Gaus mean : 36.4149
Gaus Width: 3.6040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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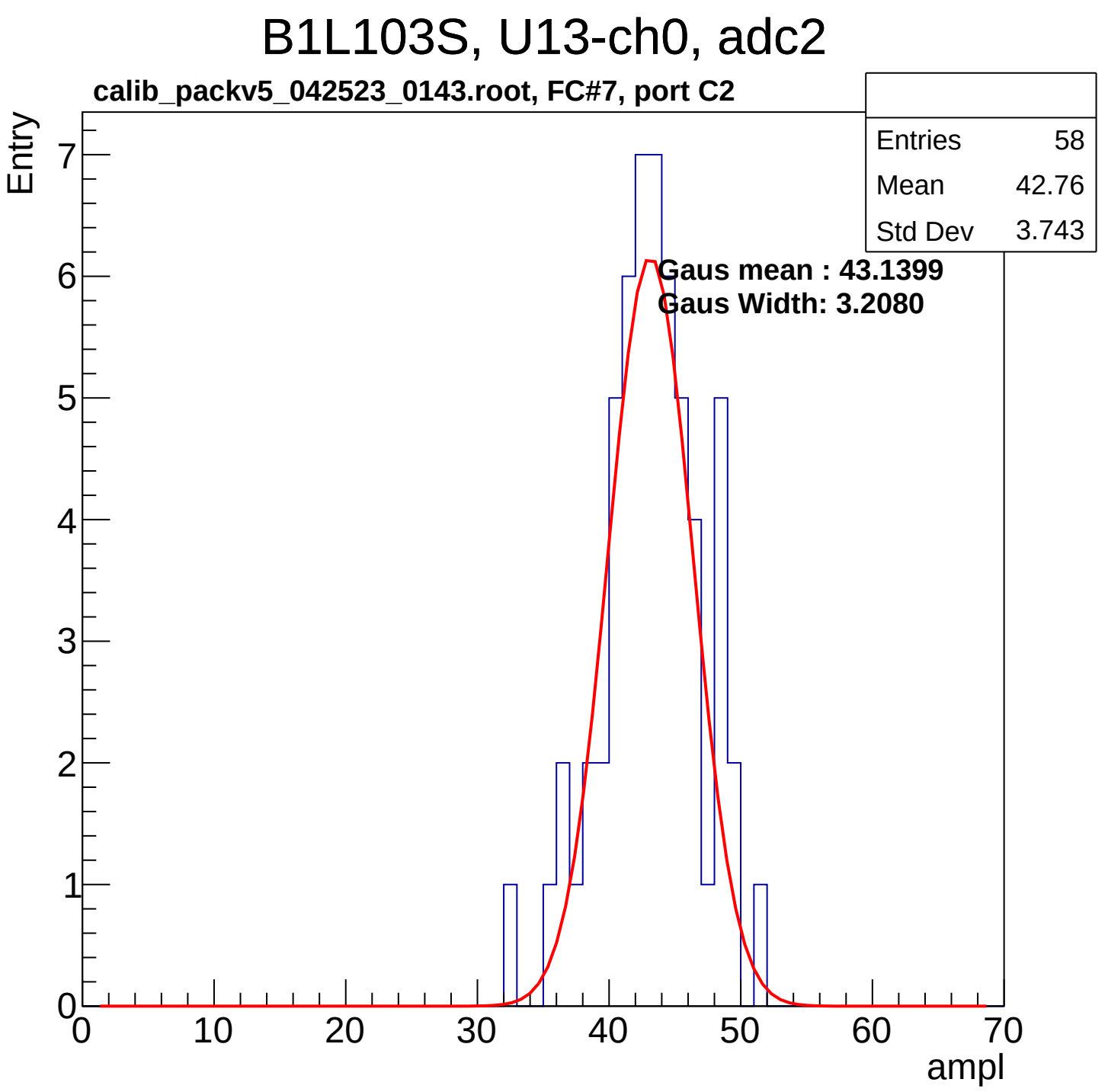
B1L103S, U13-ch0, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 42.76
Std Dev 3.743
Gaus mean : 43.1399
Gaus Width: 3.2080
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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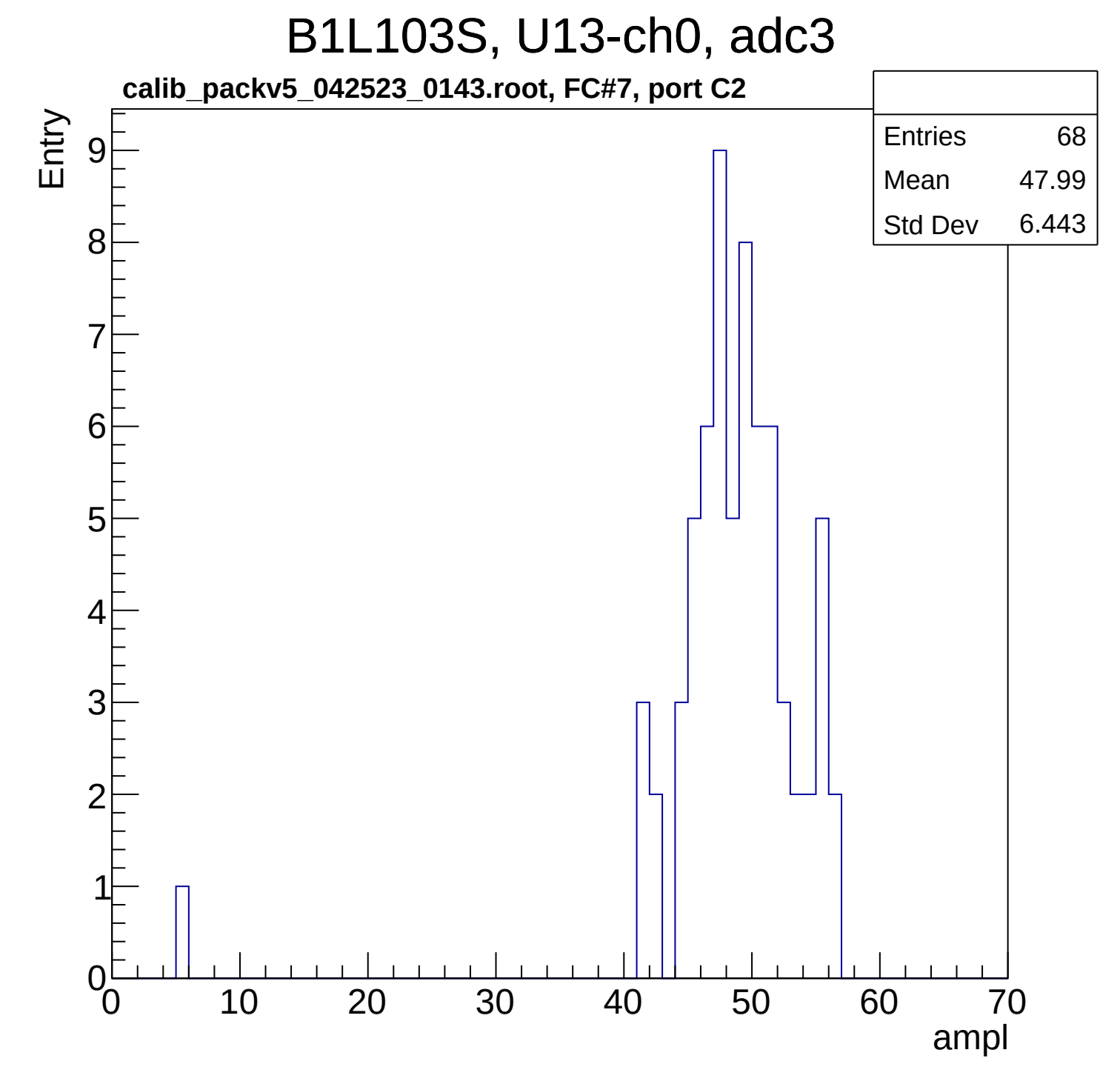
B1L103S, U13-ch0, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 47.99
Std Dev 6.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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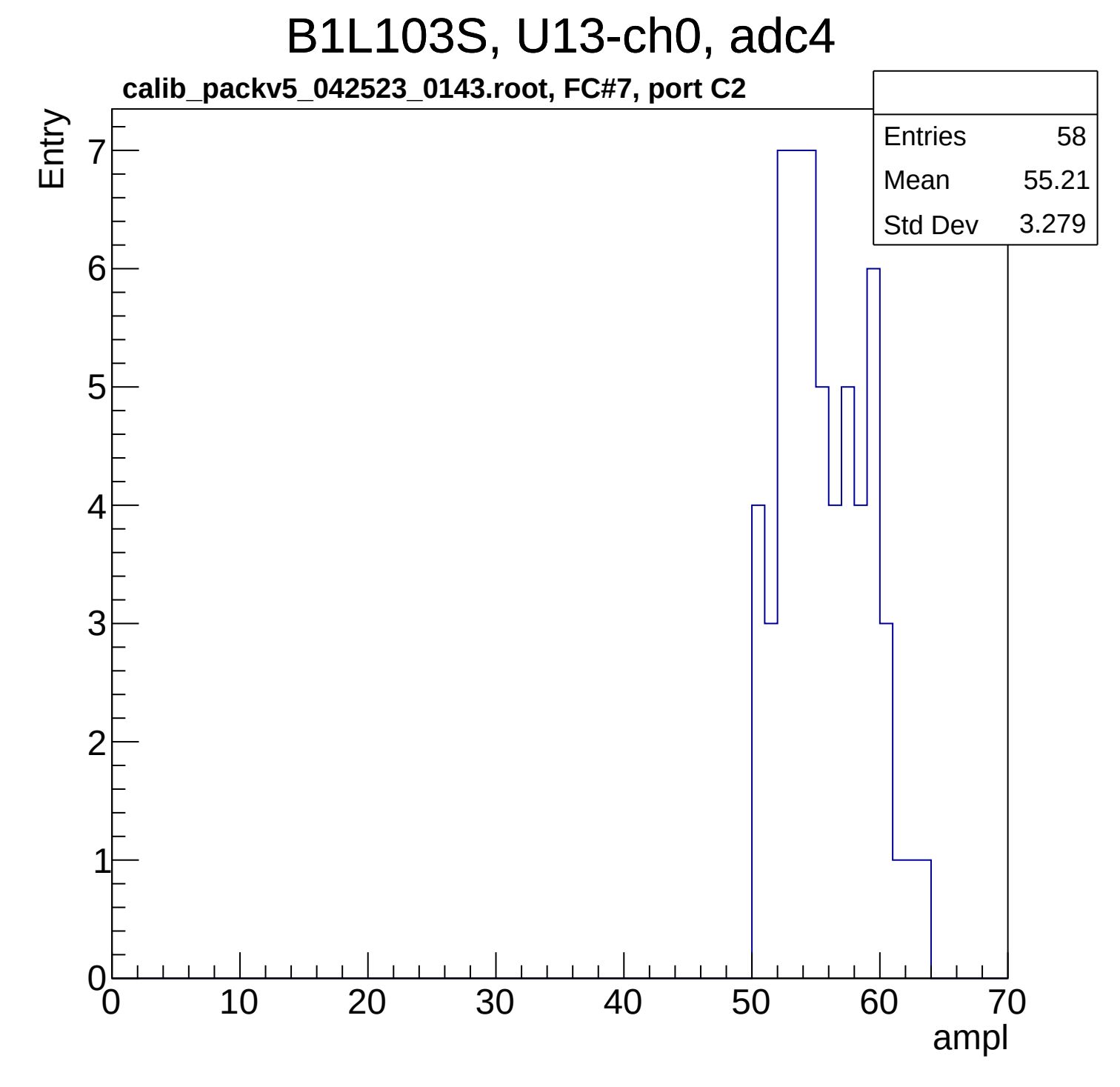
B1L103S, U13-ch0, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 55.21
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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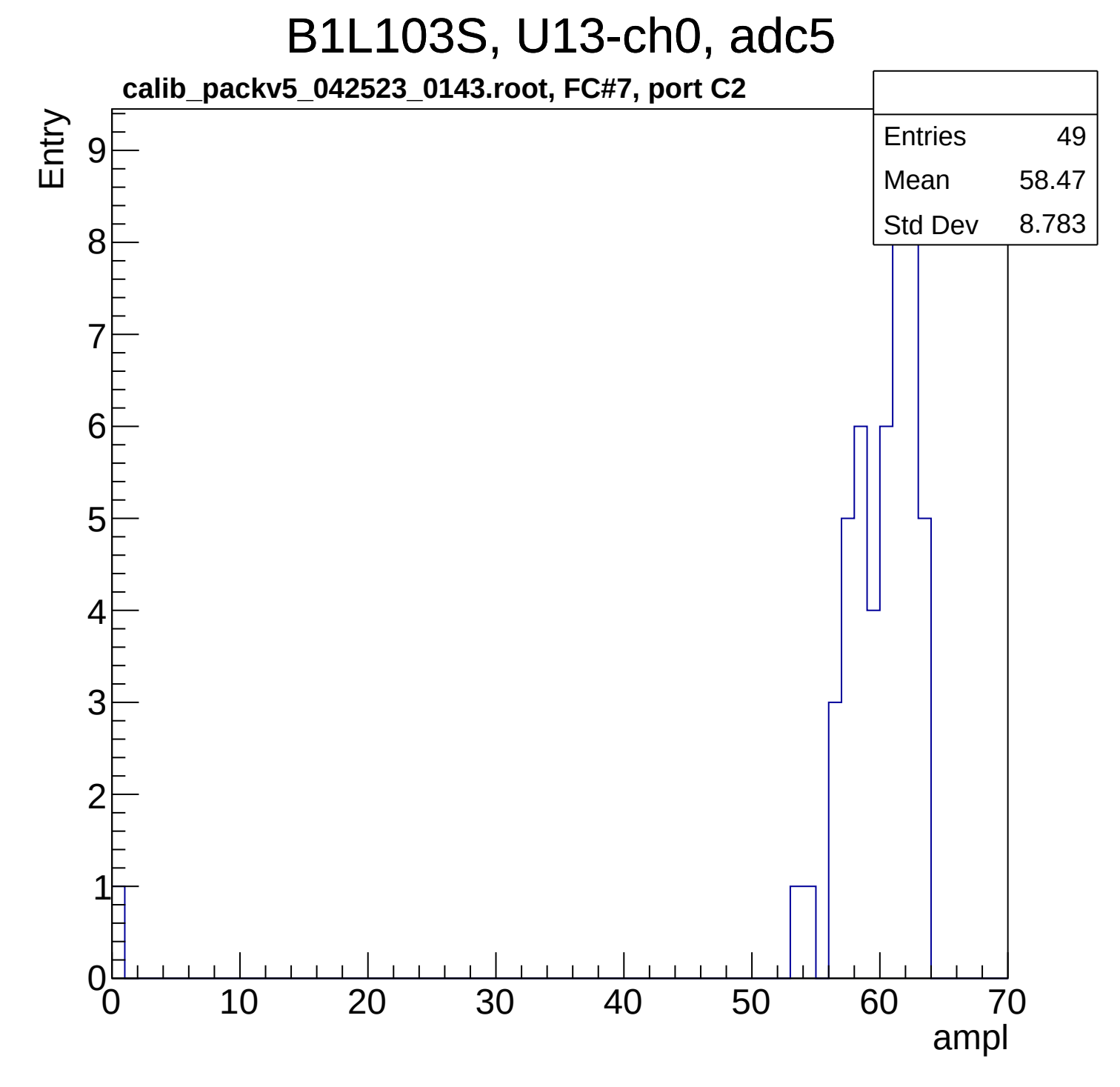
B1L103S, U13-ch0, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.47
Std Dev 8.783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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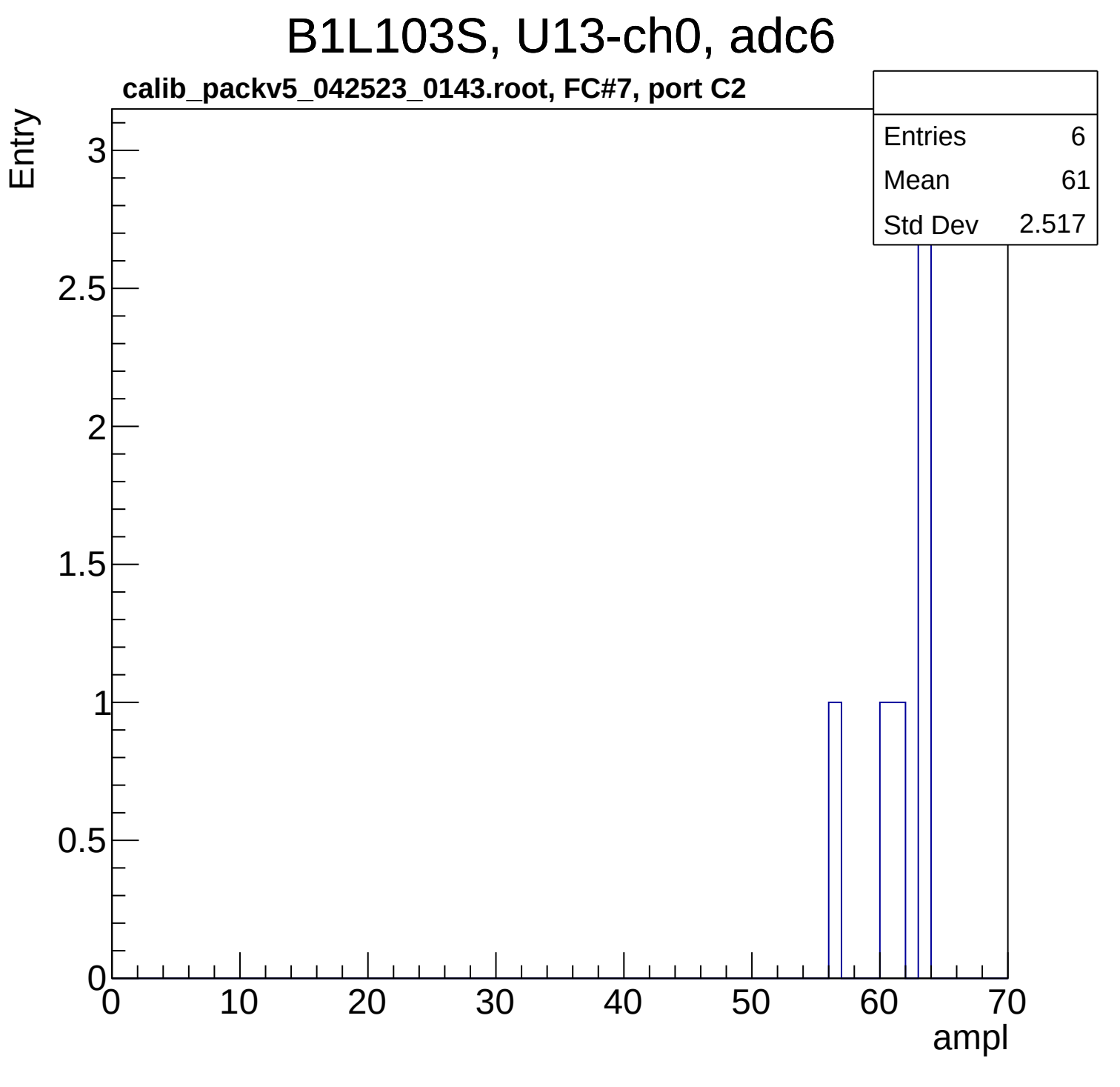
B1L103S, U13-ch0, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61
Std Dev 2.517
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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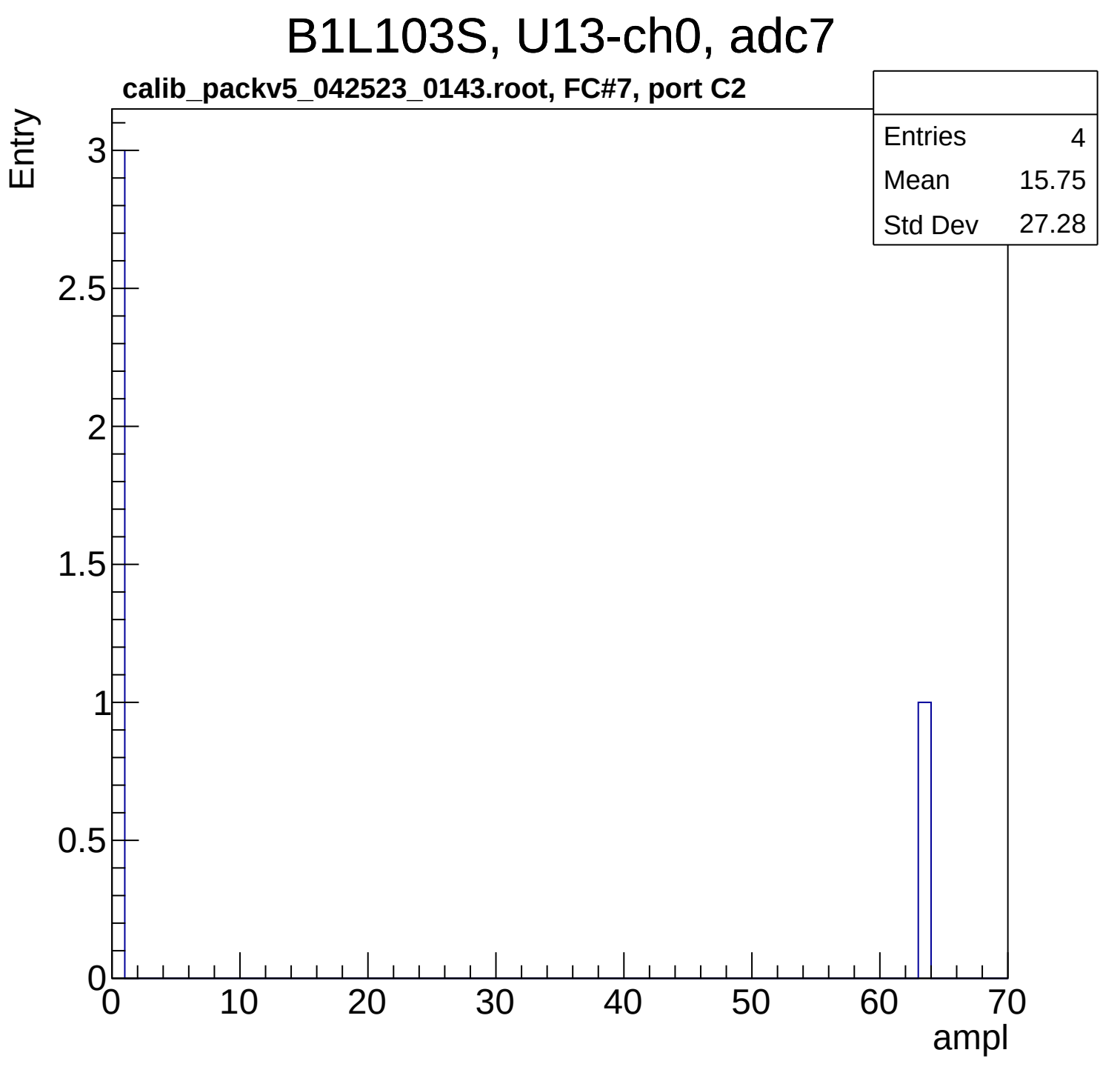
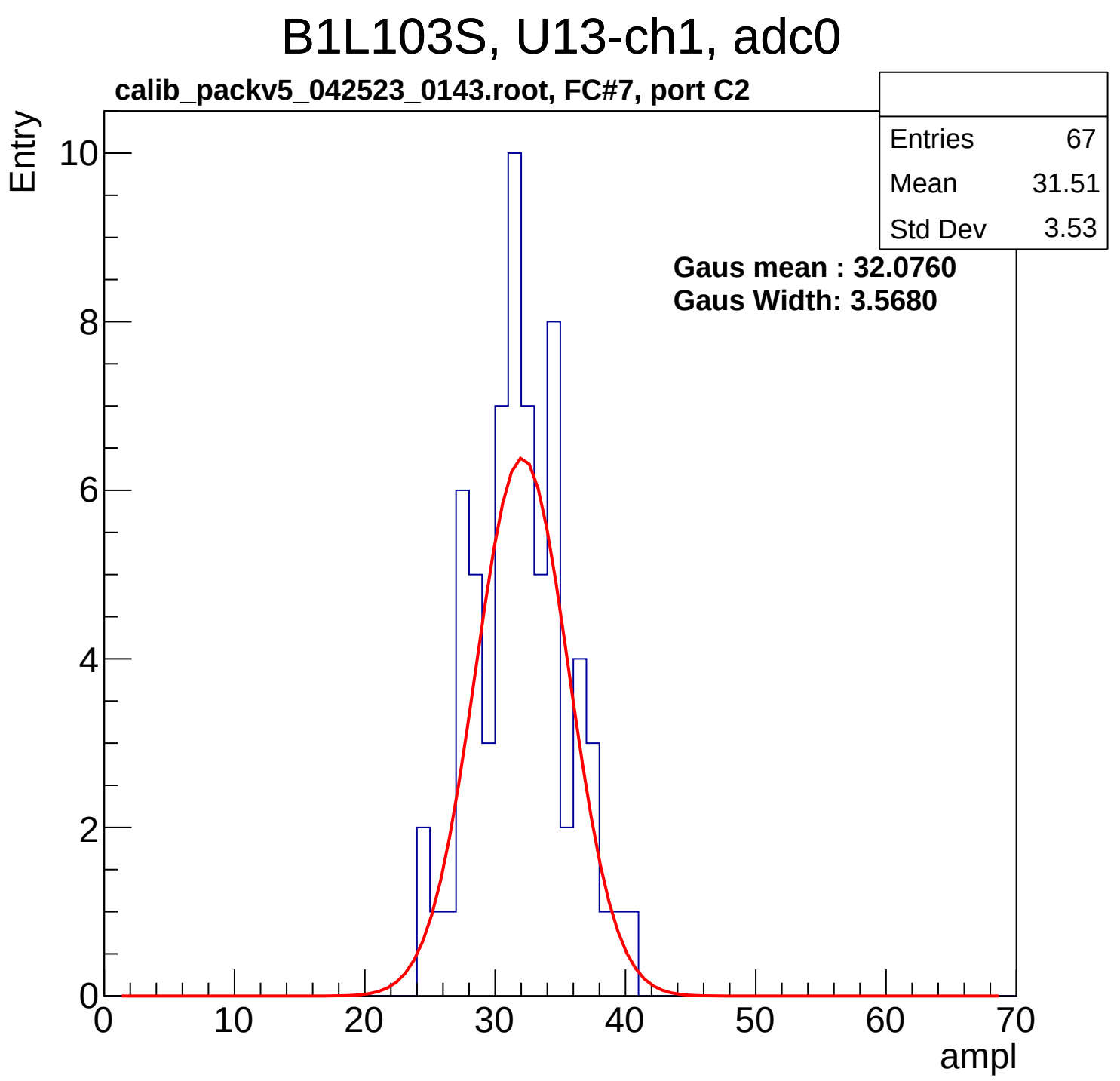
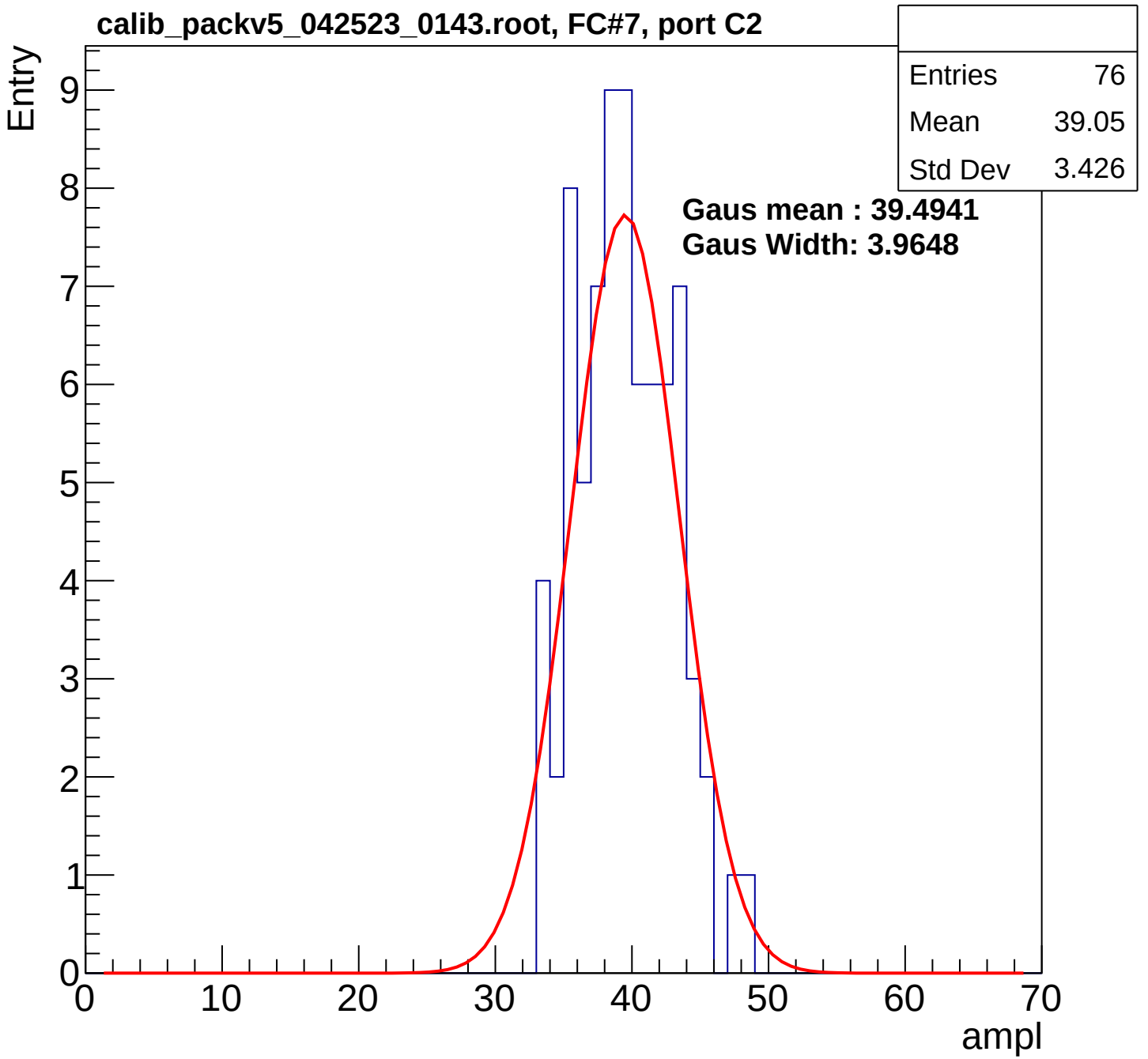
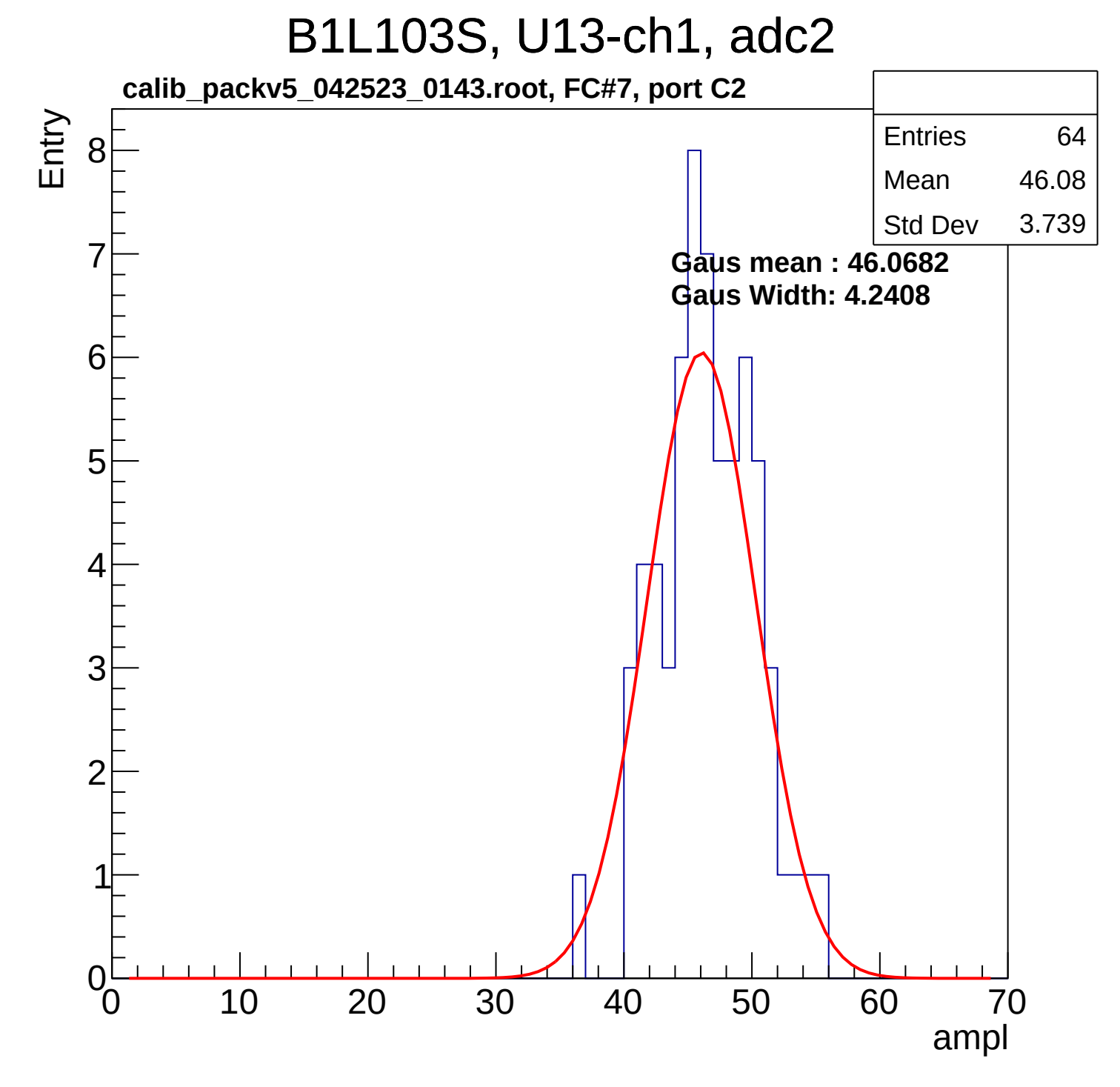

B1L103S, U13-ch0, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch1, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 31.51
Std Dev 3.53
Gaus mean : 32.0760
Gaus Width: 3.5680
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch1, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 39.05
Std Dev 3.426
Gaus mean : 39.4941
Gaus Width: 3.9648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch1, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 46.08
Std Dev 3.739
Gaus mean : 46.0682
Gaus Width: 4.2408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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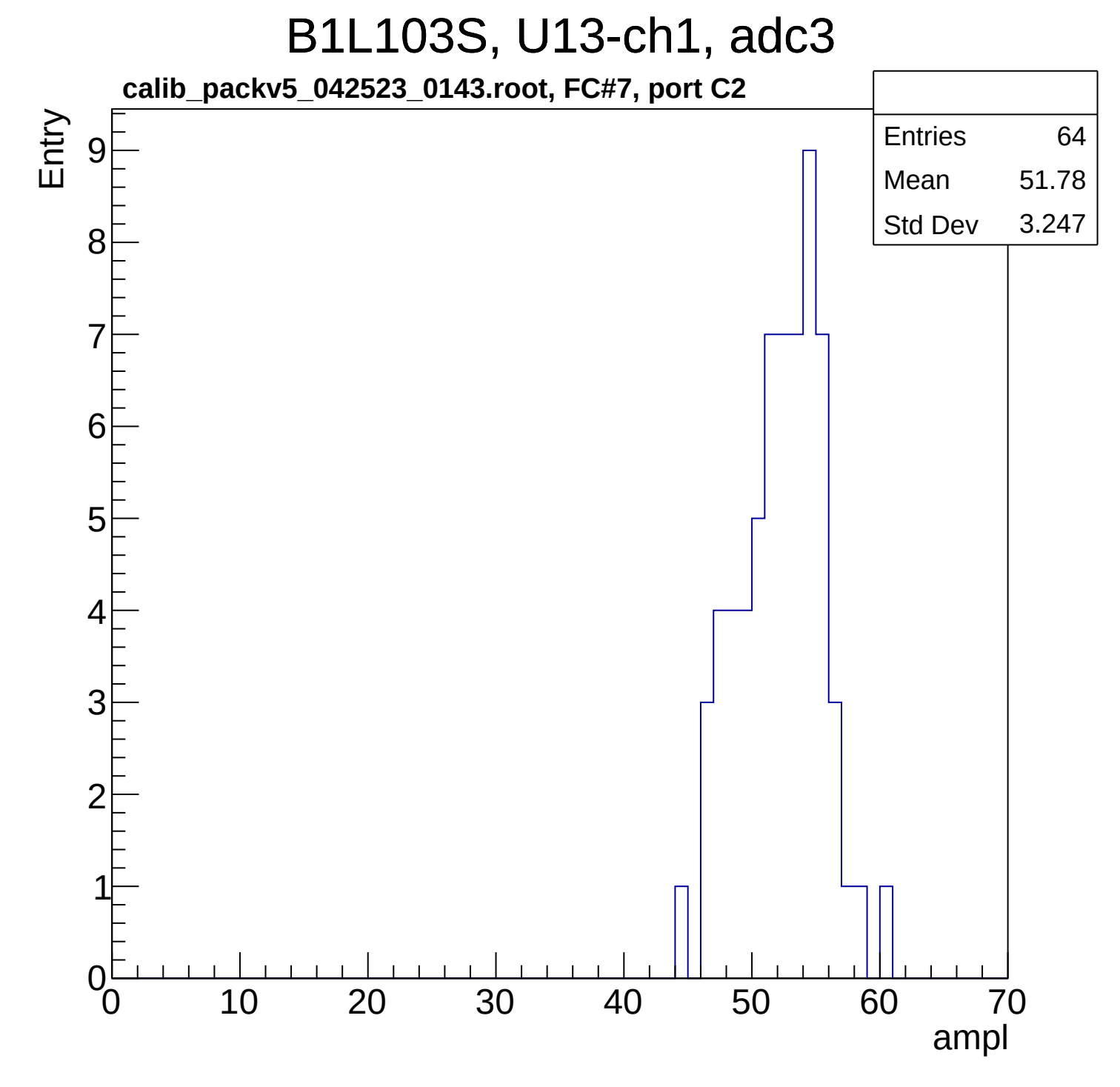
B1L103S, U13-ch1, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 51.78
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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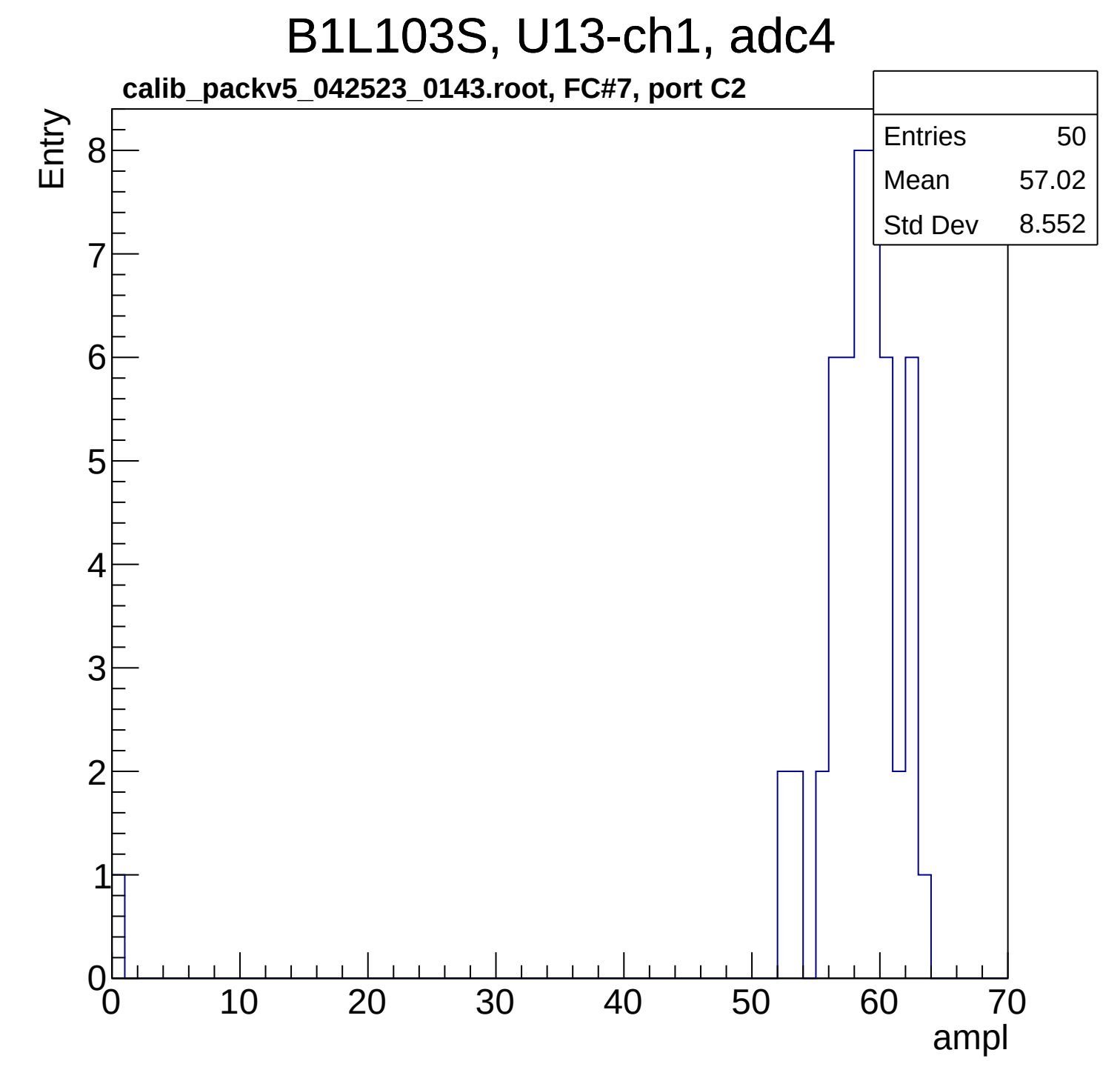
B1L103S, U13-ch1, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 57.02
Std Dev 8.552
Entry
ampl
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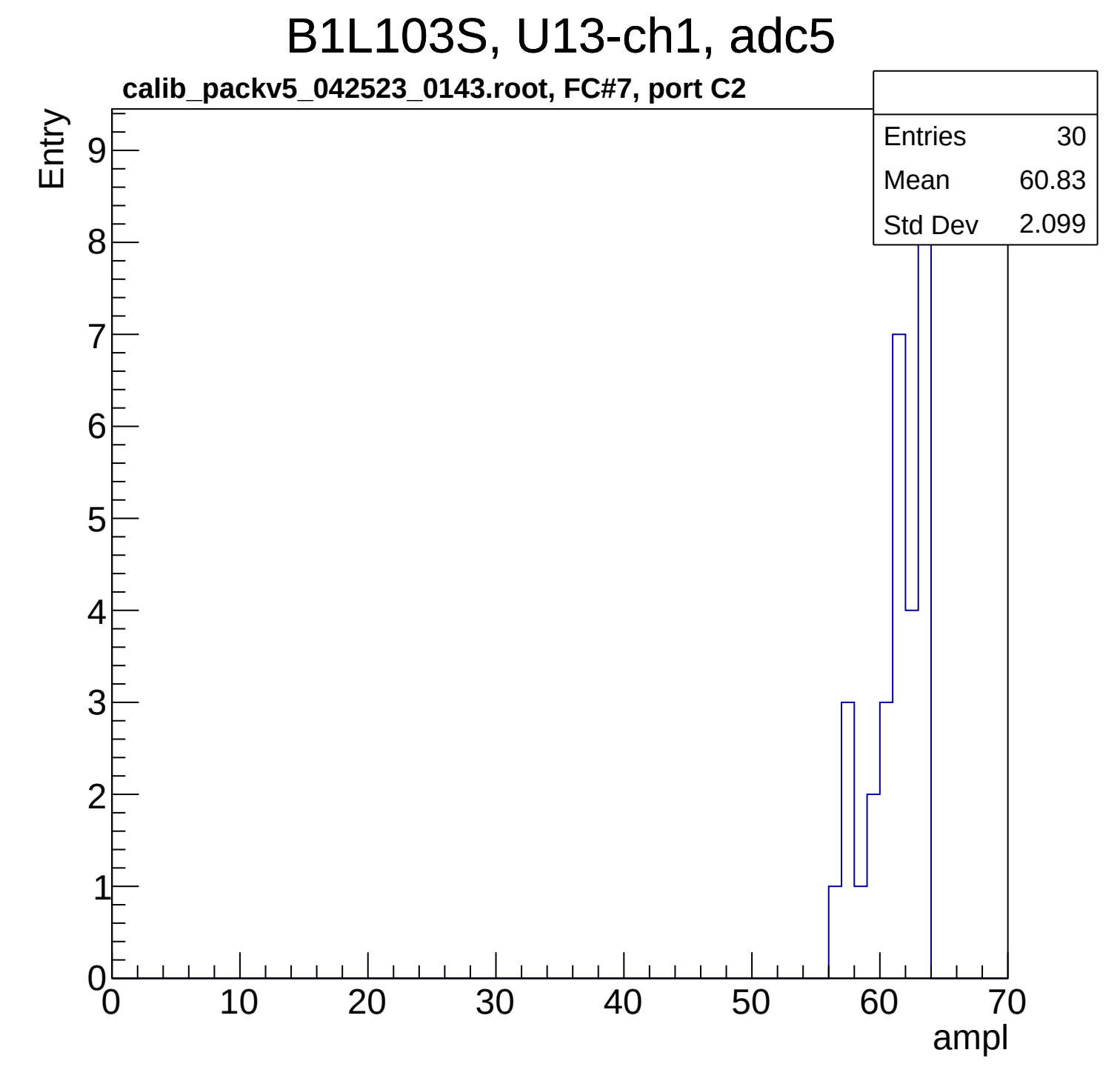
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch1, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 60.83
Std Dev 2.099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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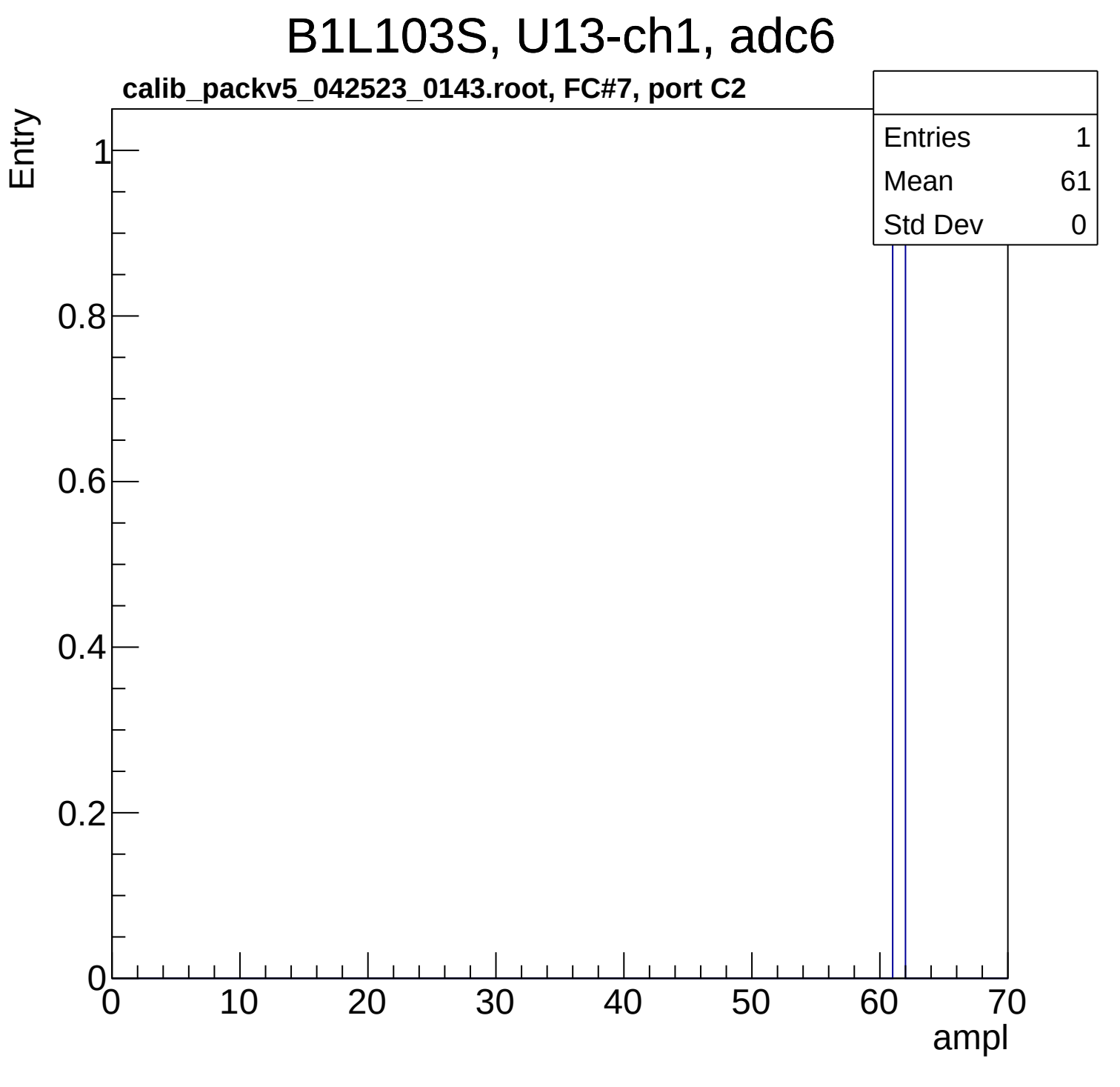
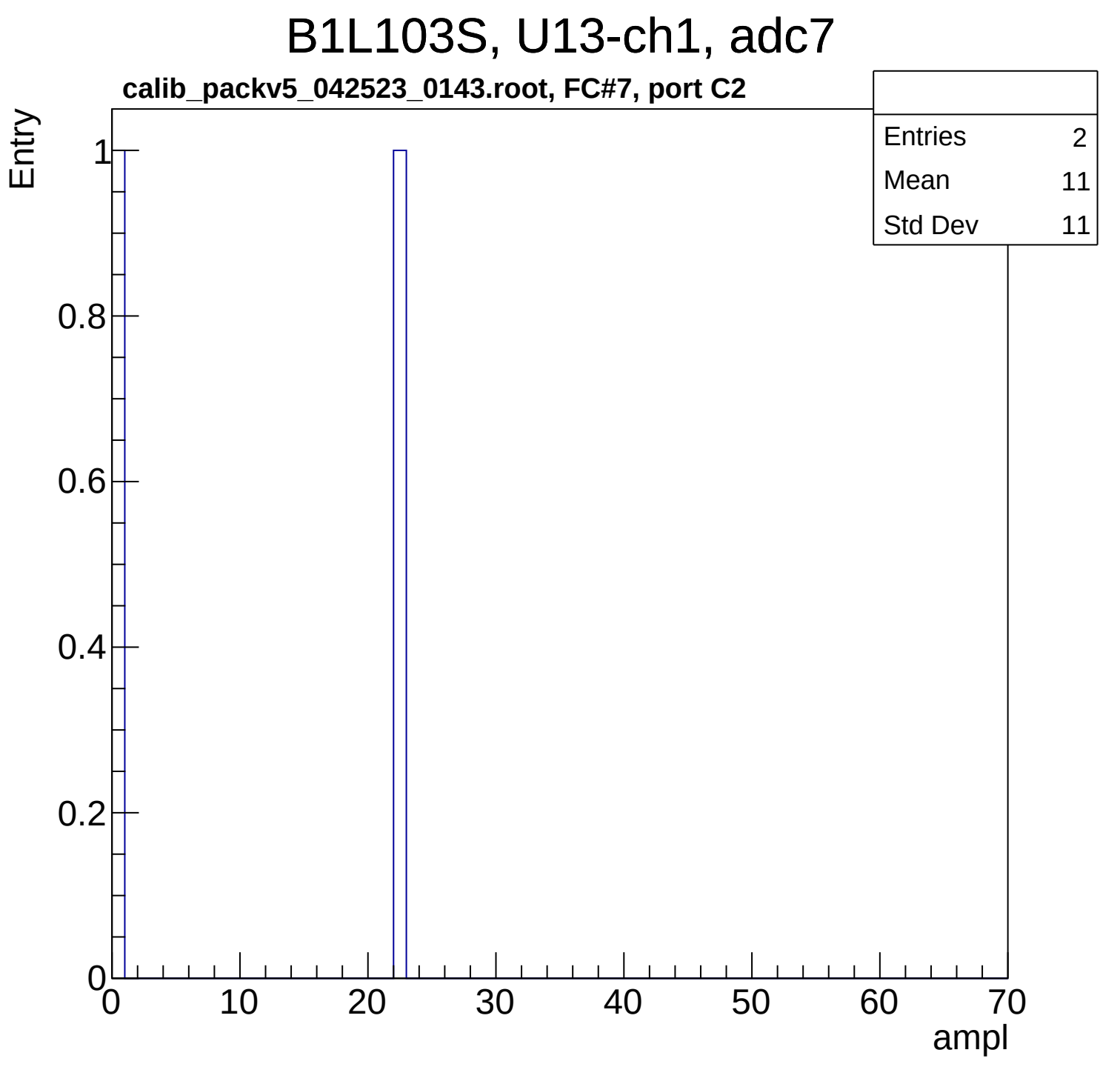
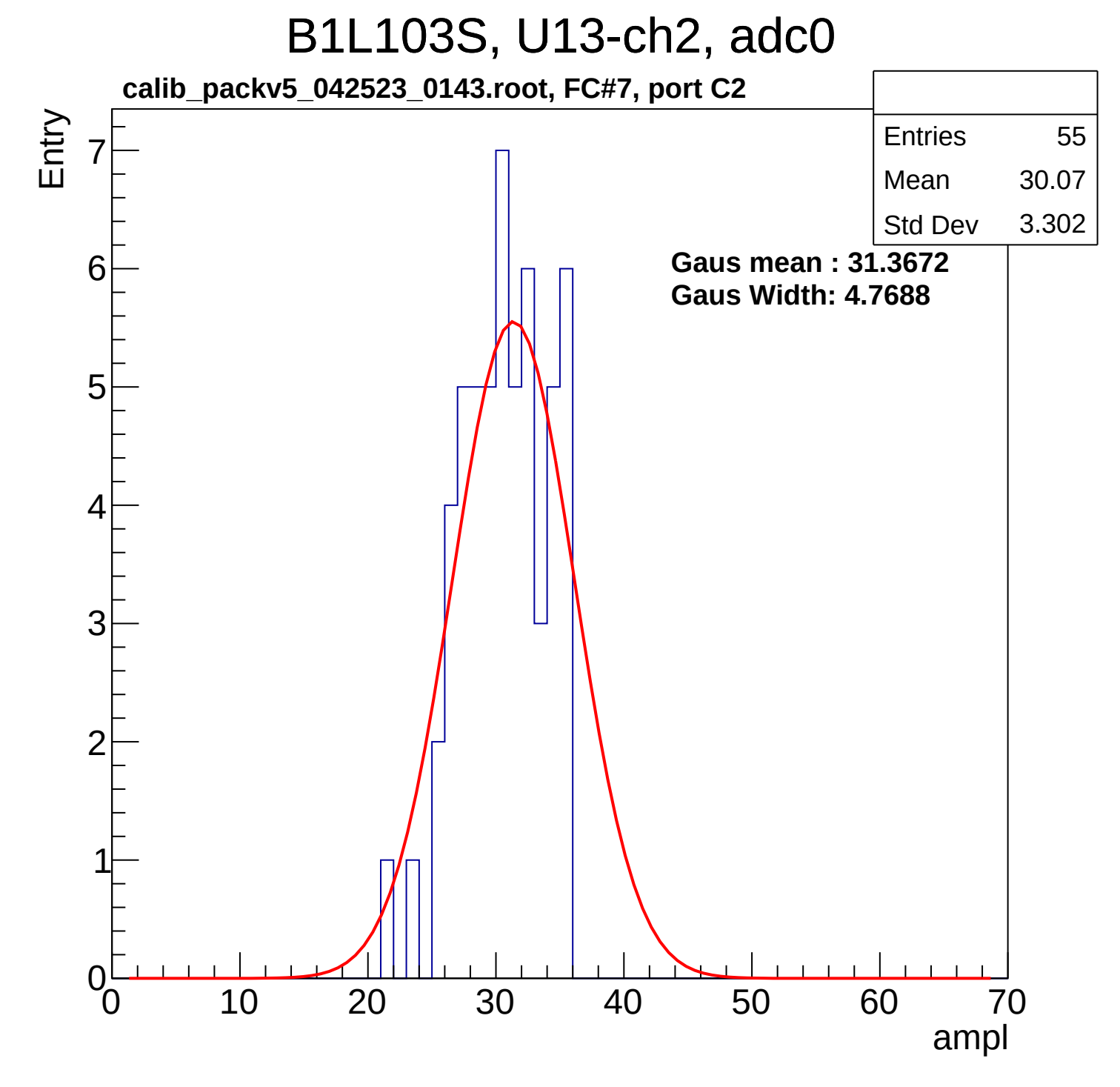
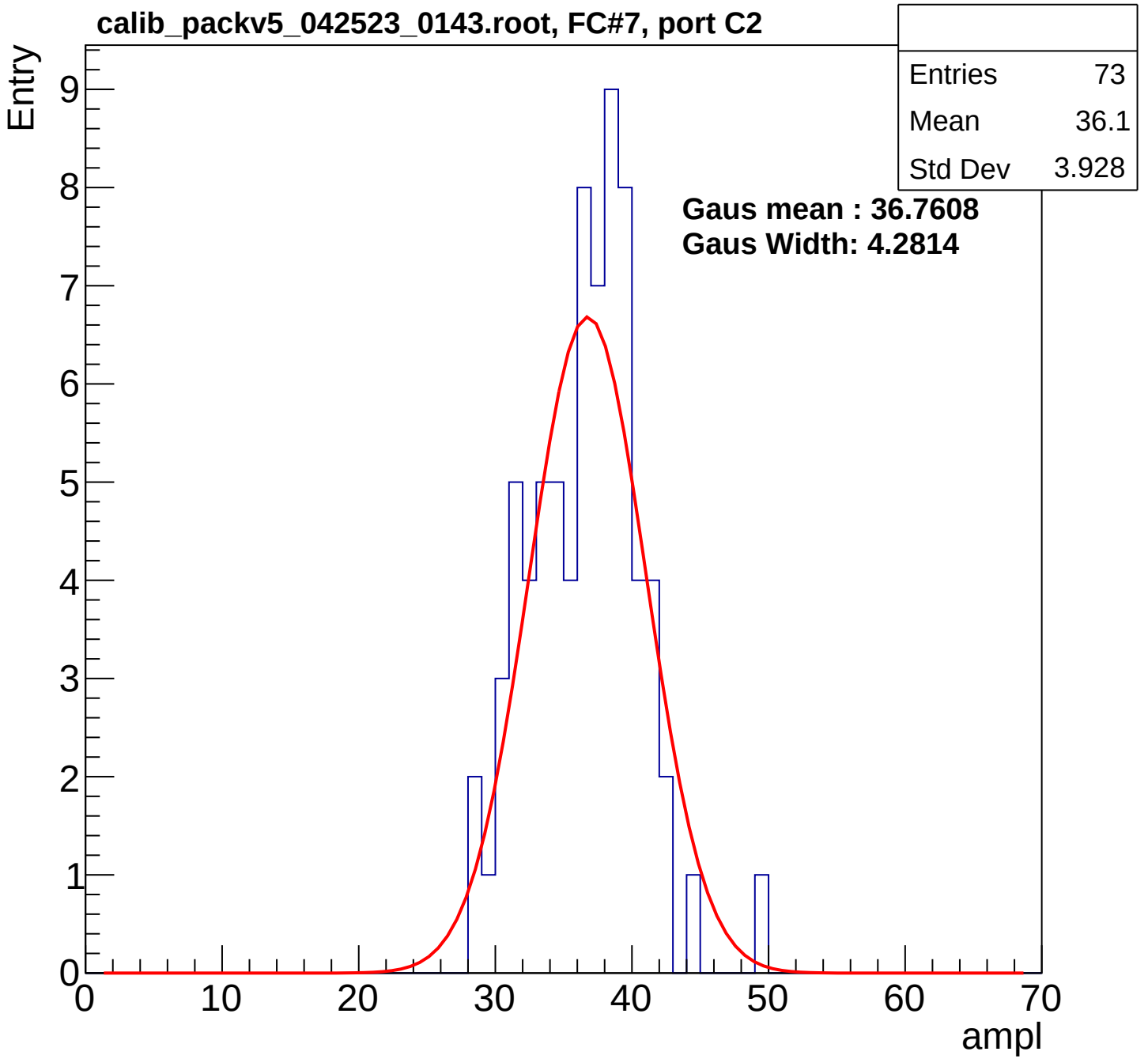

B1L103S, U13-ch1, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch1, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11
Std Dev 11
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch2, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 30.07
Std Dev 3.302
Gaus mean : 31.3672
Gaus Width: 4.7688
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch2, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.1
Std Dev 3.928
Gaus mean : 36.7608
Gaus Width: 4.2814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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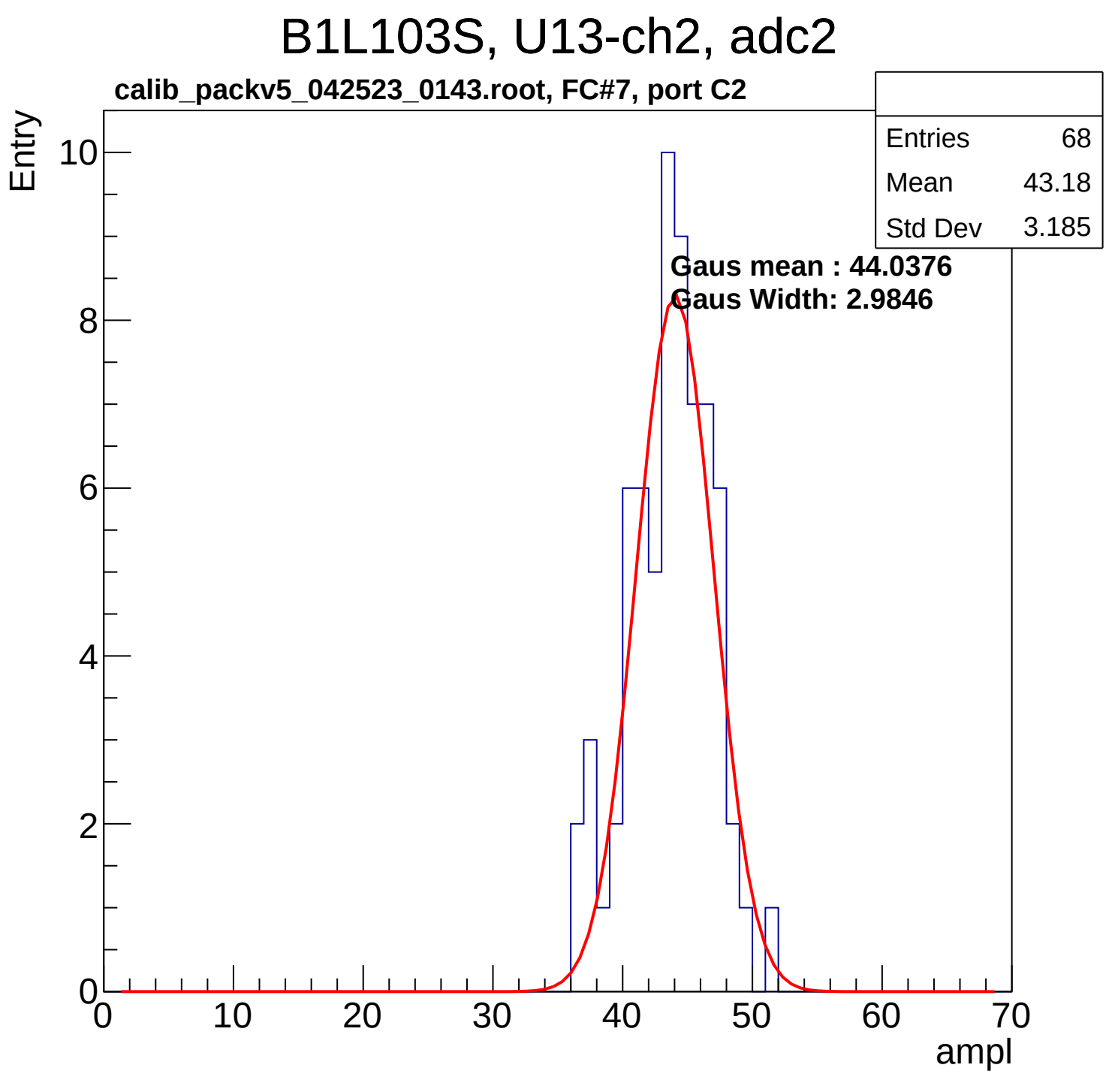
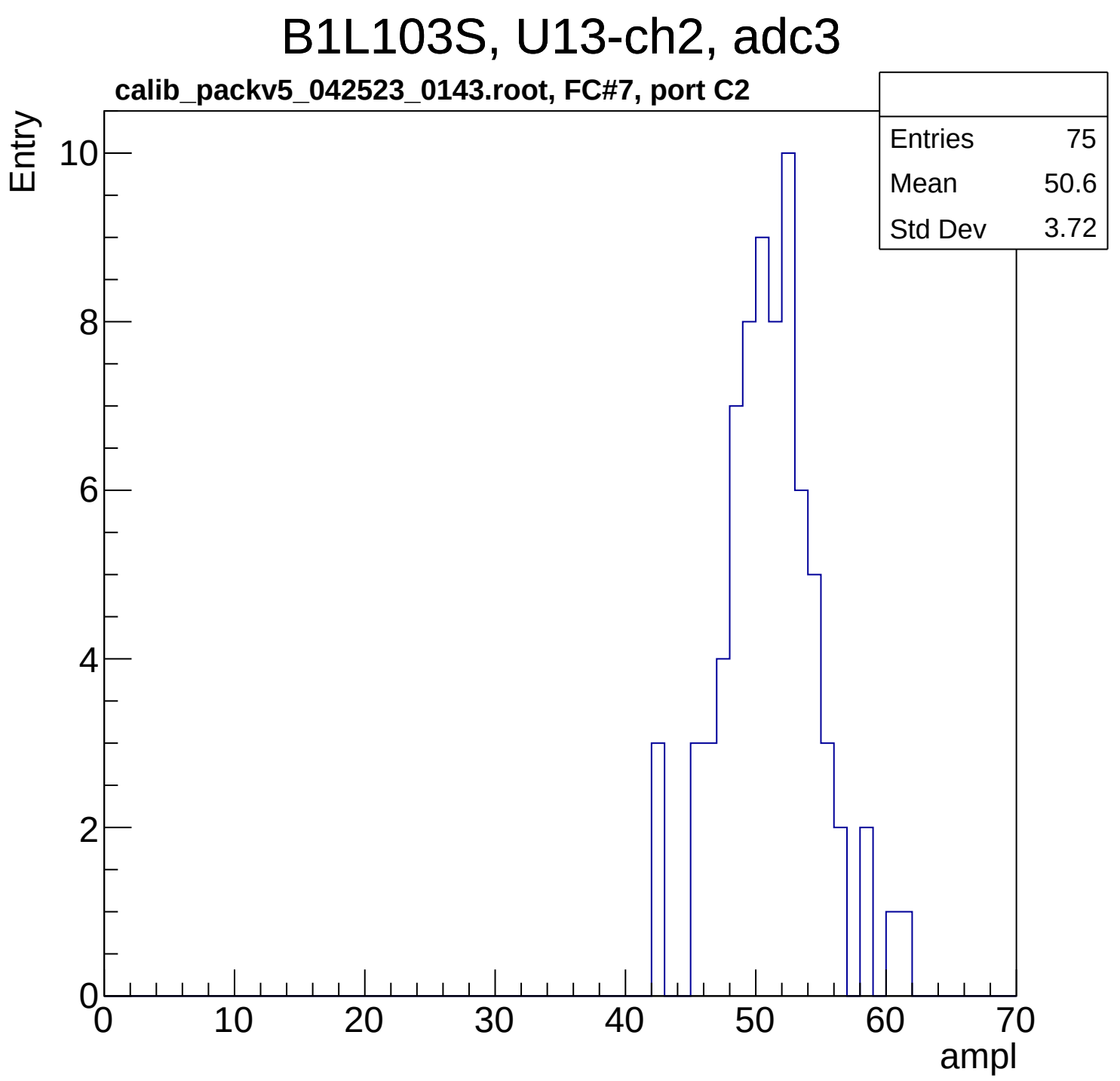
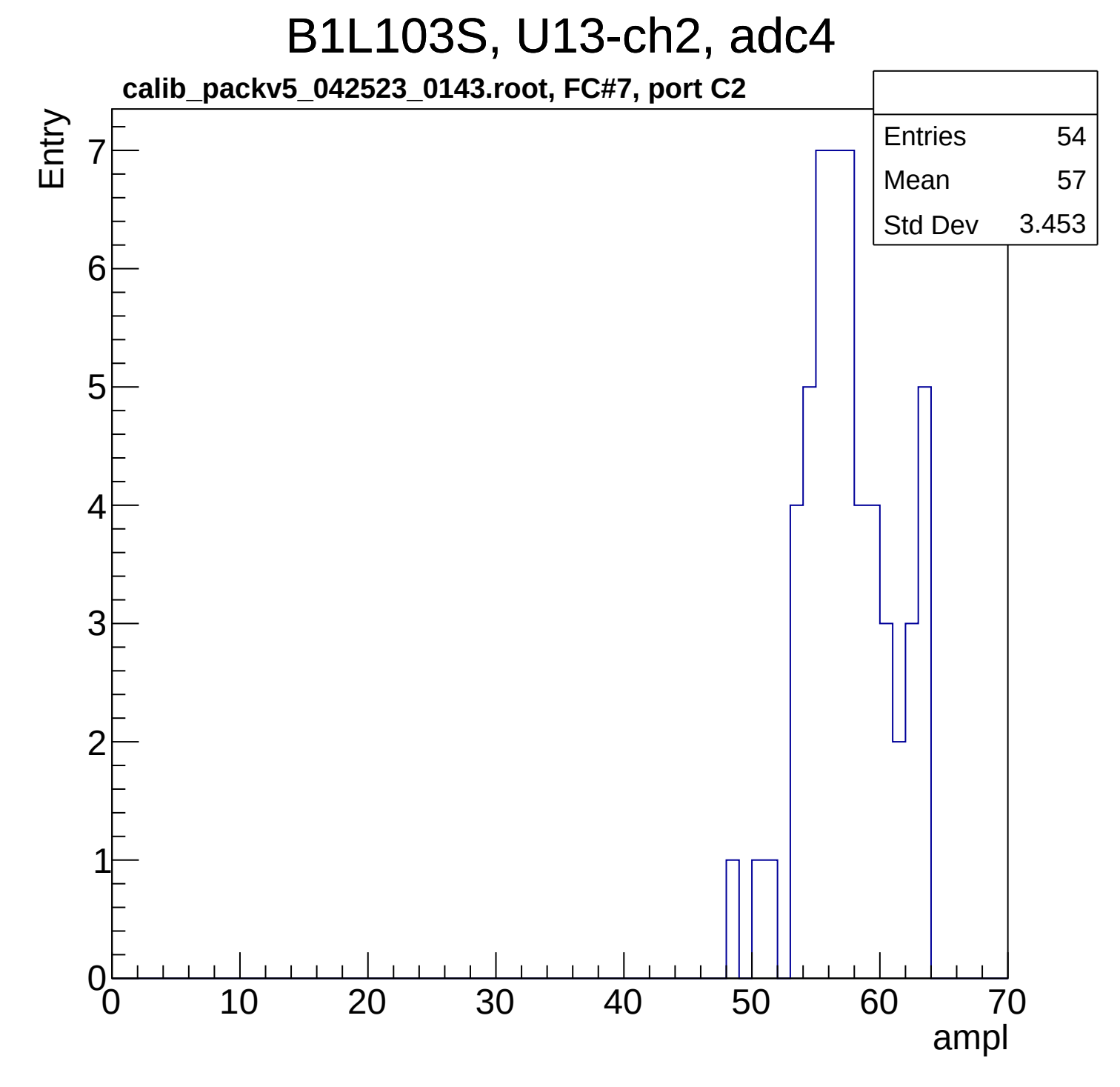

B1L103S, U13-ch2, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 43.18
Std Dev 3.185
Gaus mean : 44.0376
Gaus Width: 2.9846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch2, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 50.6
Std Dev 3.72
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch2, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57
Std Dev 3.453
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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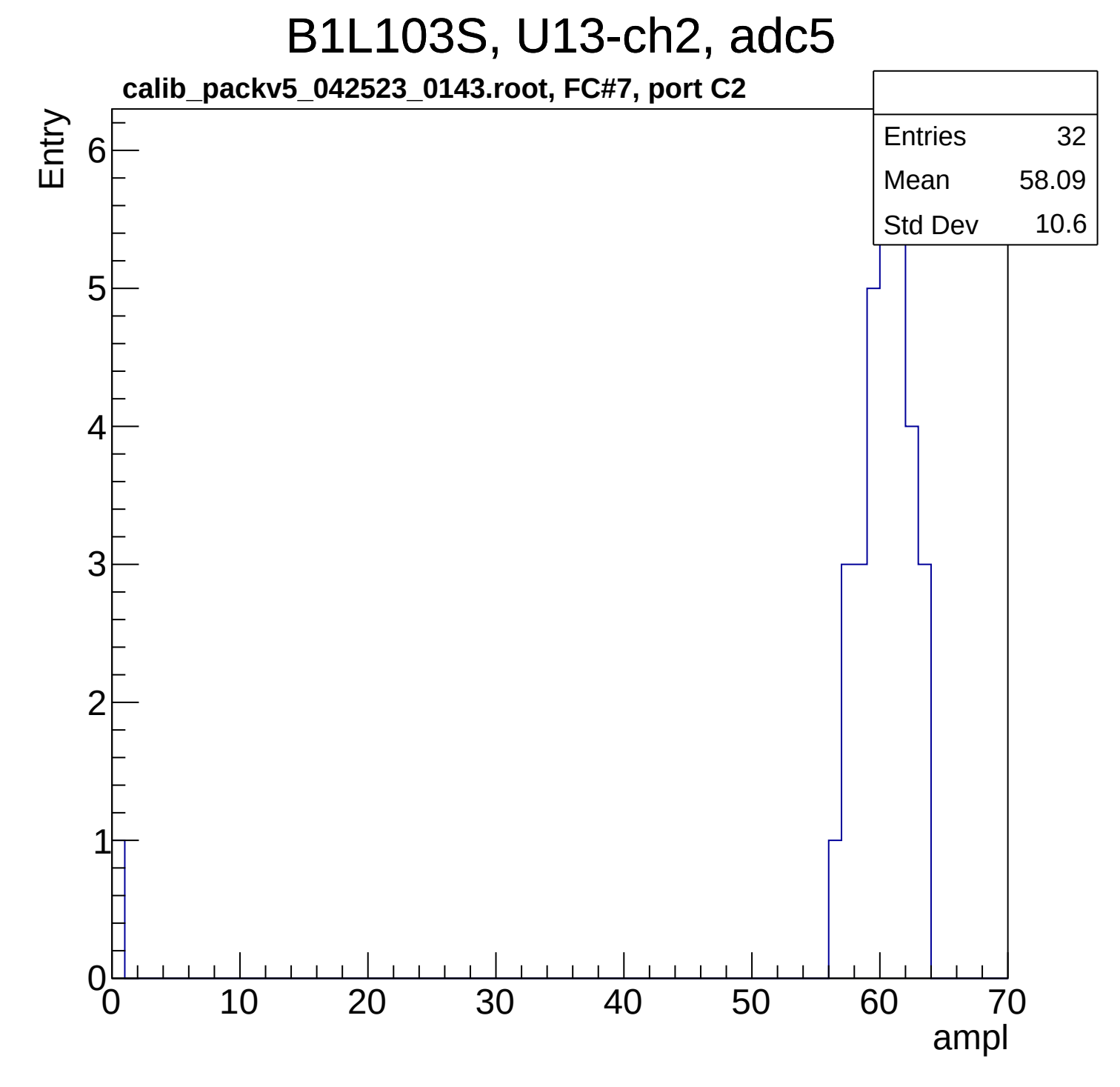
B1L103S, U13-ch2, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.09
Std Dev 10.6
Entry
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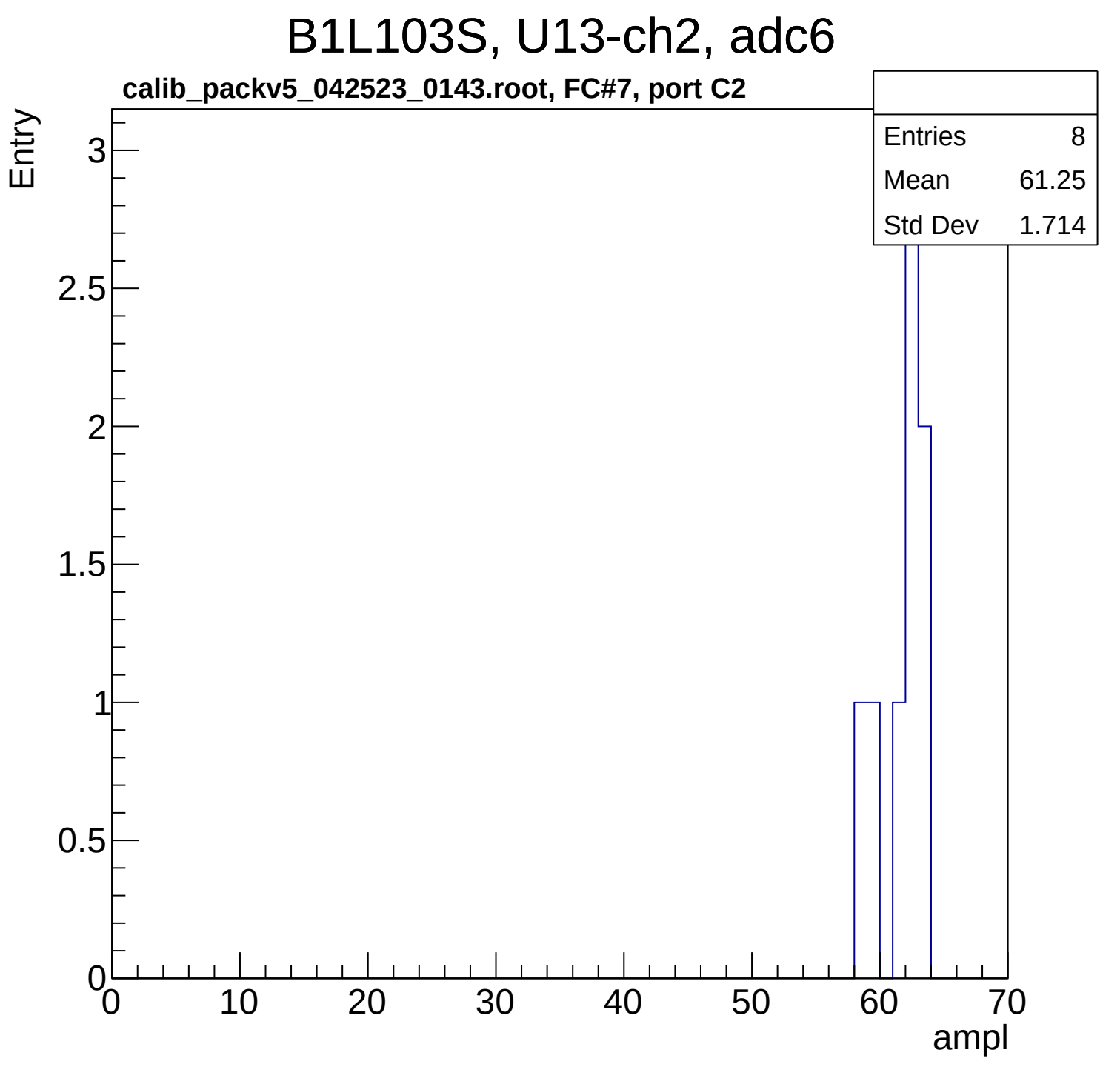
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch2, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.25
Std Dev 1.714
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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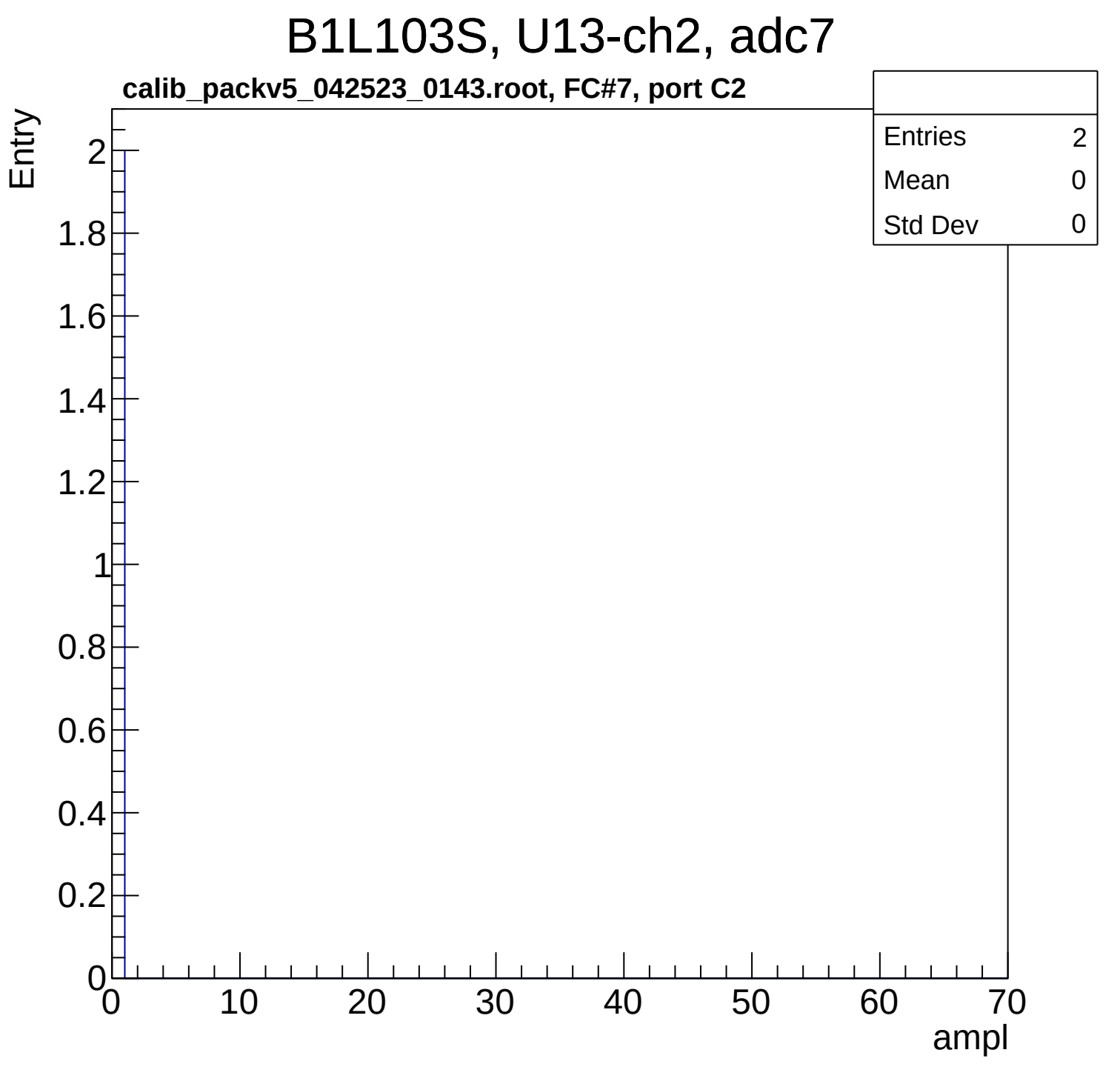
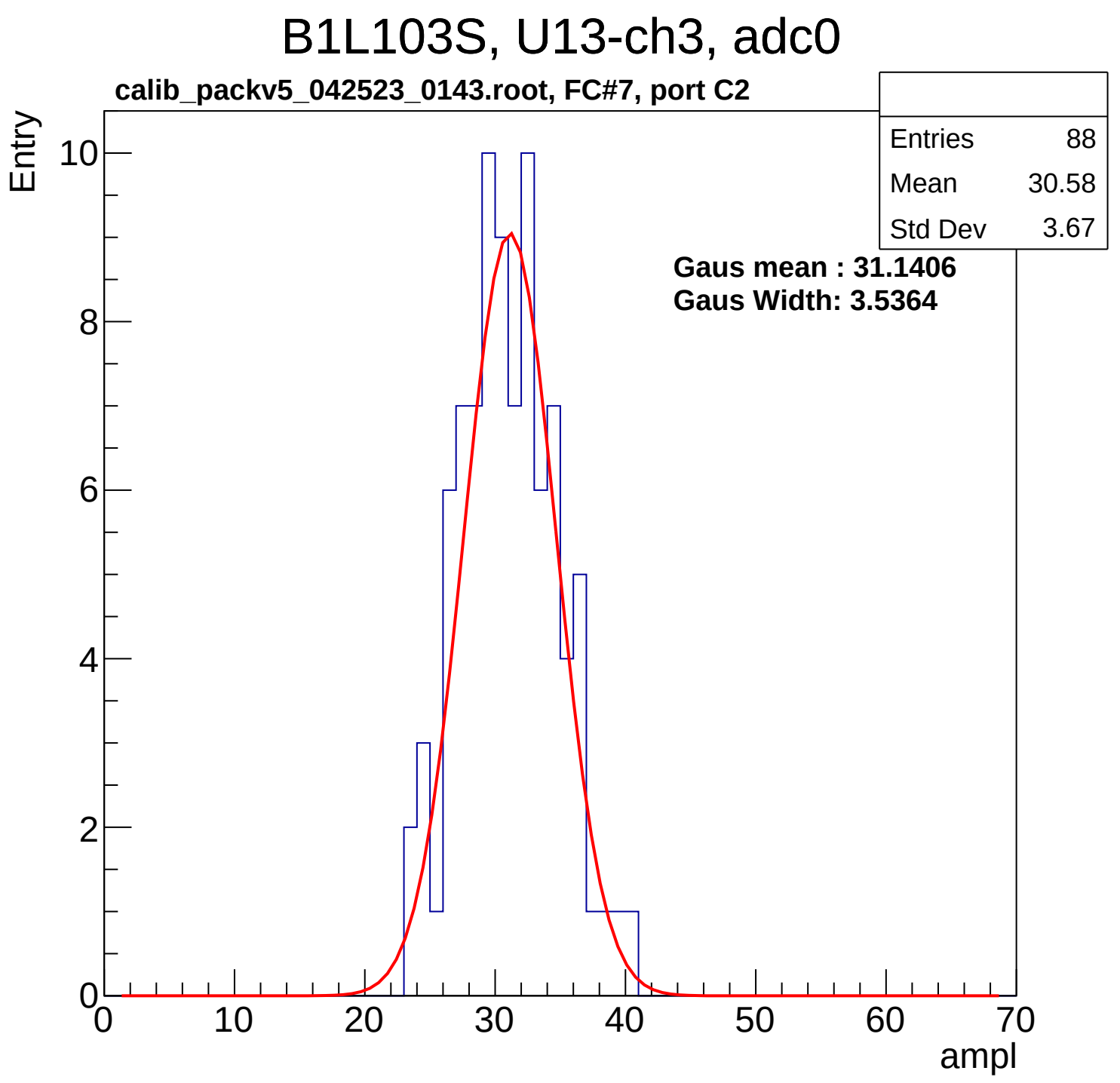
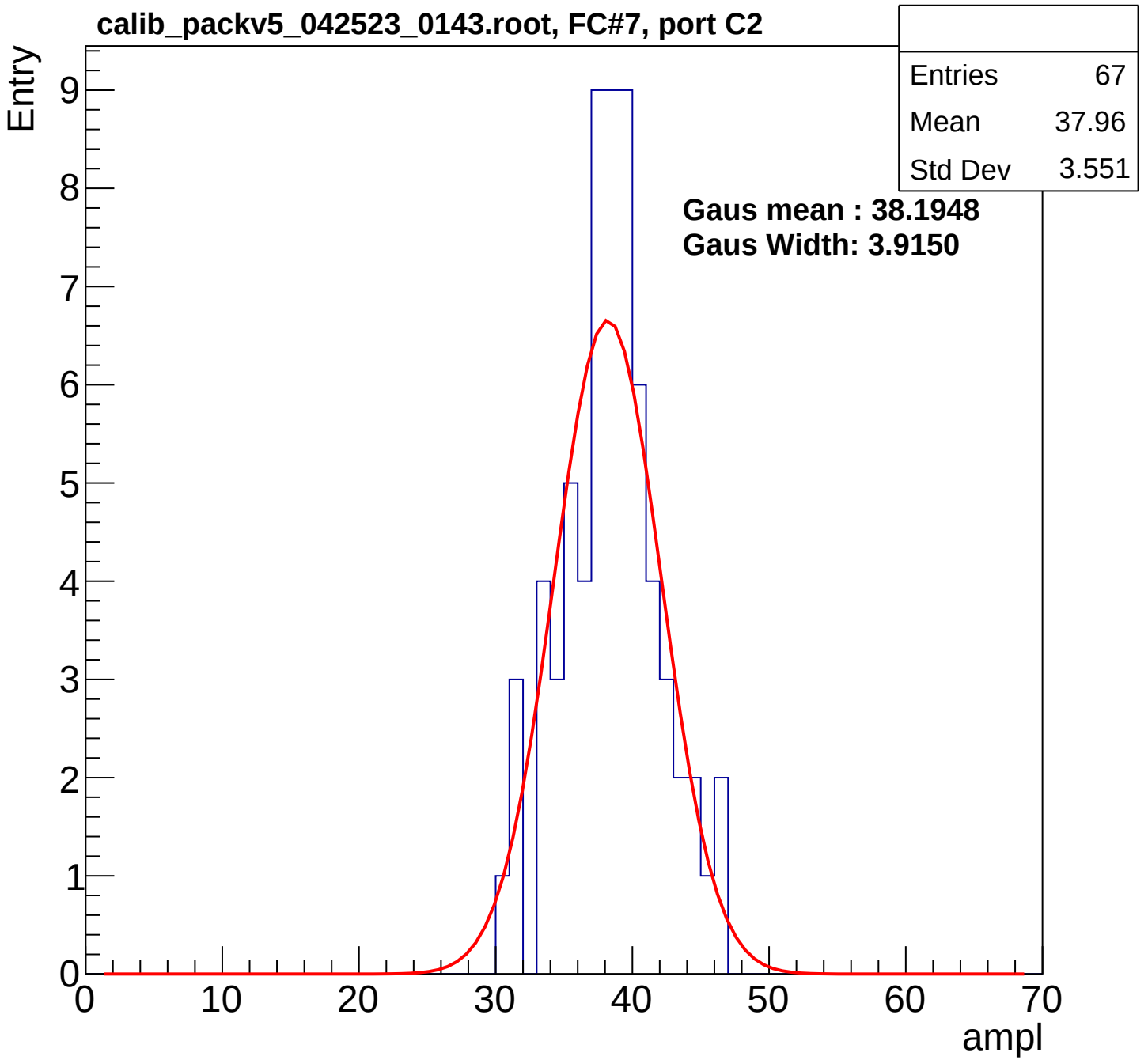

B1L103S, U13-ch2, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch3, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 30.58
Std Dev 3.67
Gaus mean : 31.1406
Gaus Width: 3.5364
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch3, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.96
Std Dev 3.551
Gaus mean : 38.1948
Gaus Width: 3.9150
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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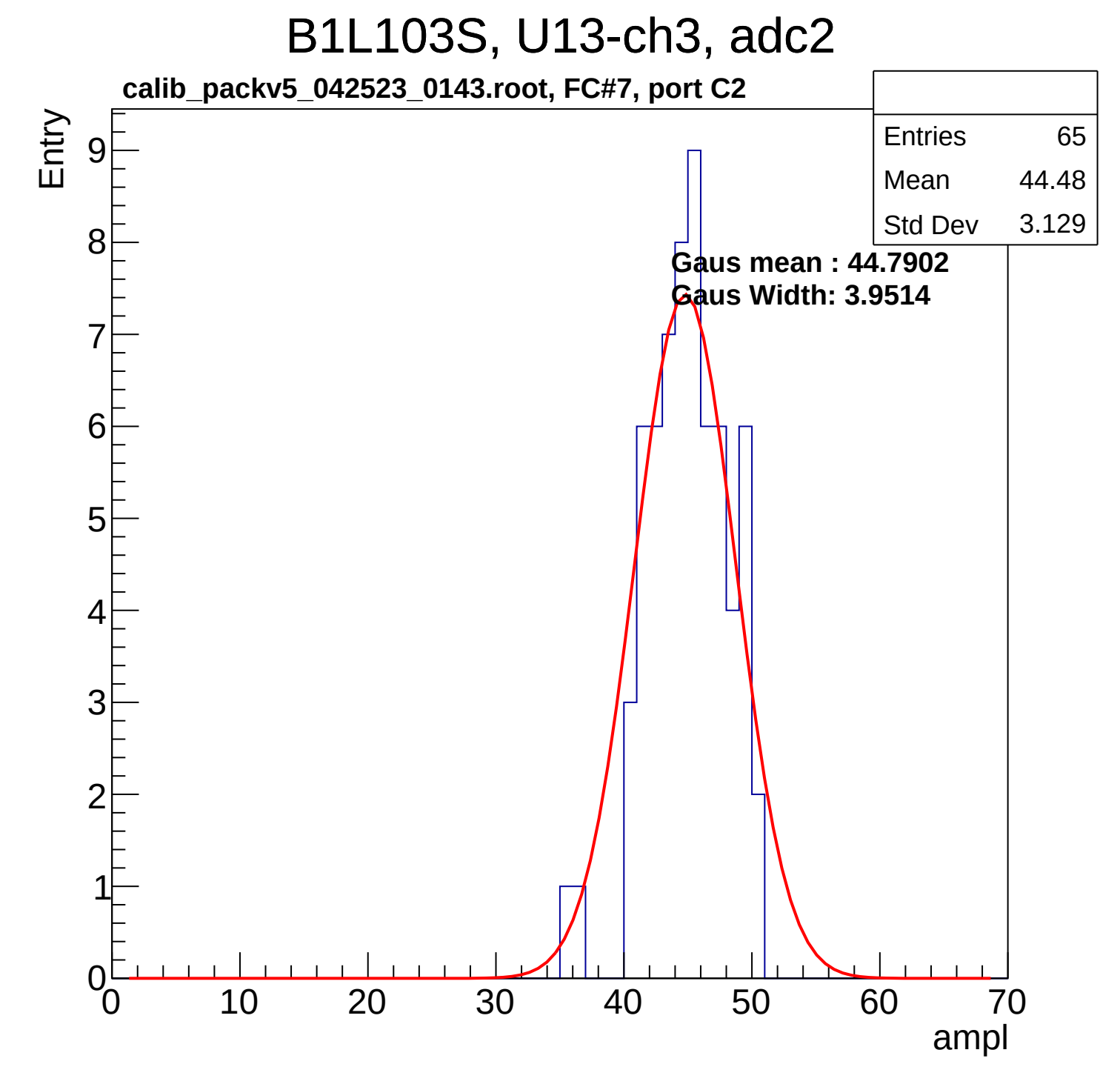
B1L103S, U13-ch3, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 44.48
Std Dev 3.129
Gaus mean : 44.7902
Gaus Width: 3.9514
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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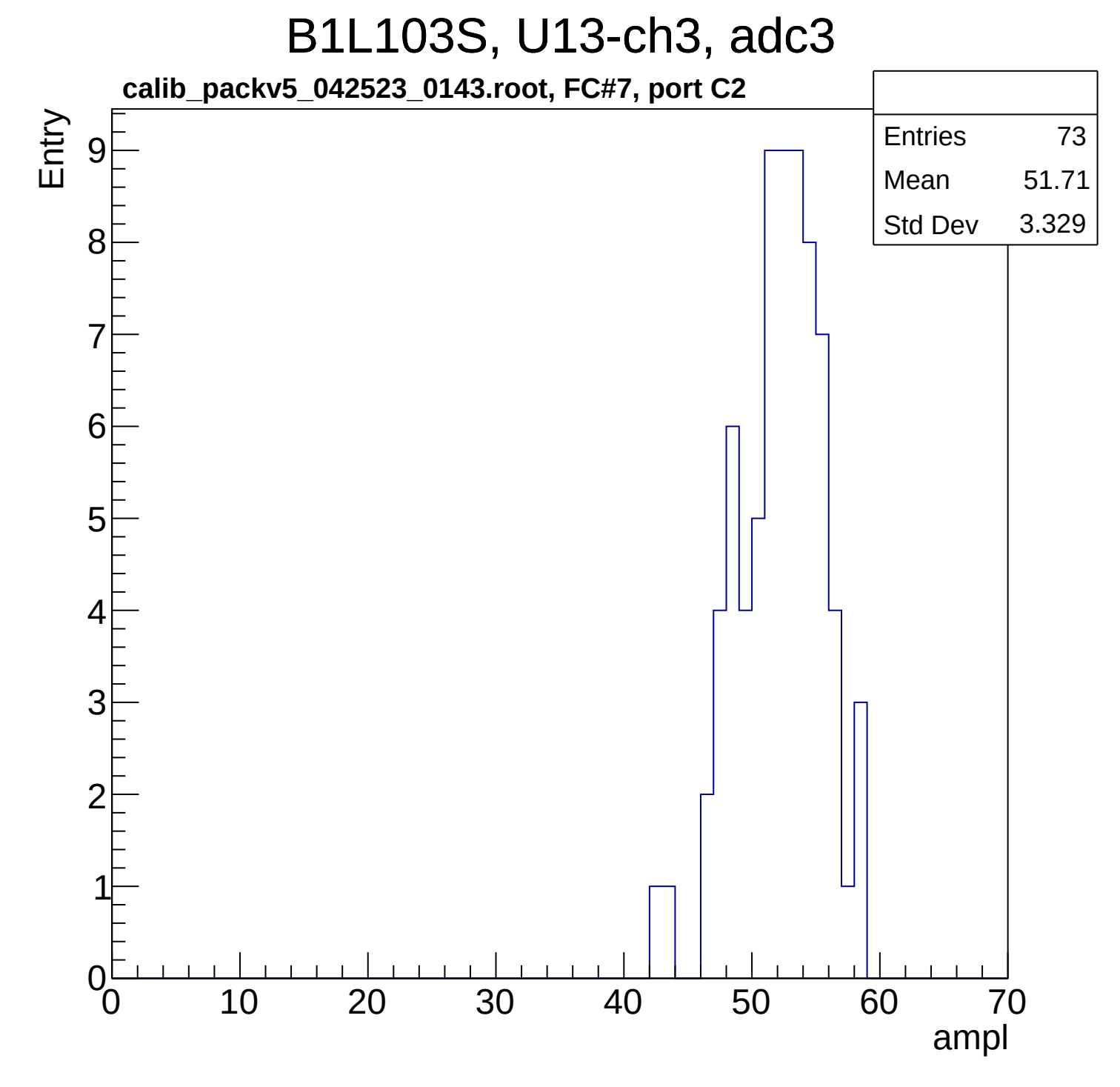
B1L103S, U13-ch3, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 51.71
Std Dev 3.329
Entry
ampl
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3
4
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6
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8
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0
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60
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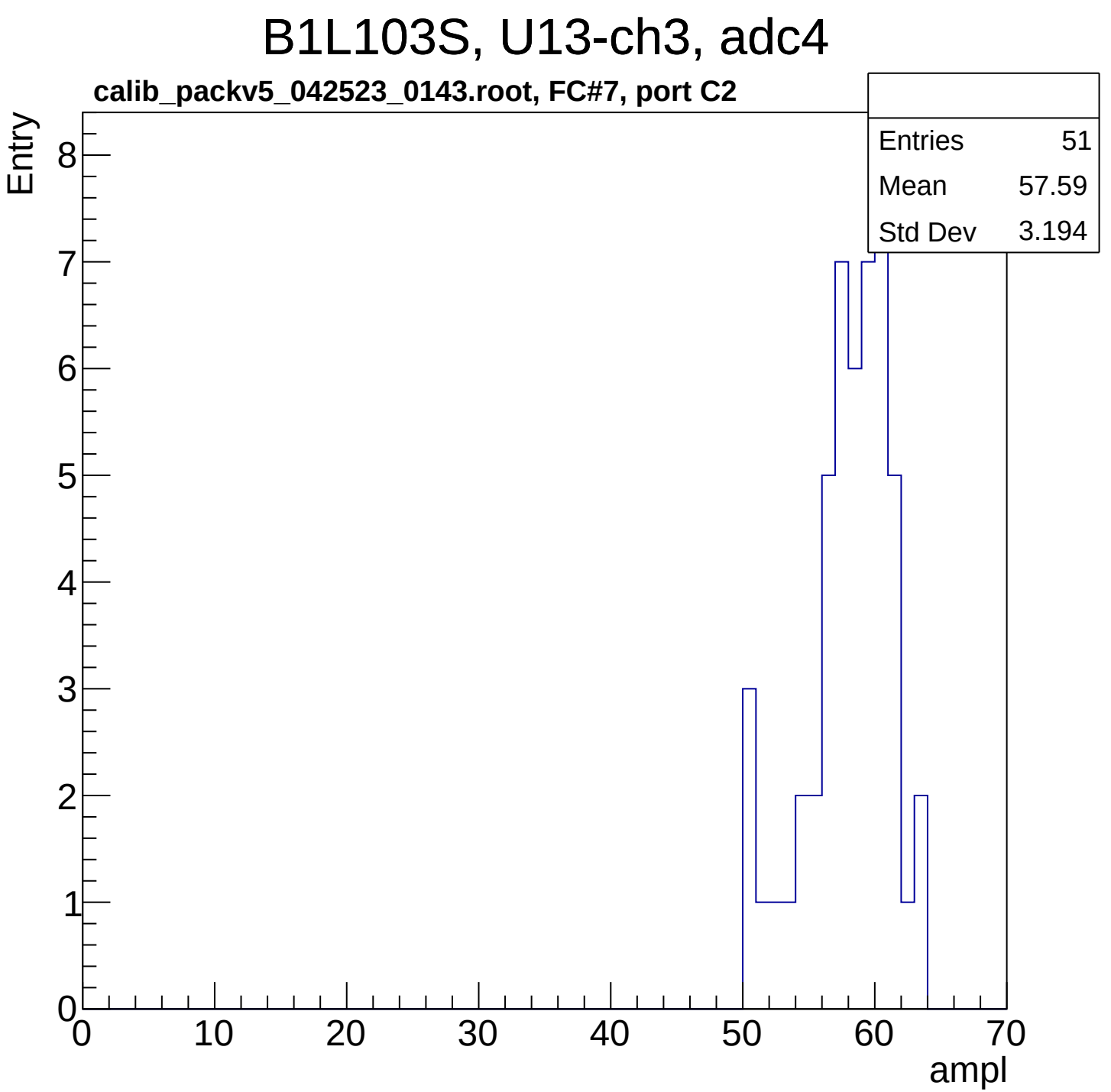
B1L103S, U13-ch3, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.59
Std Dev 3.194
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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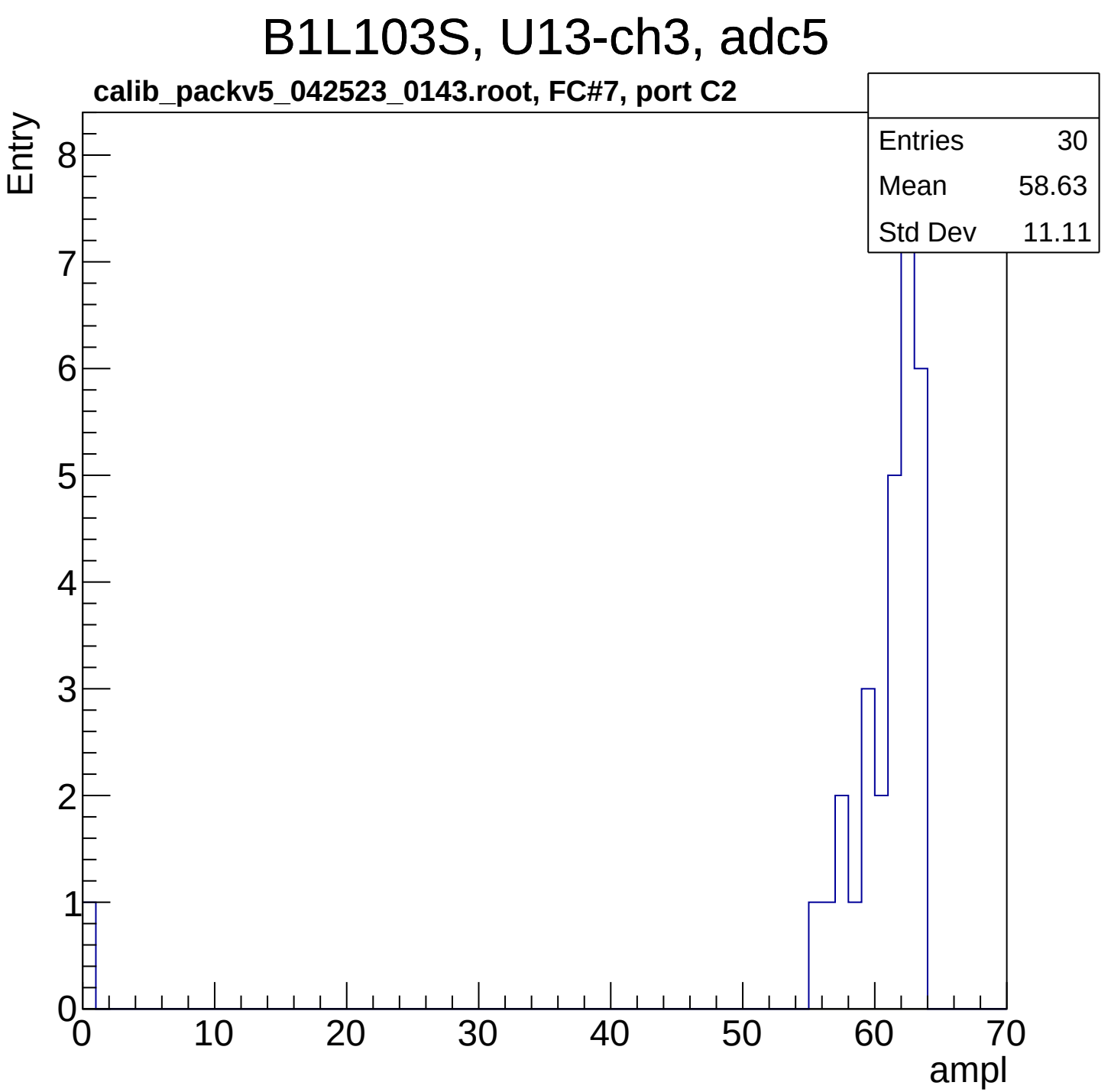
B1L103S, U13-ch3, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 58.63
Std Dev 11.11
Entry
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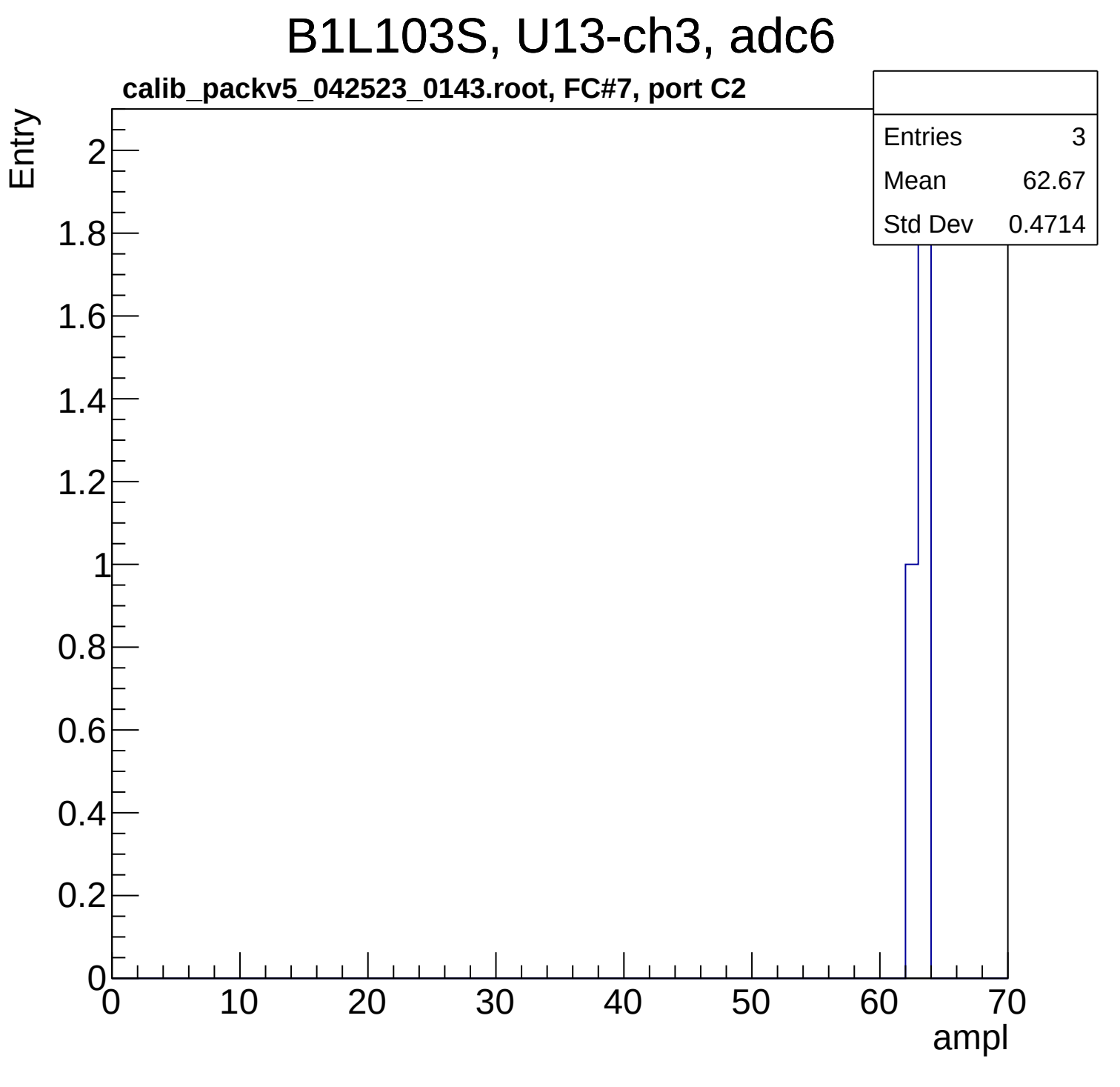
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch3, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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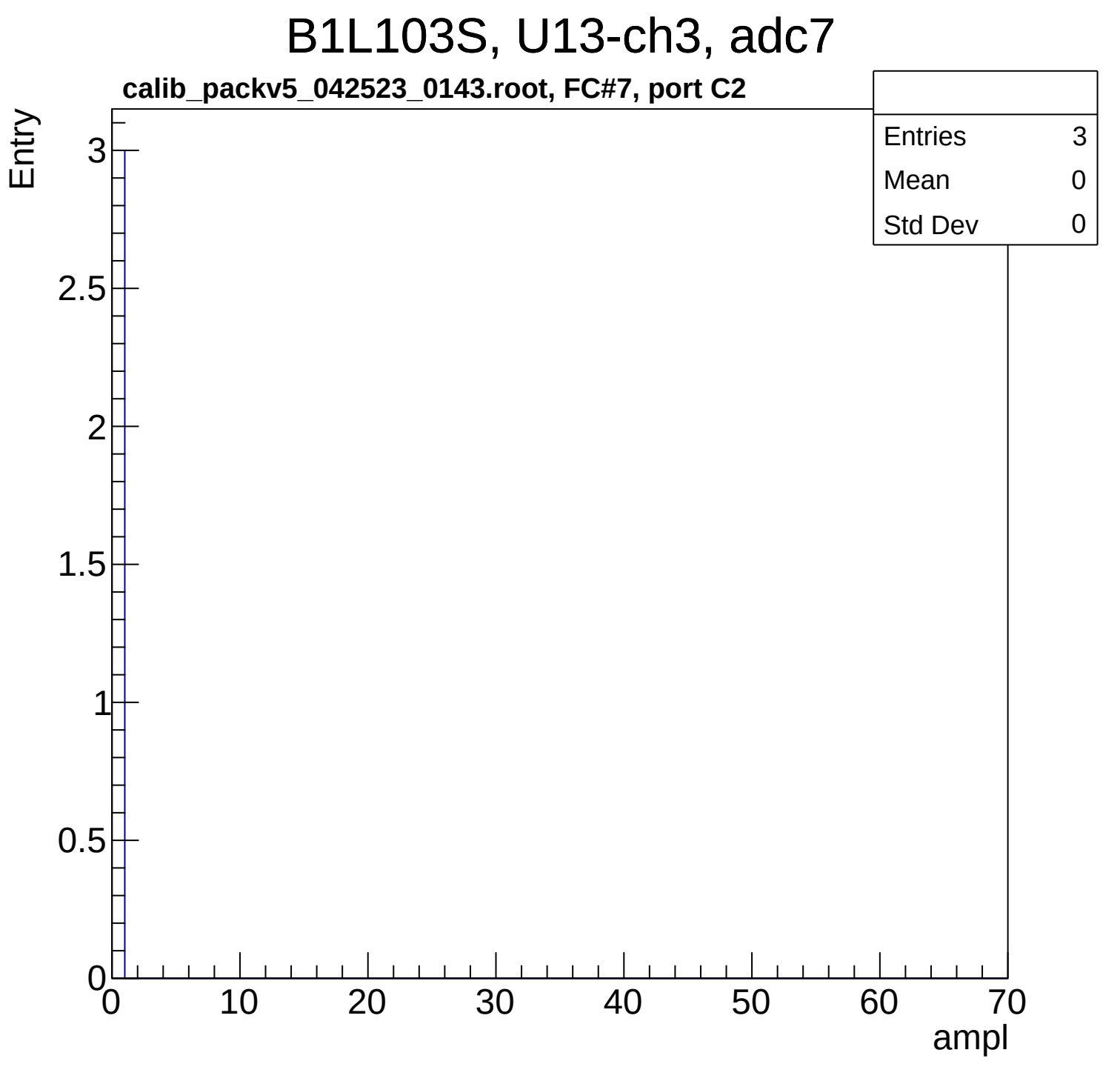
B1L103S, U13-ch3, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
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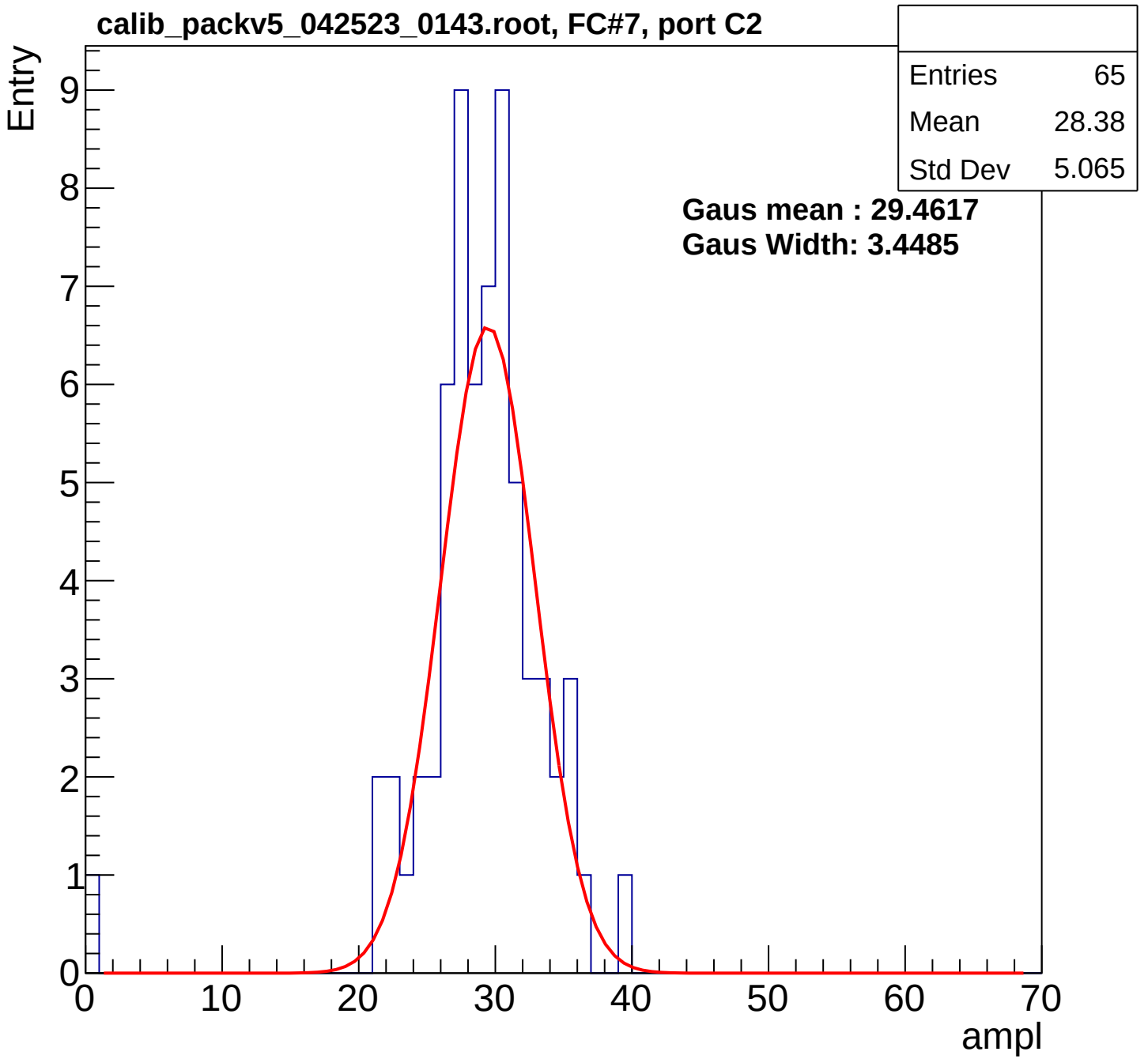
60
70
ampl


B1L103S, U13-ch4, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 28.38
Std Dev 5.065
Gaus mean : 29.4617
Gaus Width: 3.4485
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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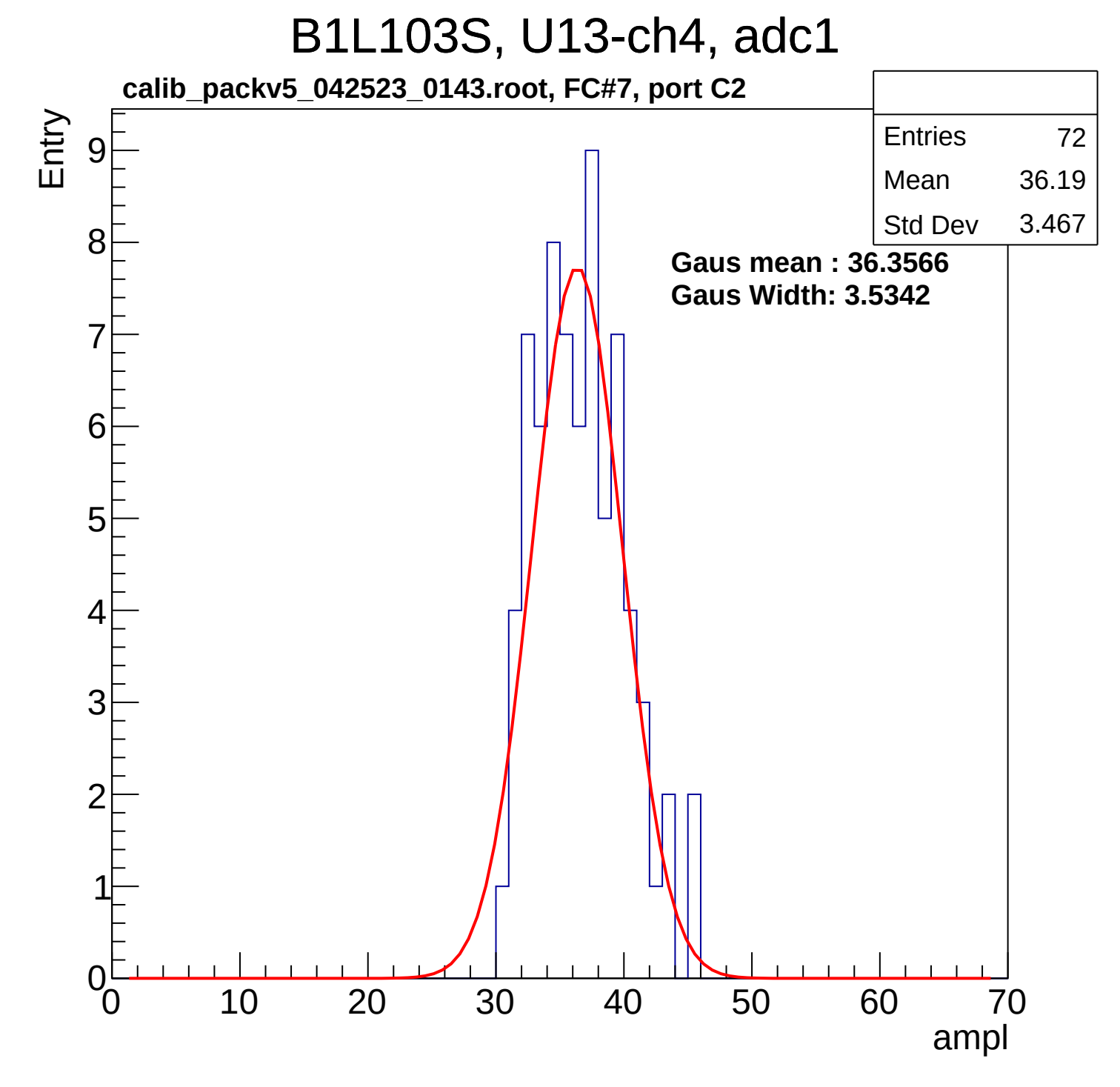
B1L103S, U13-ch4, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.19
Std Dev 3.467
Gaus mean : 36.3566
Gaus Width: 3.5342
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
30
40
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60
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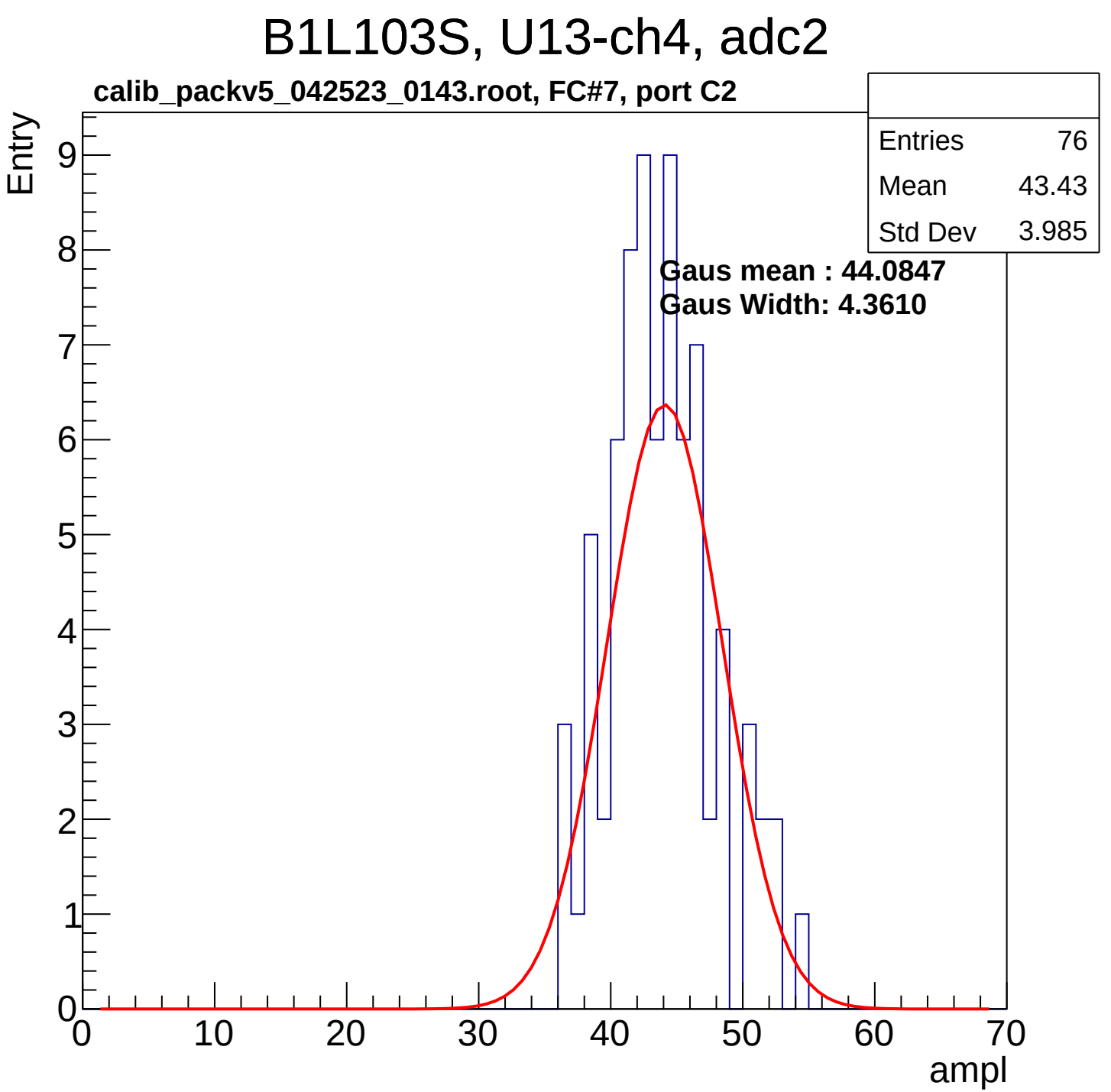
B1L103S, U13-ch4, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 43.43
Std Dev 3.985
Gaus mean : 44.0847
Gaus Width: 4.3610
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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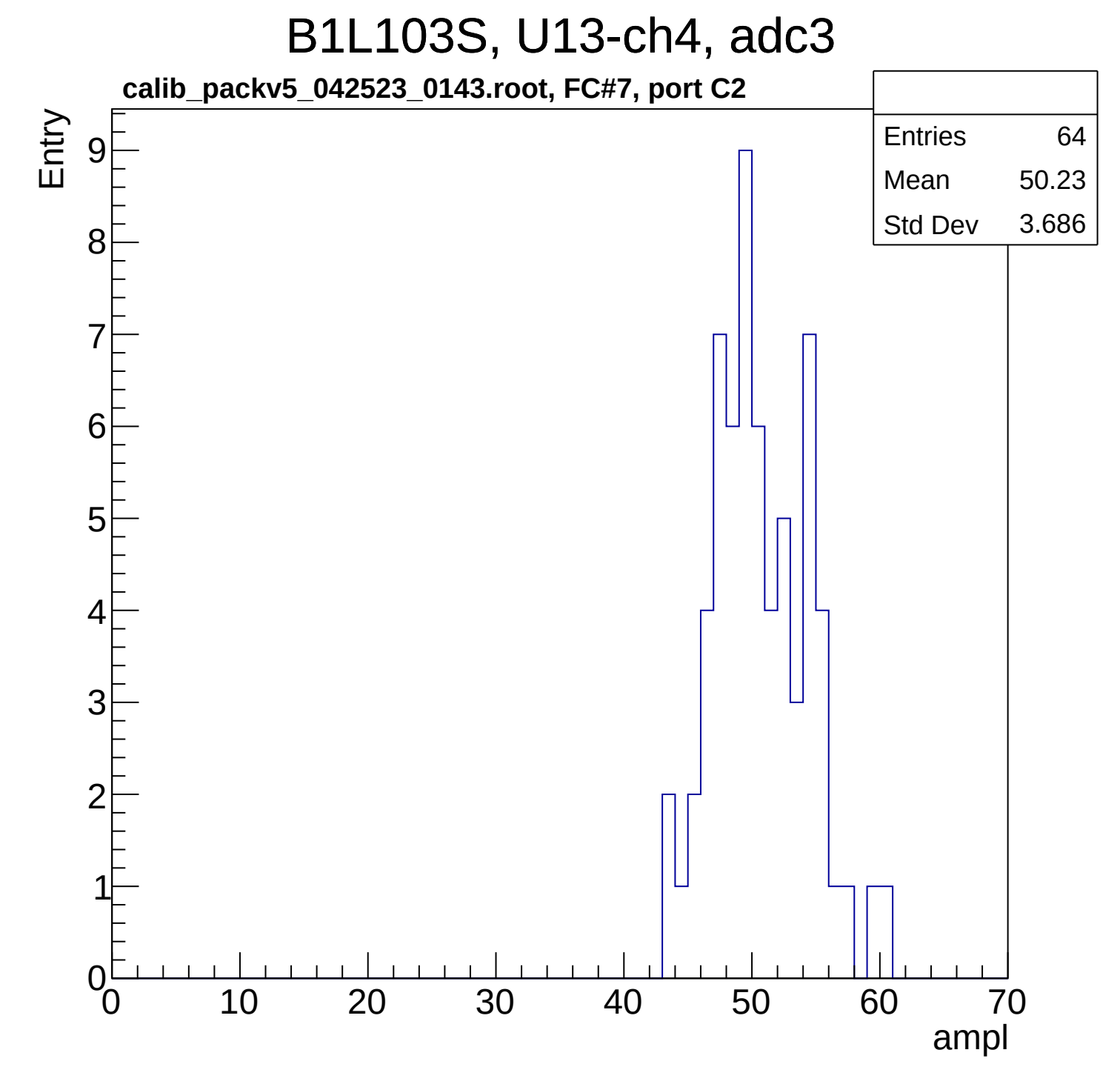
B1L103S, U13-ch4, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 50.23
Std Dev 3.686
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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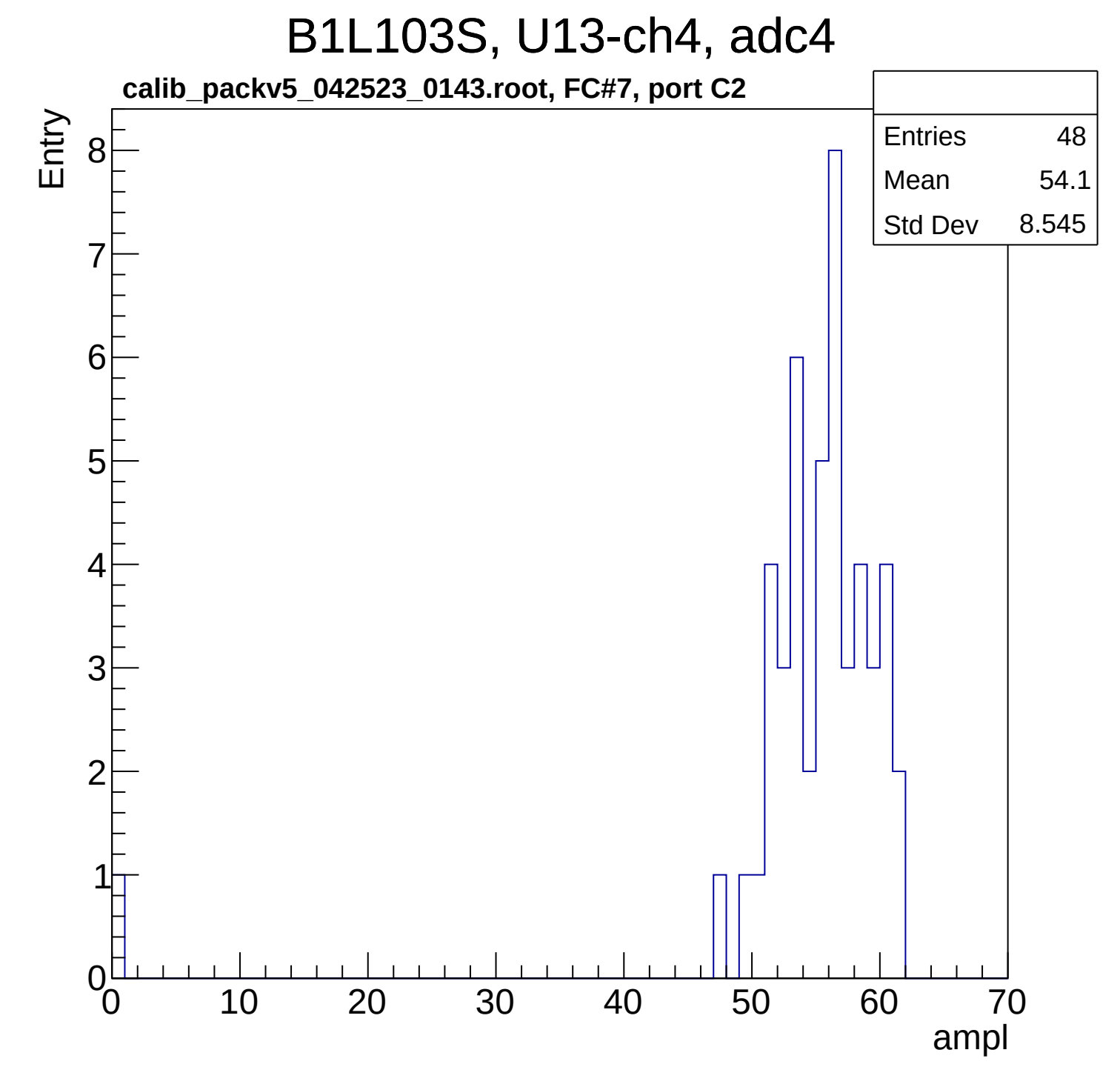
B1L103S, U13-ch4, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.1
Std Dev 8.545
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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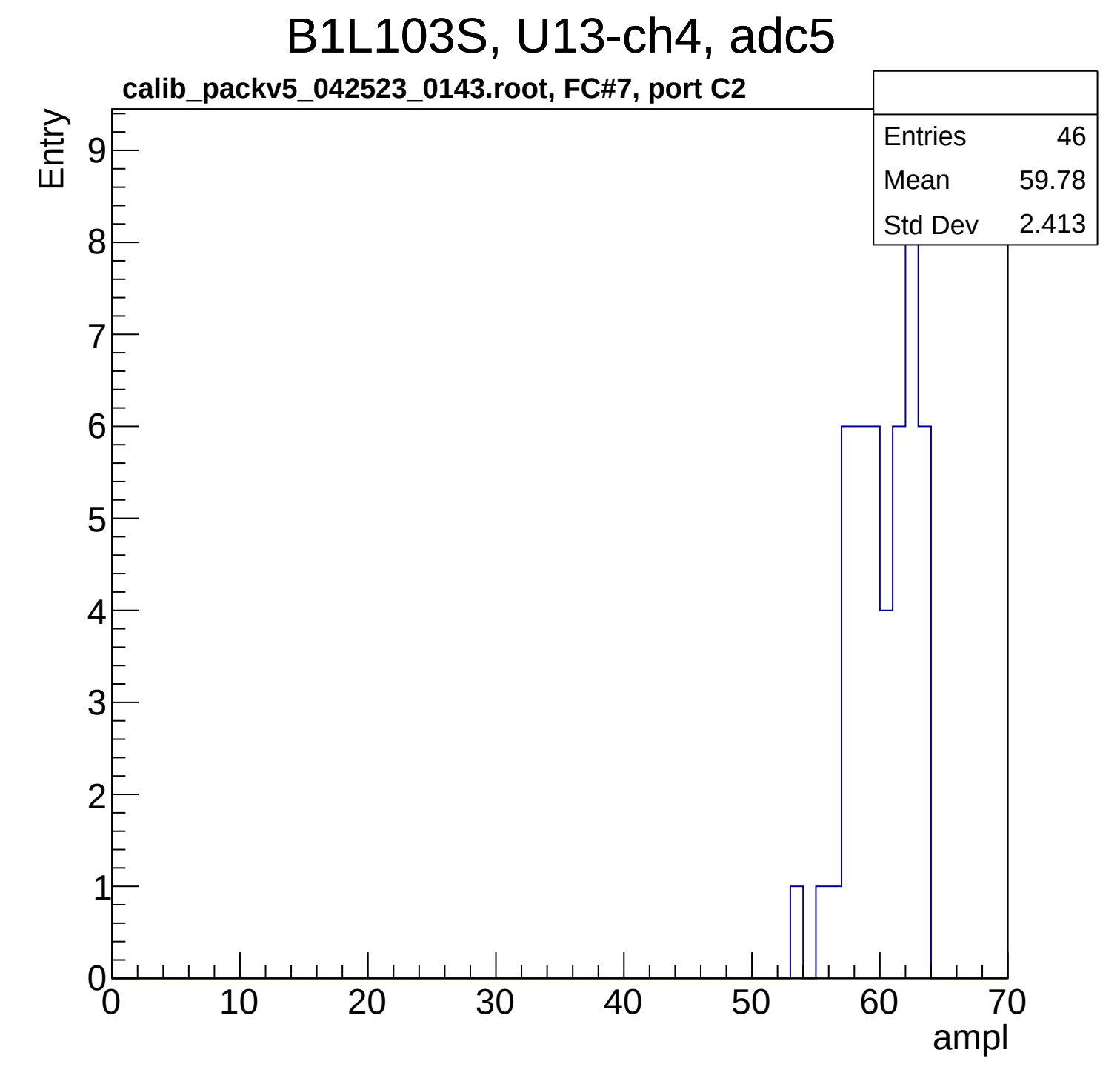
B1L103S, U13-ch4, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.78
Std Dev 2.413
Entry
ampl
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2
3
4
5
6
7
8
9
0
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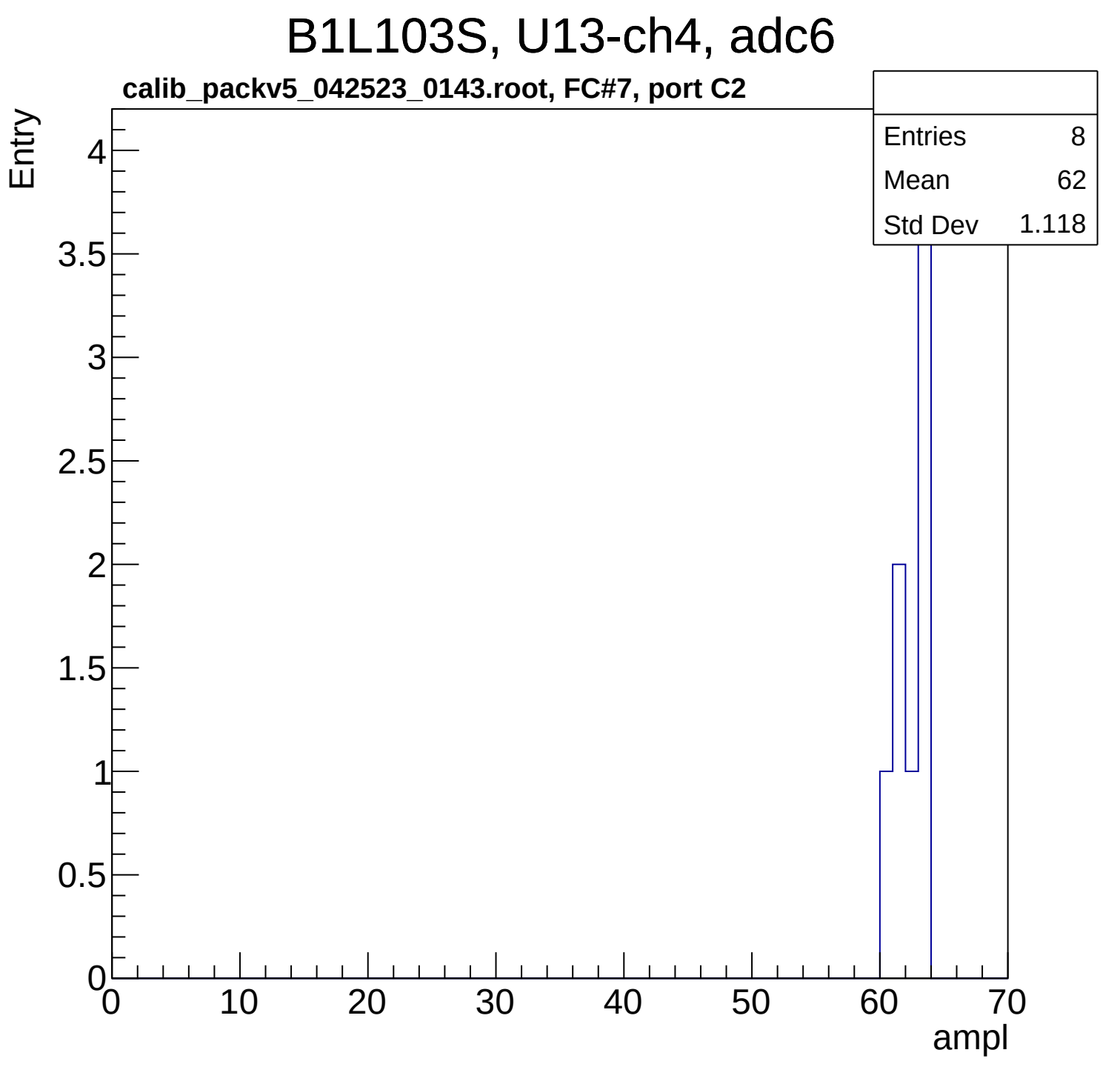
B1L103S, U13-ch4, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 62
Std Dev 1.118
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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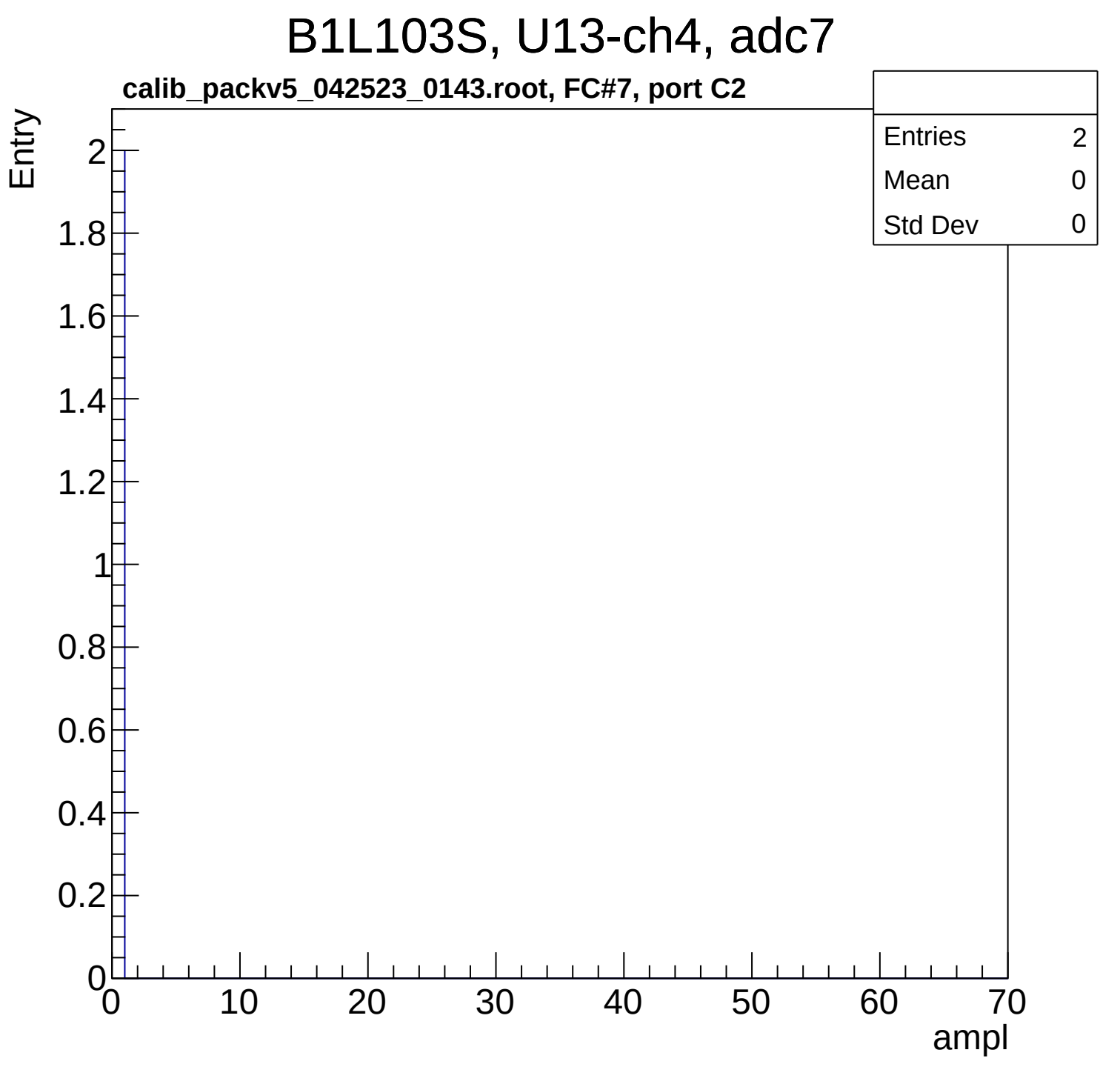
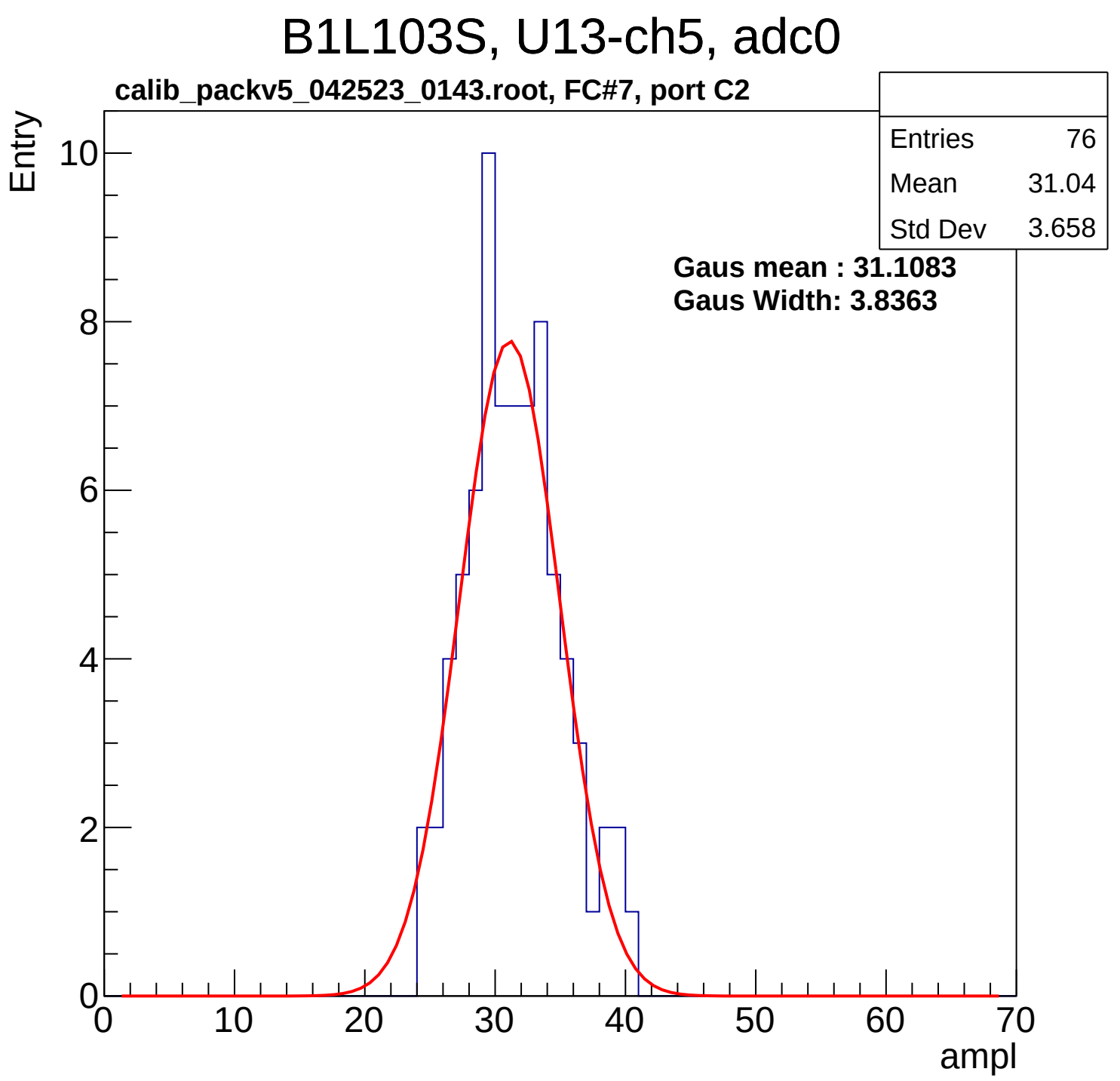
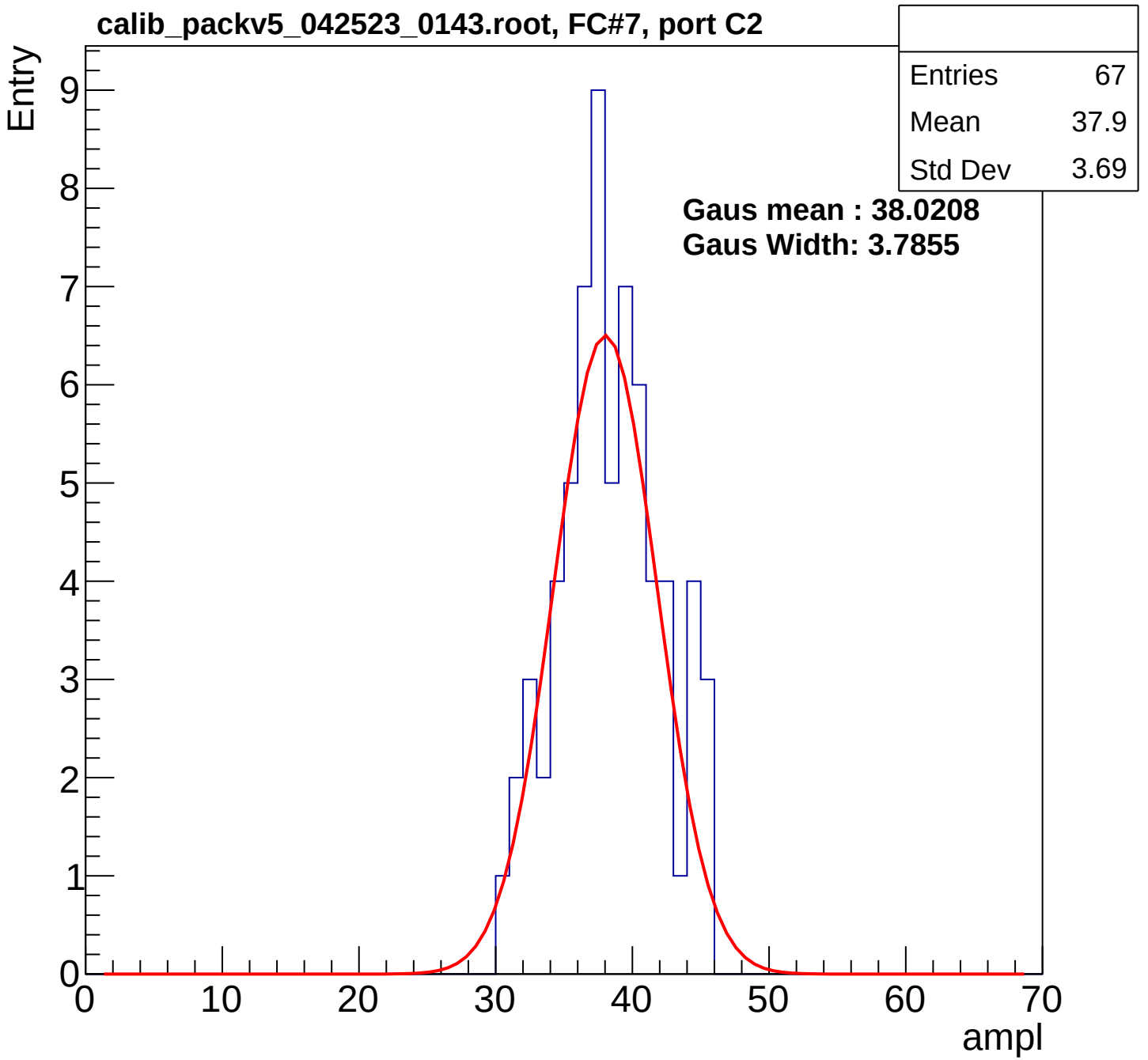
60
70


B1L103S, U13-ch4, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch5, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 31.04
Std Dev 3.658
Gaus mean : 31.1083
Gaus Width: 3.8363
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch5, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.9
Std Dev 3.69
Gaus mean : 38.0208
Gaus Width: 3.7855
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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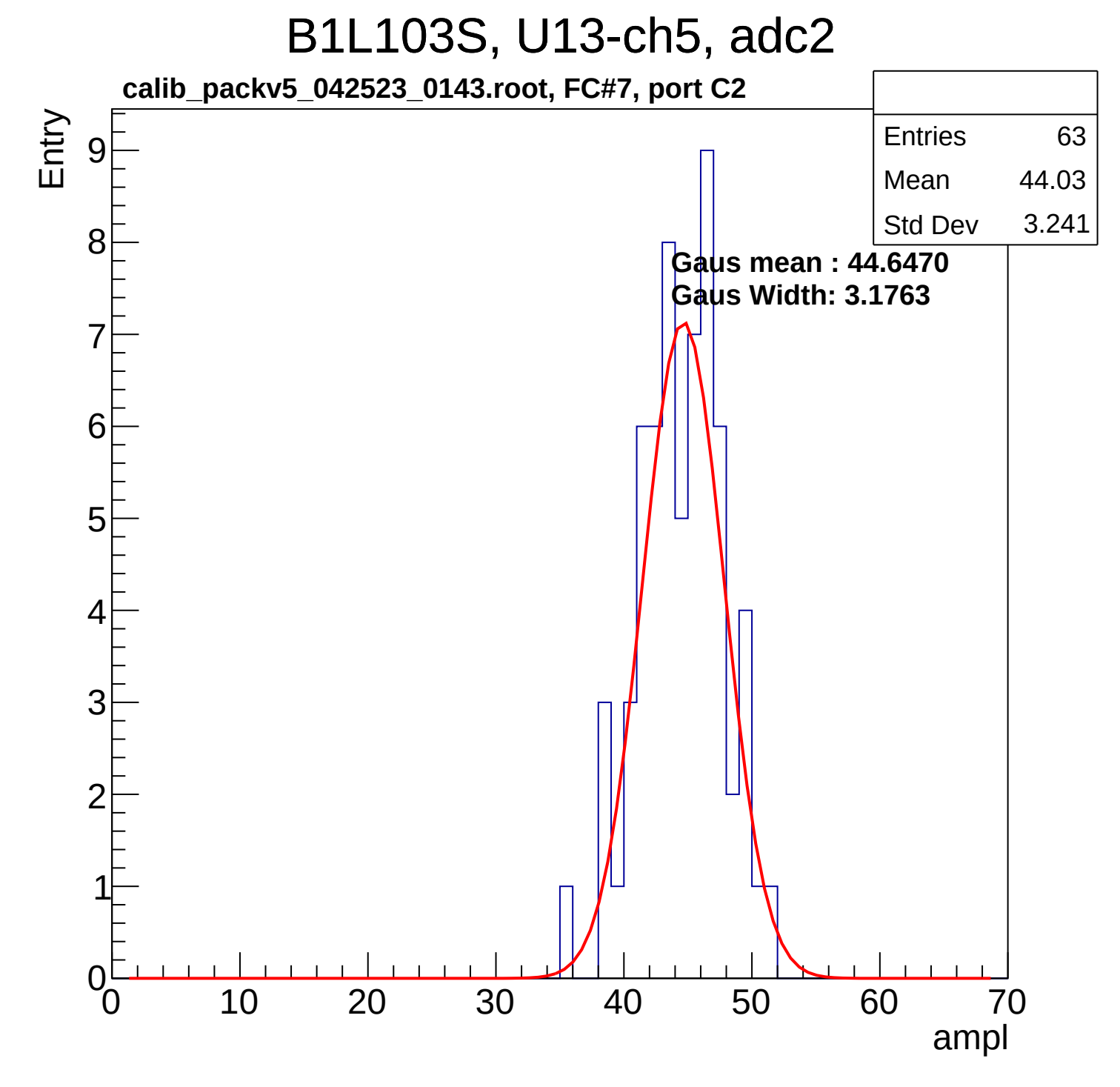
B1L103S, U13-ch5, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 44.03
Std Dev 3.241
Gaus mean : 44.6470
Gaus Width: 3.1763
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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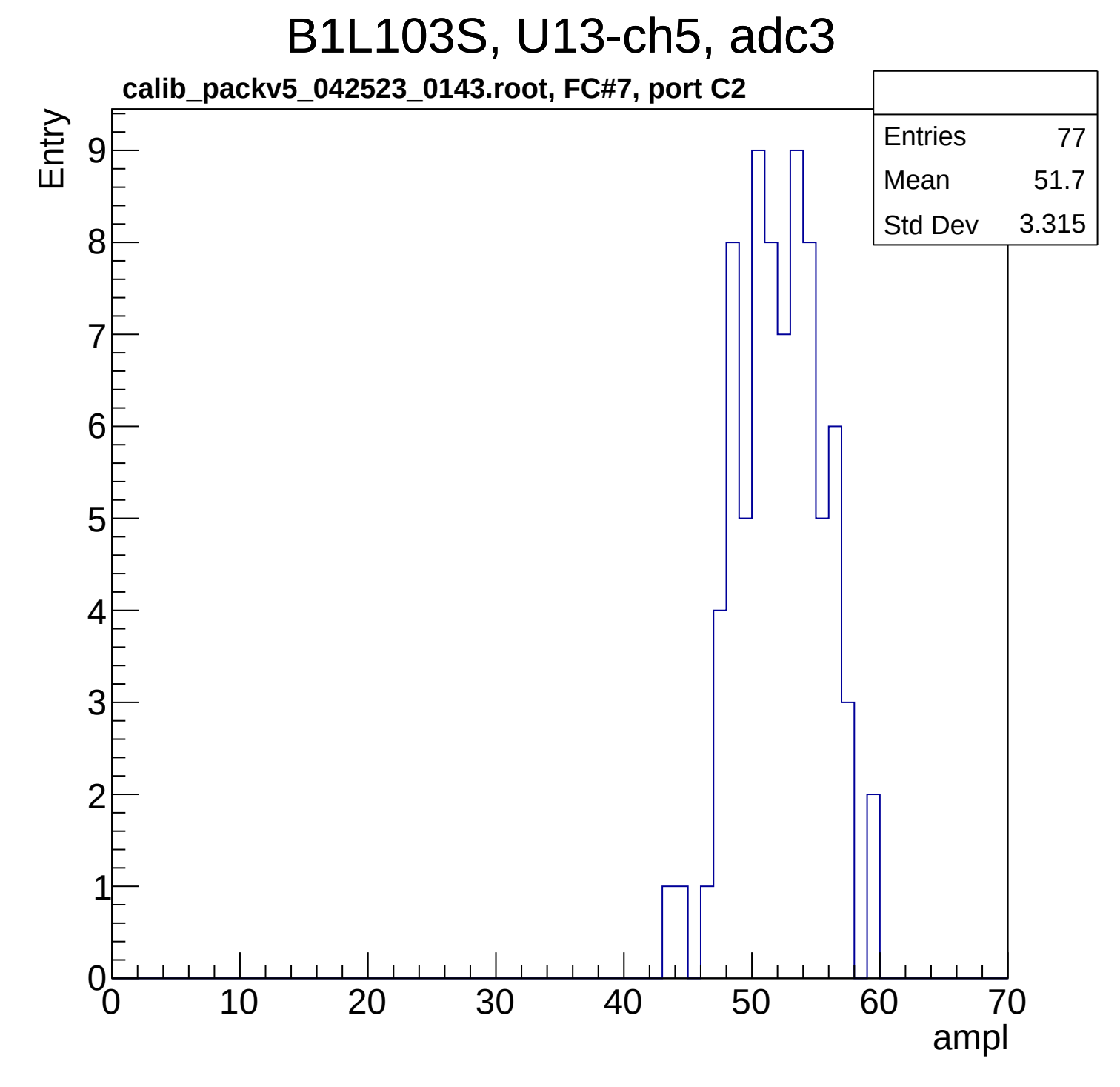
B1L103S, U13-ch5, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 51.7
Std Dev 3.315
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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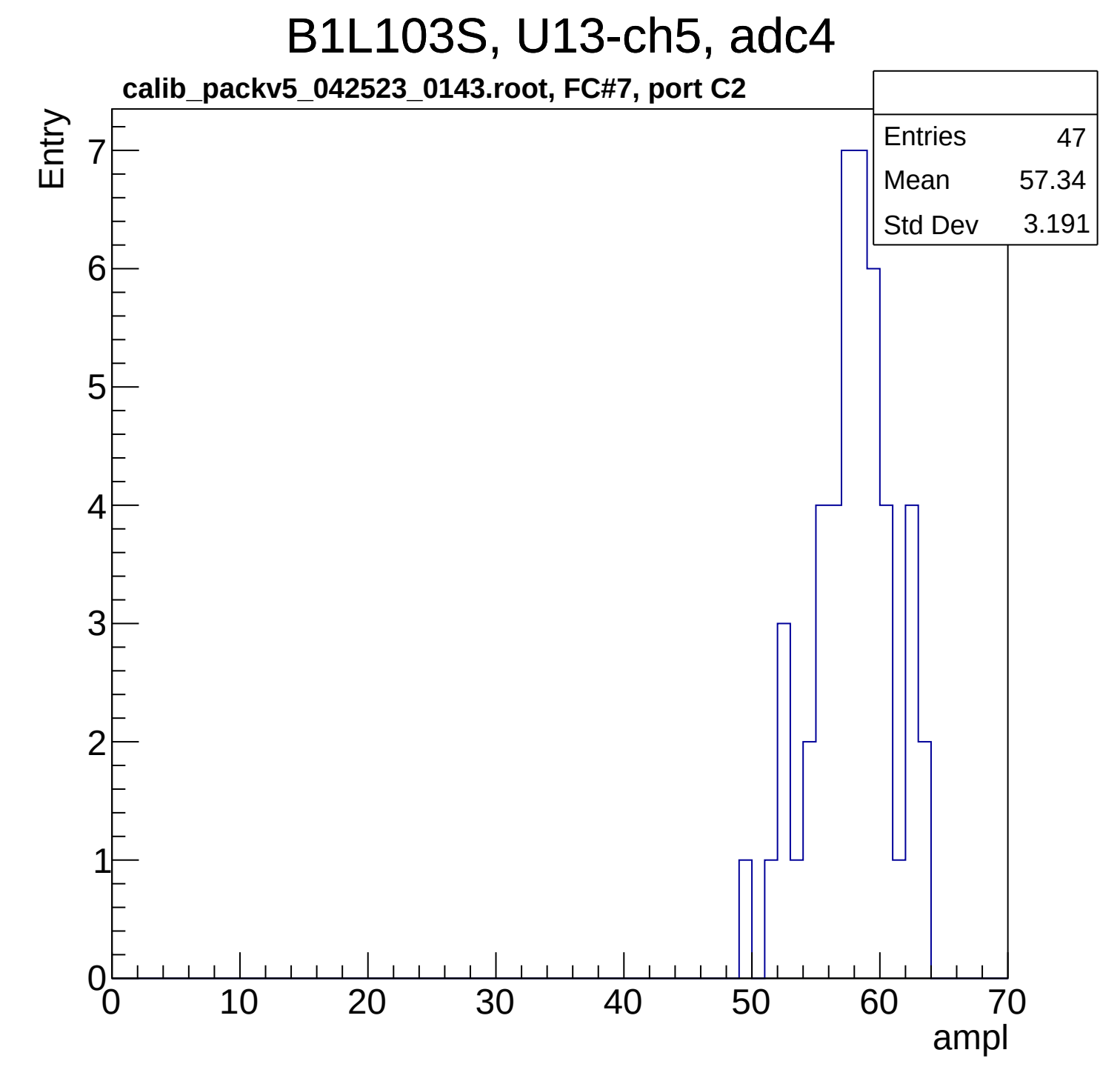
B1L103S, U13-ch5, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.34
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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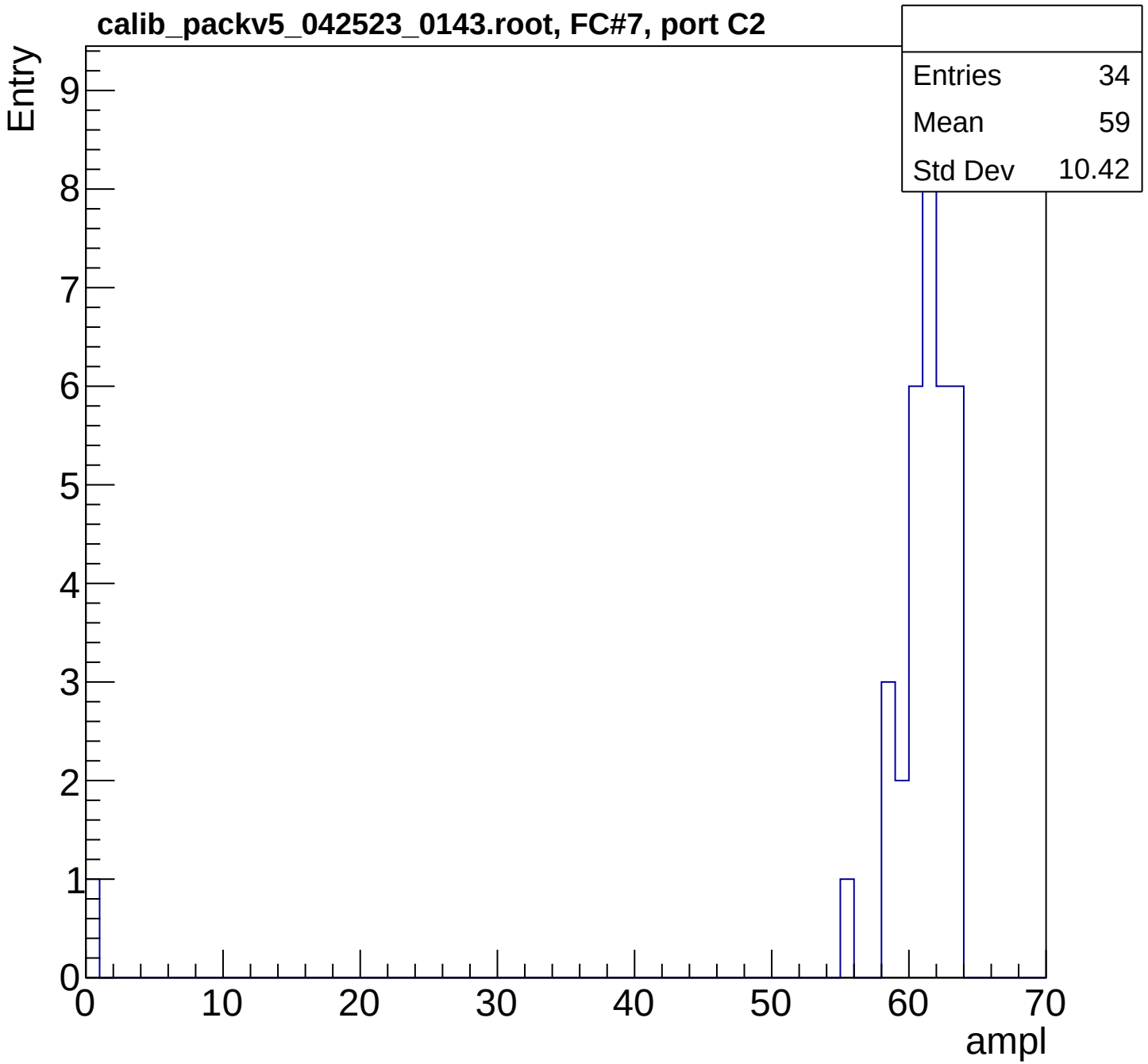
B1L103S, U13-ch5, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59
Std Dev 10.42
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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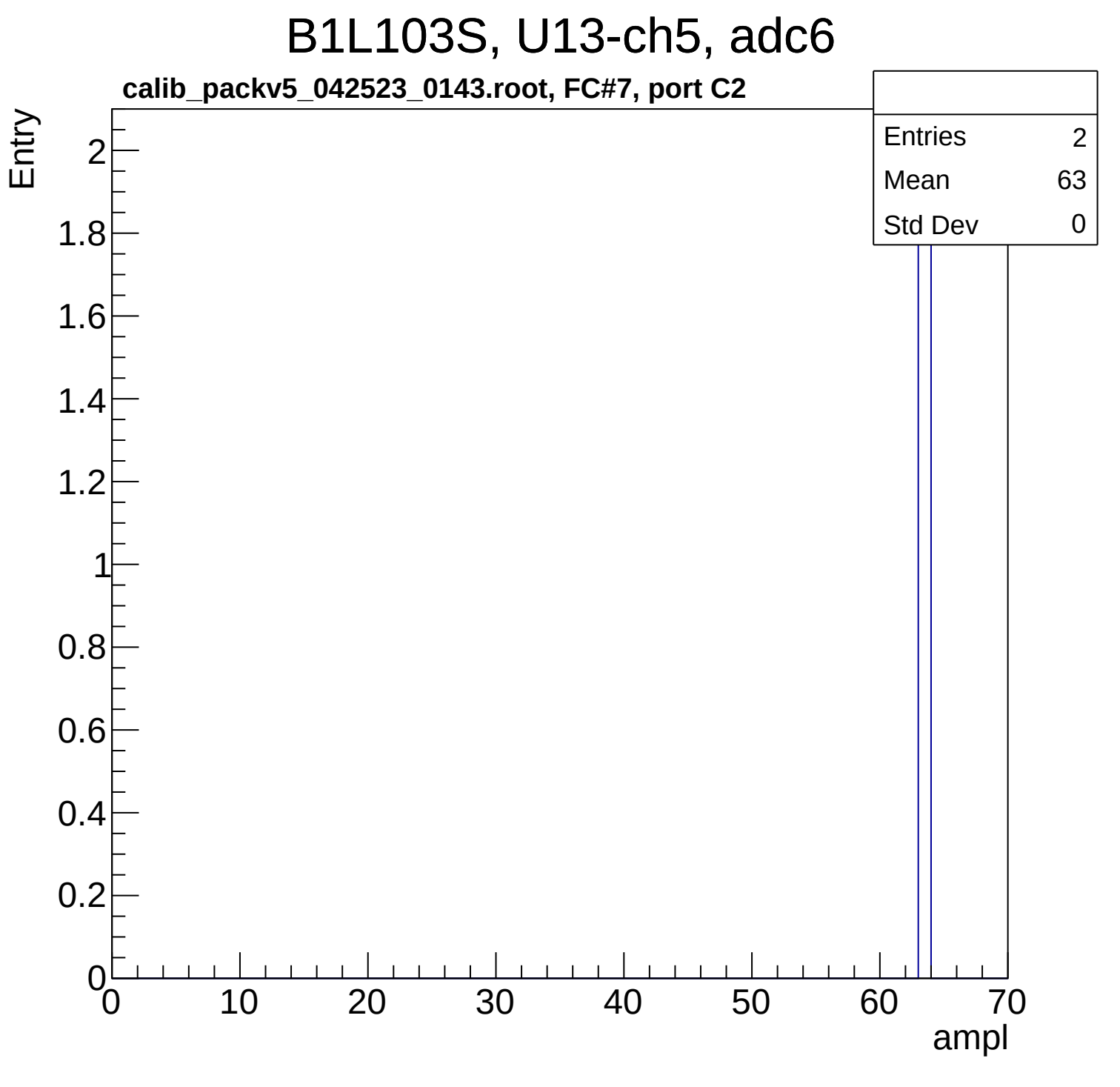
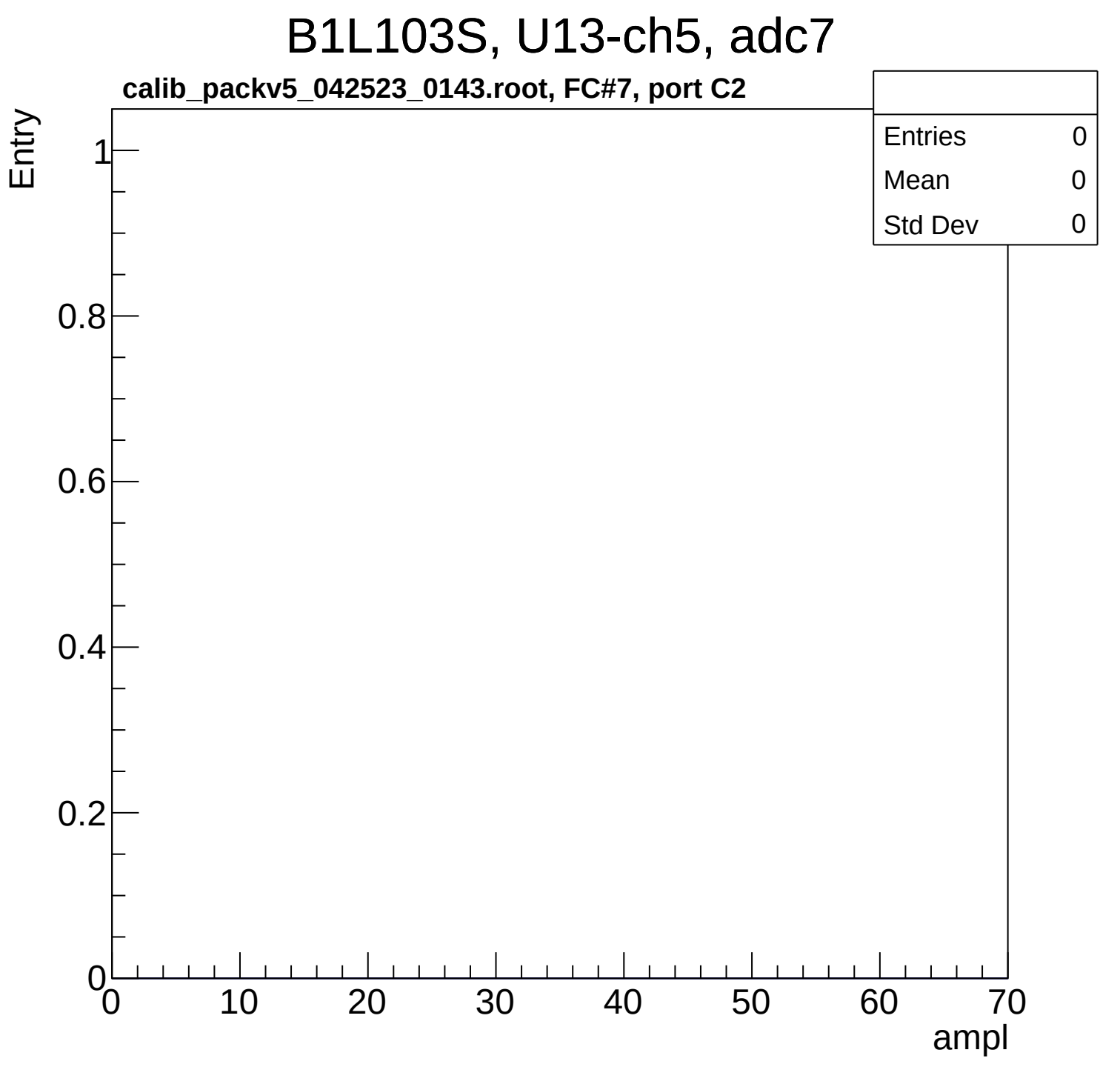
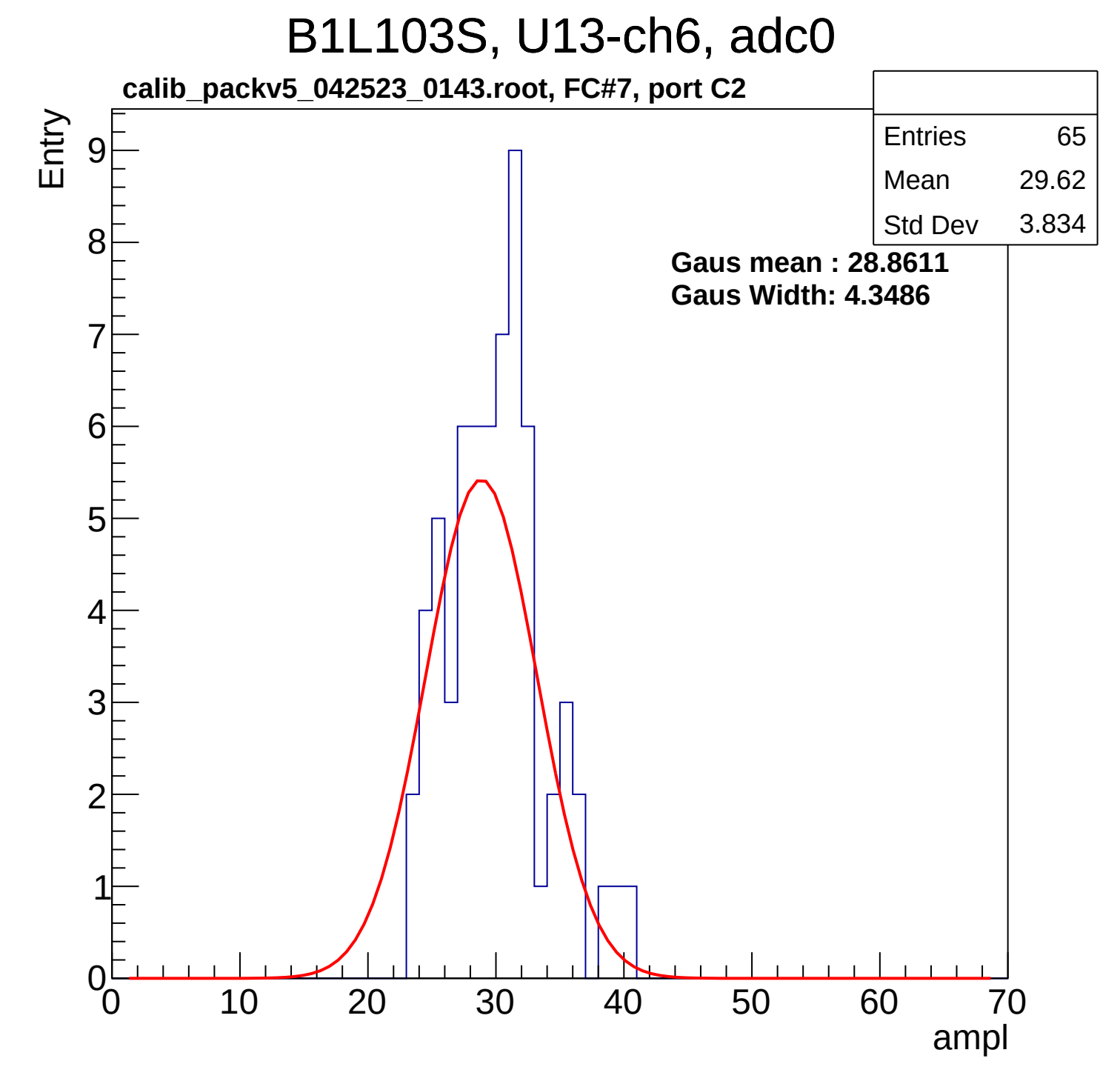

B1L103S, U13-ch5, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch5, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch6, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 29.62
Std Dev 3.834
Gaus mean : 28.8611
Gaus Width: 4.3486
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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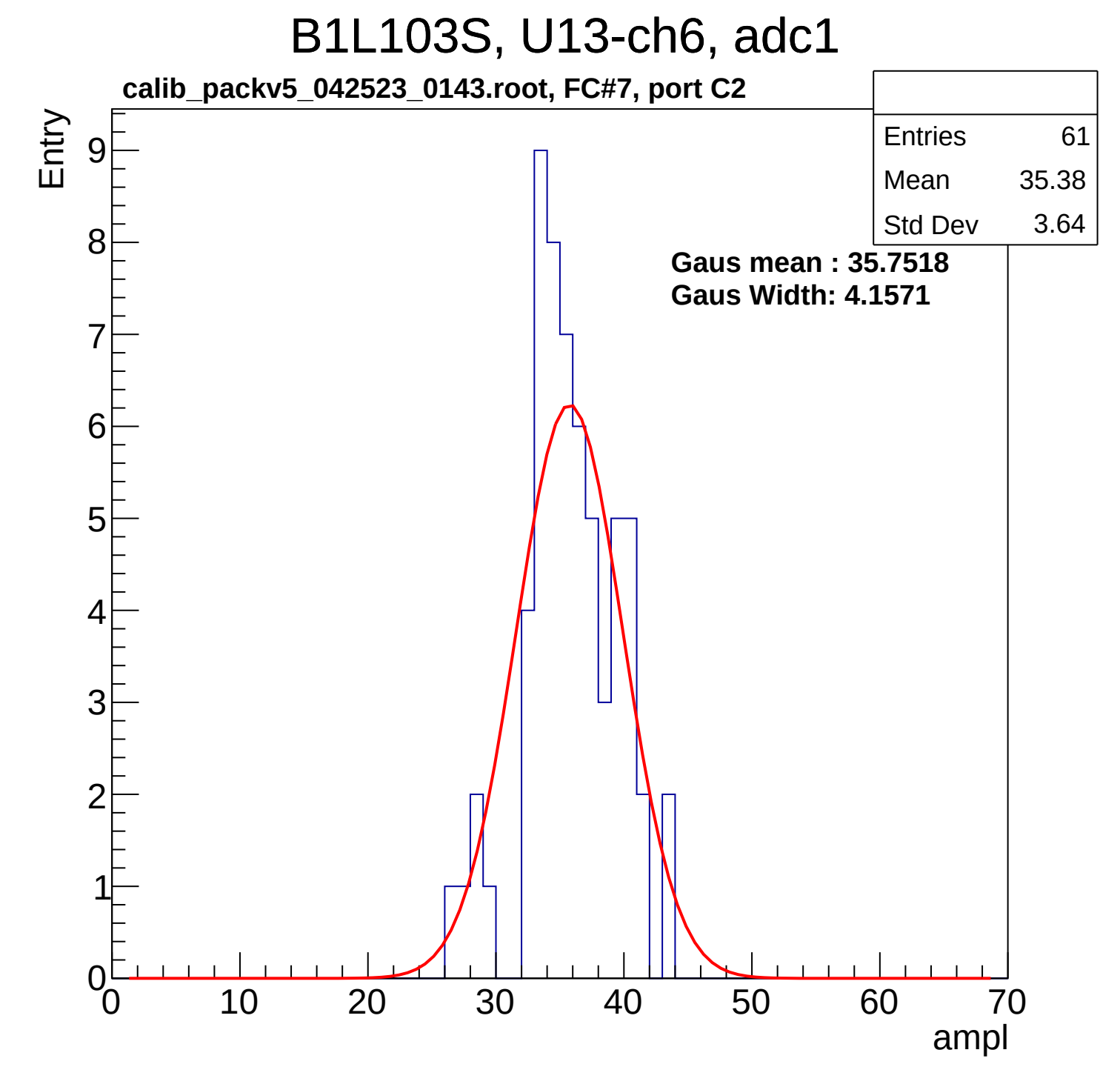
40
50
60
70


B1L103S, U13-ch6, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 35.38
Std Dev 3.64
Gaus mean : 35.7518
Gaus Width: 4.1571
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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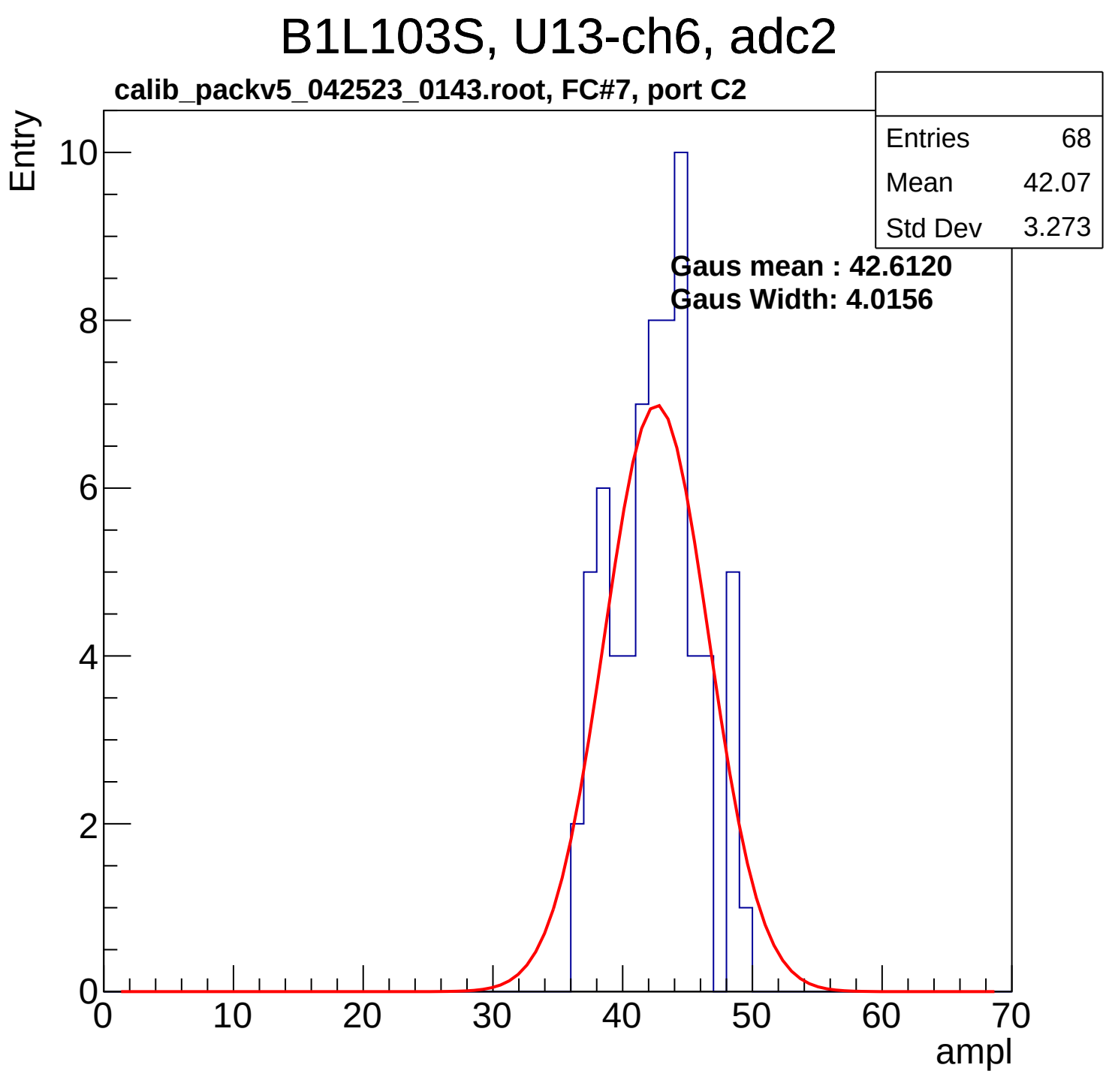
B1L103S, U13-ch6, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 42.07
Std Dev 3.273
Gaus mean : 42.6120
Gaus Width: 4.0156
Entry
ampl
0
2
4
6
8
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0
10
20
30
40
50
60
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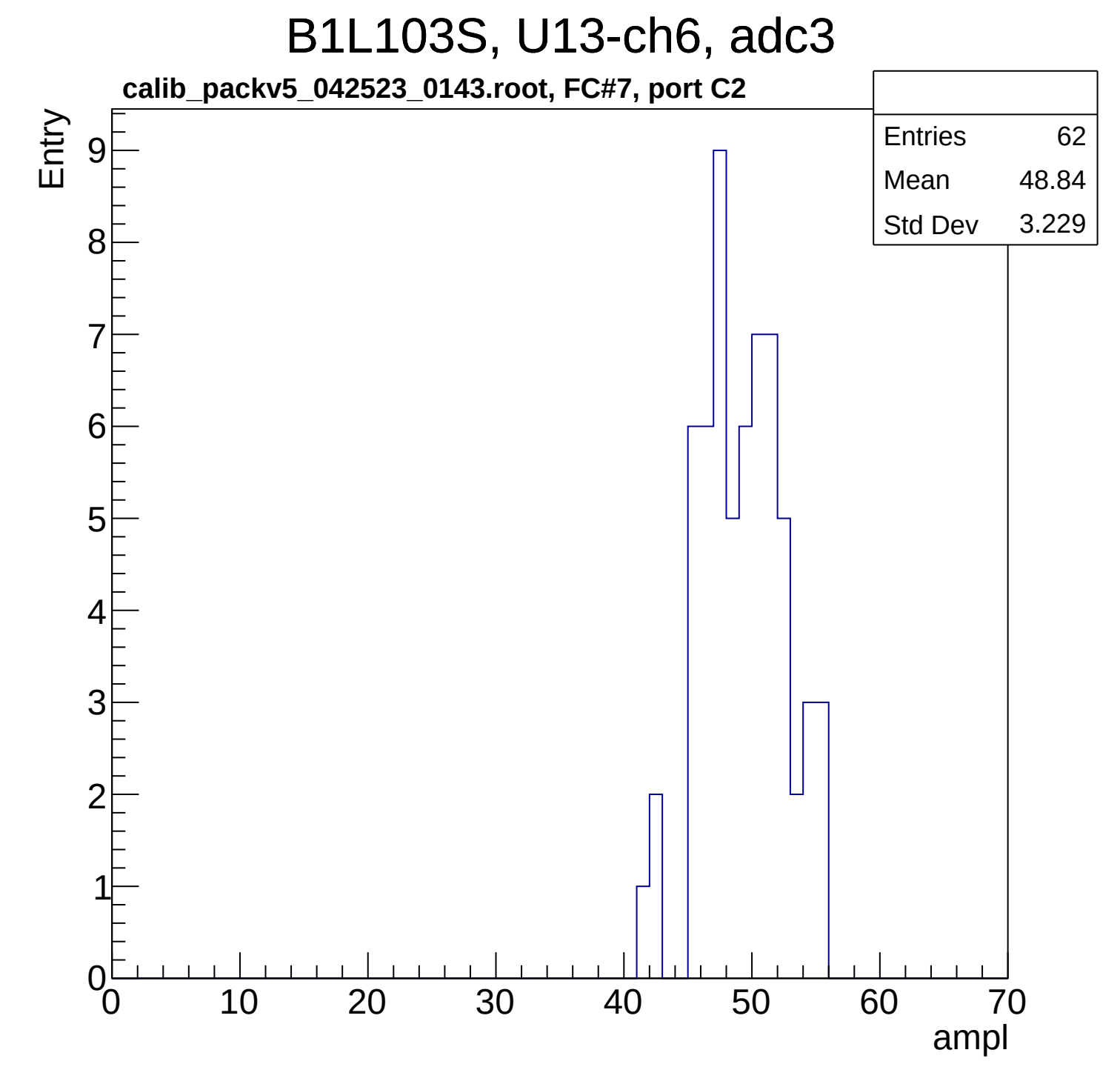
B1L103S, U13-ch6, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 48.84
Std Dev 3.229
Entry
ampl
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3
4
5
6
7
8
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0
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60
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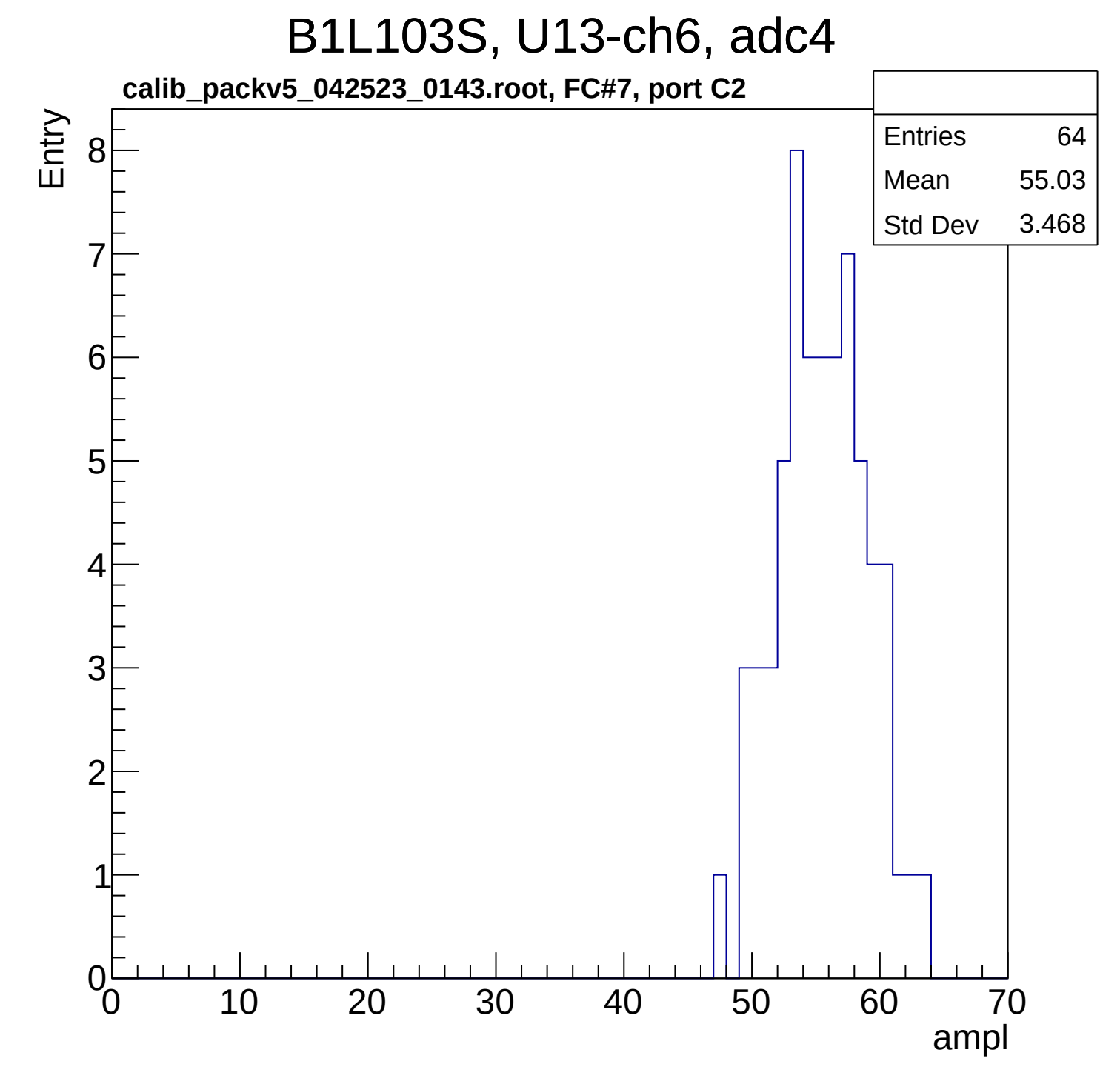
B1L103S, U13-ch6, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.03
Std Dev 3.468
Entry
ampl
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5
6
7
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0
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20
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50
60
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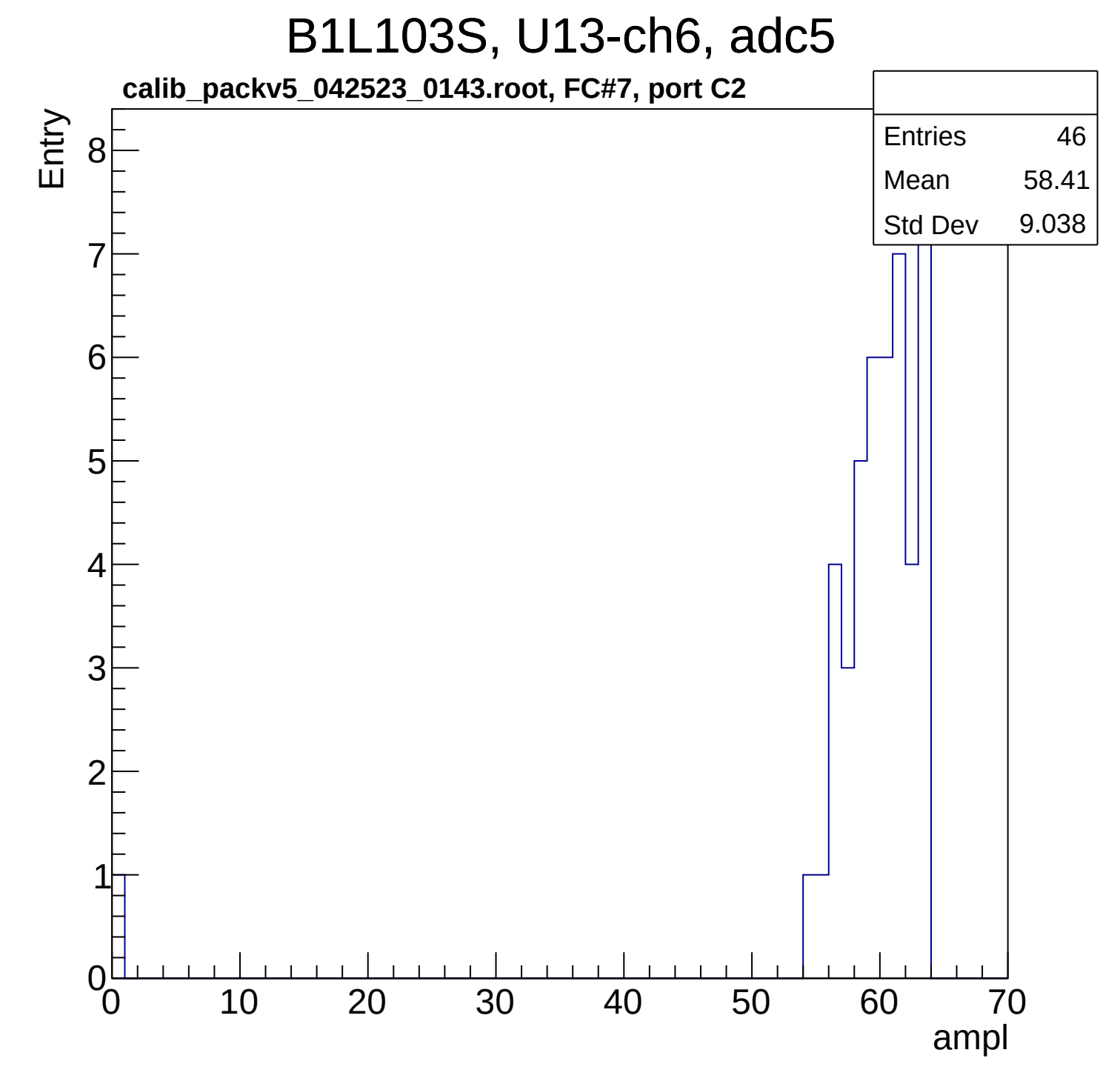
B1L103S, U13-ch6, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.41
Std Dev 9.038
Entry
ampl
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1
2
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4
5
6
7
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0
10
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40
50
60
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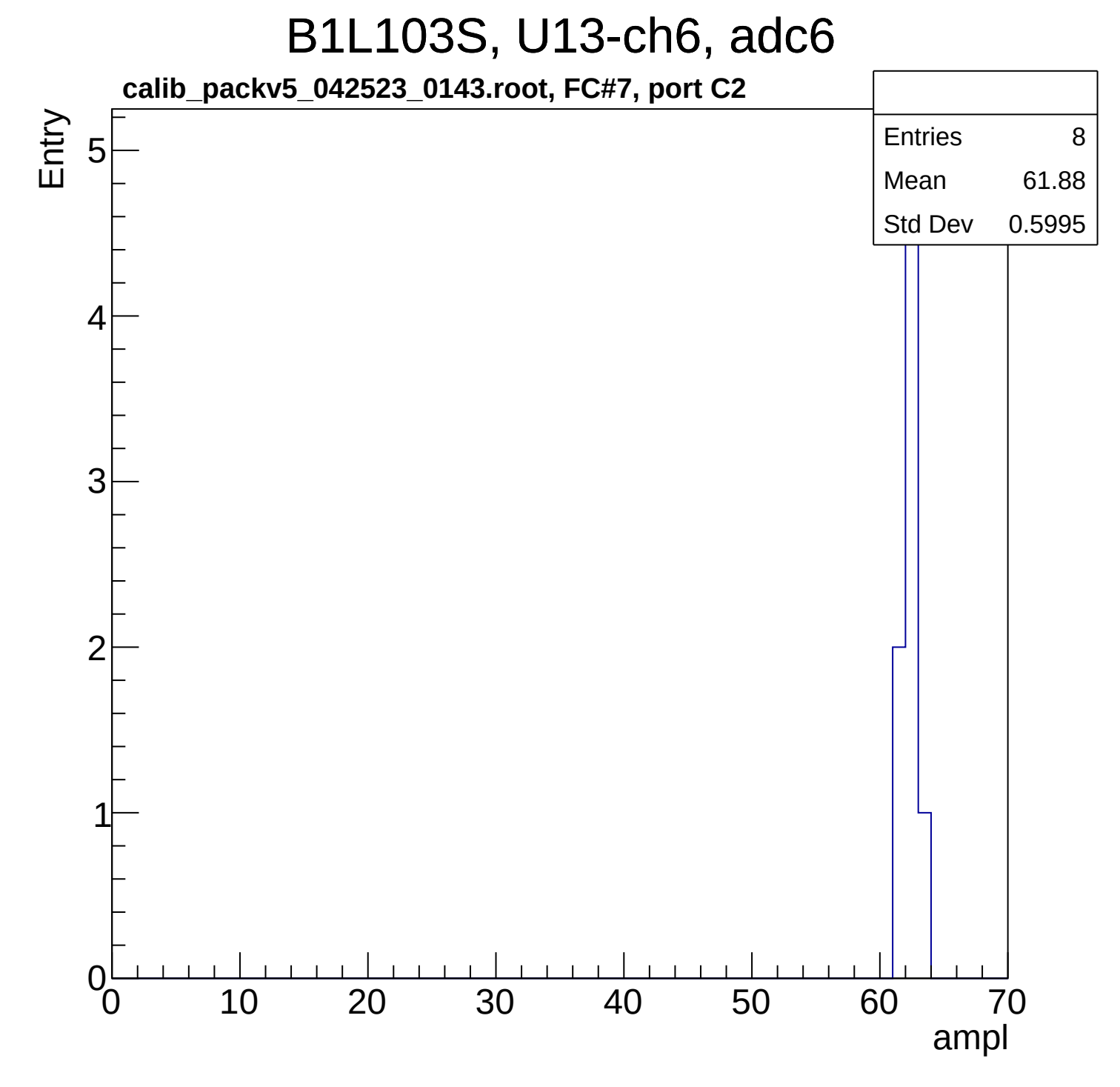
B1L103S, U13-ch6, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.88
Std Dev 0.5995
Entry
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2
3
4
5
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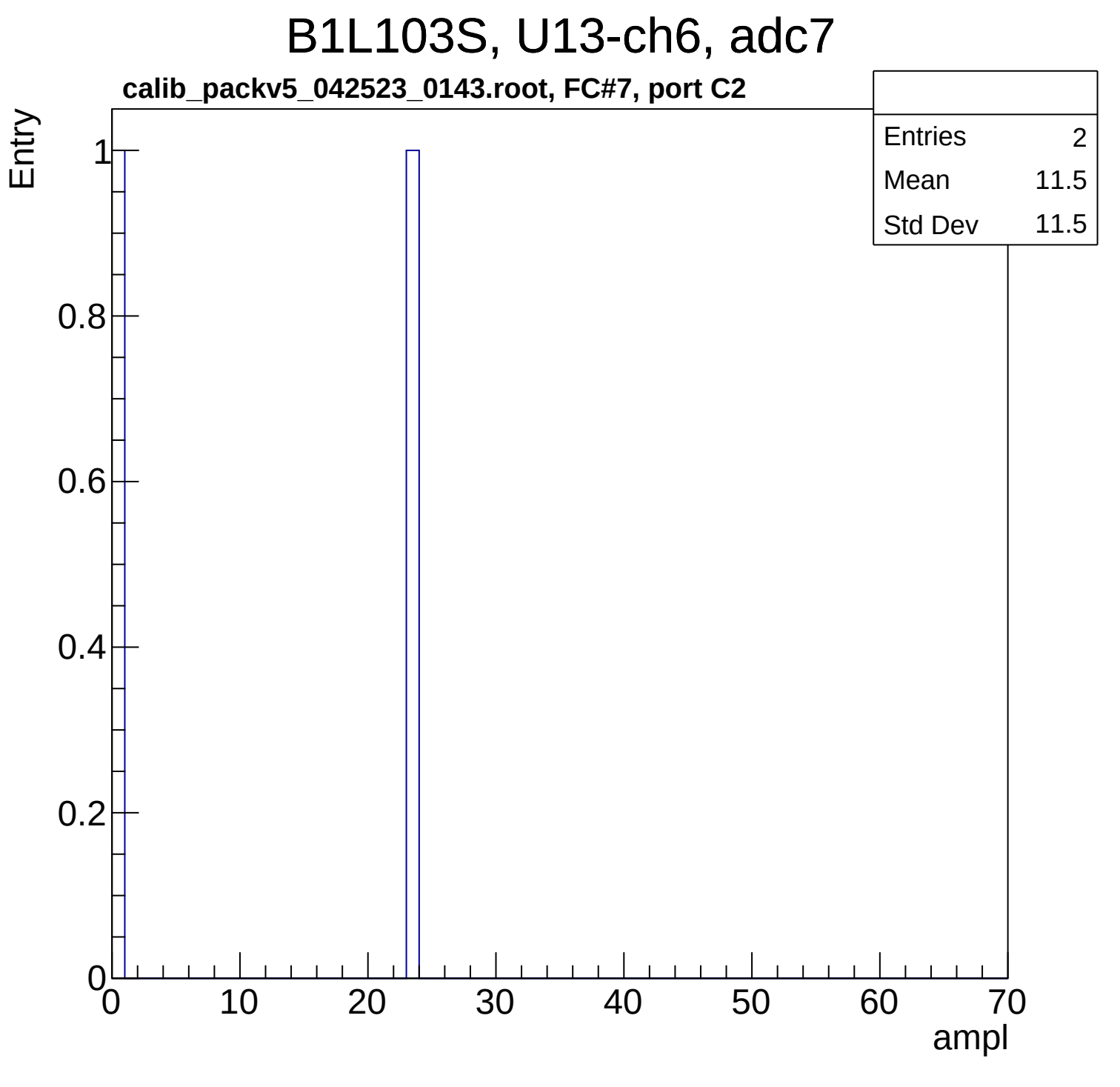
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch6, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
0
0
10
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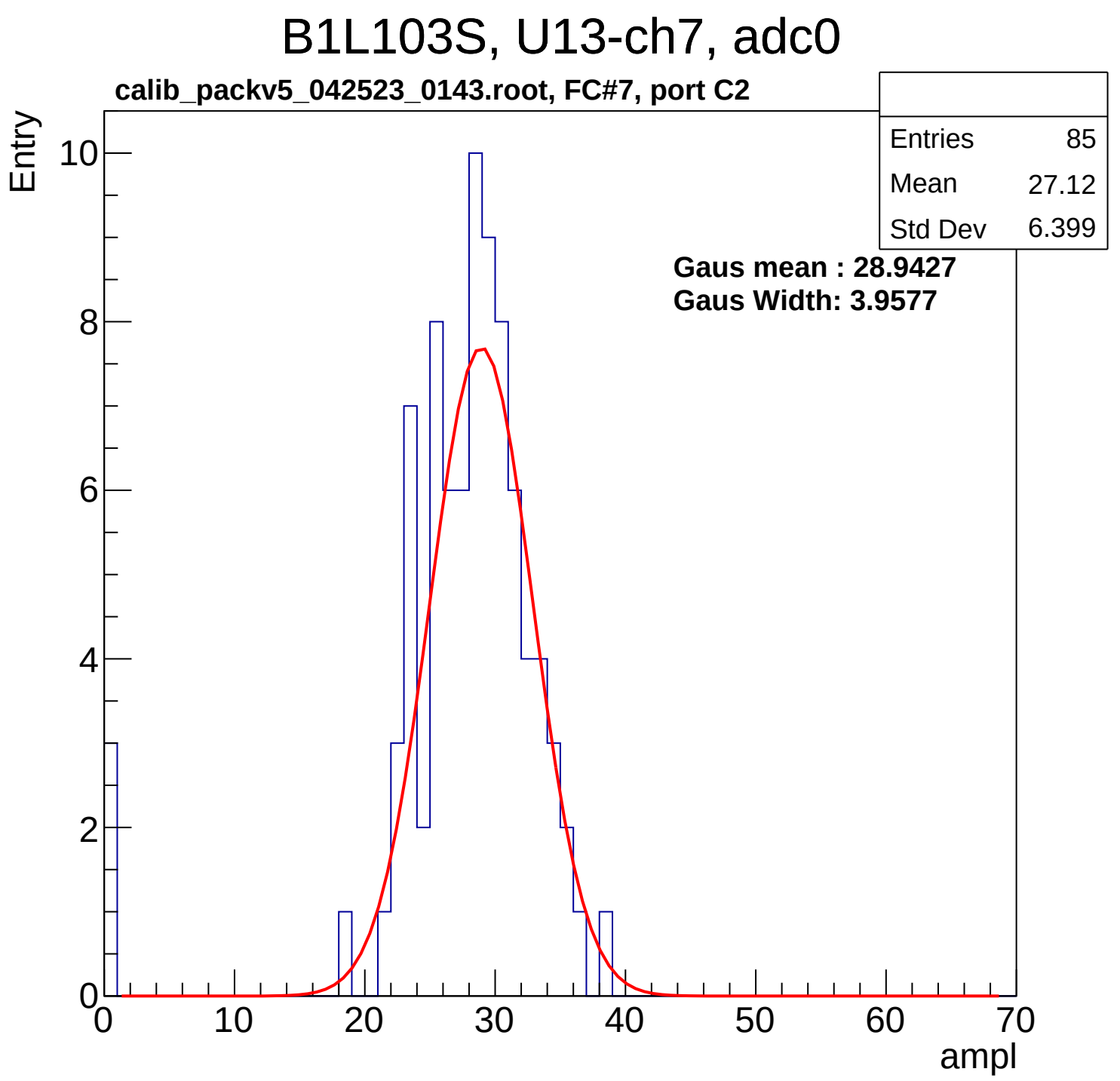
60
70
ampl


B1L103S, U13-ch7, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 27.12
Std Dev 6.399
Gaus mean : 28.9427
Gaus Width: 3.9577
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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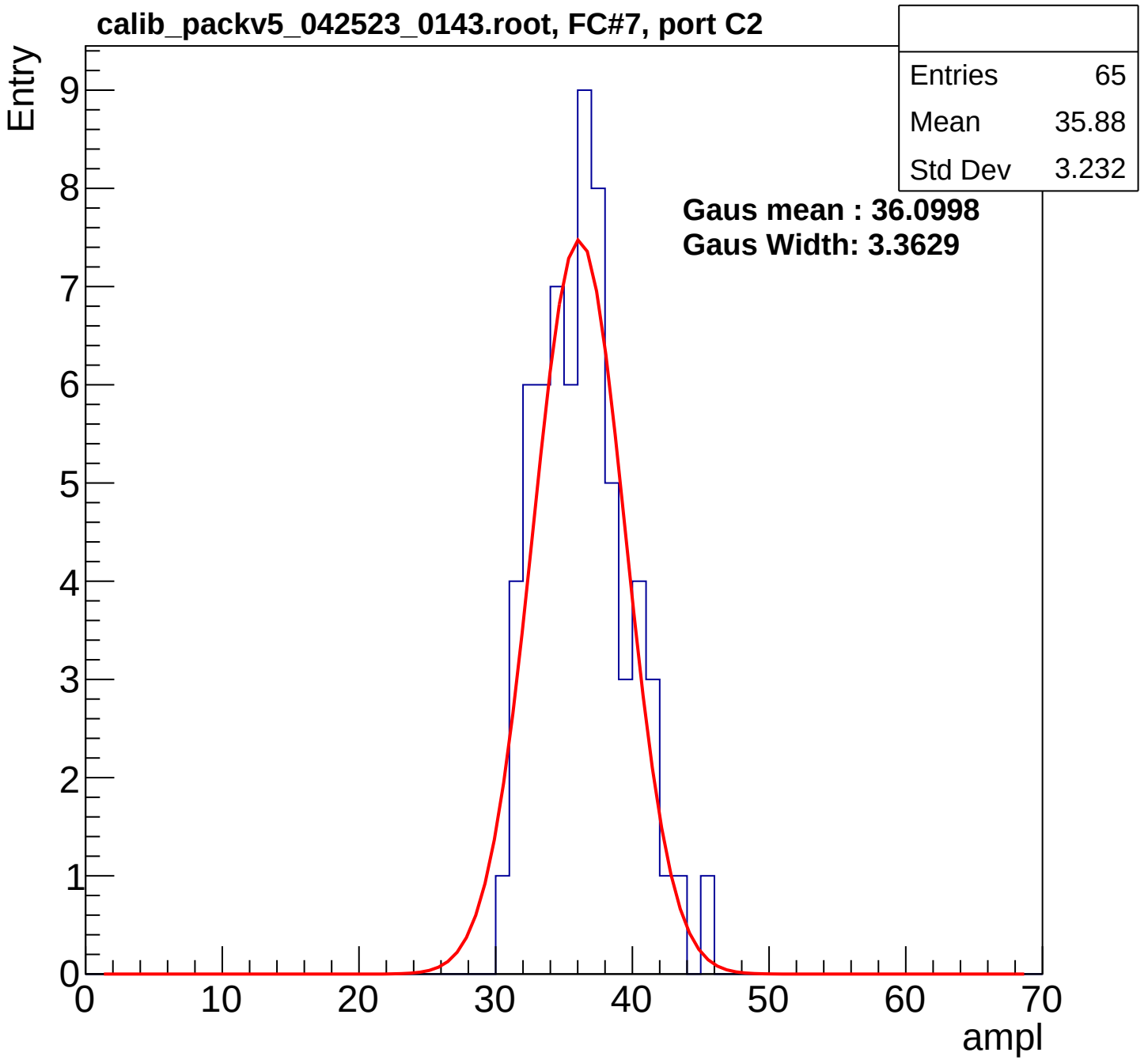
B1L103S, U13-ch7, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.88
Std Dev 3.232
Gaus mean : 36.0998
Gaus Width: 3.3629
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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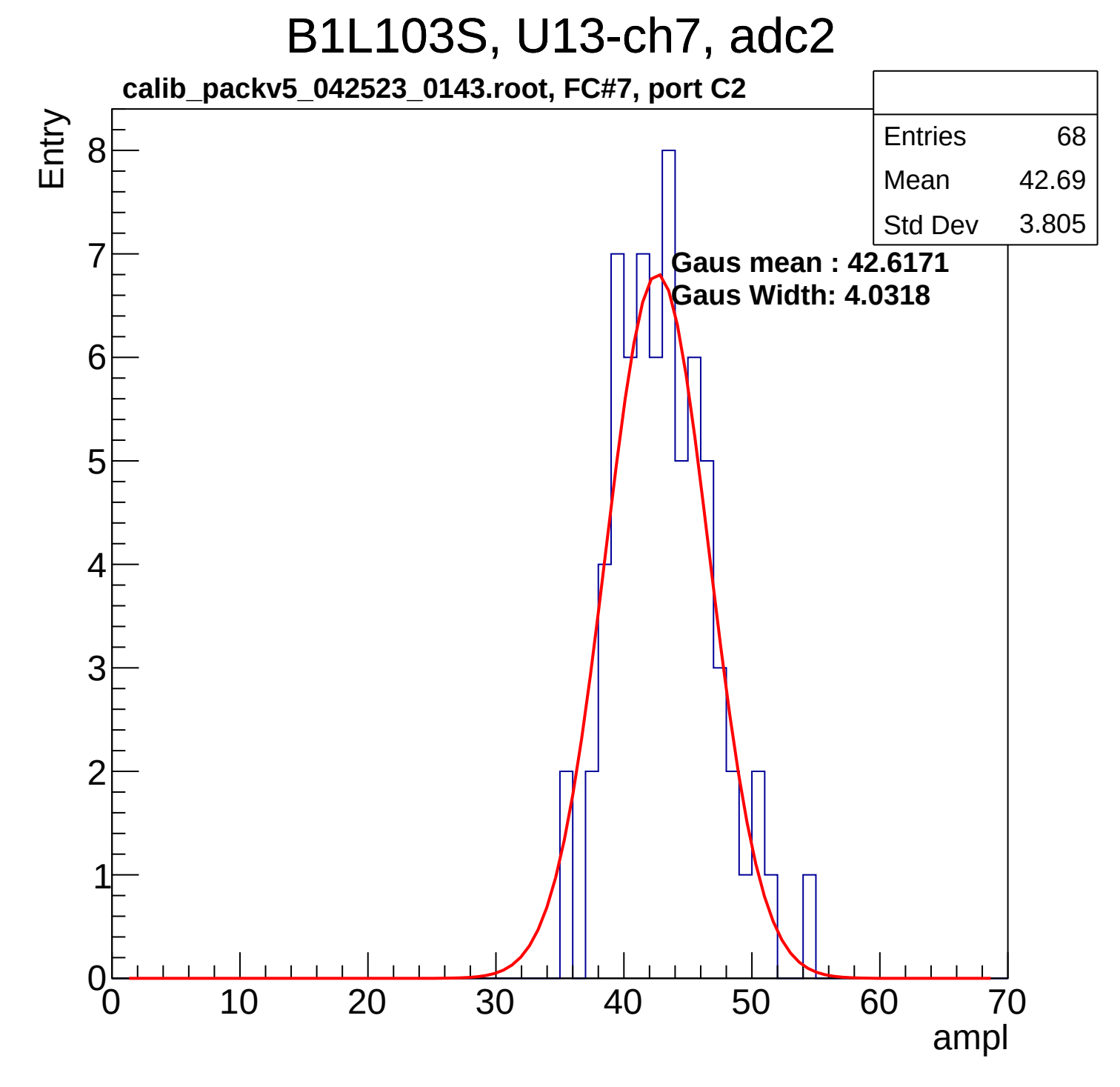
B1L103S, U13-ch7, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 42.69
Std Dev 3.805
Gaus mean : 42.6171
Gaus Width: 4.0318
Entry
ampl
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1
2
3
4
5
6
7
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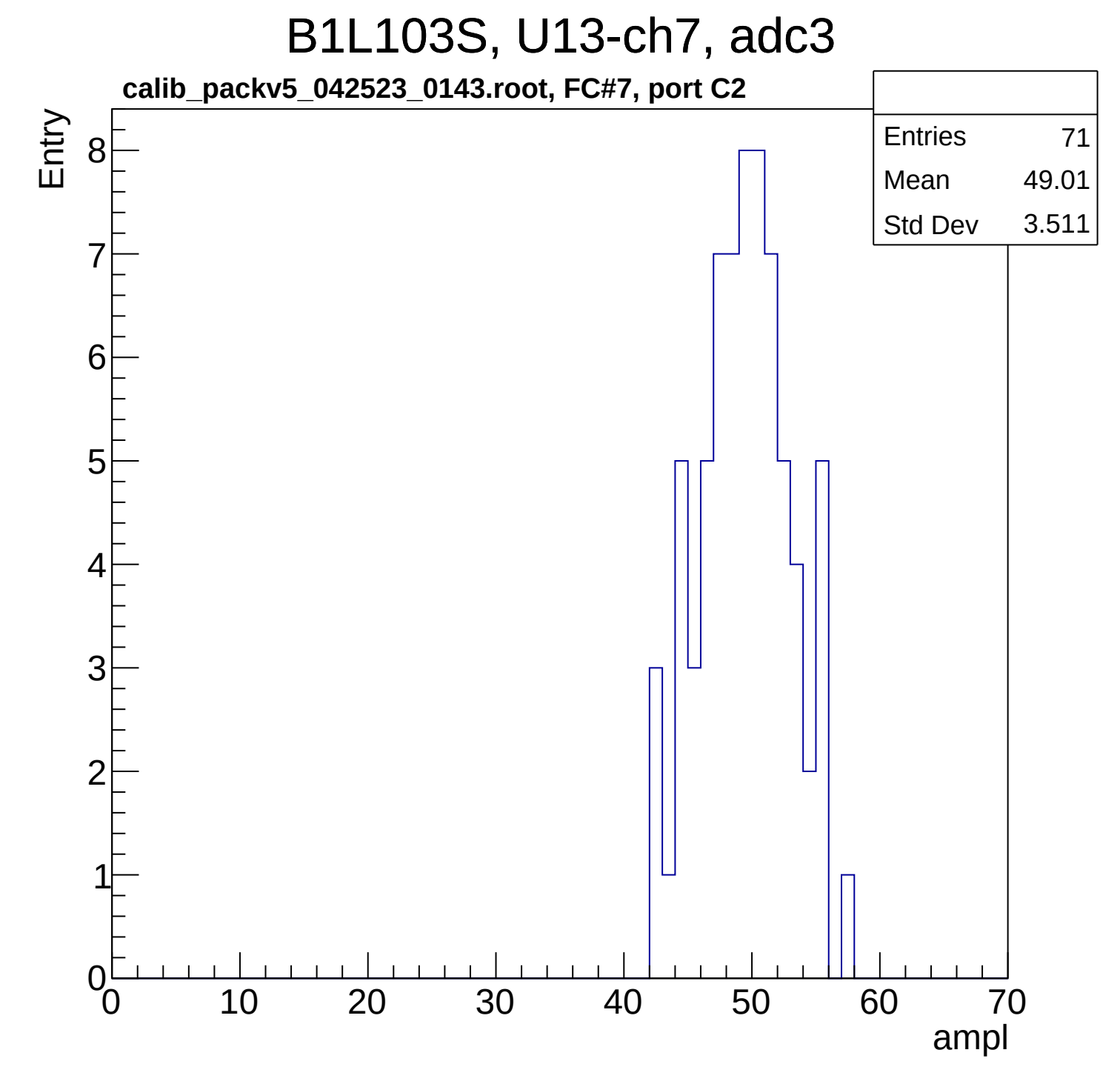
B1L103S, U13-ch7, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.01
Std Dev 3.511
Entry
ampl
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4
5
6
7
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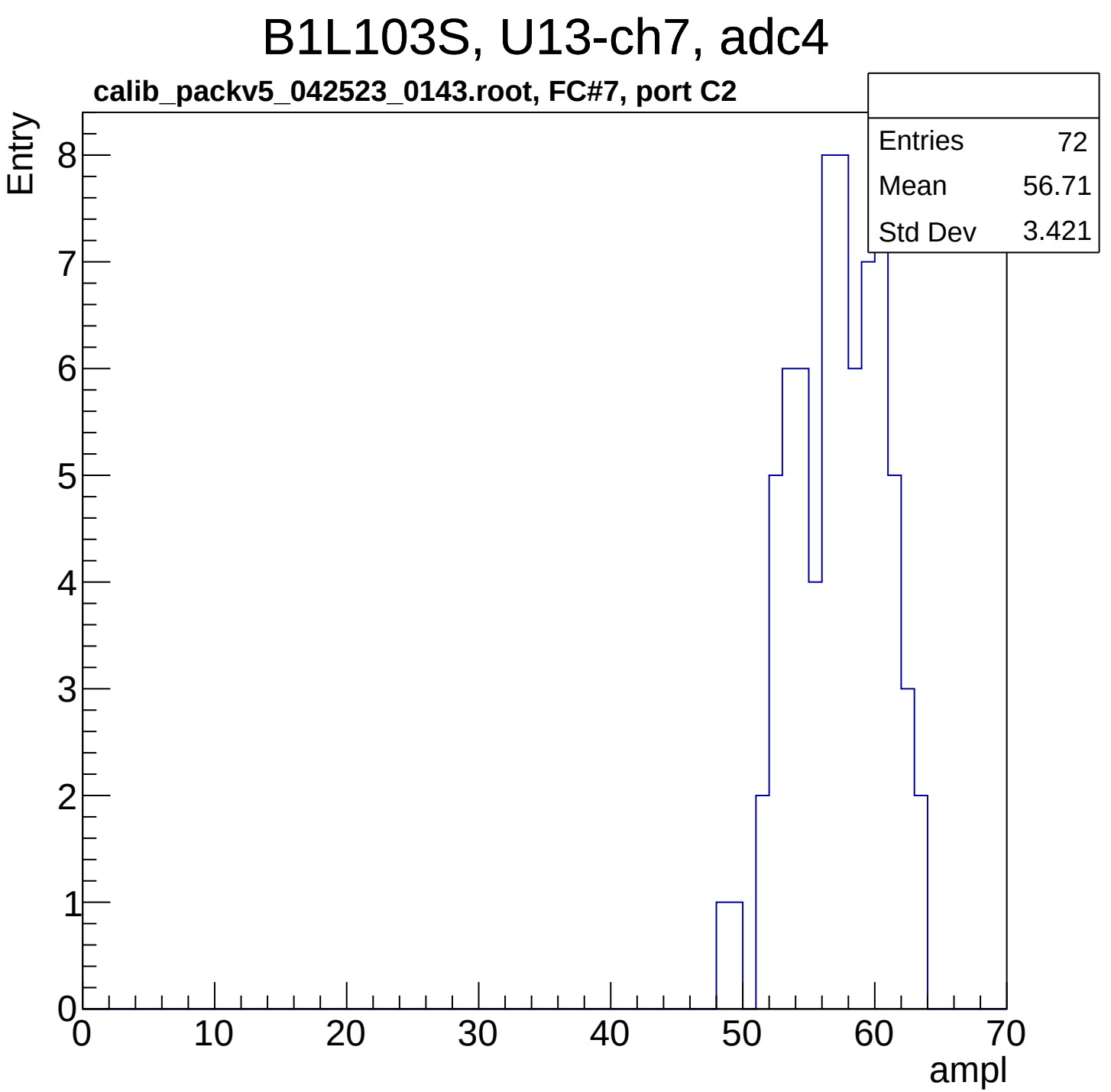
B1L103S, U13-ch7, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 56.71
Std Dev 3.421
Entry
ampl
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7
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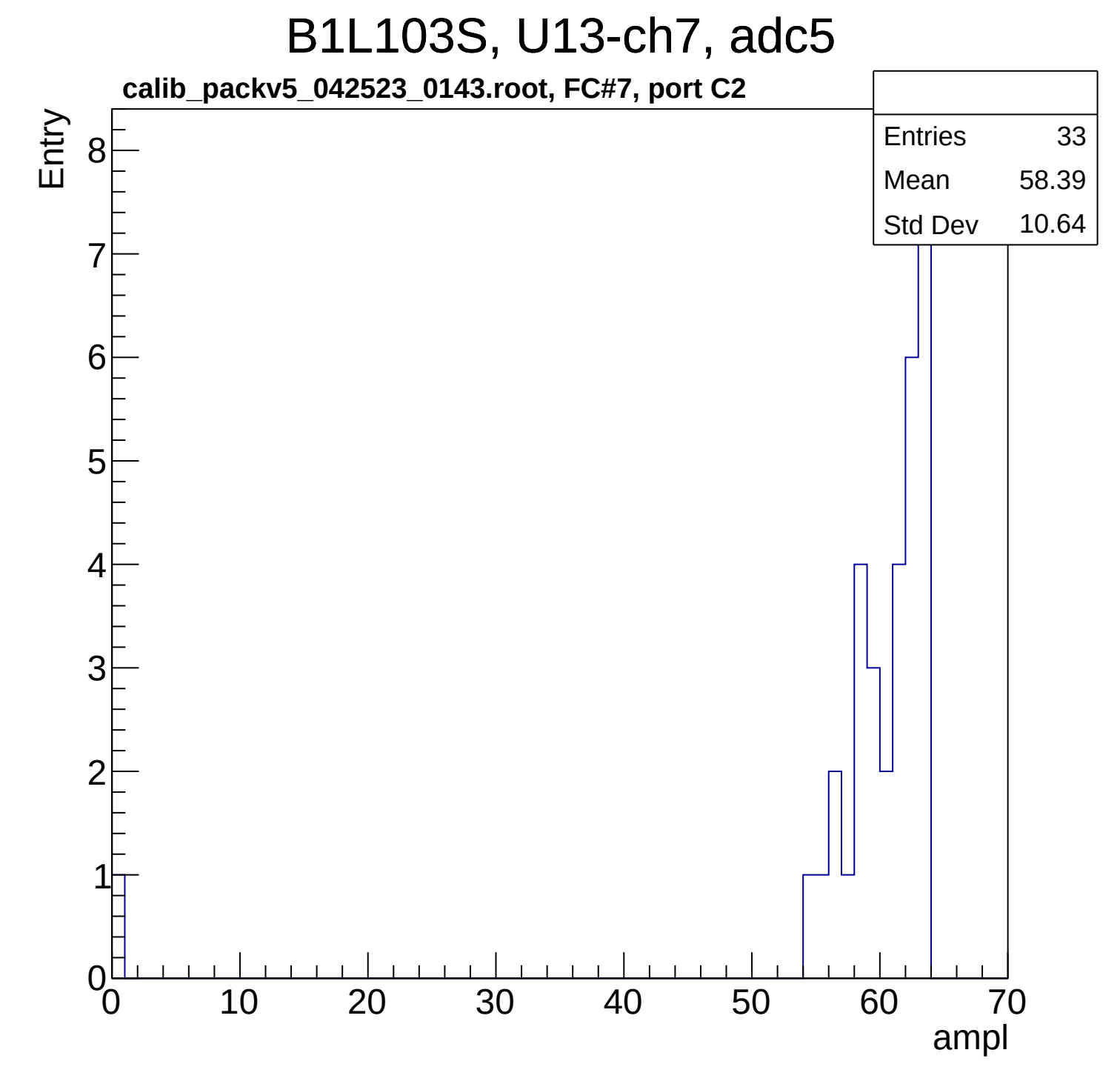
B1L103S, U13-ch7, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.39
Std Dev 10.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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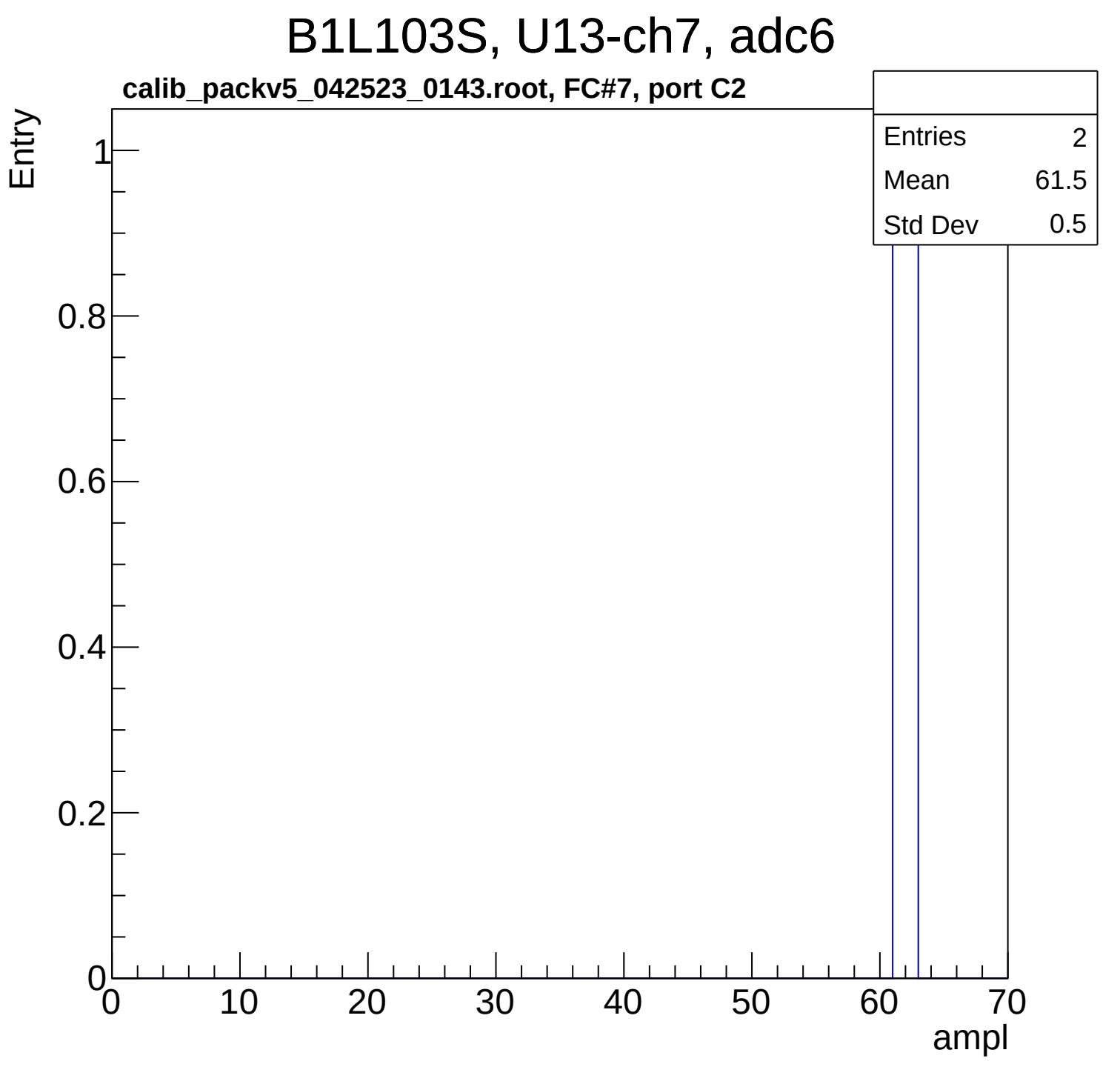
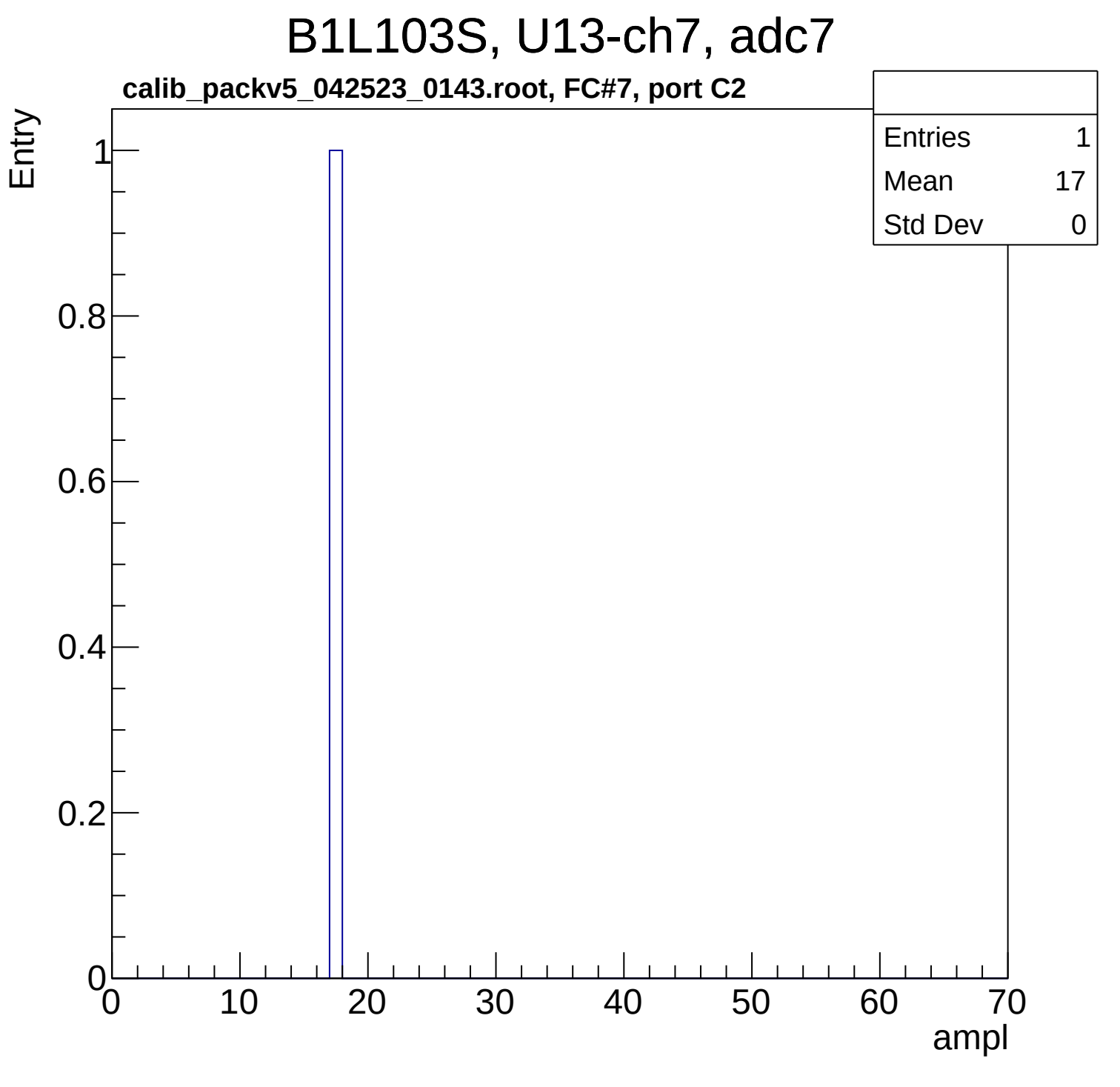
40
50
60
70


B1L103S, U13-ch7, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch7, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 17
Std Dev 0
Entry
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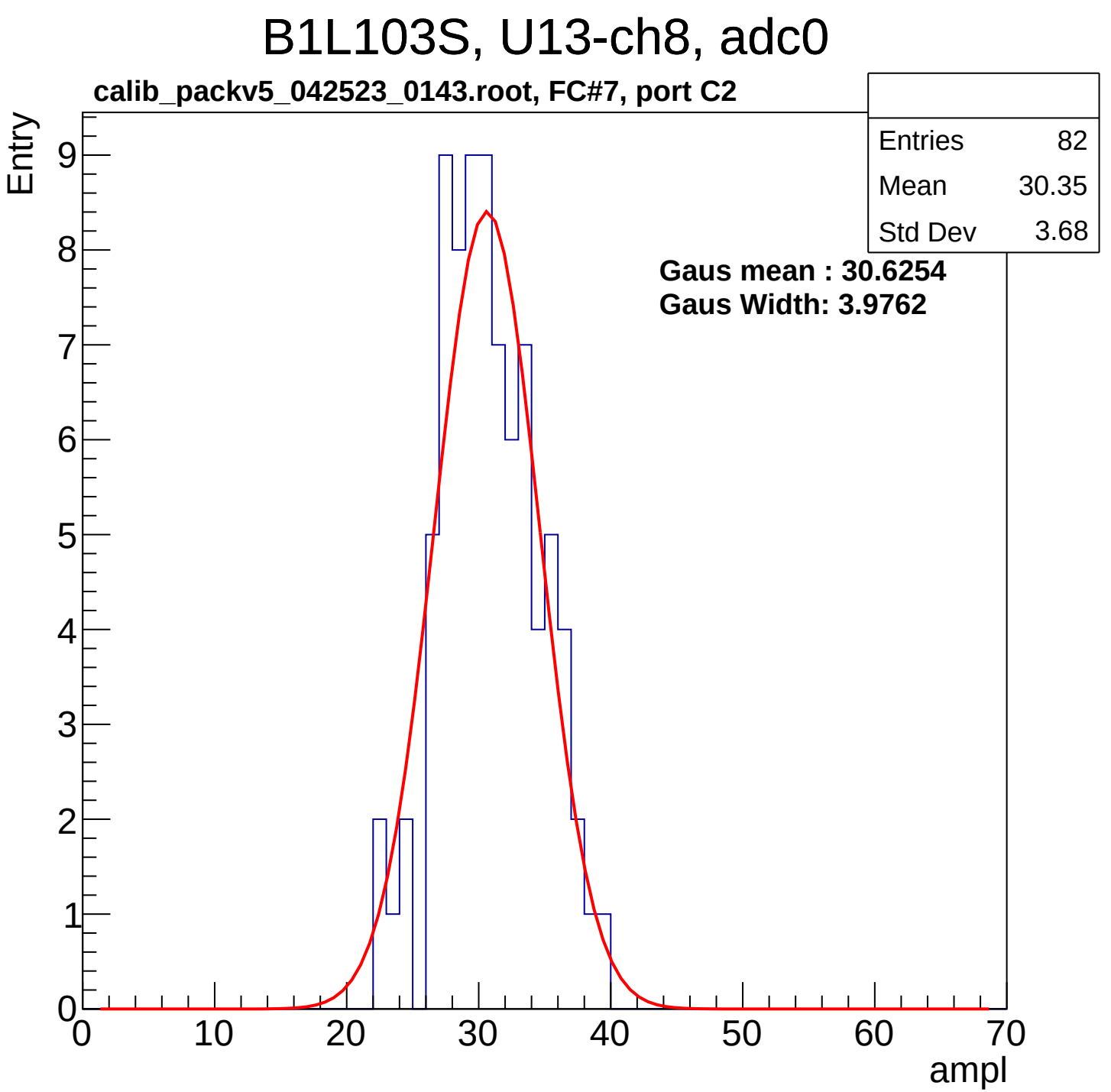
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch8, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 30.35
Std Dev 3.68
Gaus mean : 30.6254
Gaus Width: 3.9762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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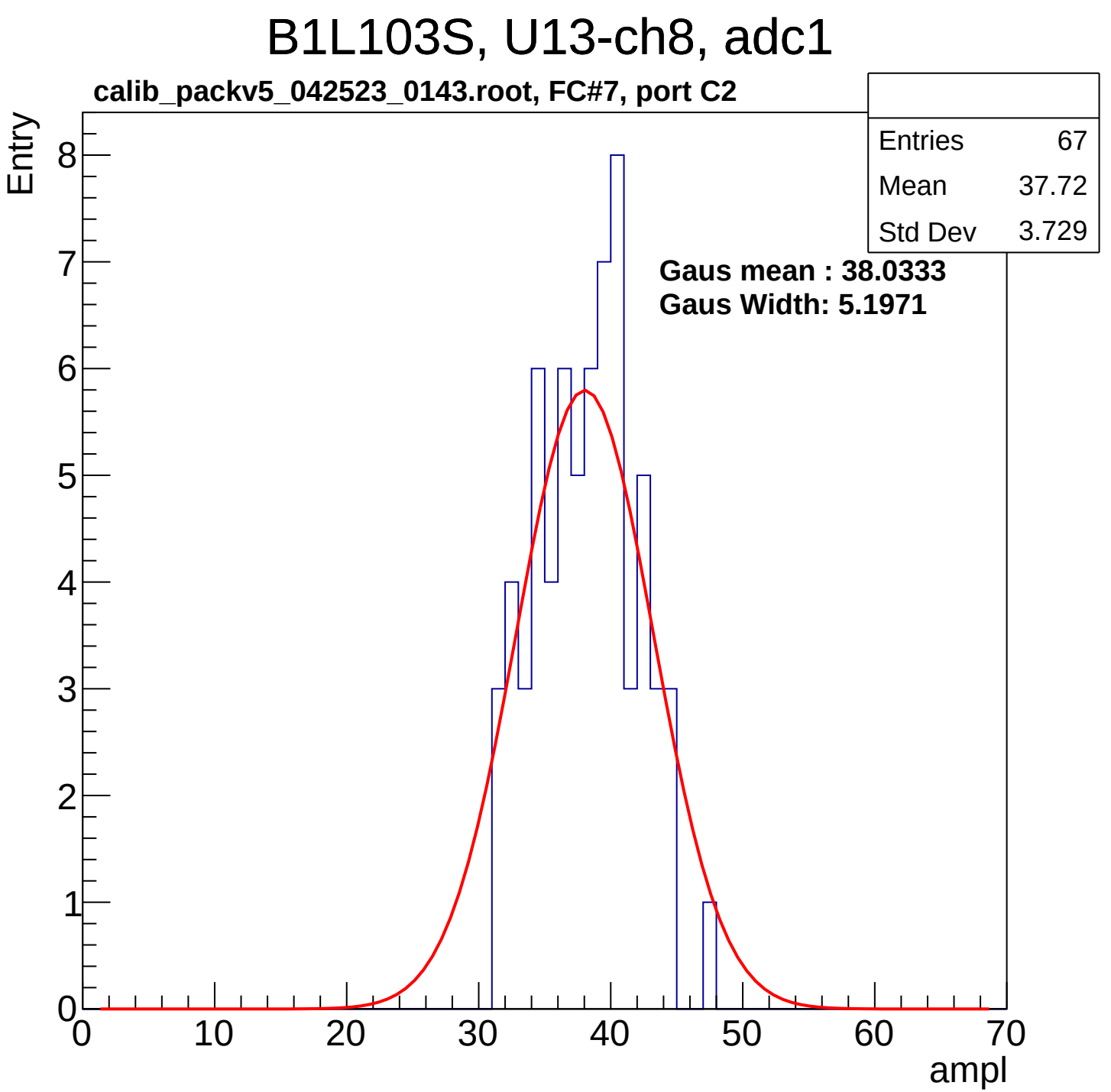
B1L103S, U13-ch8, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.72
Std Dev 3.729
Gaus mean : 38.0333
Gaus Width: 5.1971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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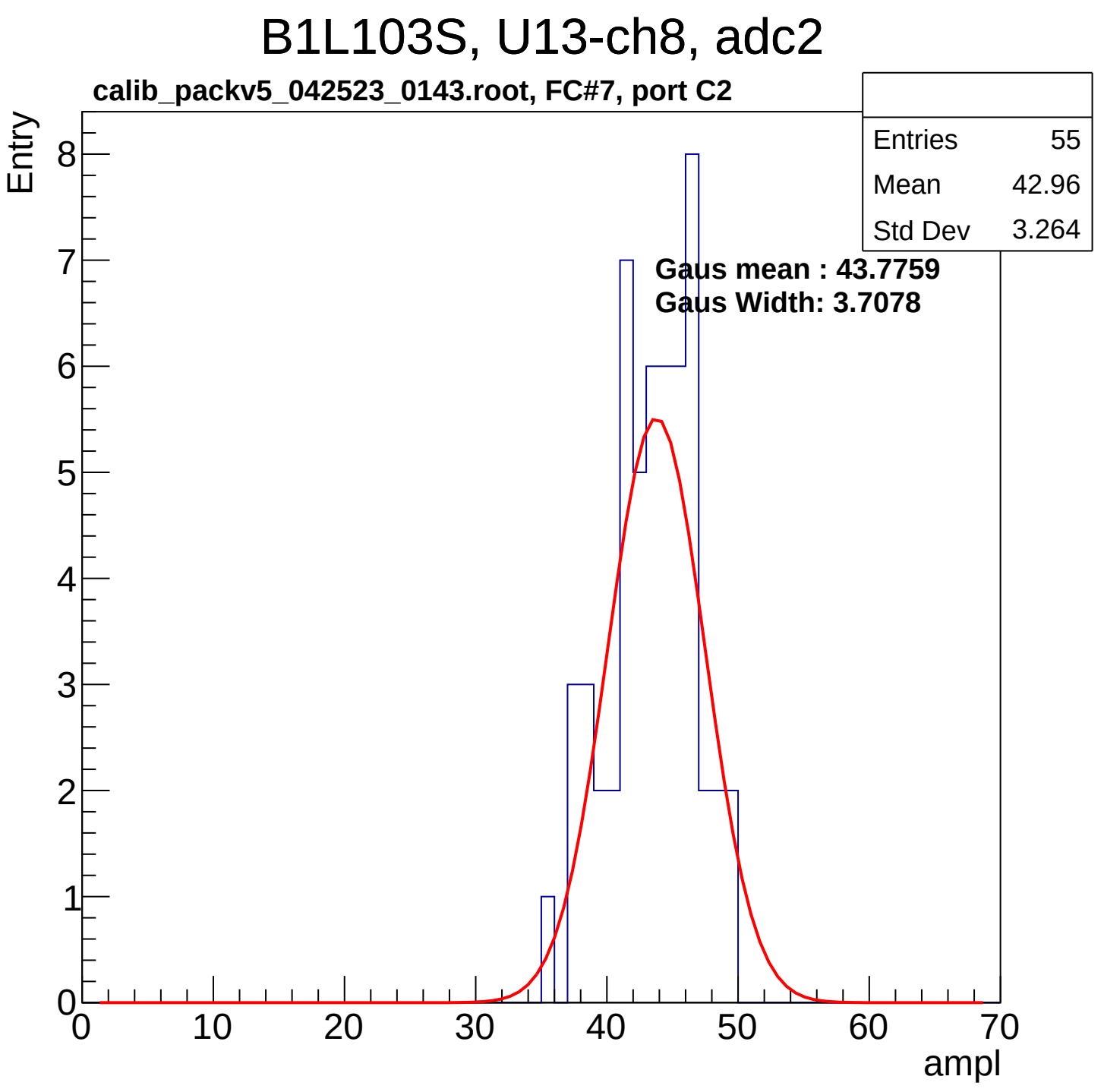
B1L103S, U13-ch8, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 42.96
Std Dev 3.264
Gaus mean : 43.7759
Gaus Width: 3.7078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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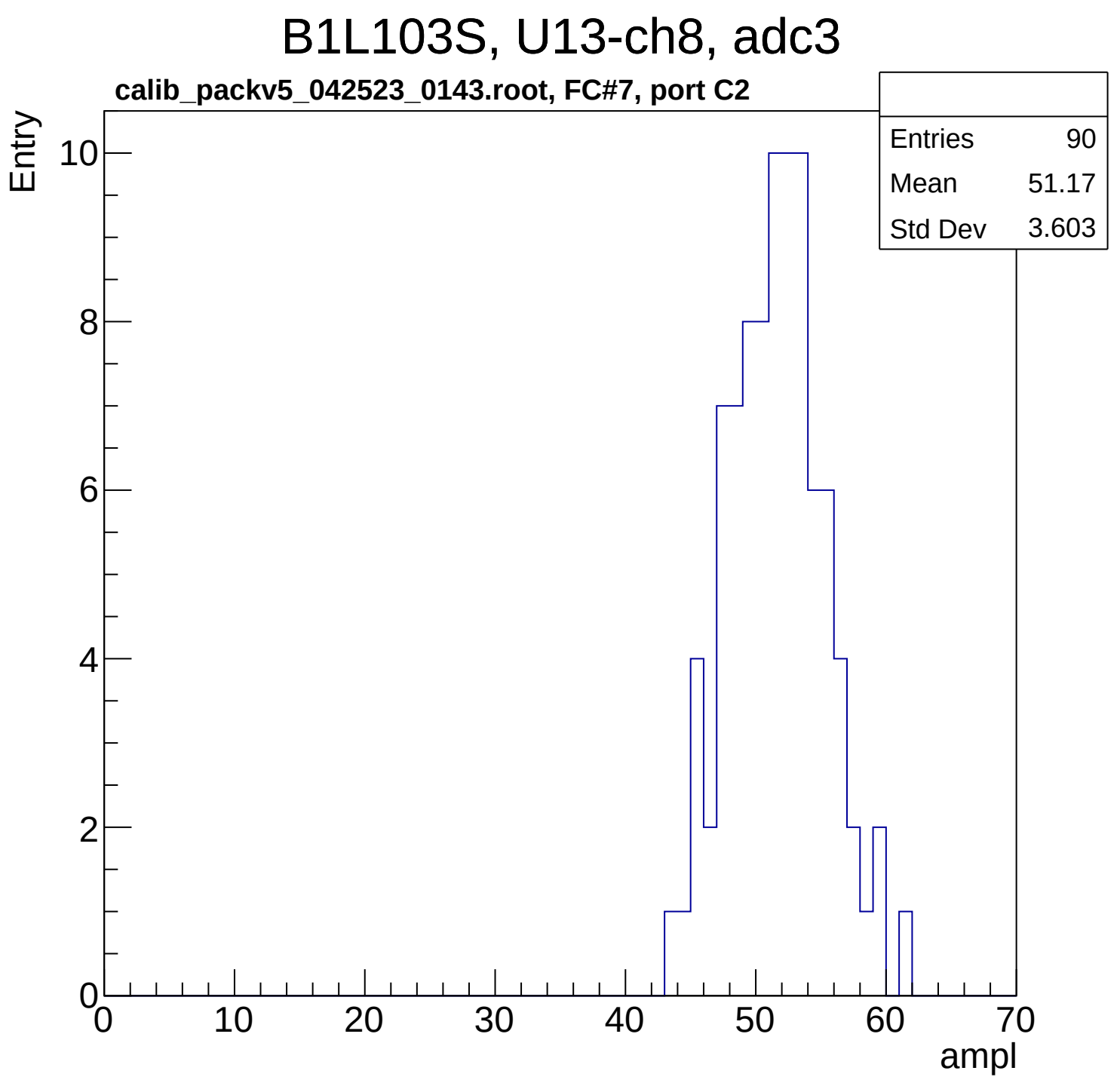
B1L103S, U13-ch8, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 51.17
Std Dev 3.603
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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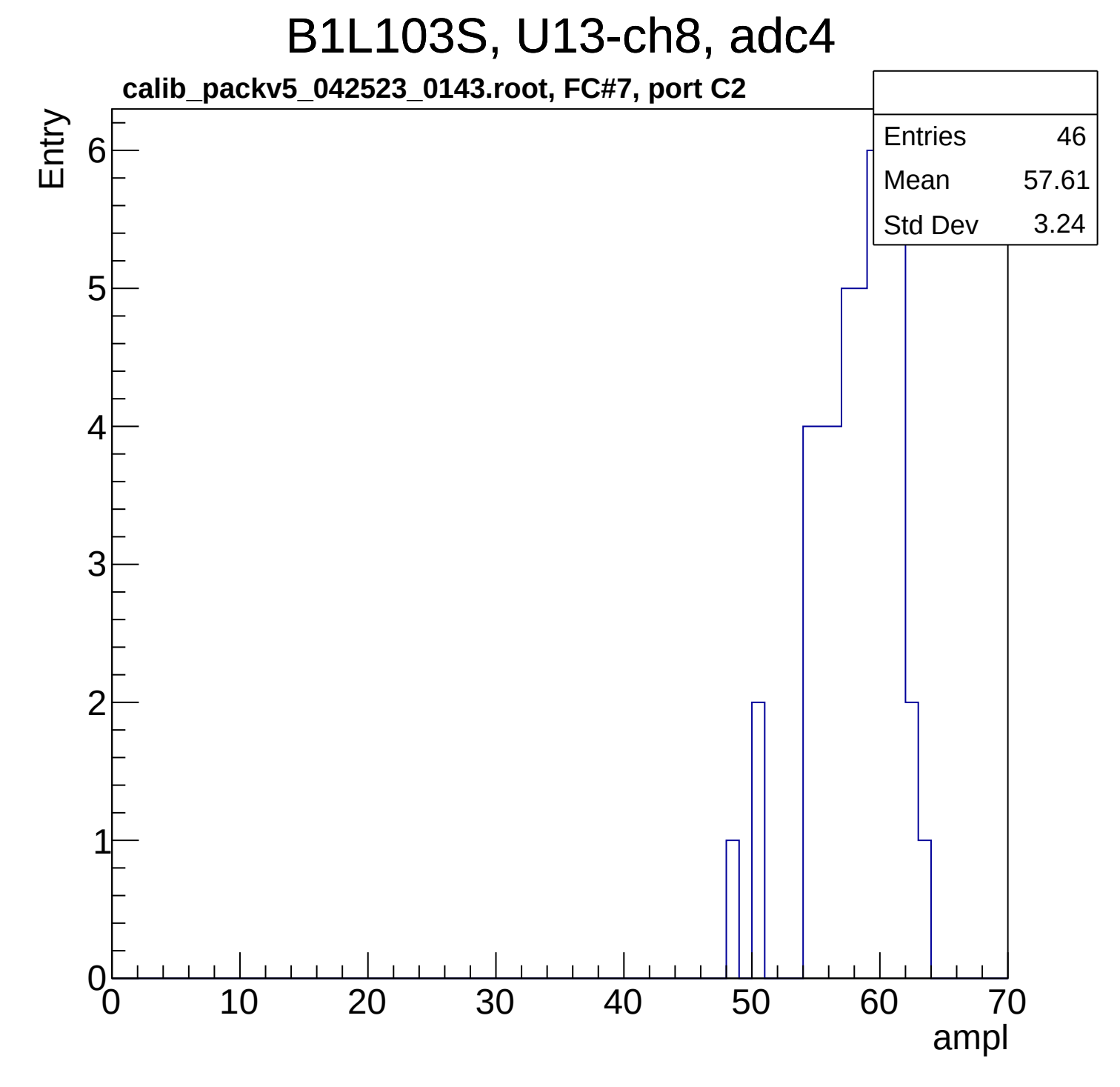
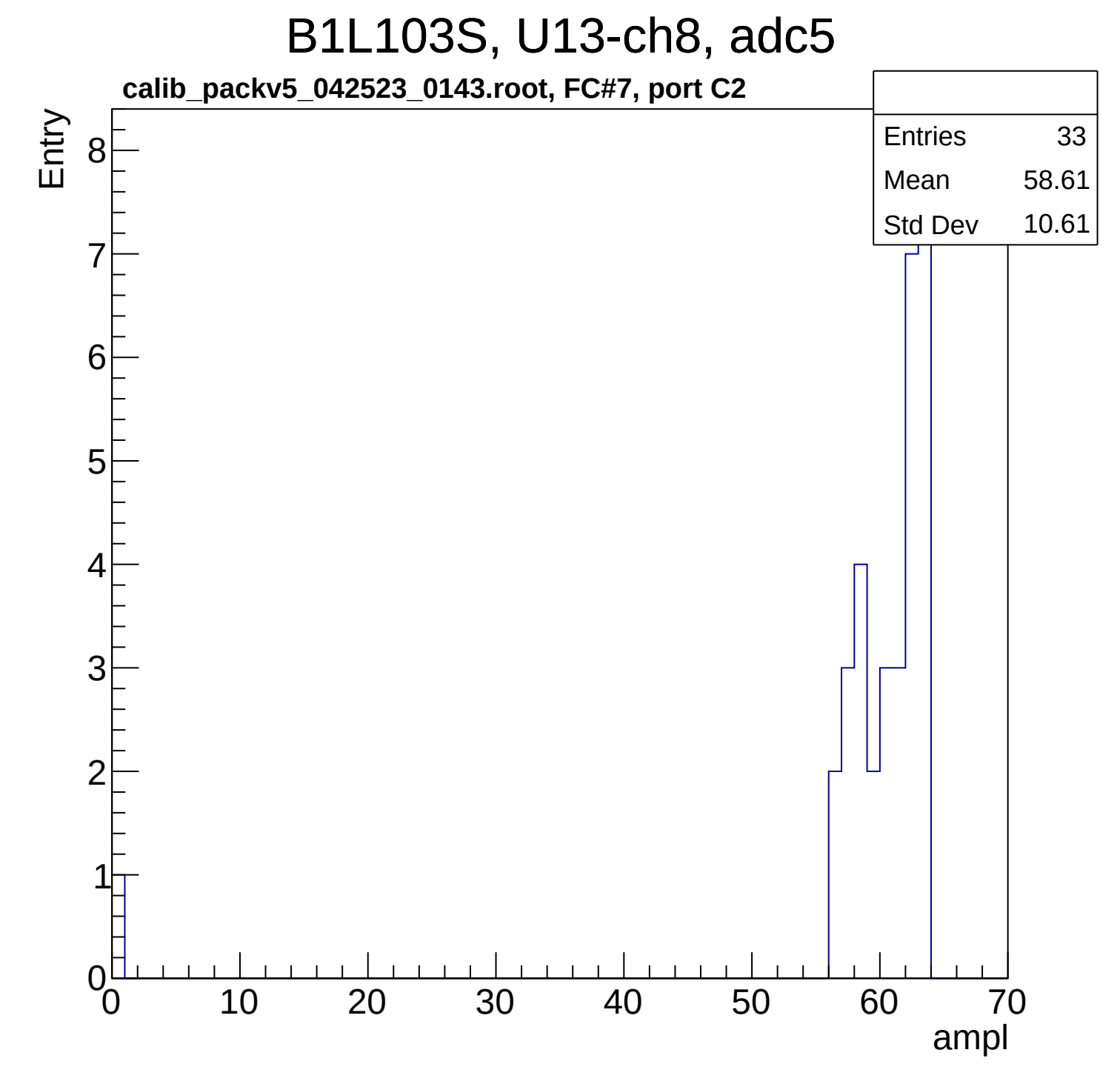

B1L103S, U13-ch8, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 57.61
Std Dev 3.24
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch8, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.61
Std Dev 10.61
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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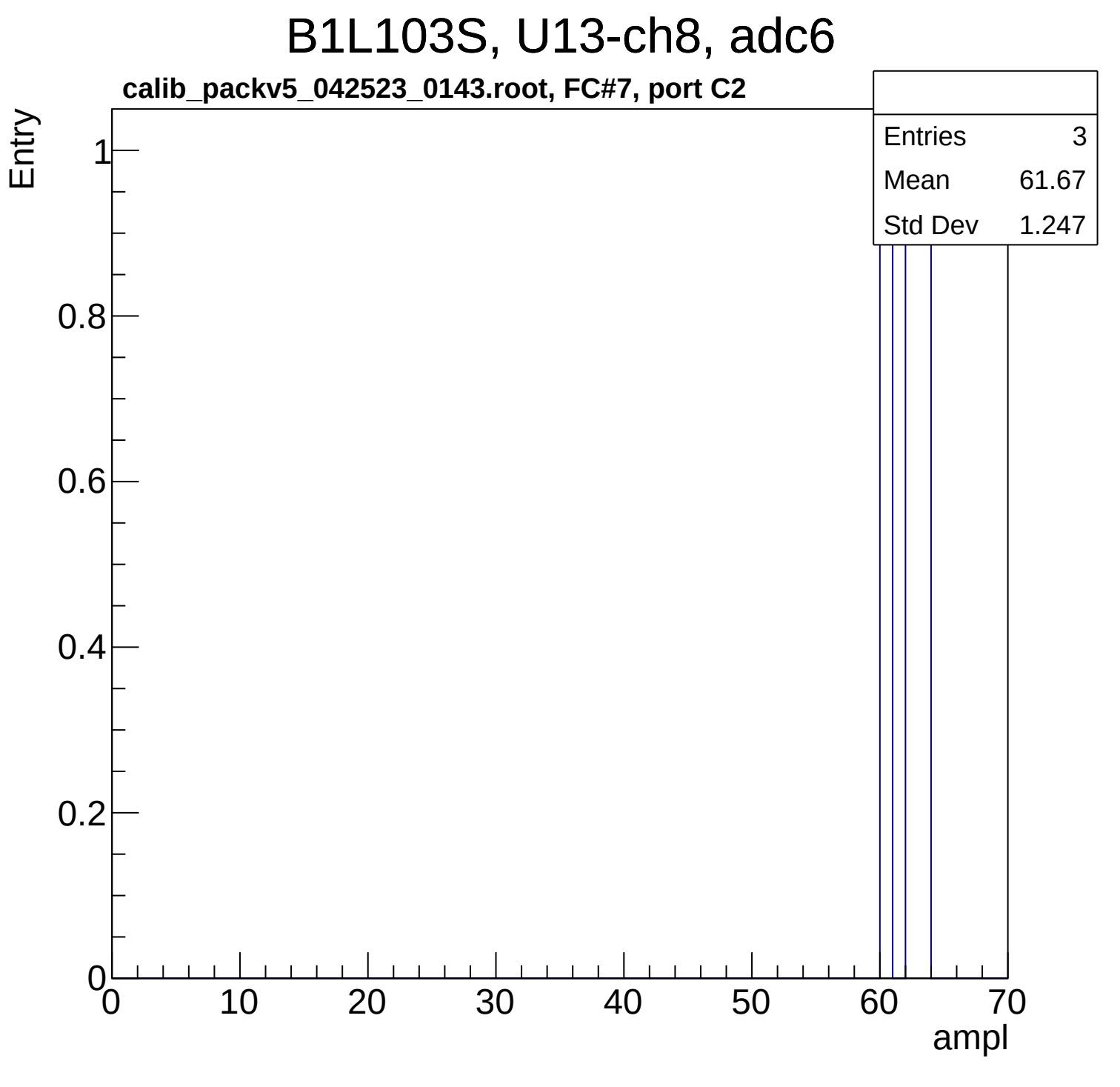
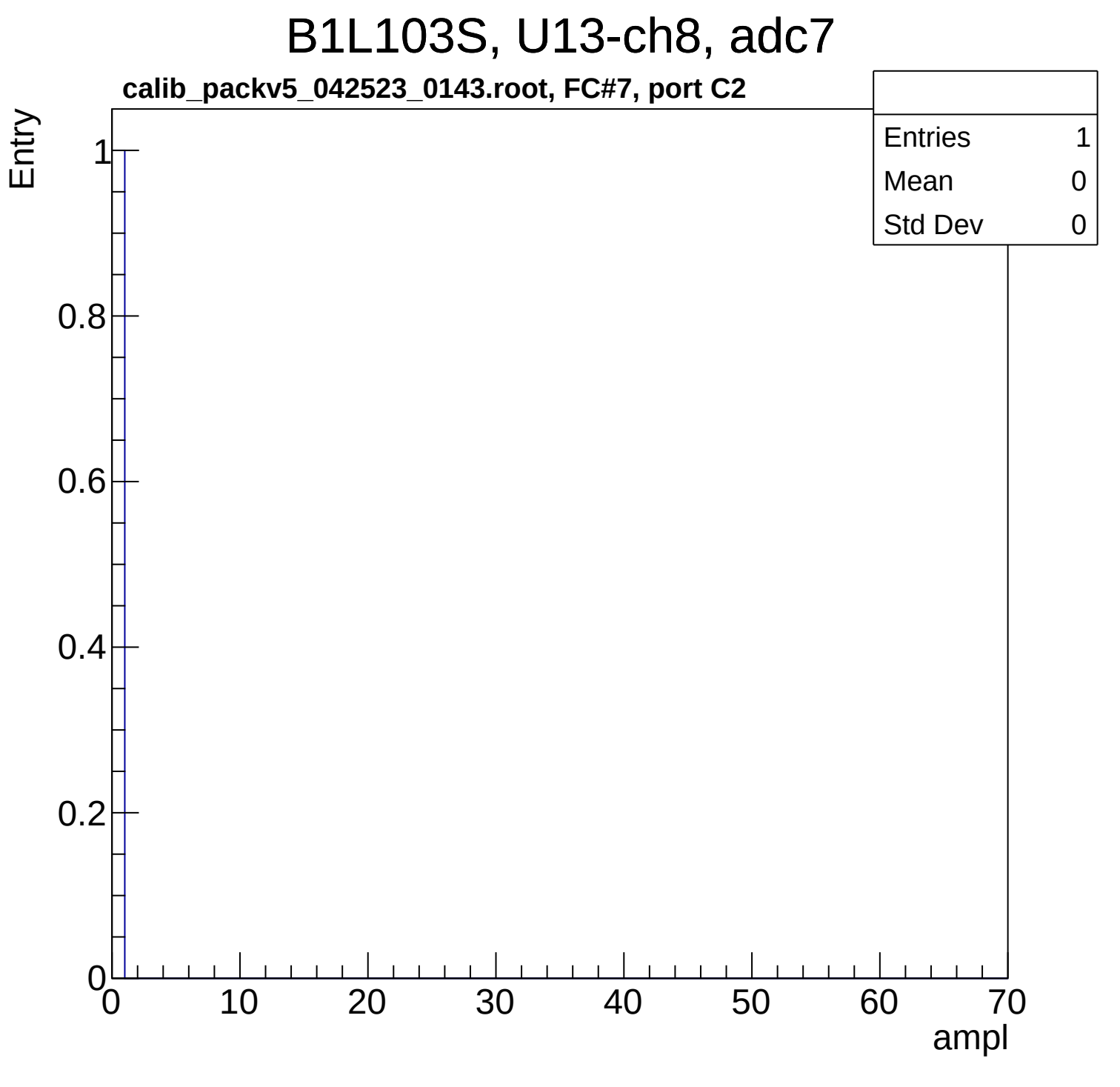
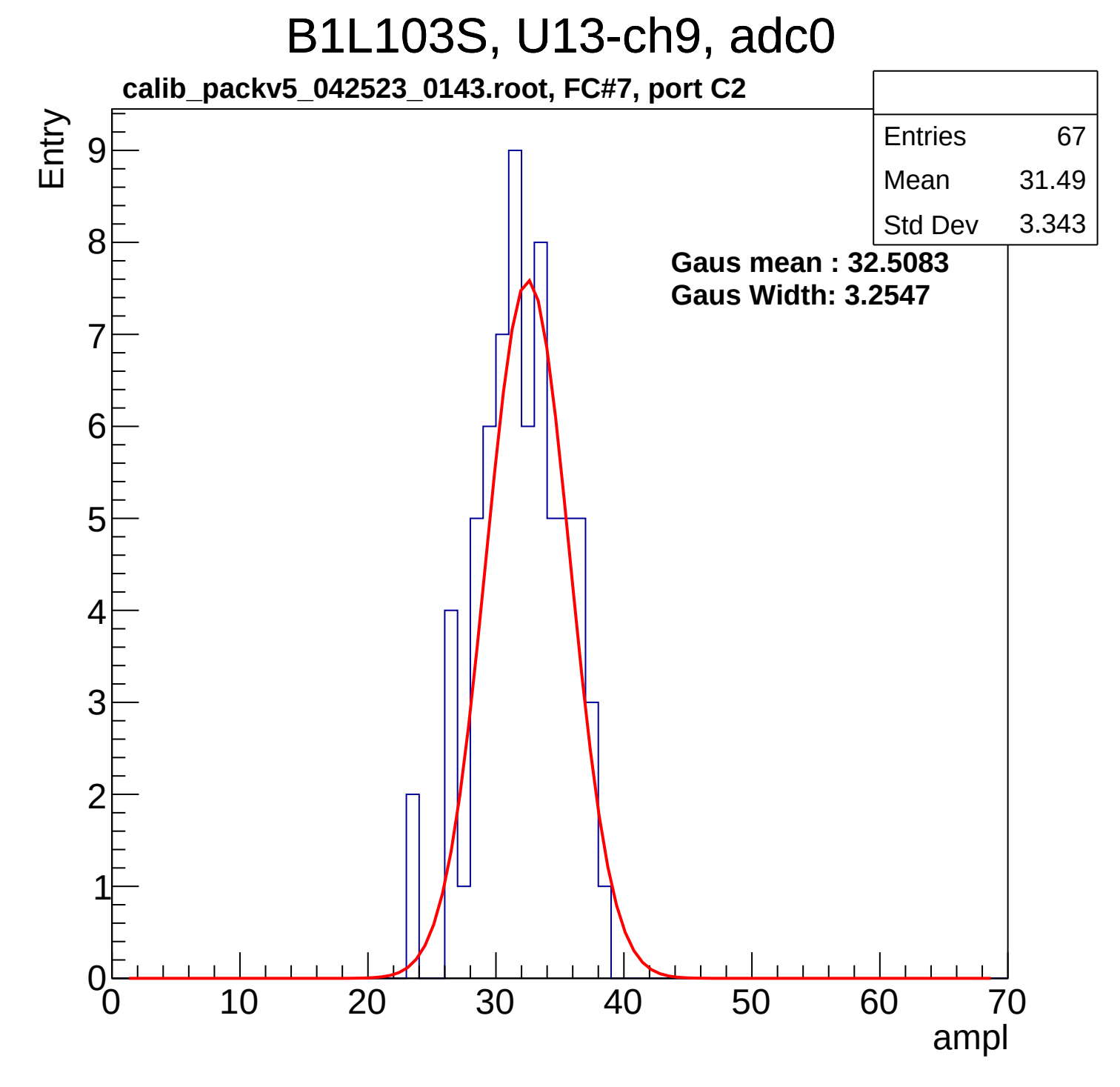
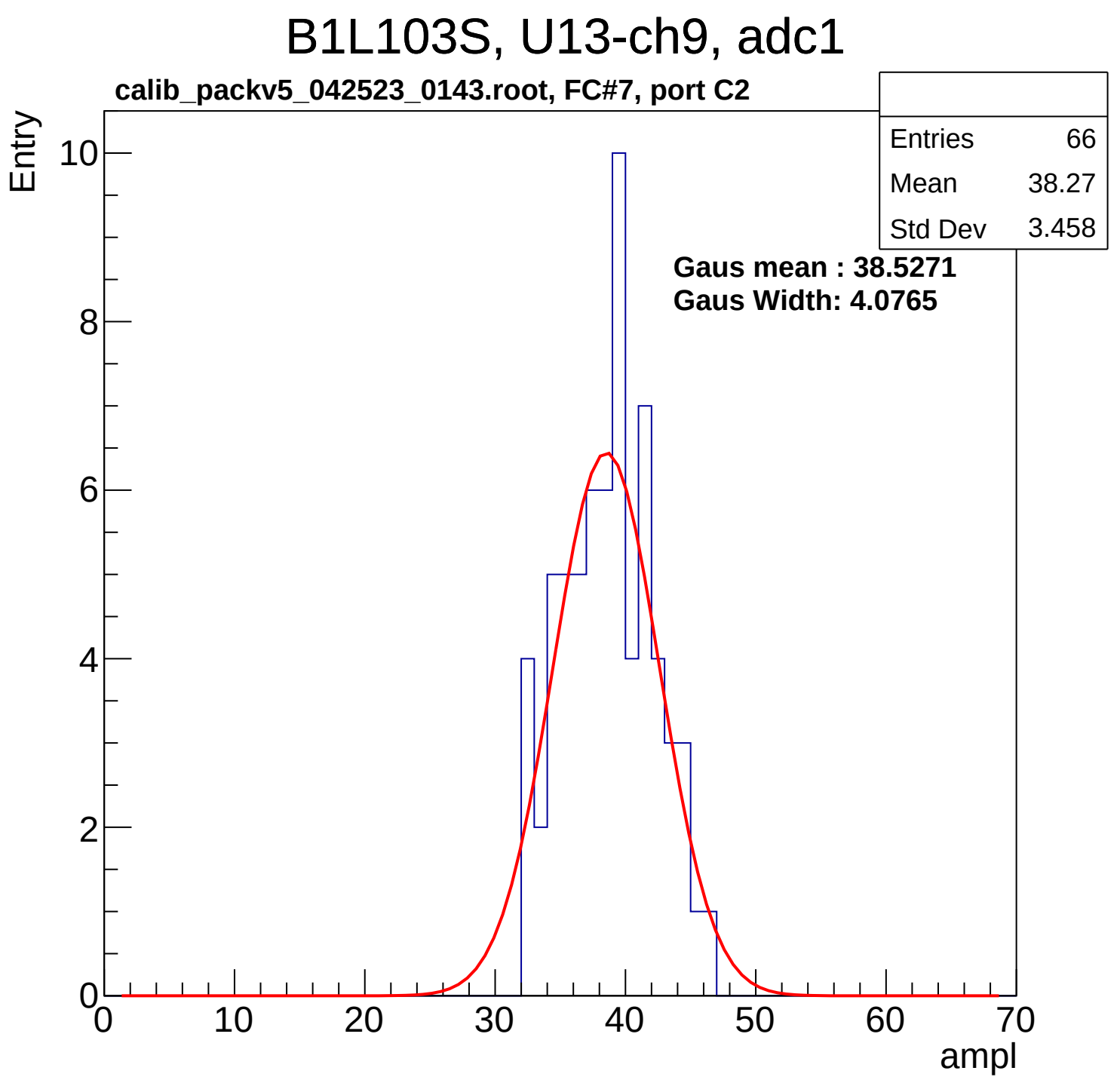
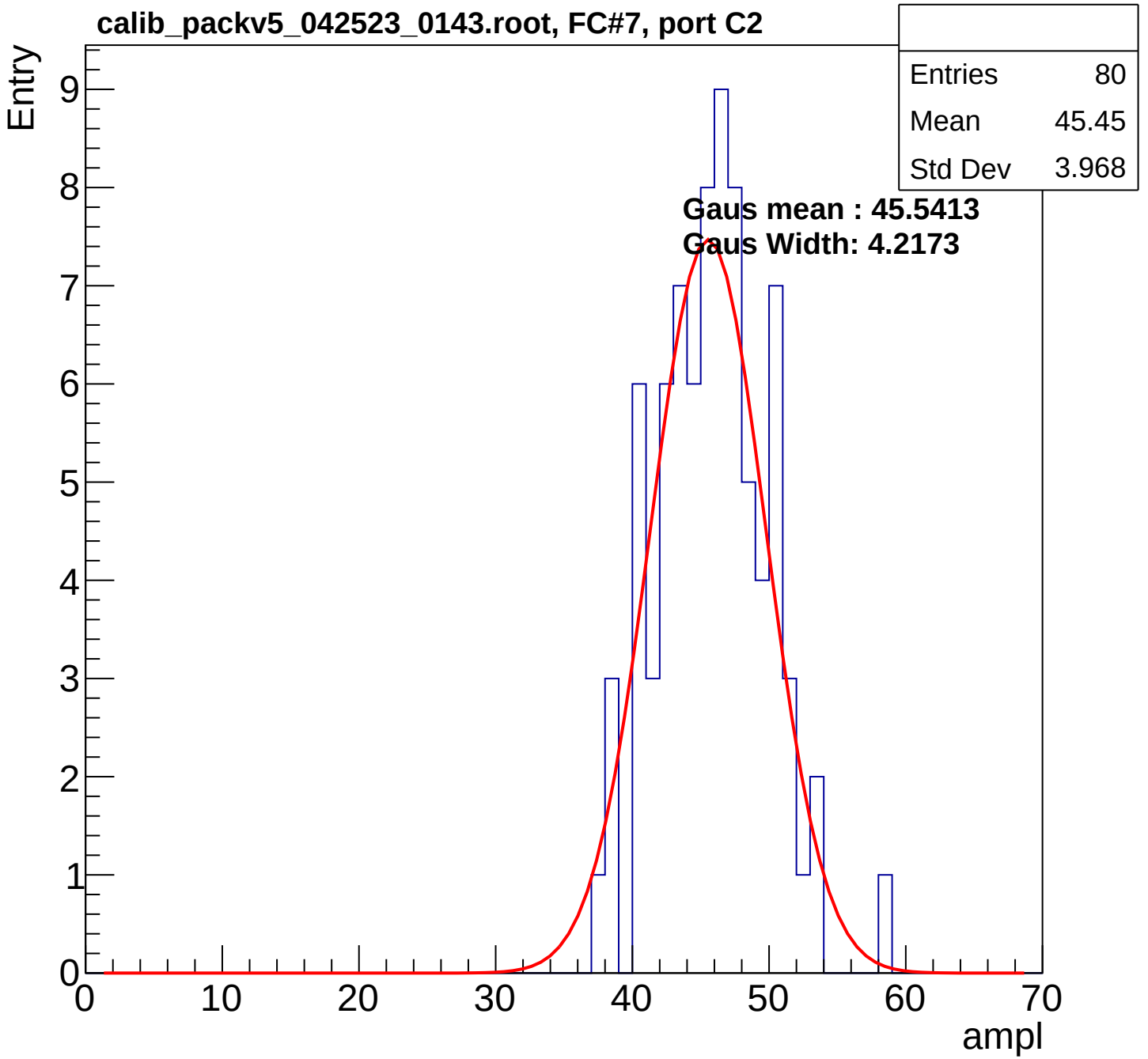

B1L103S, U13-ch8, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch8, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch9, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 31.49
Std Dev 3.343
Gaus mean : 32.5083
Gaus Width: 3.2547
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch9, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 38.27
Std Dev 3.458
Gaus mean : 38.5271
Gaus Width: 4.0765
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch9, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 45.45
Std Dev 3.968
Gaus mean : 45.5413
Gaus Width: 4.2173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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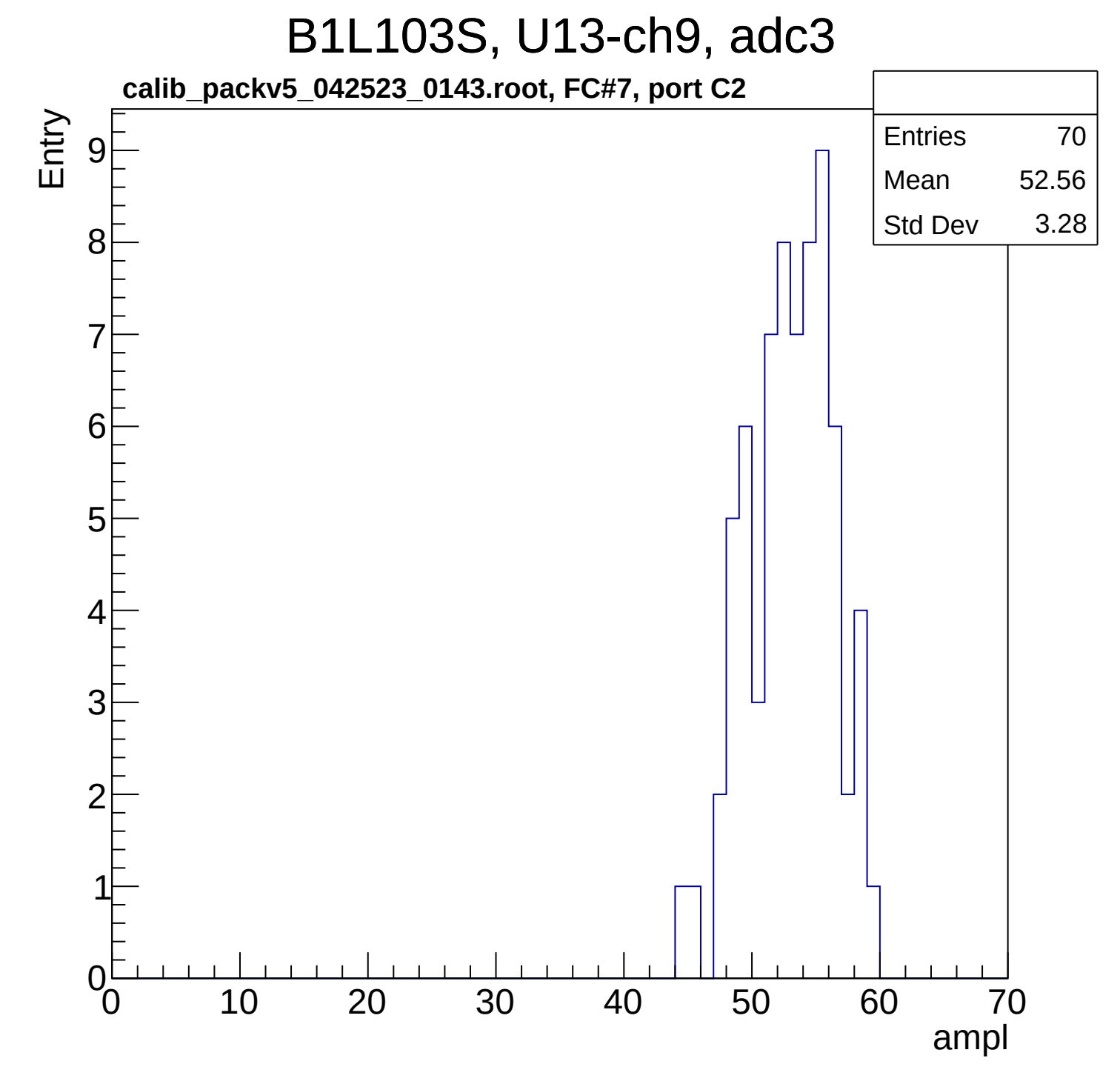
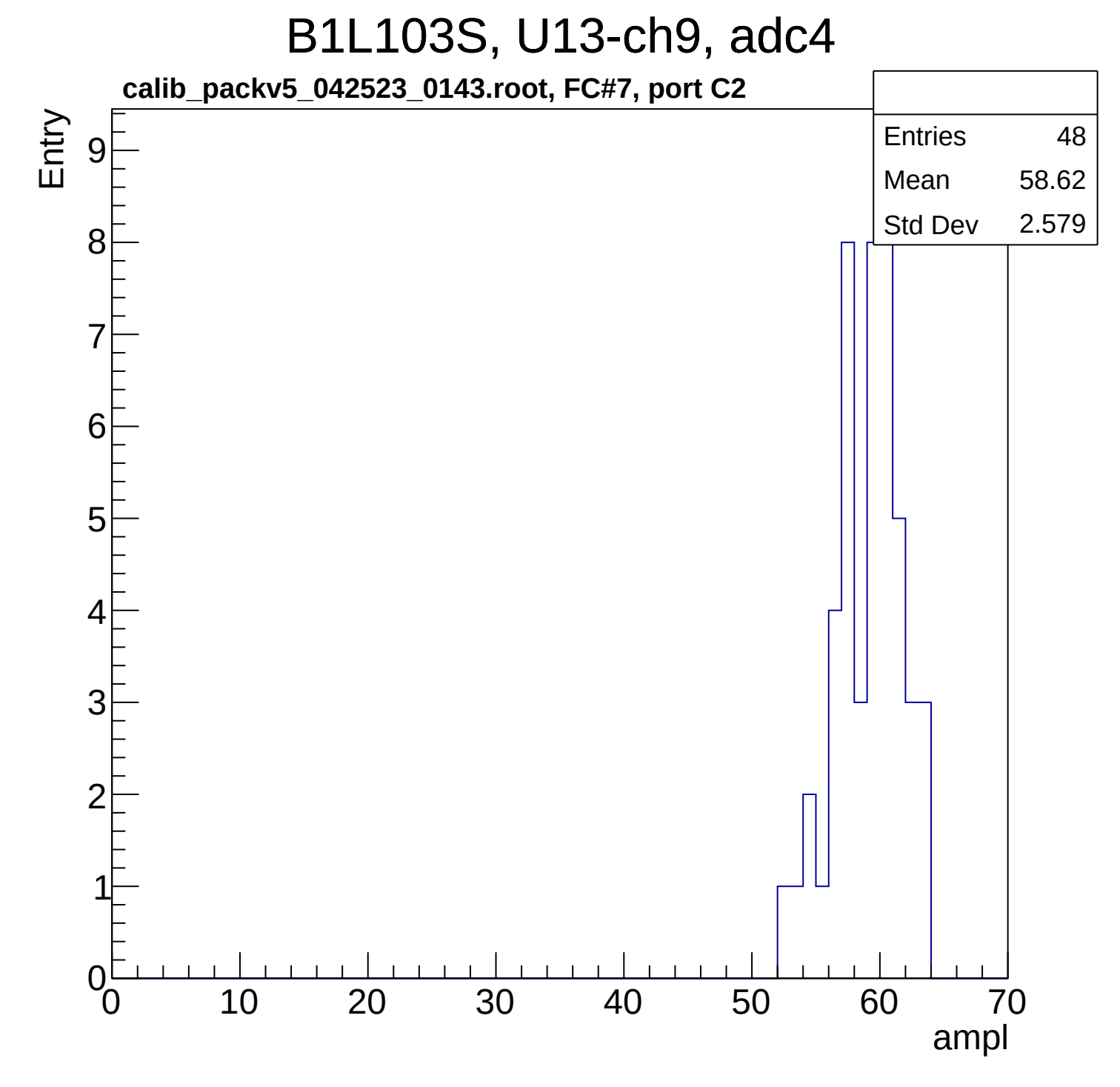

B1L103S, U13-ch9, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 52.56
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch9, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.62
Std Dev 2.579
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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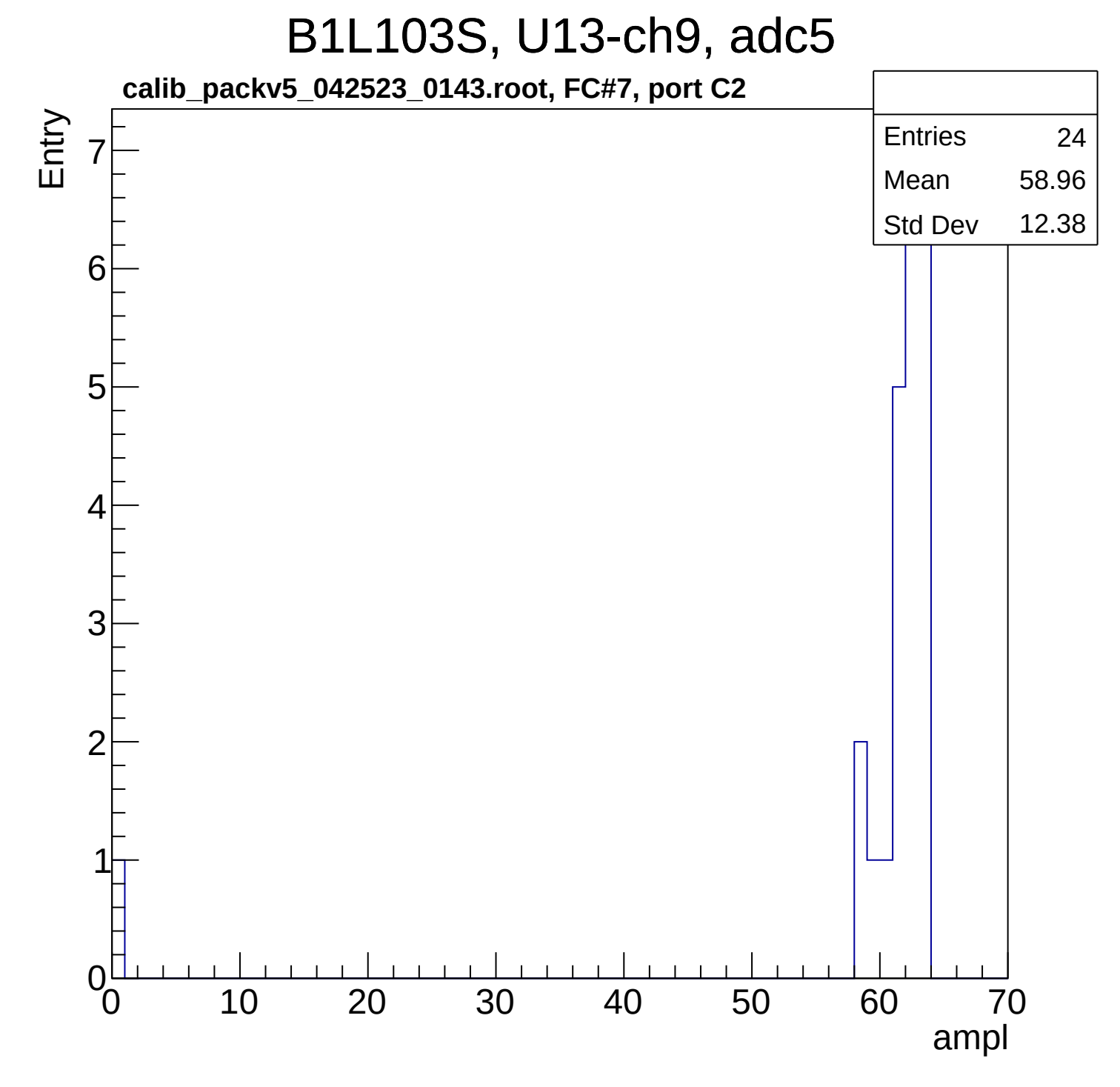
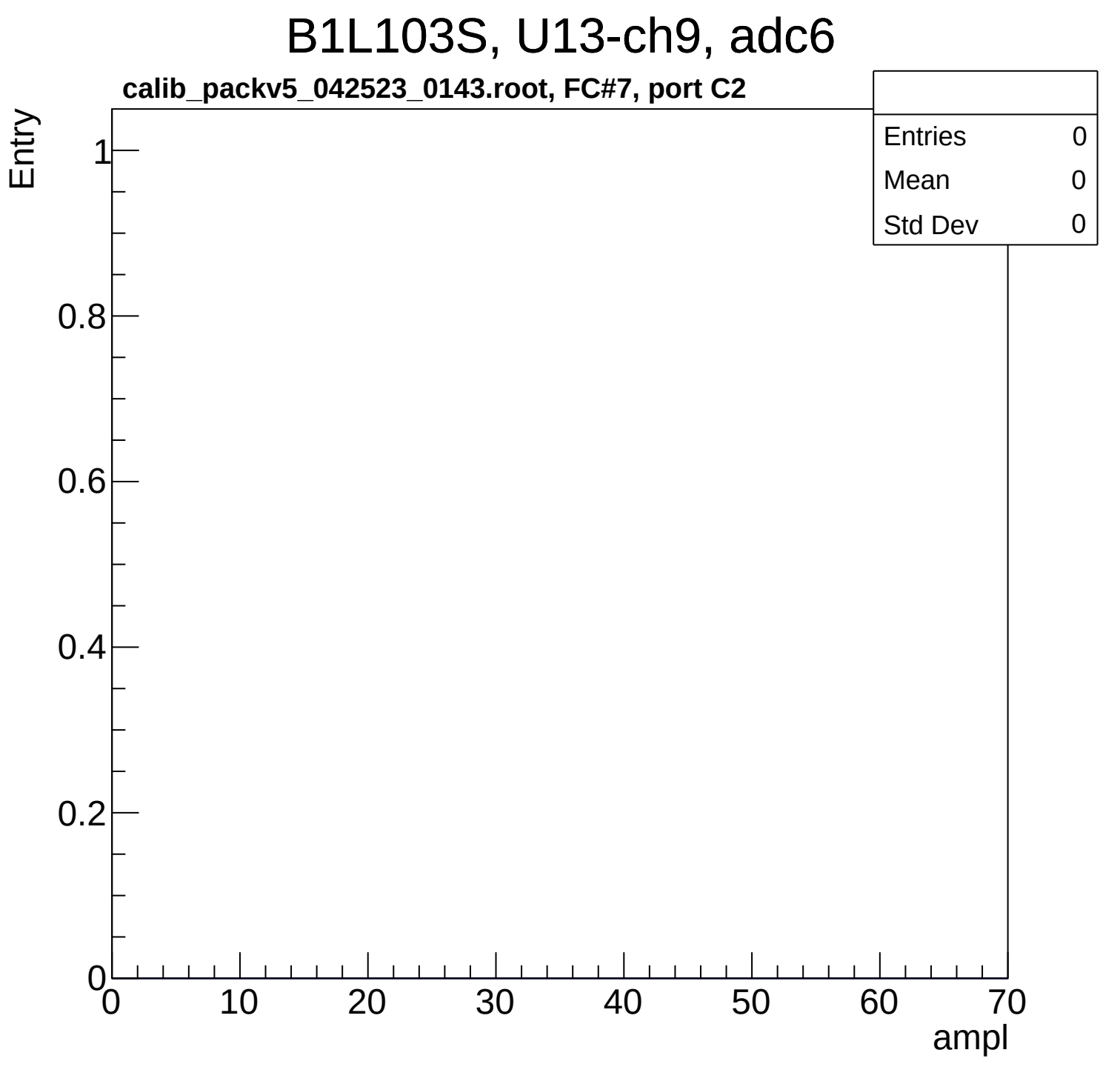
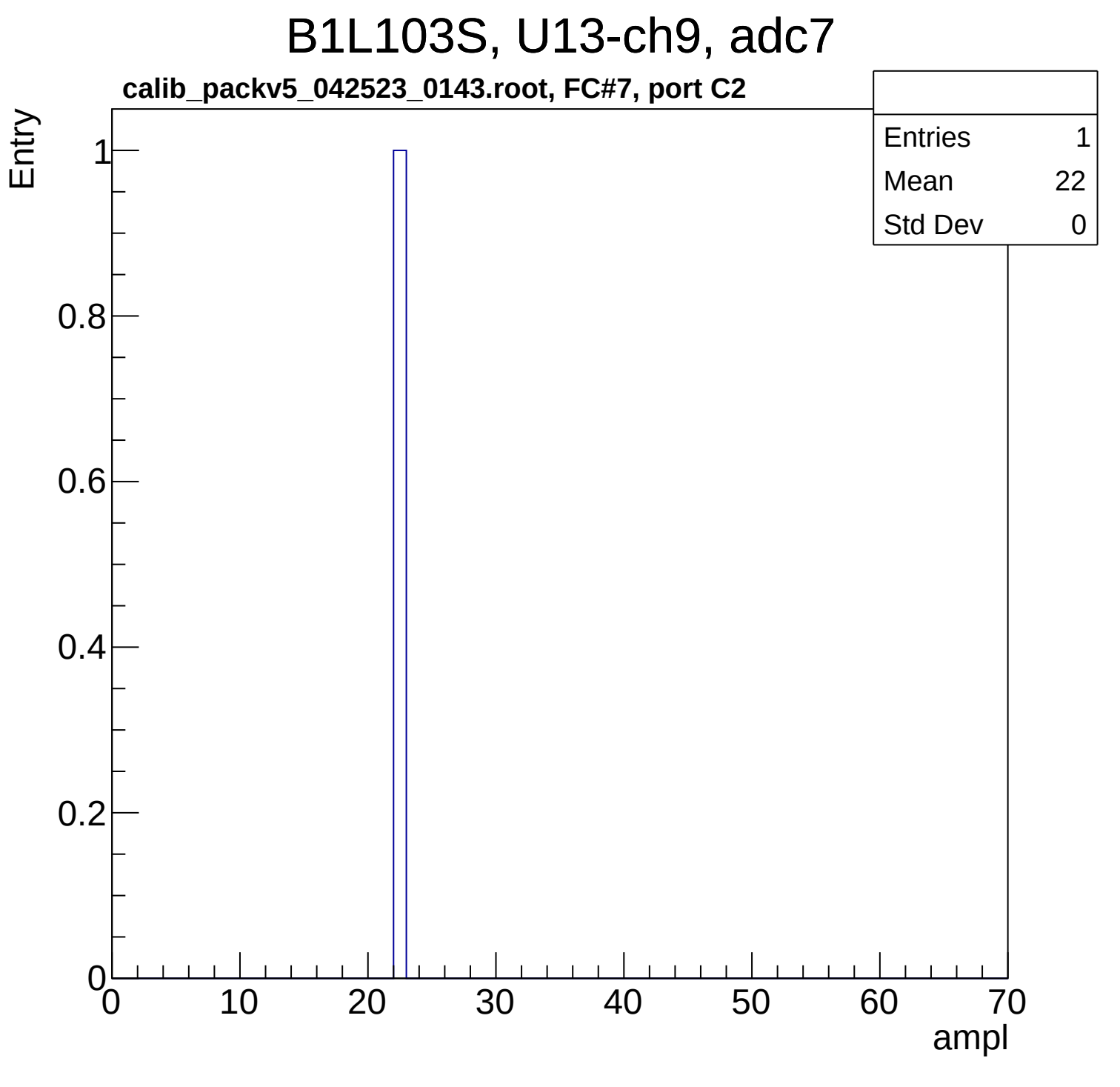
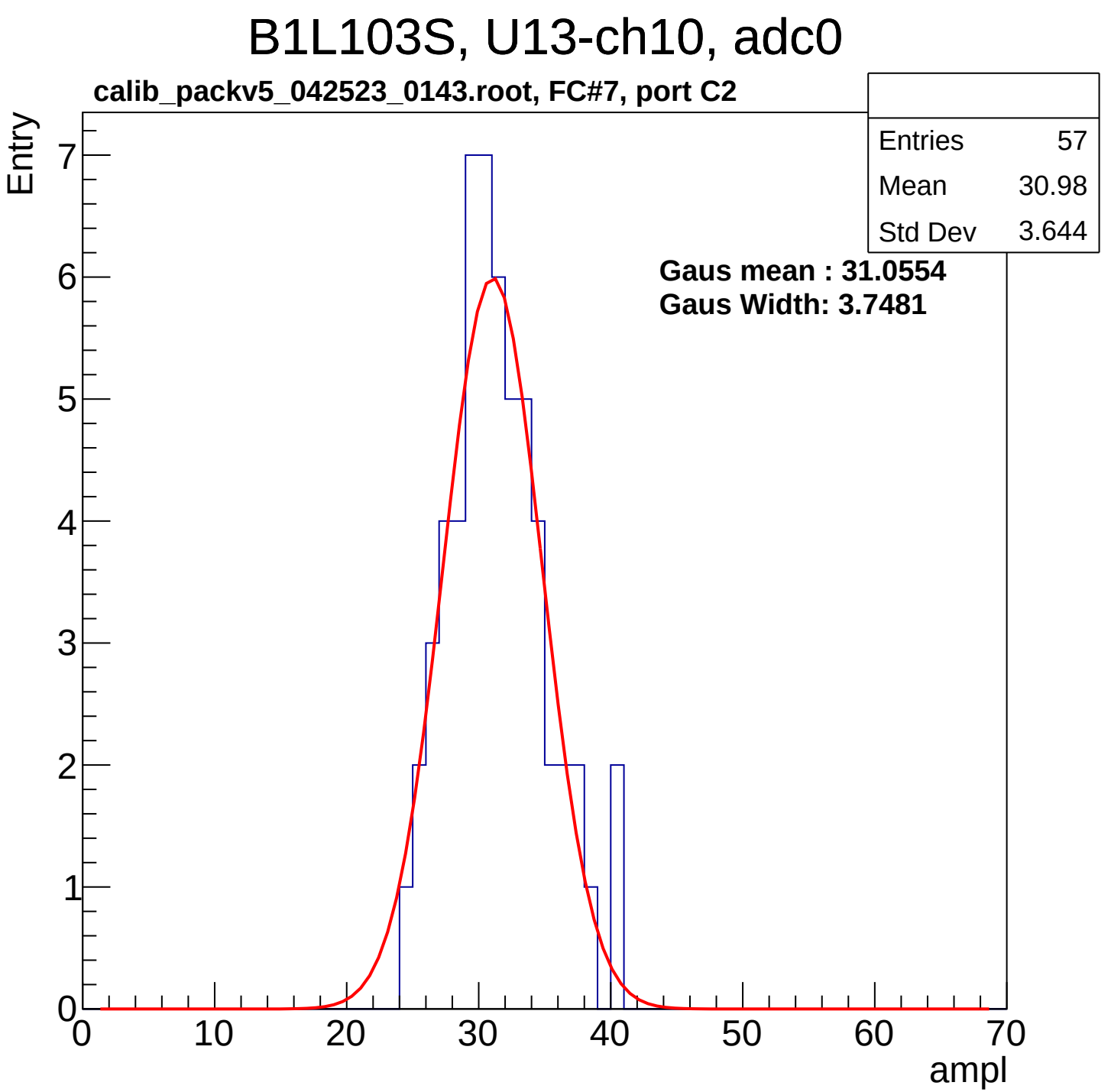
40
50
60
70


B1L103S, U13-ch9, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 58.96
Std Dev 12.38
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch9, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch9, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch10, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 30.98
Std Dev 3.644
Gaus mean : 31.0554
Gaus Width: 3.7481
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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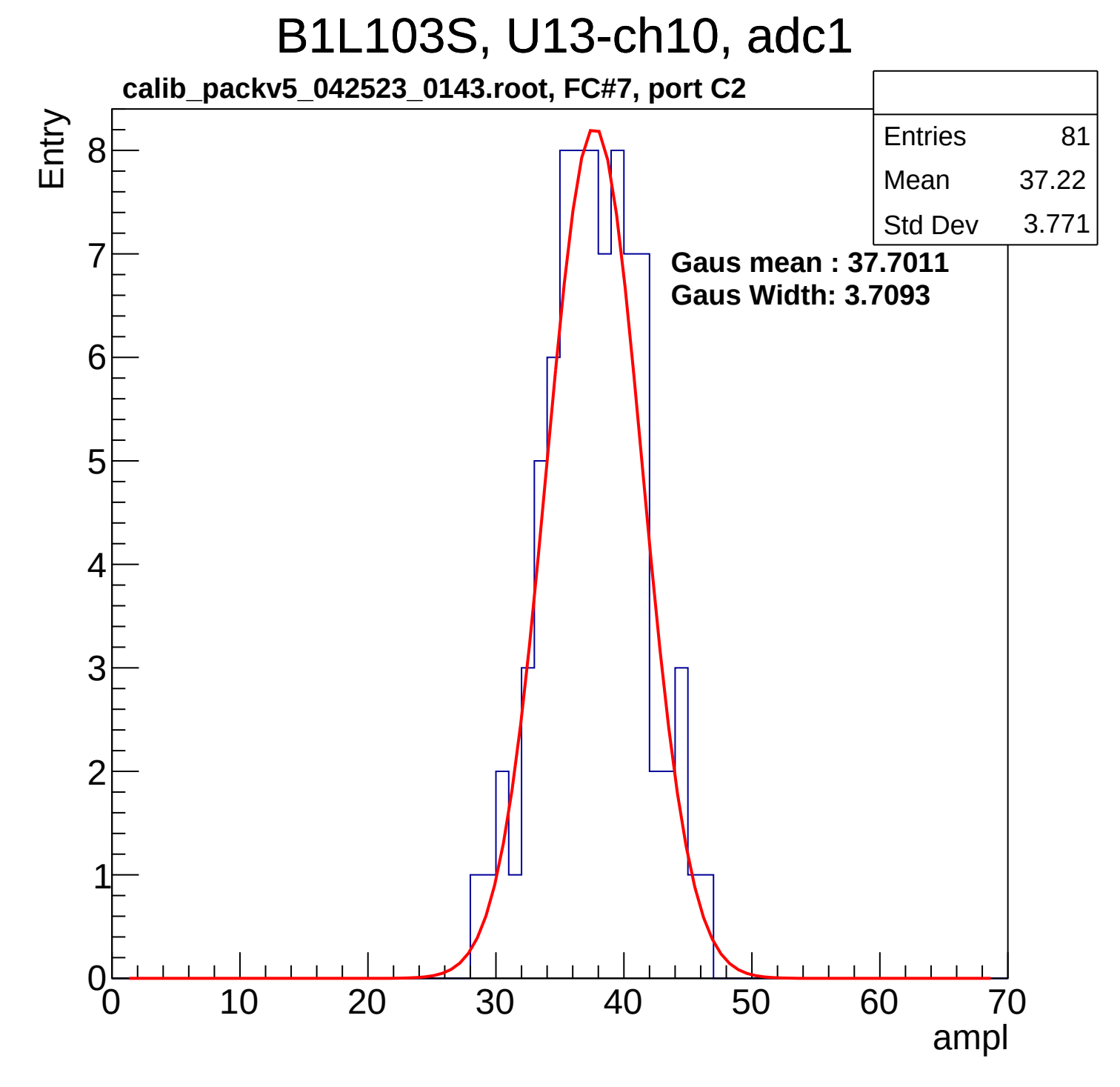
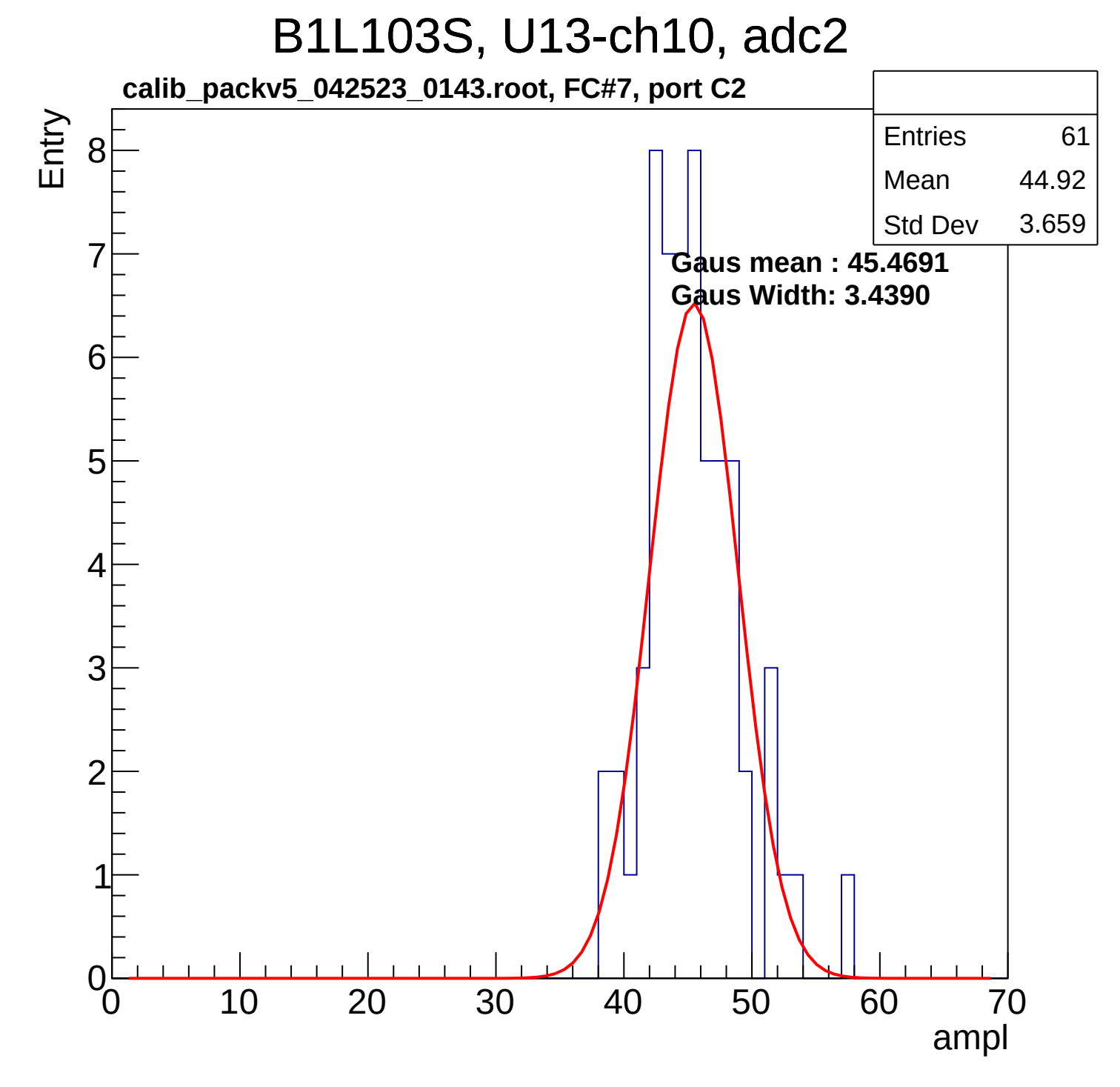

B1L103S, U13-ch10, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 37.22
Std Dev 3.771
Gaus mean : 37.7011
Gaus Width: 3.7093
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch10, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 44.92
Std Dev 3.659
Gaus mean : 45.4691
Gaus Width: 3.4390
Entry
ampl
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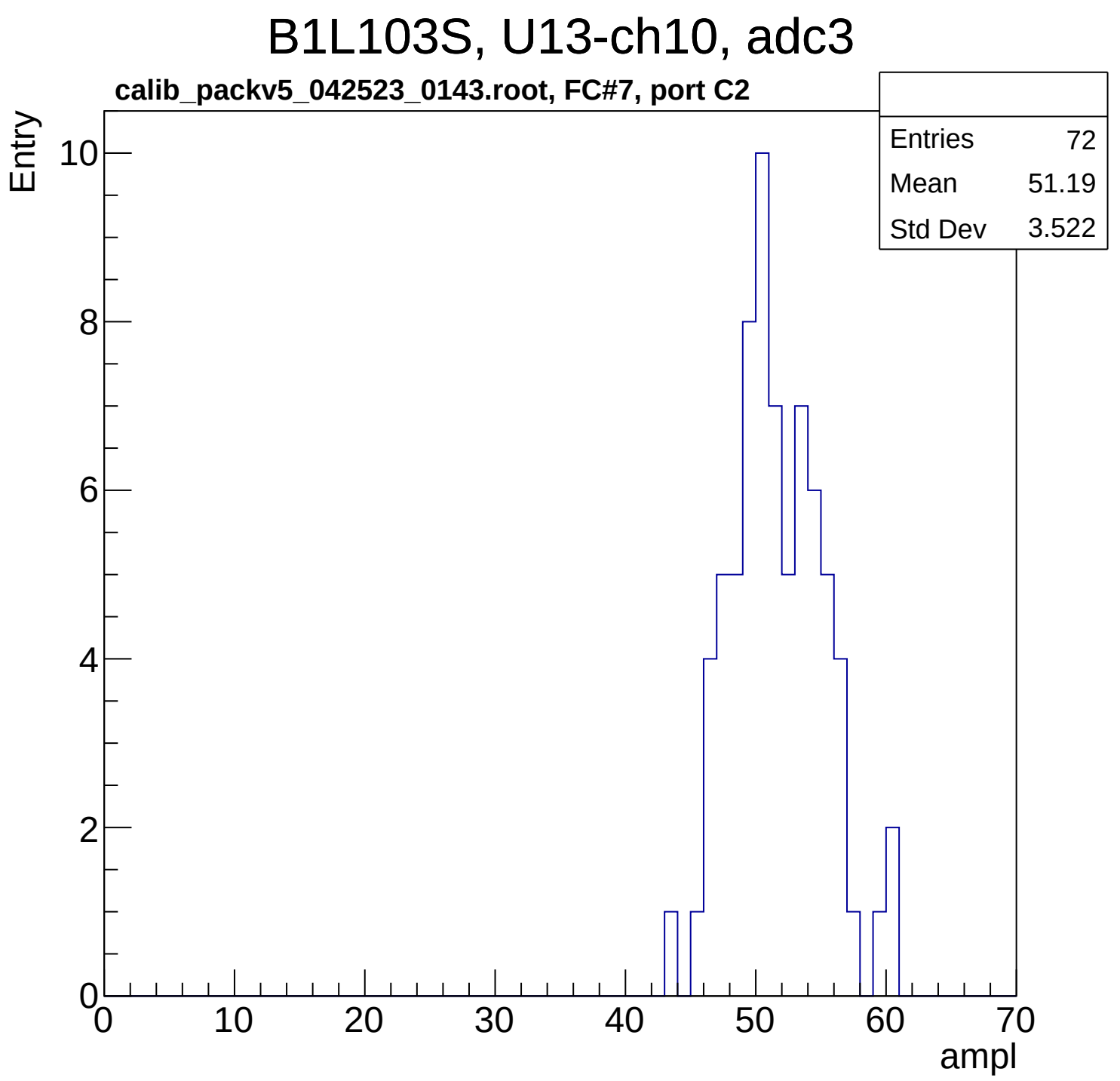
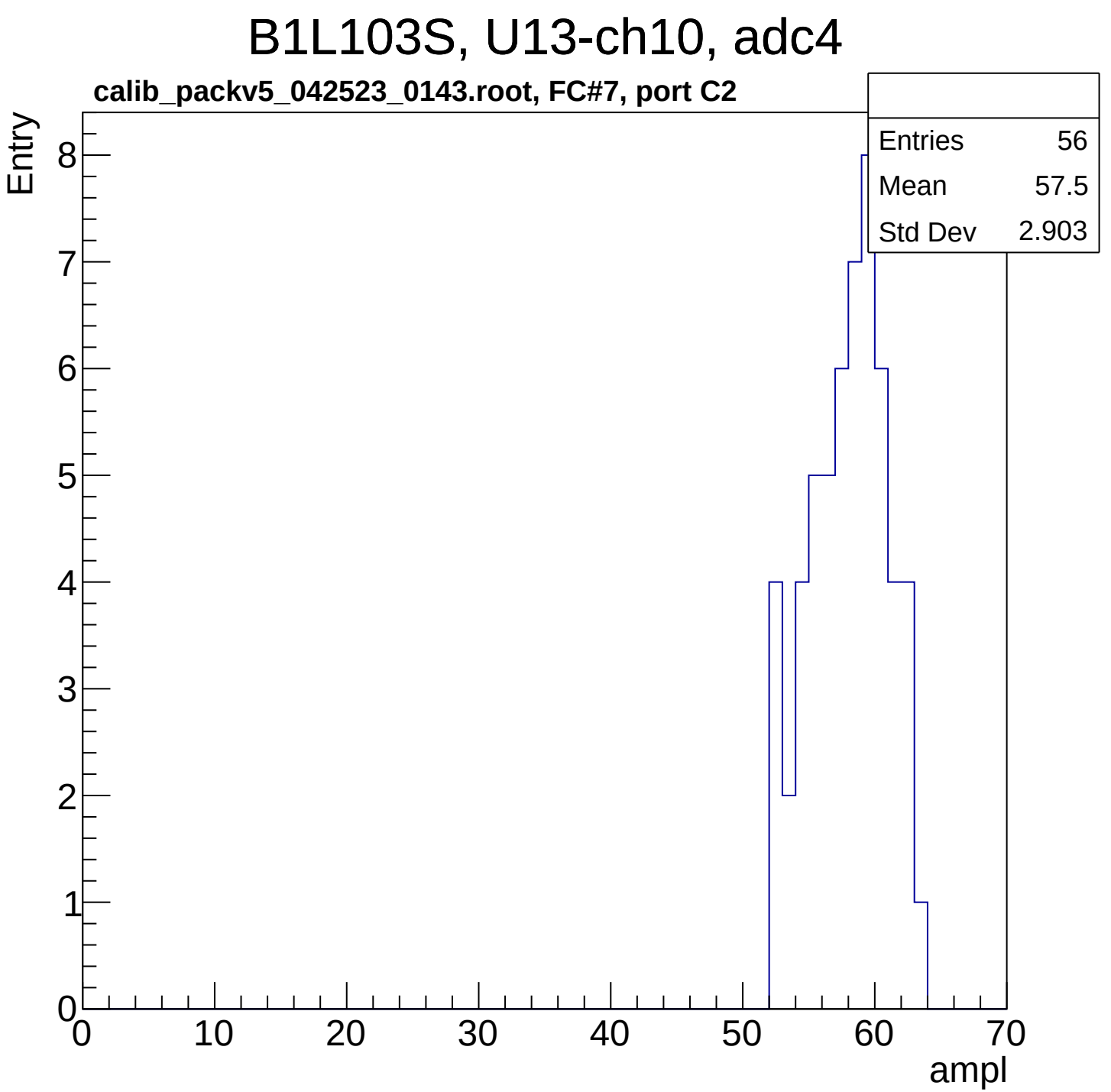
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch10, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 51.19
Std Dev 3.522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch10, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.5
Std Dev 2.903
Entry
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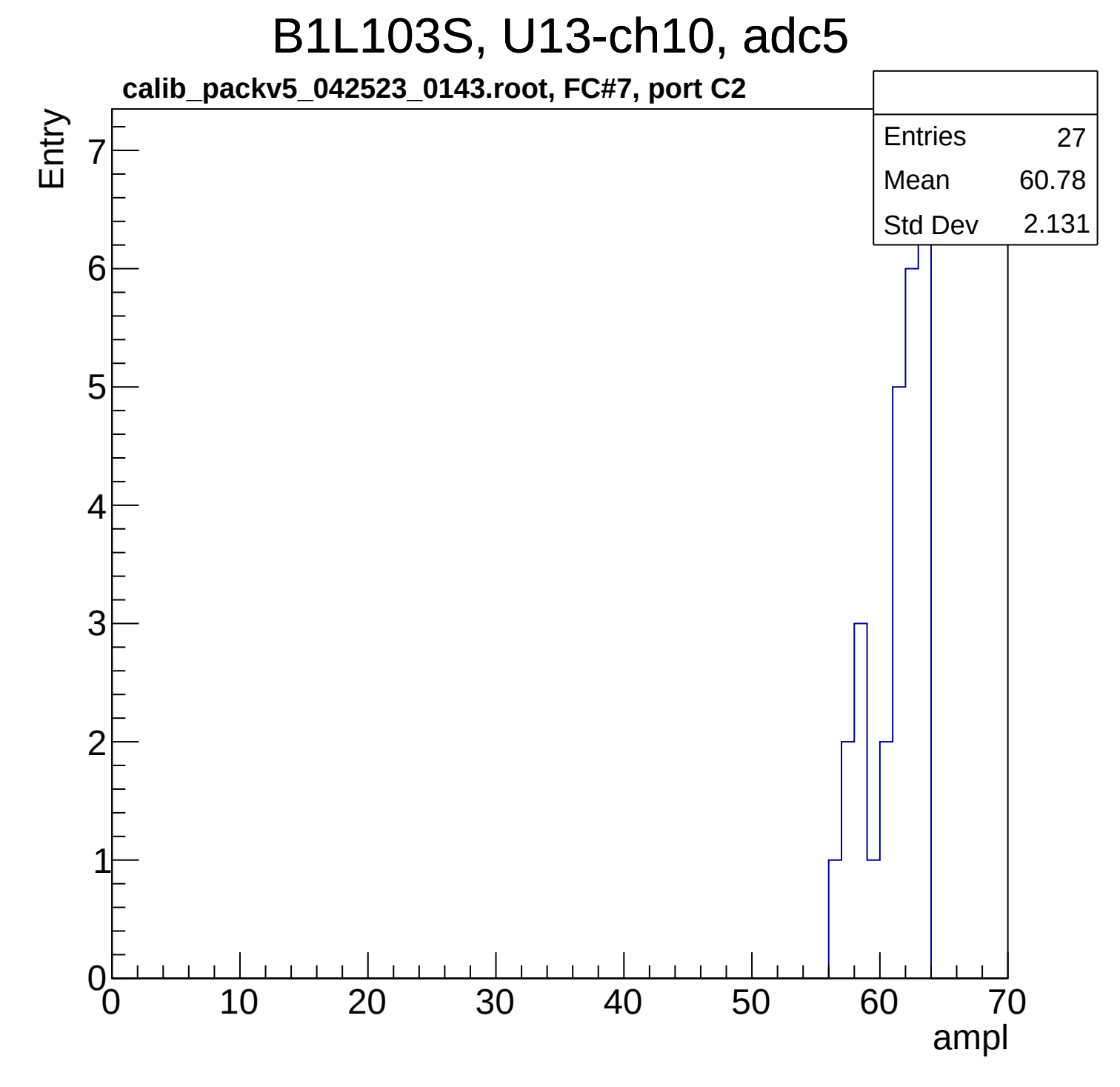
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch10, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 27
Mean 60.78
Std Dev 2.131
Entry
ampl
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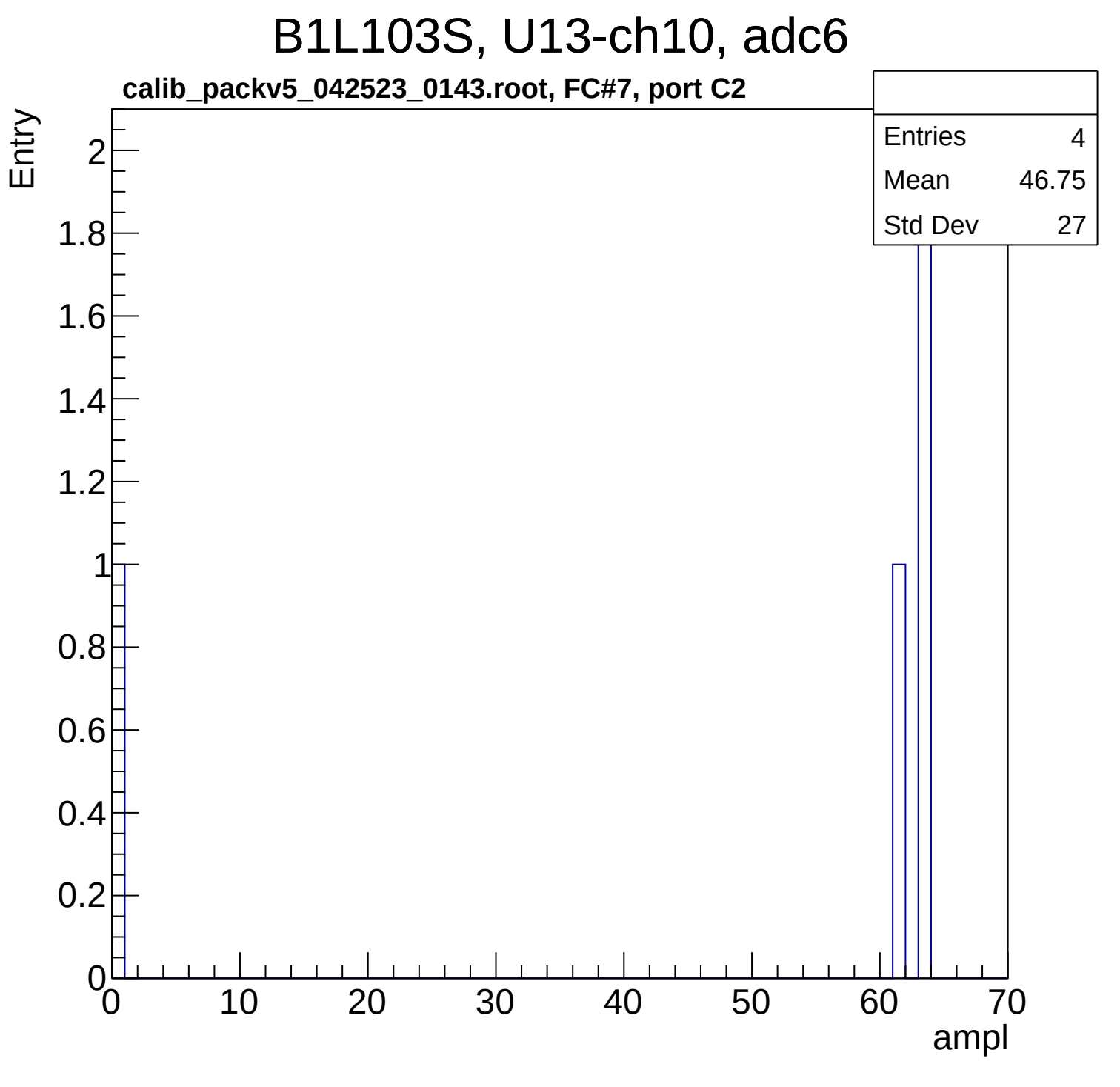
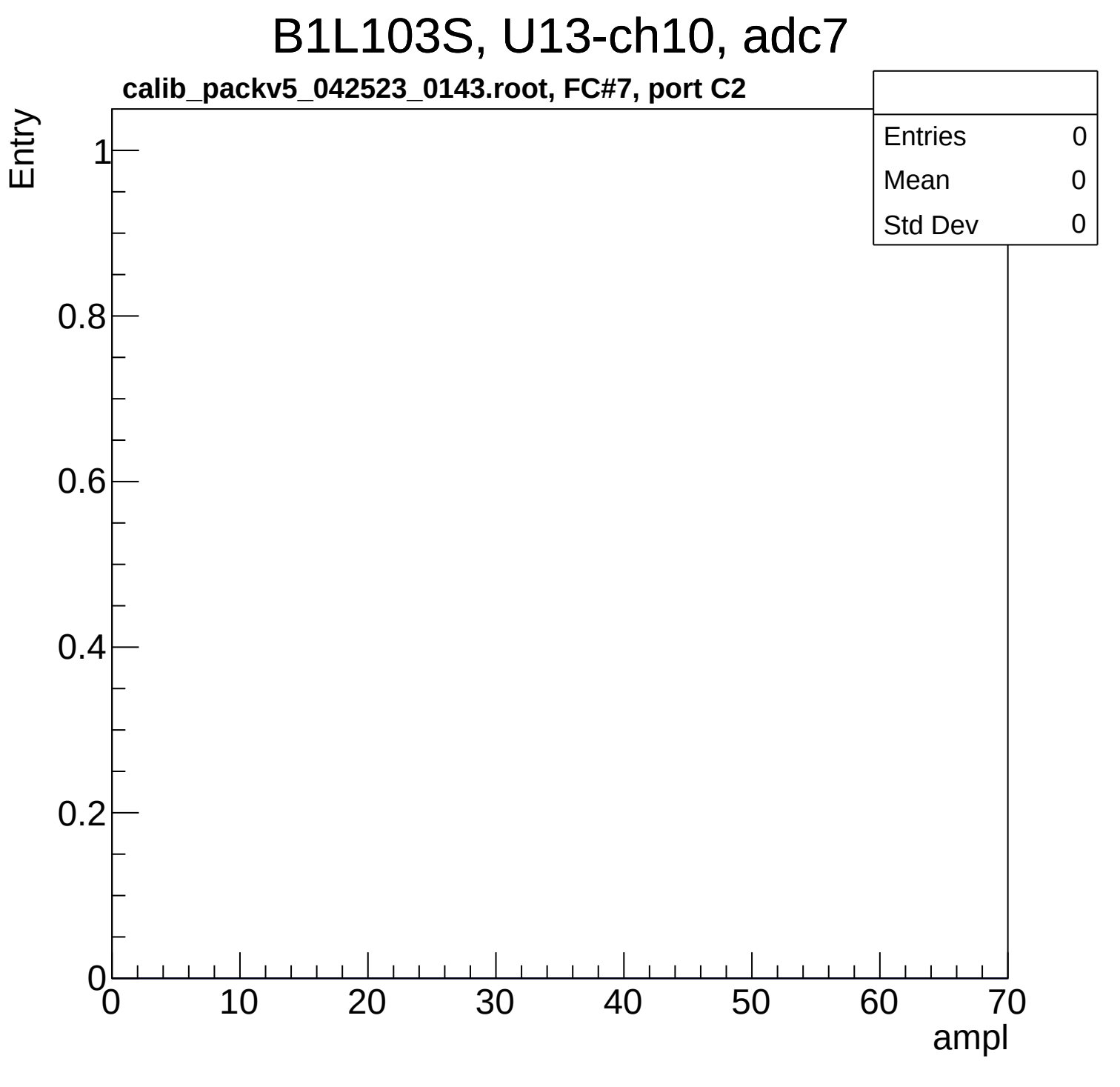
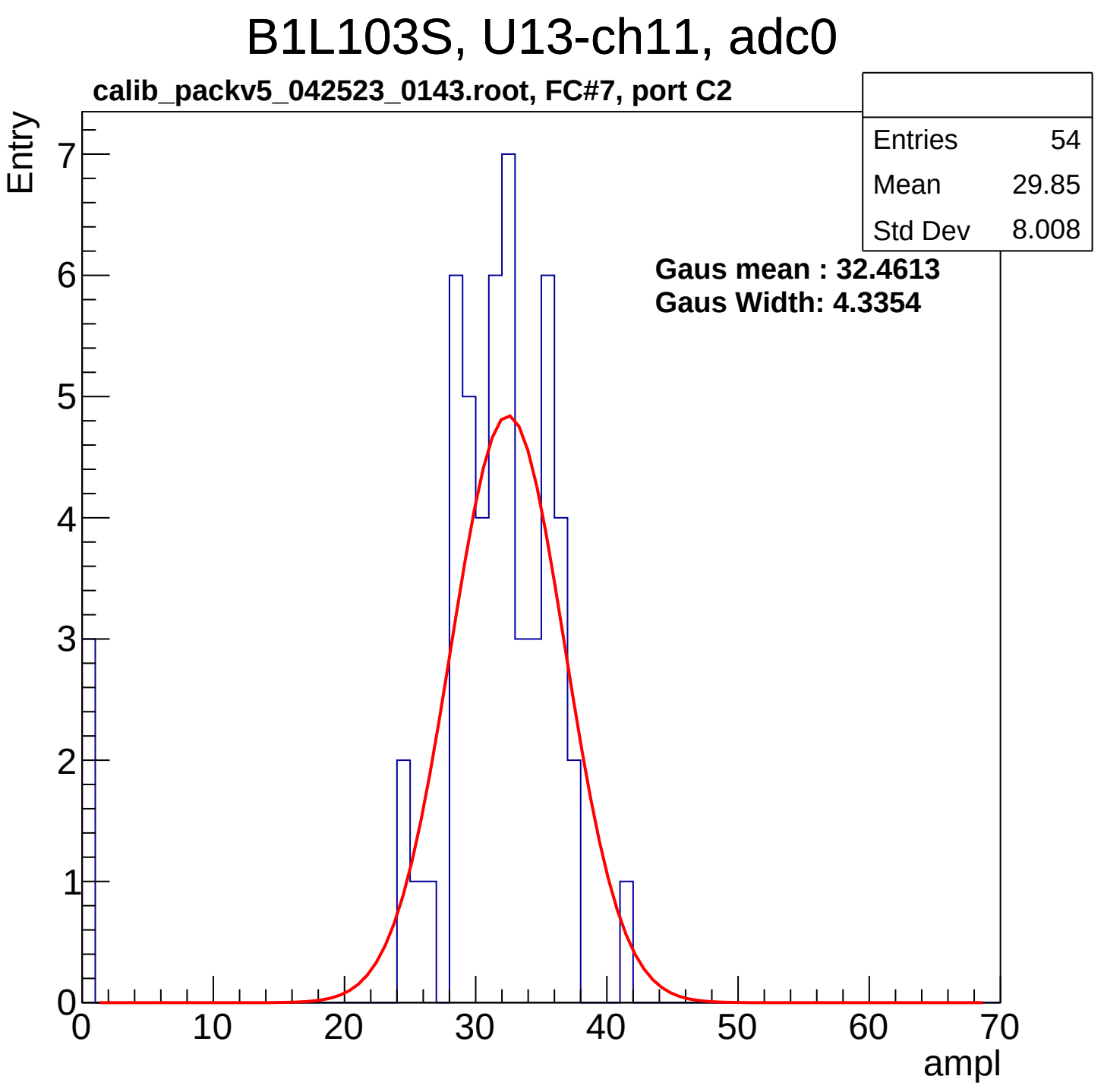
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch10, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch10, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch11, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 29.85
Std Dev 8.008
Gaus mean : 32.4613
Gaus Width: 4.3354
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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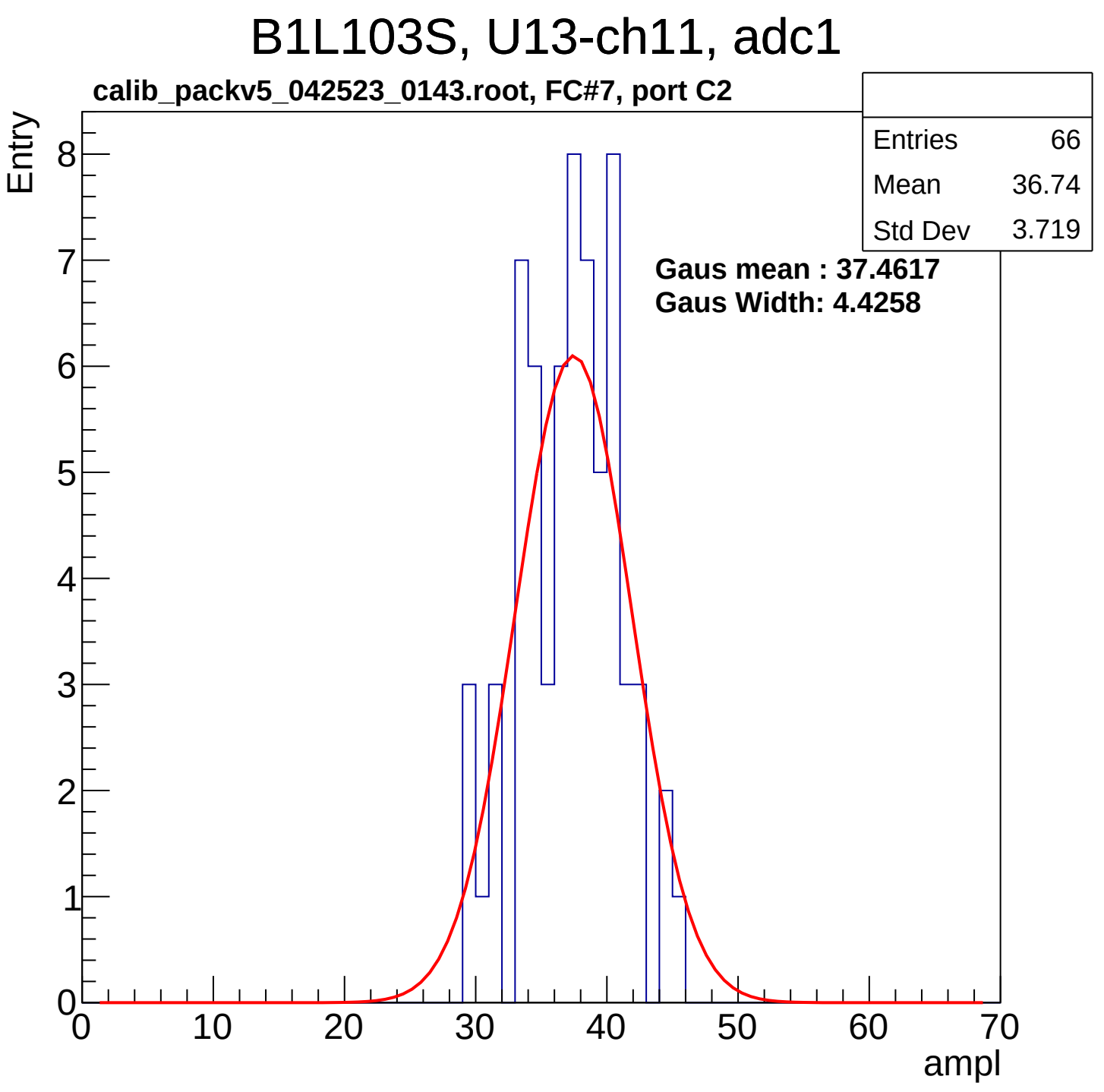
B1L103S, U13-ch11, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 36.74
Std Dev 3.719
Gaus mean : 37.4617
Gaus Width: 4.4258
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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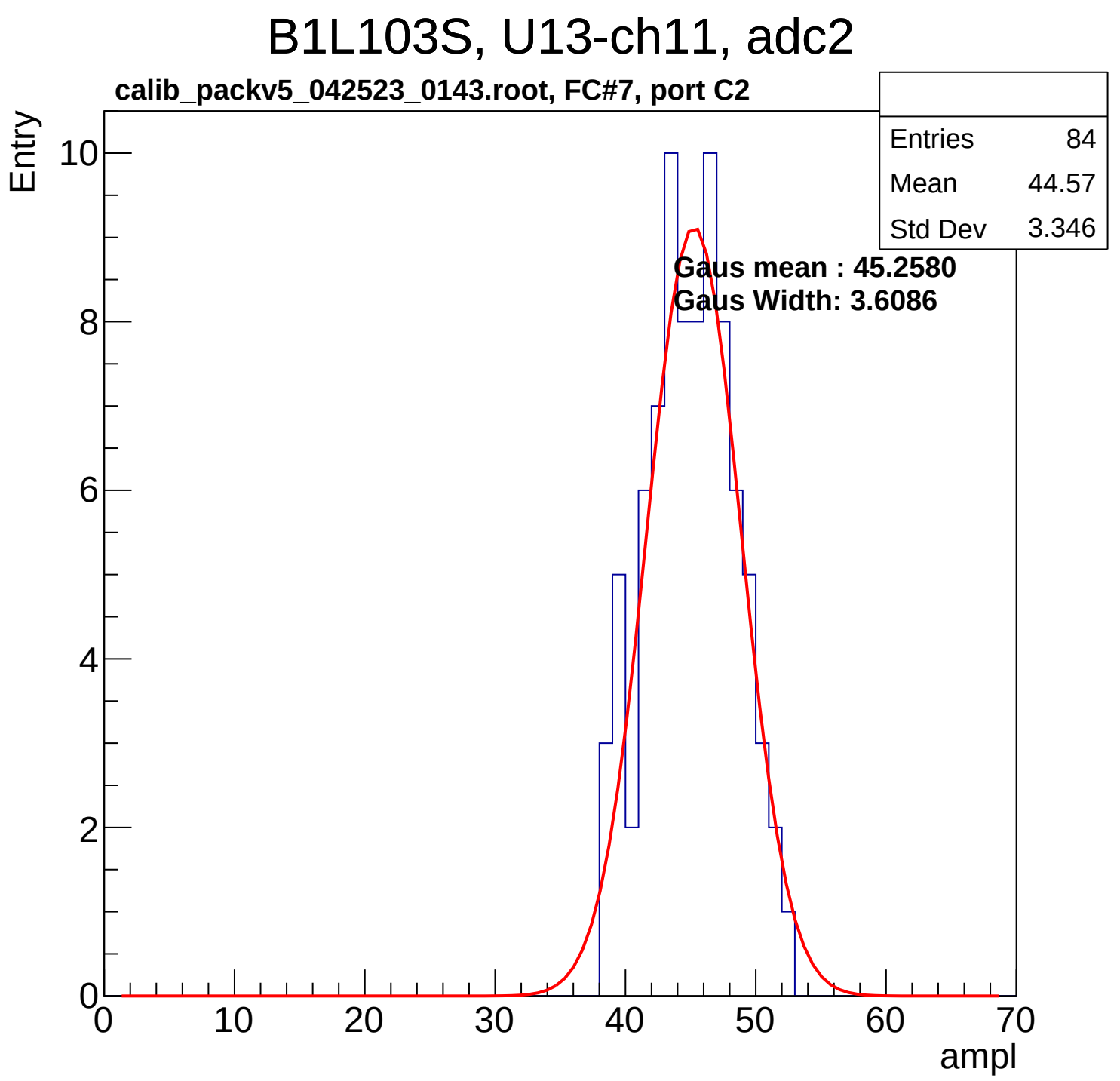
B1L103S, U13-ch11, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 44.57
Std Dev 3.346
Gaus mean : 45.2580
Gaus Width: 3.6086
Entry
ampl
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4
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0
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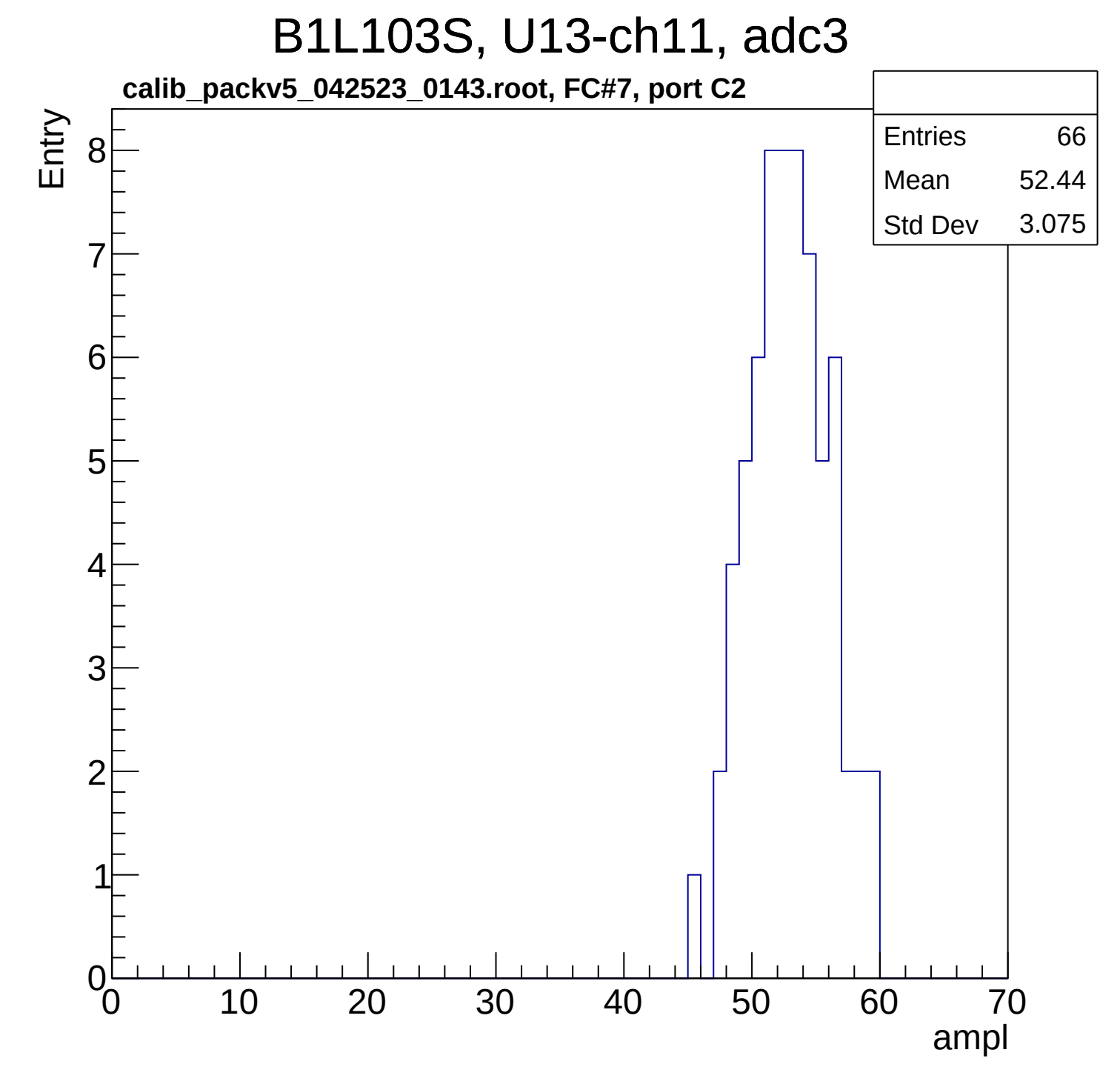
B1L103S, U13-ch11, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 52.44
Std Dev 3.075
Entry
ampl
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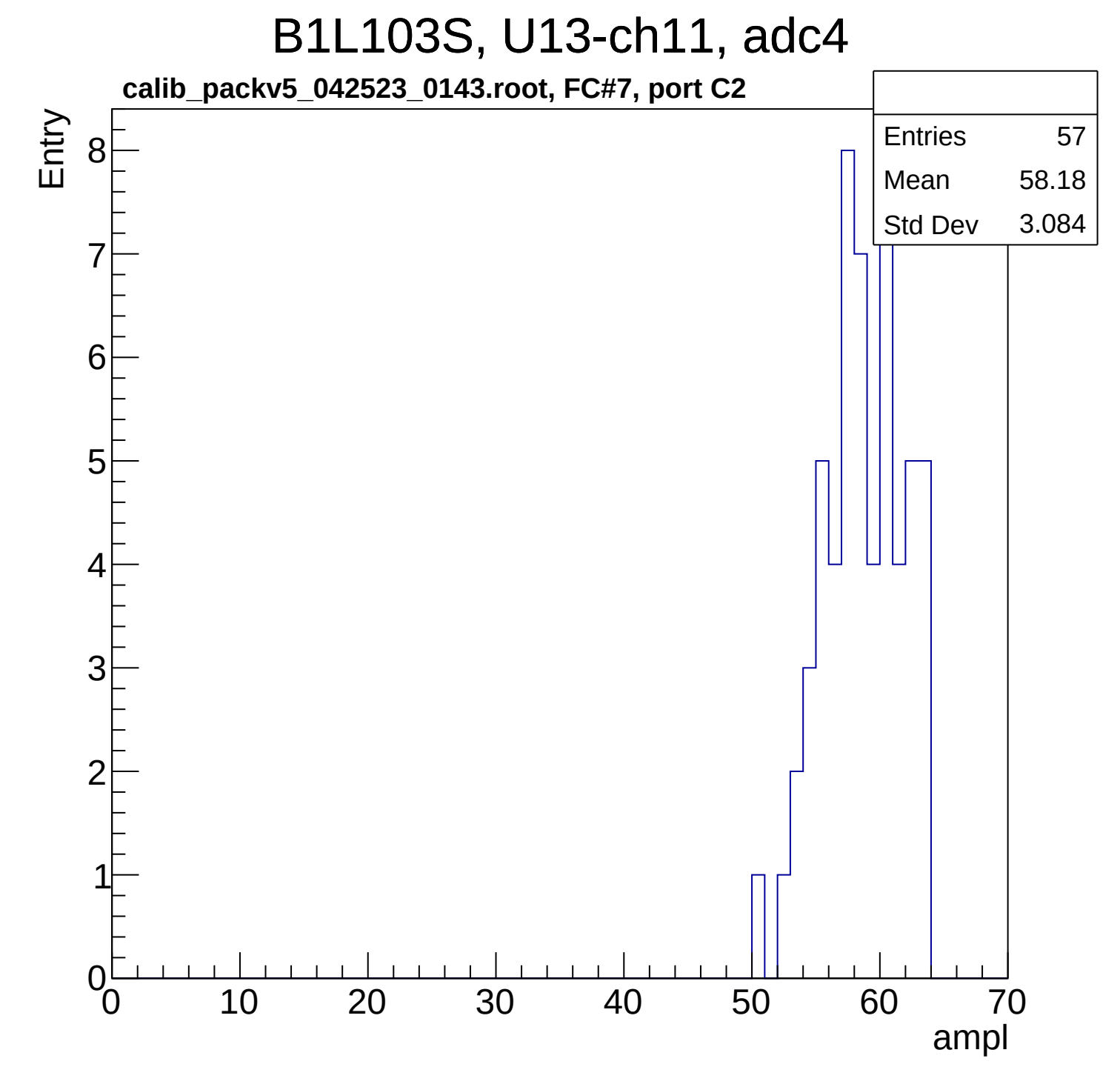
B1L103S, U13-ch11, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 58.18
Std Dev 3.084
Entry
ampl
0
1
2
3
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5
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7
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0
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60
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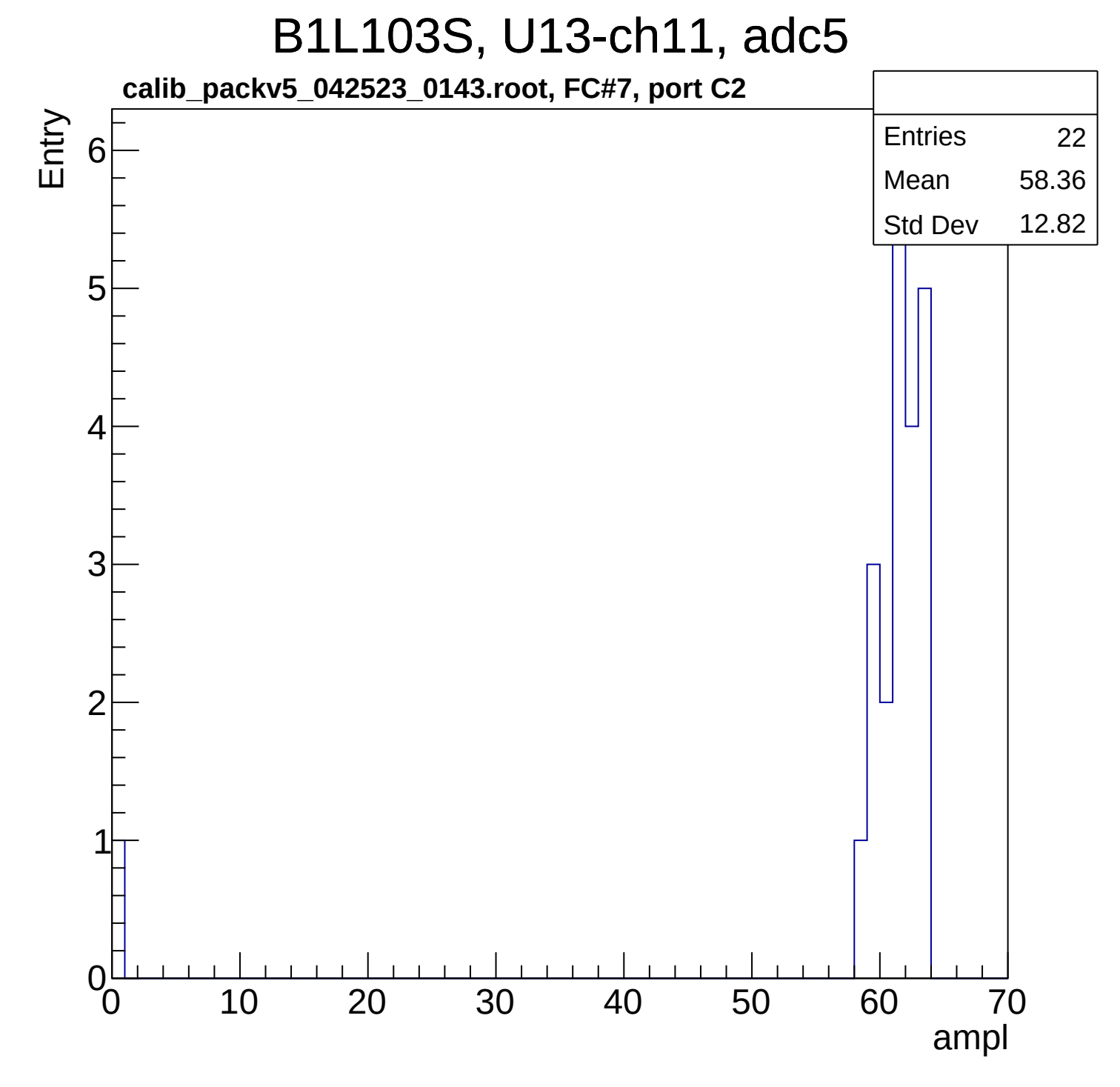
B1L103S, U13-ch11, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.36
Std Dev 12.82
Entry
0
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2
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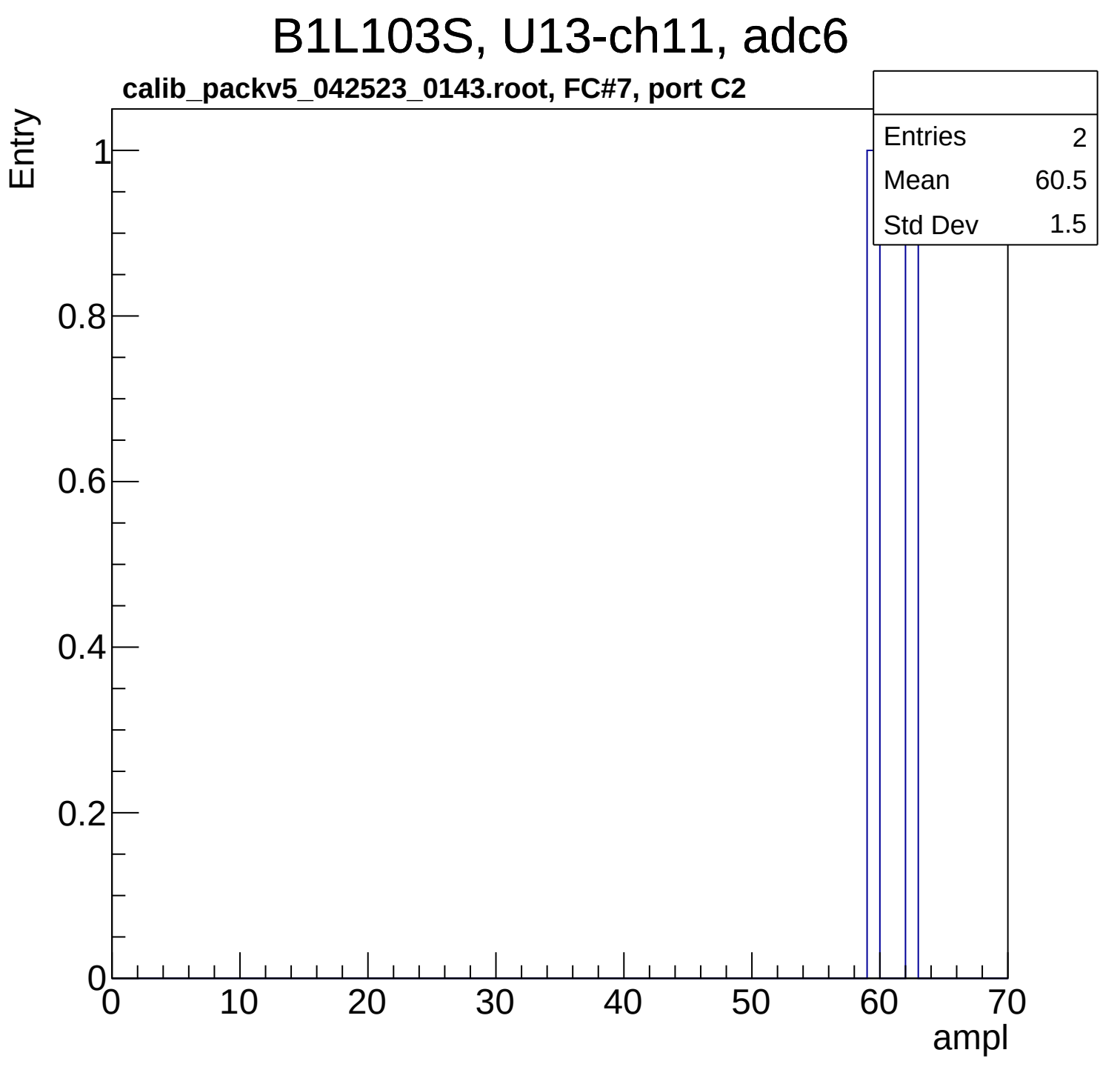
60
70
ampl


B1L103S, U13-ch11, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 60.5
Std Dev 1.5
Entry
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0.8
0.6
0.4
0.2
0
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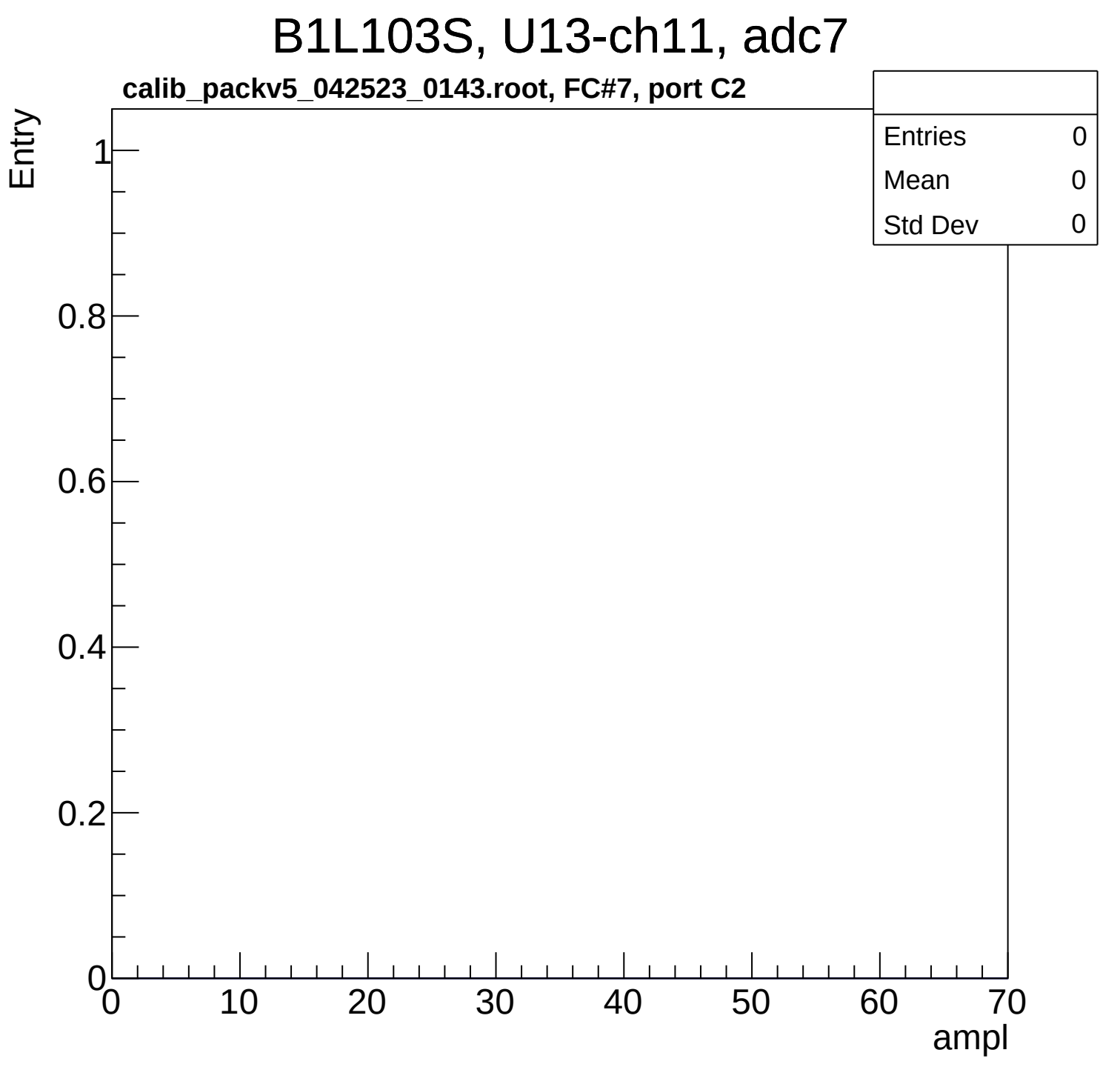
70
ampl


B1L103S, U13-ch11, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
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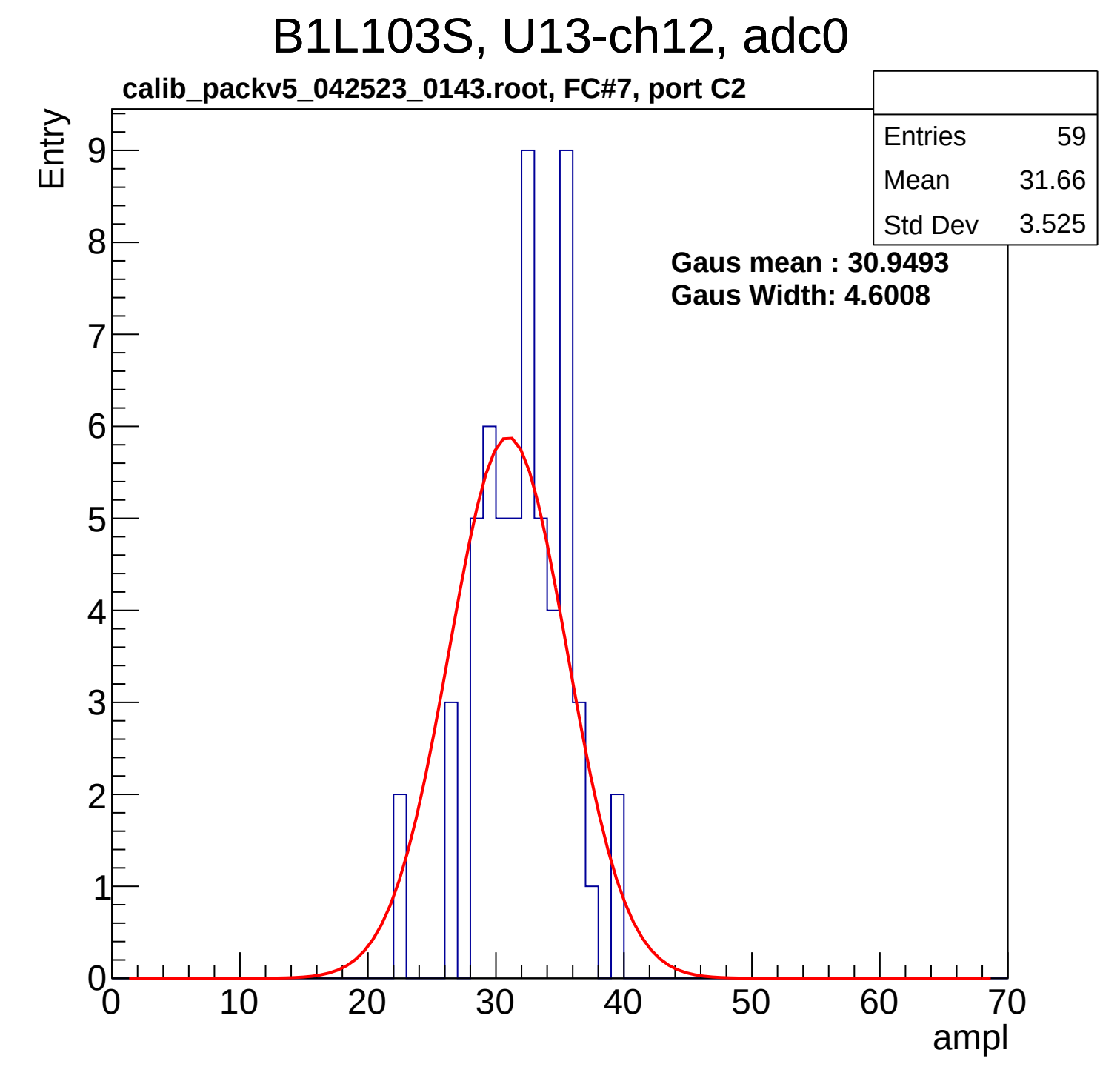
50
60
70
ampl


B1L103S, U13-ch12, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 31.66
Std Dev 3.525
Gaus mean : 30.9493
Gaus Width: 4.6008
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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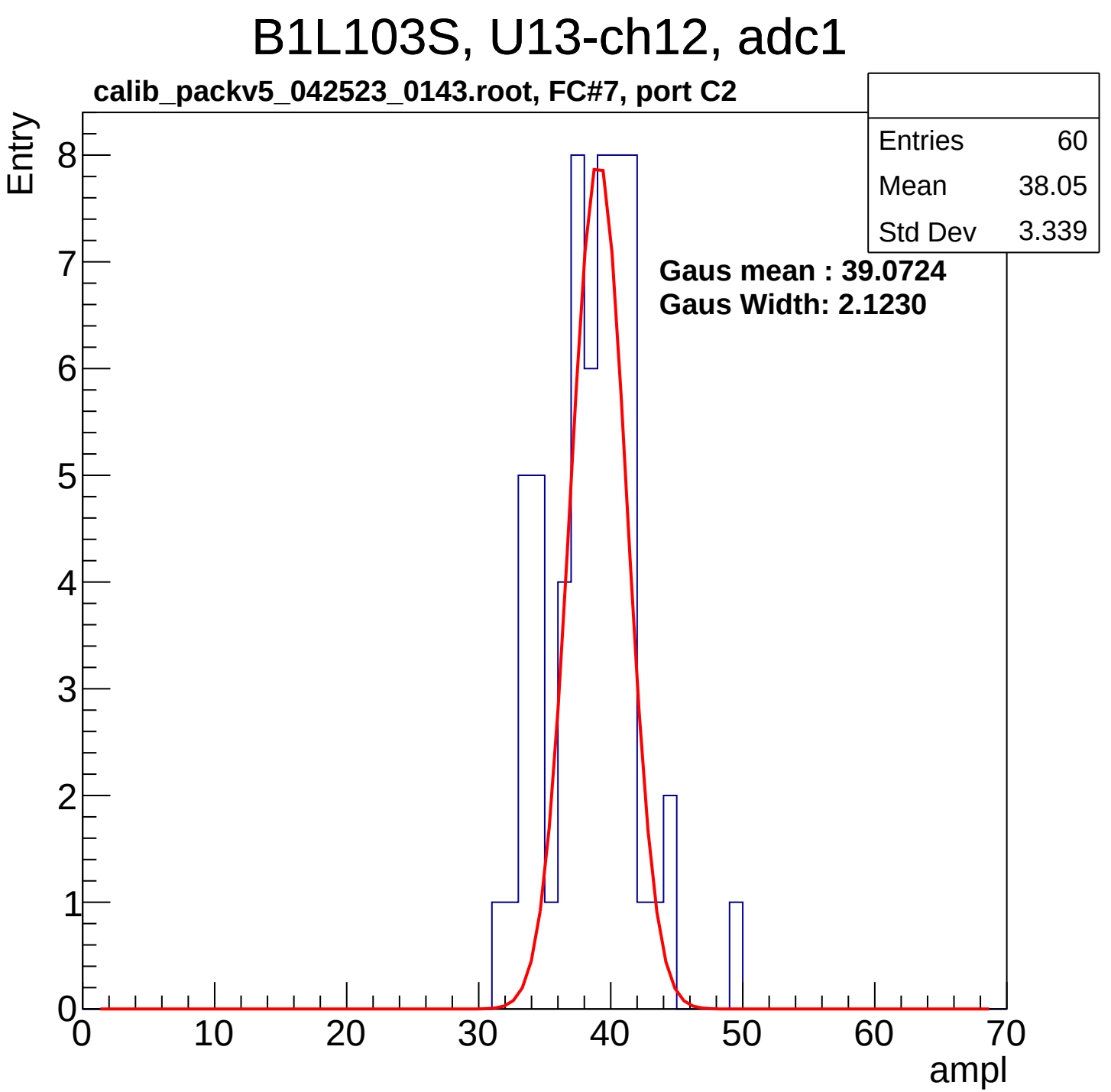
B1L103S, U13-ch12, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 38.05
Std Dev 3.339
Gaus mean : 39.0724
Gaus Width: 2.1230
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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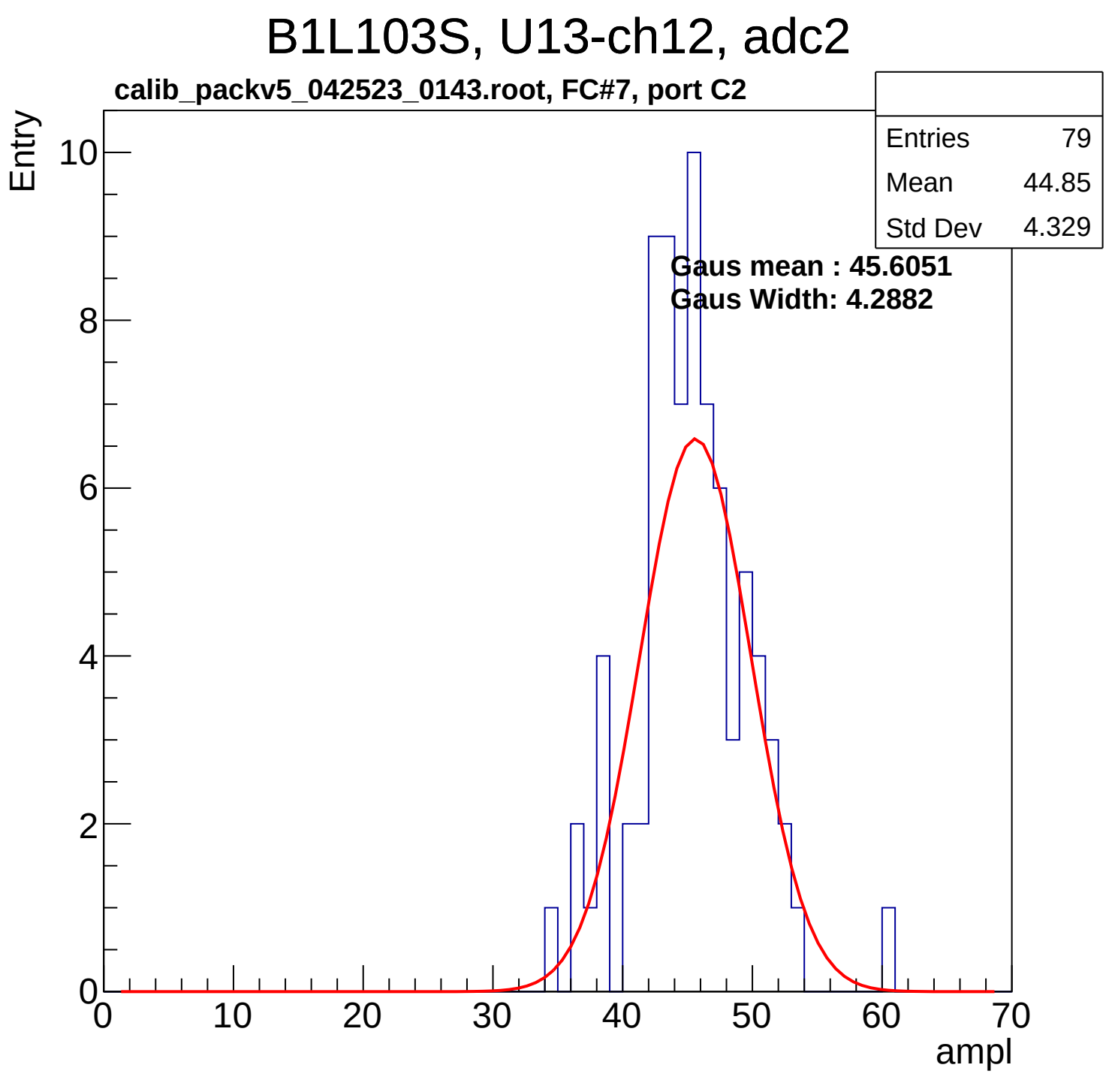
B1L103S, U13-ch12, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 44.85
Std Dev 4.329
Gaus mean : 45.6051
Gaus Width: 4.2882
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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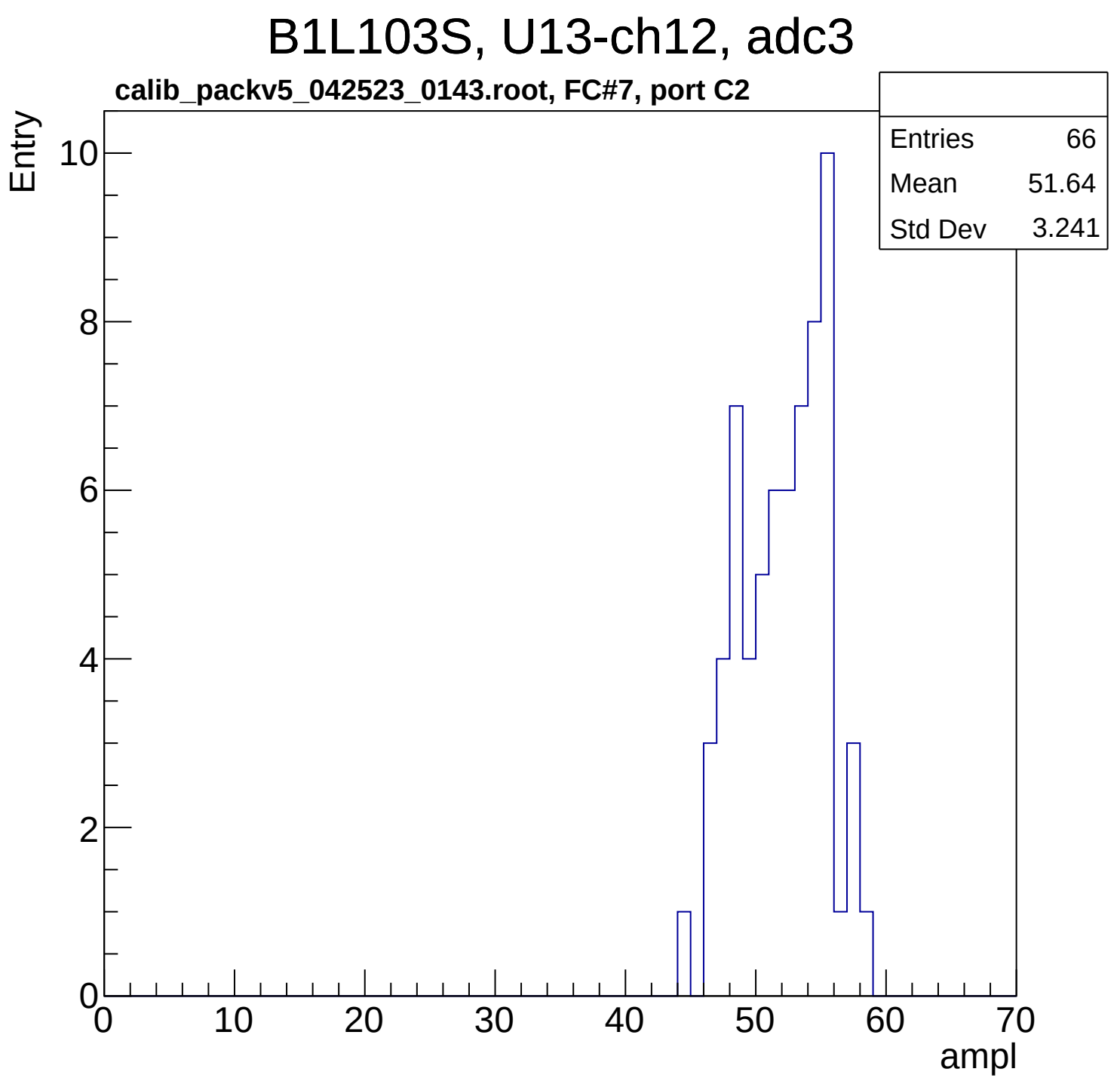
B1L103S, U13-ch12, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 51.64
Std Dev 3.241
Entry
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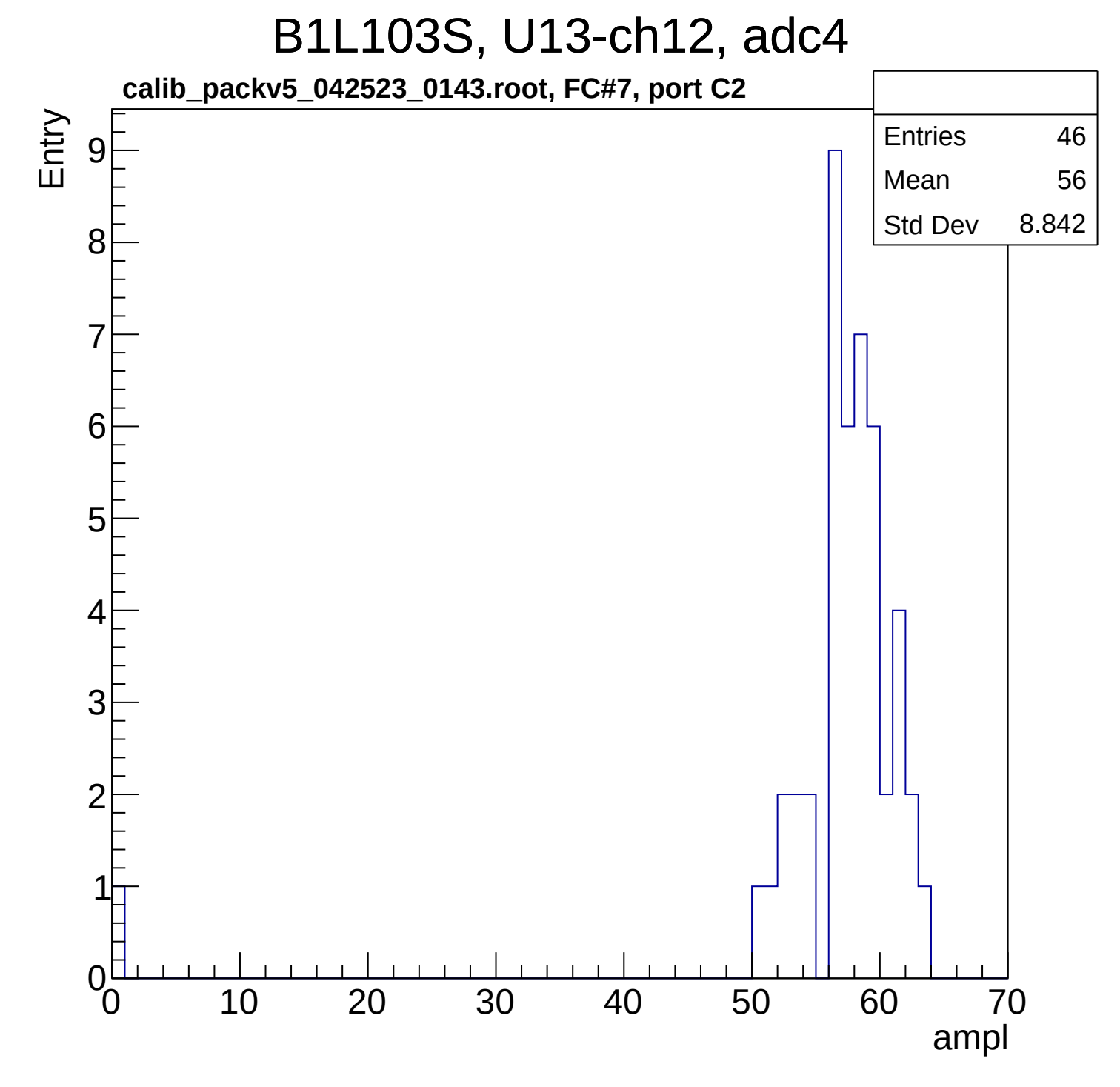
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch12, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56
Std Dev 8.842
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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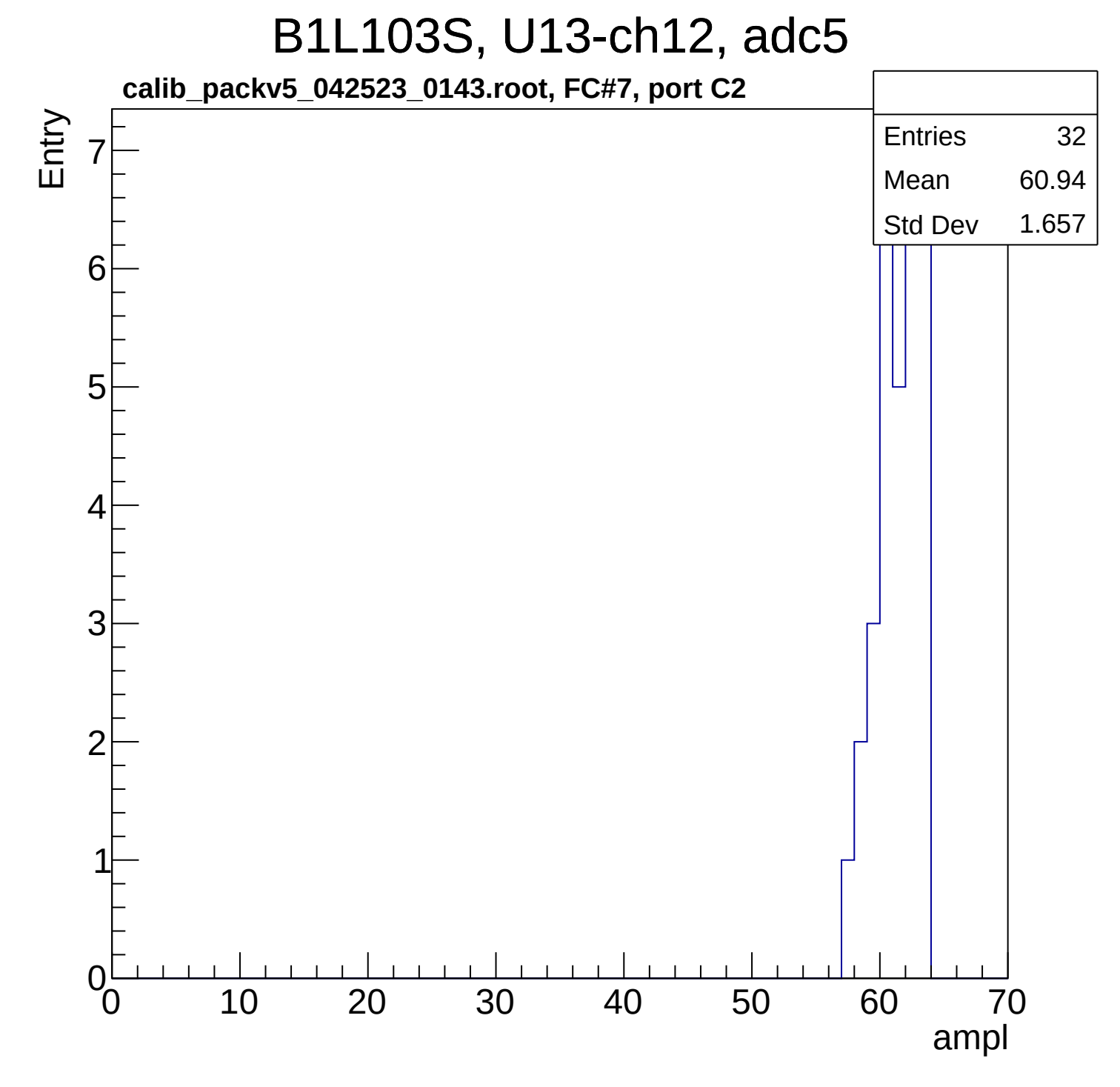
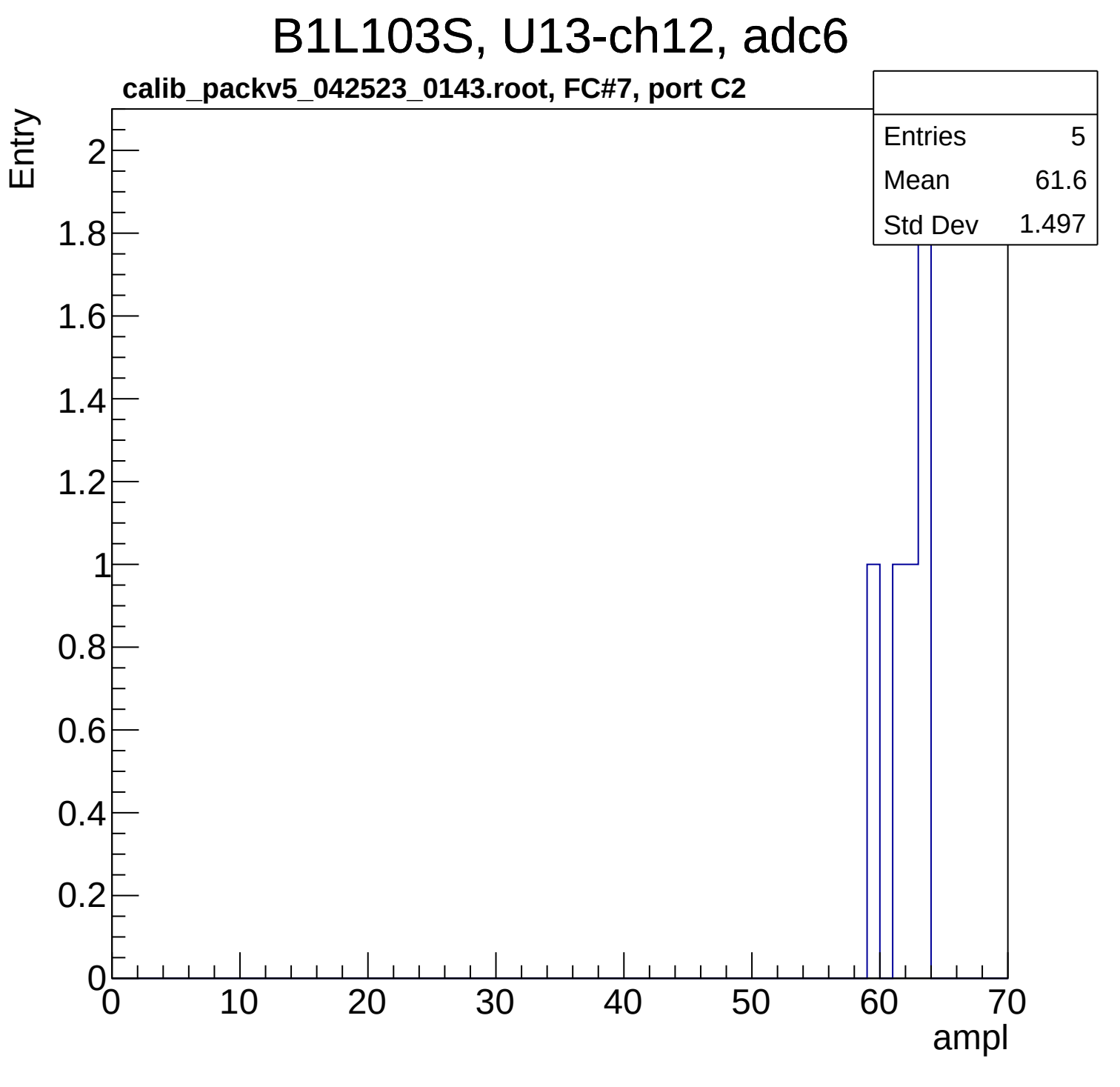
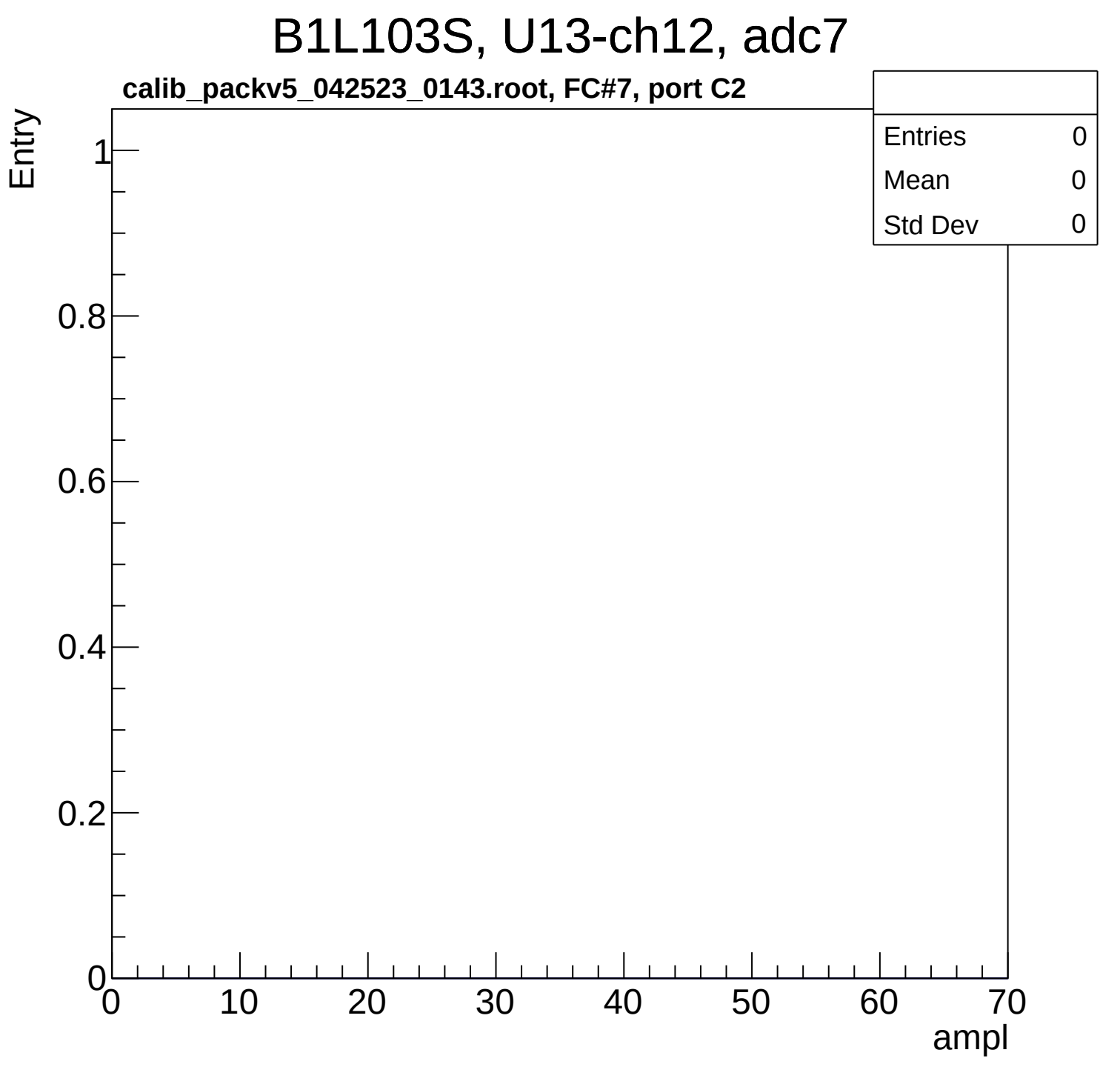
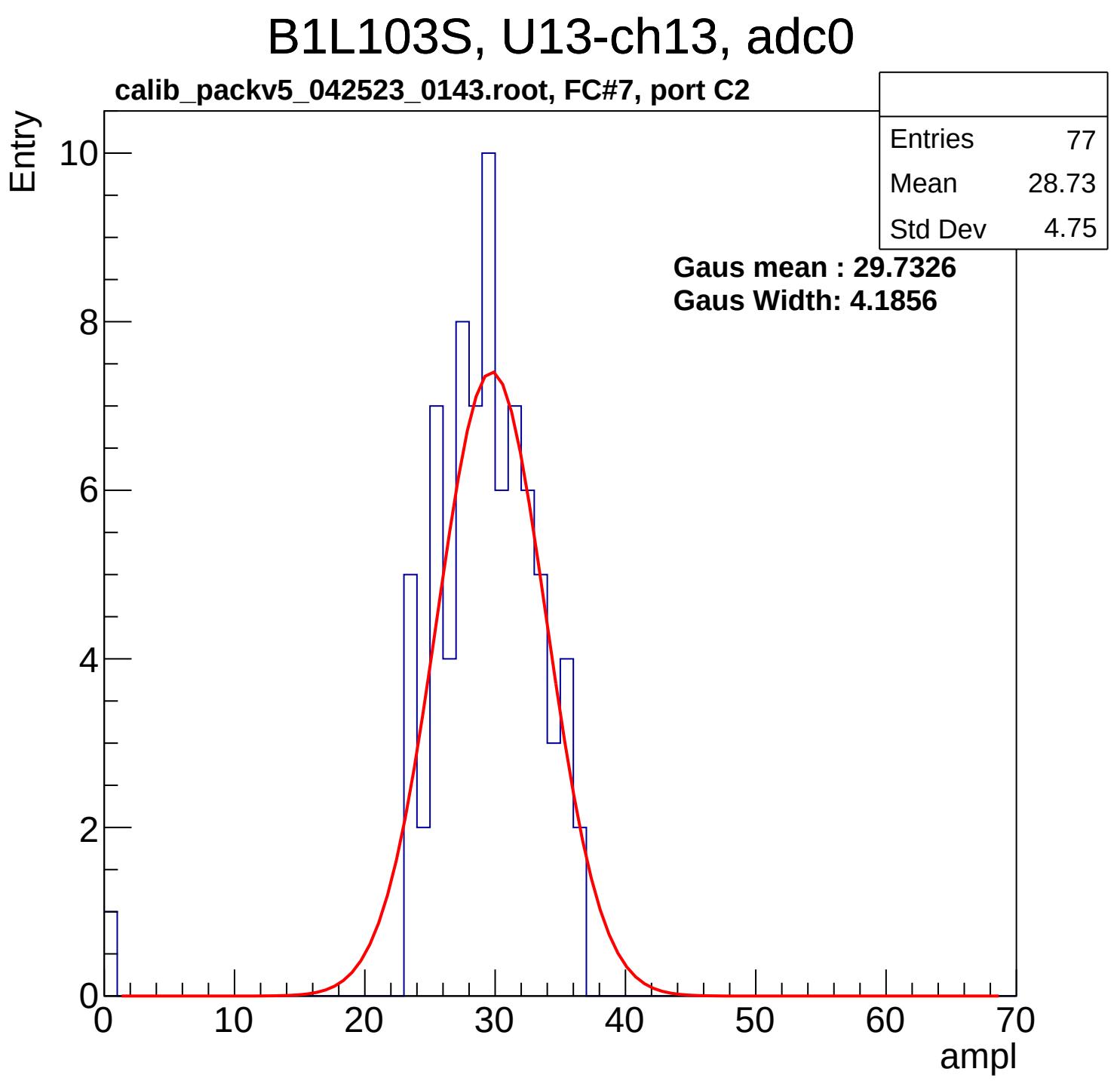

B1L103S, U13-ch12, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 60.94
Std Dev 1.657
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch12, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.6
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch12, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch13, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 28.73
Std Dev 4.75
Gaus mean : 29.7326
Gaus Width: 4.1856
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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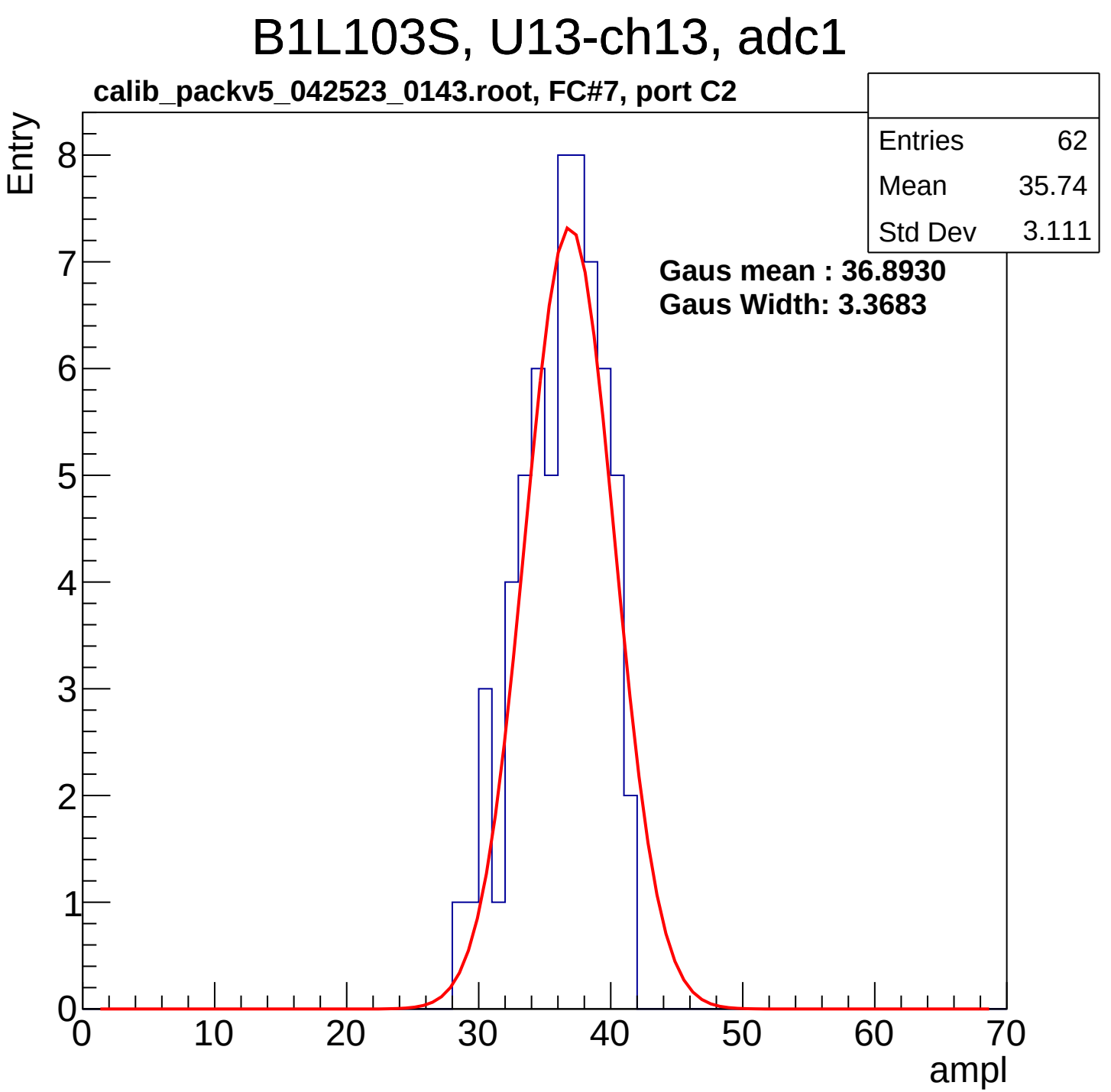
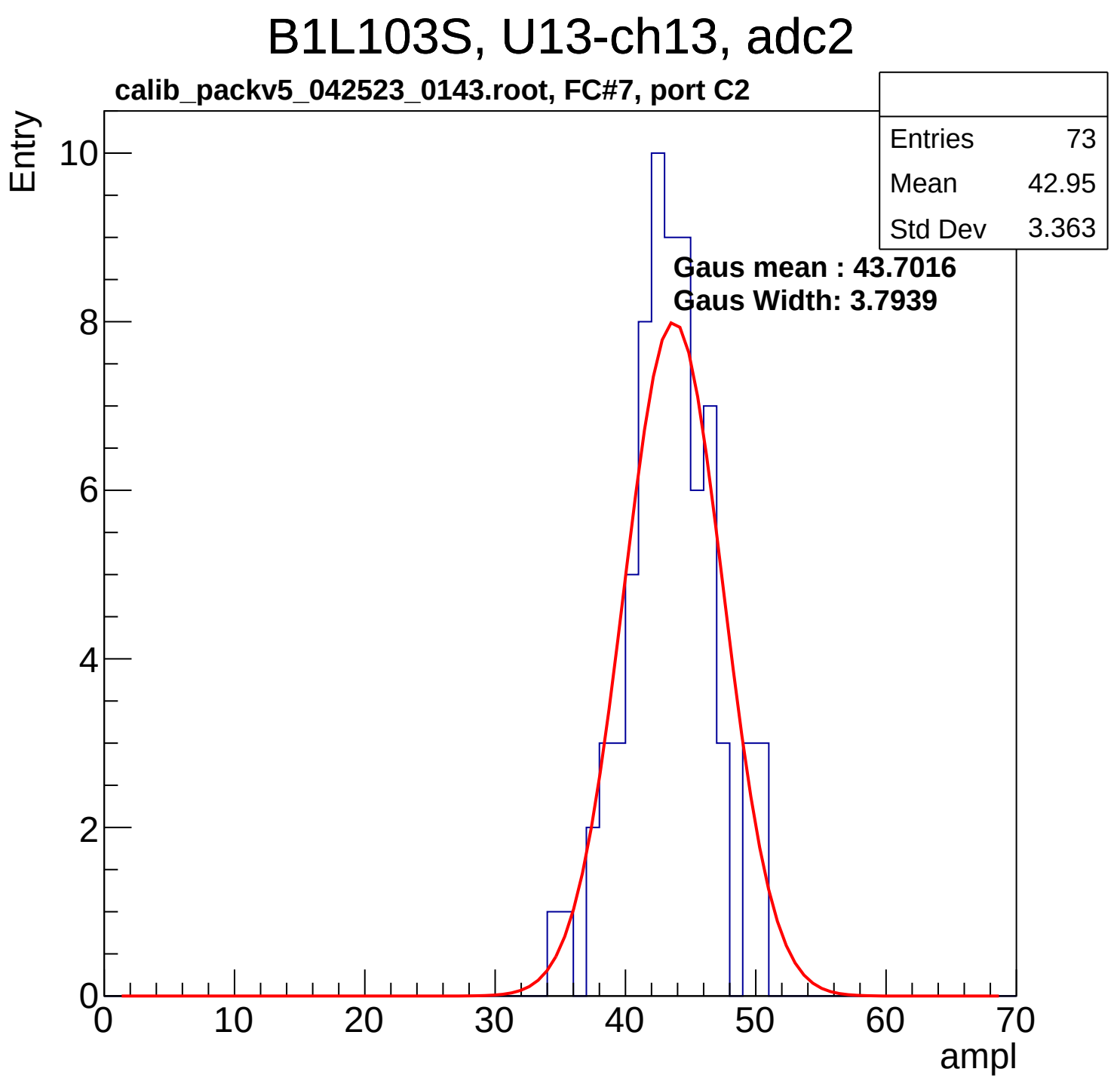
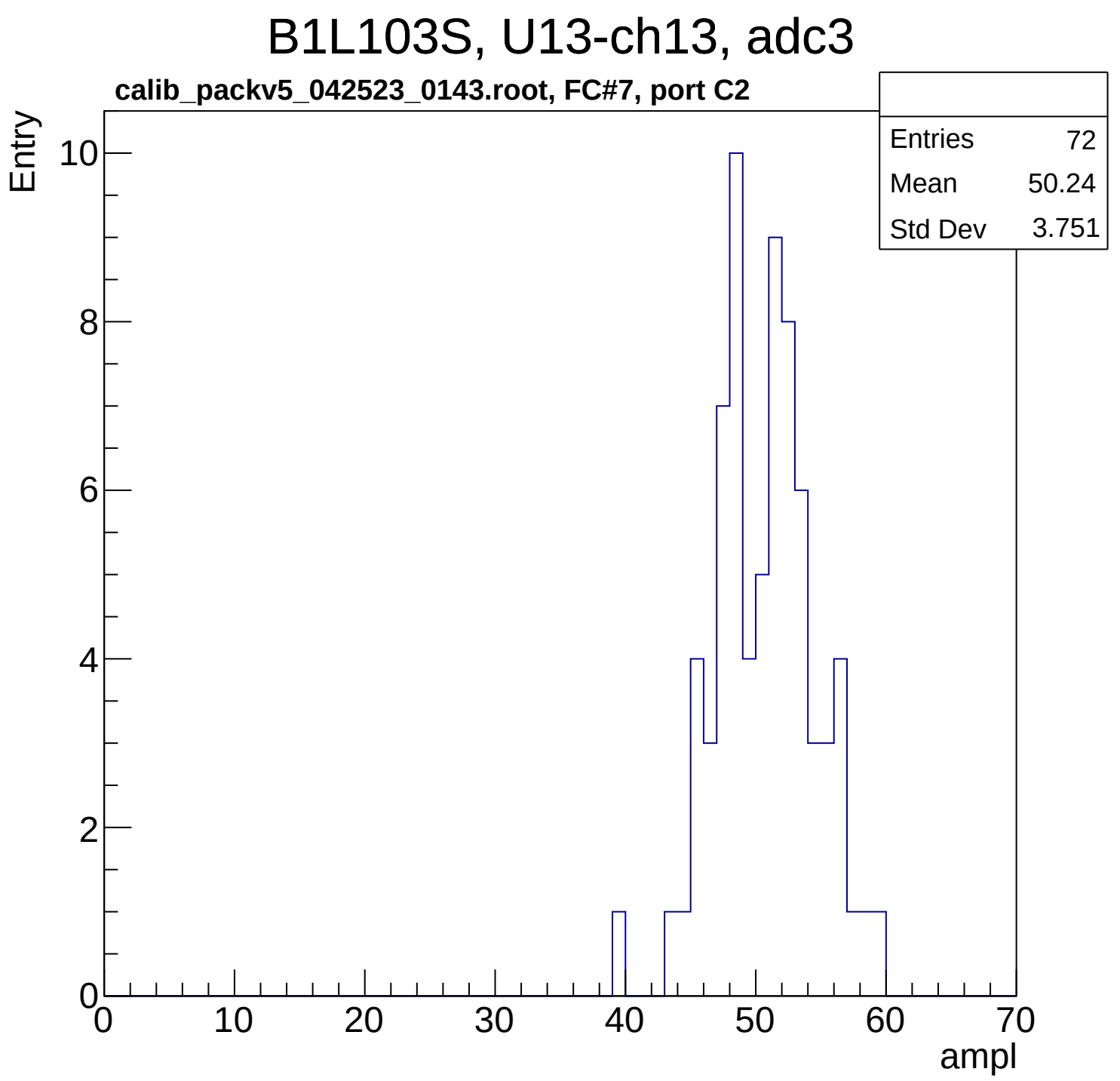
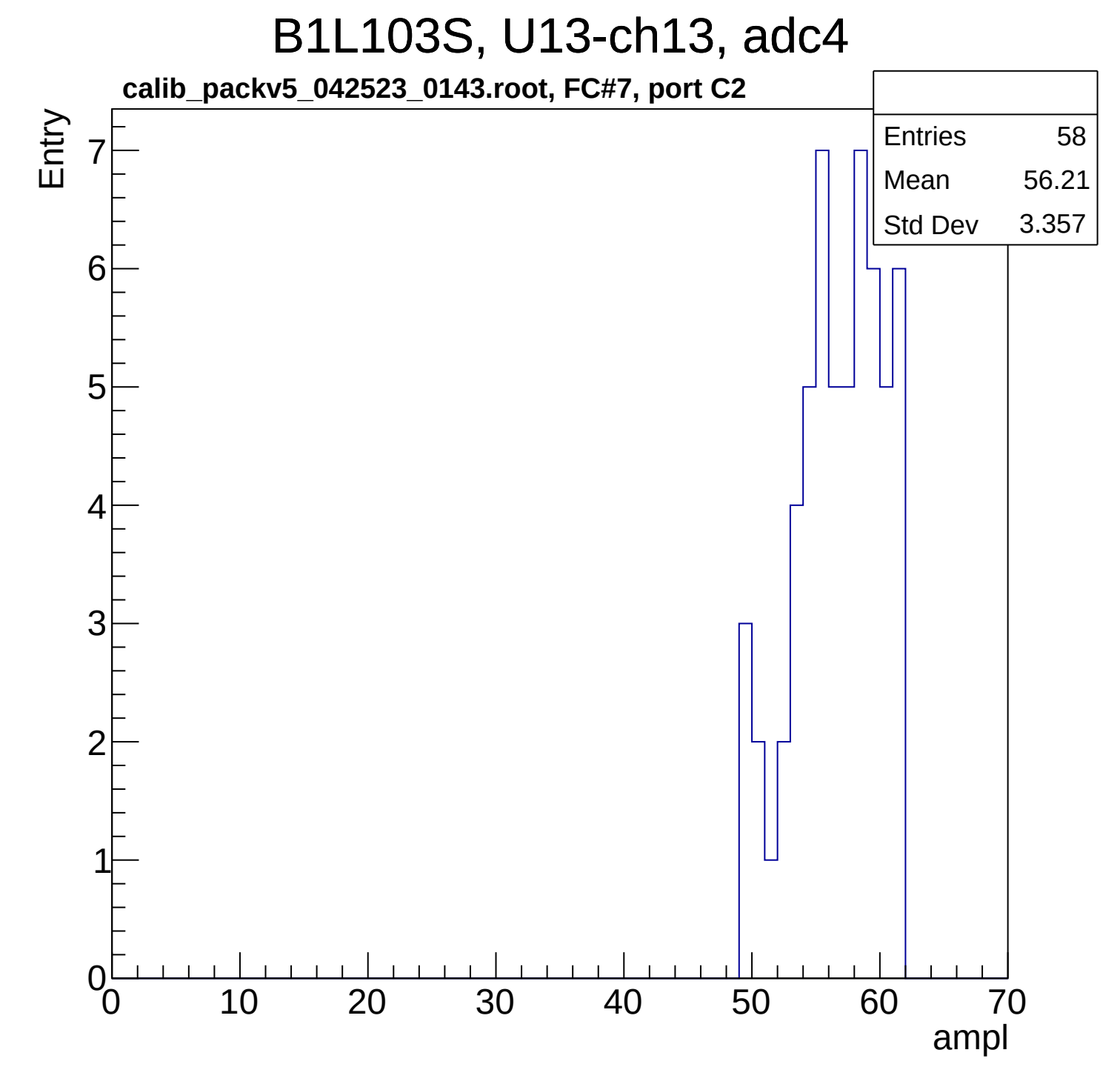
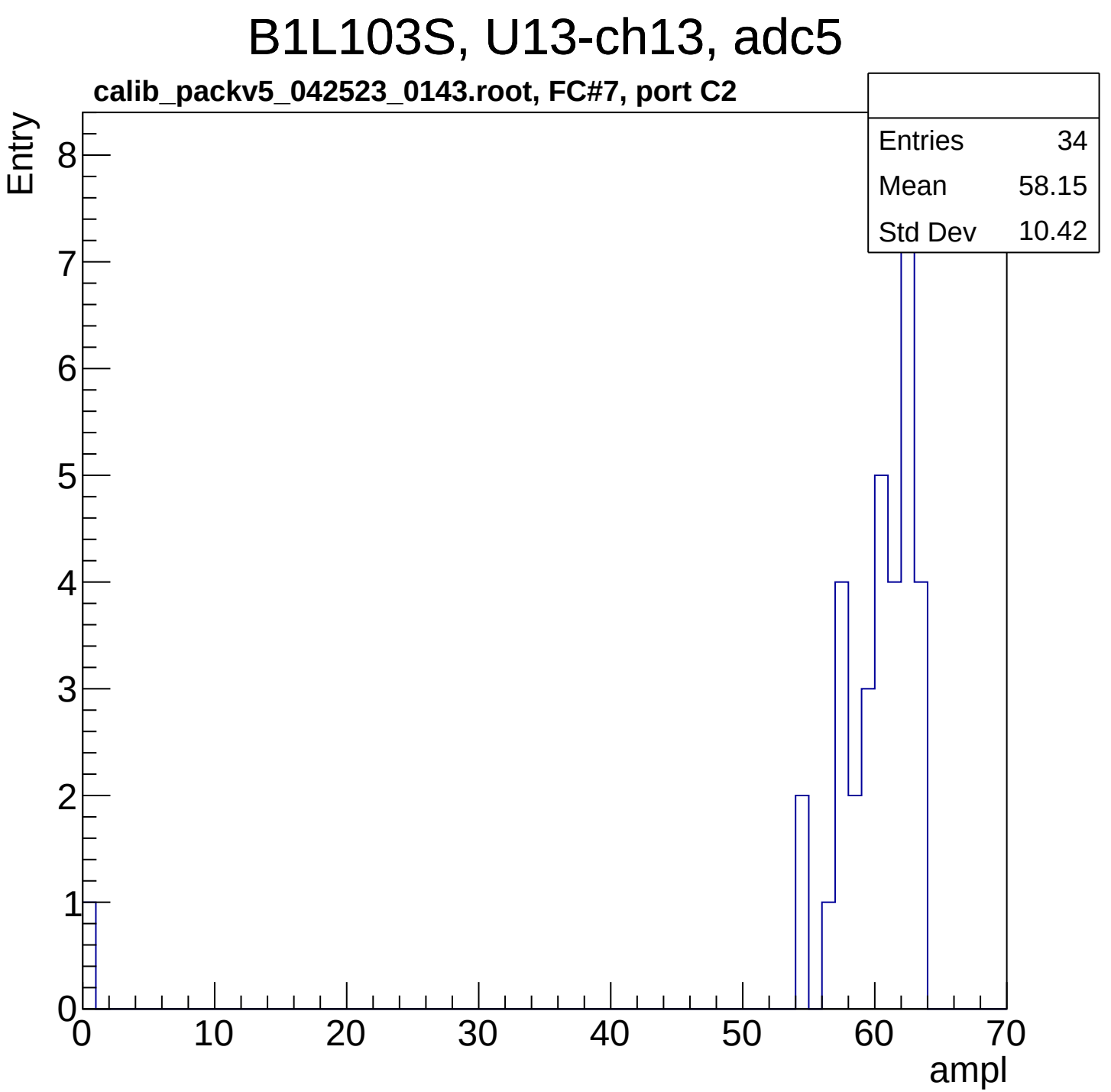
60
70


B1L103S, U13-ch13, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.74
Std Dev 3.111
Gaus mean : 36.8930
Gaus Width: 3.3683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch13, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 42.95
Std Dev 3.363
Gaus mean : 43.7016
Gaus Width: 3.7939
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch13, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.24
Std Dev 3.751
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch13, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.21
Std Dev 3.357
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch13, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.15
Std Dev 10.42
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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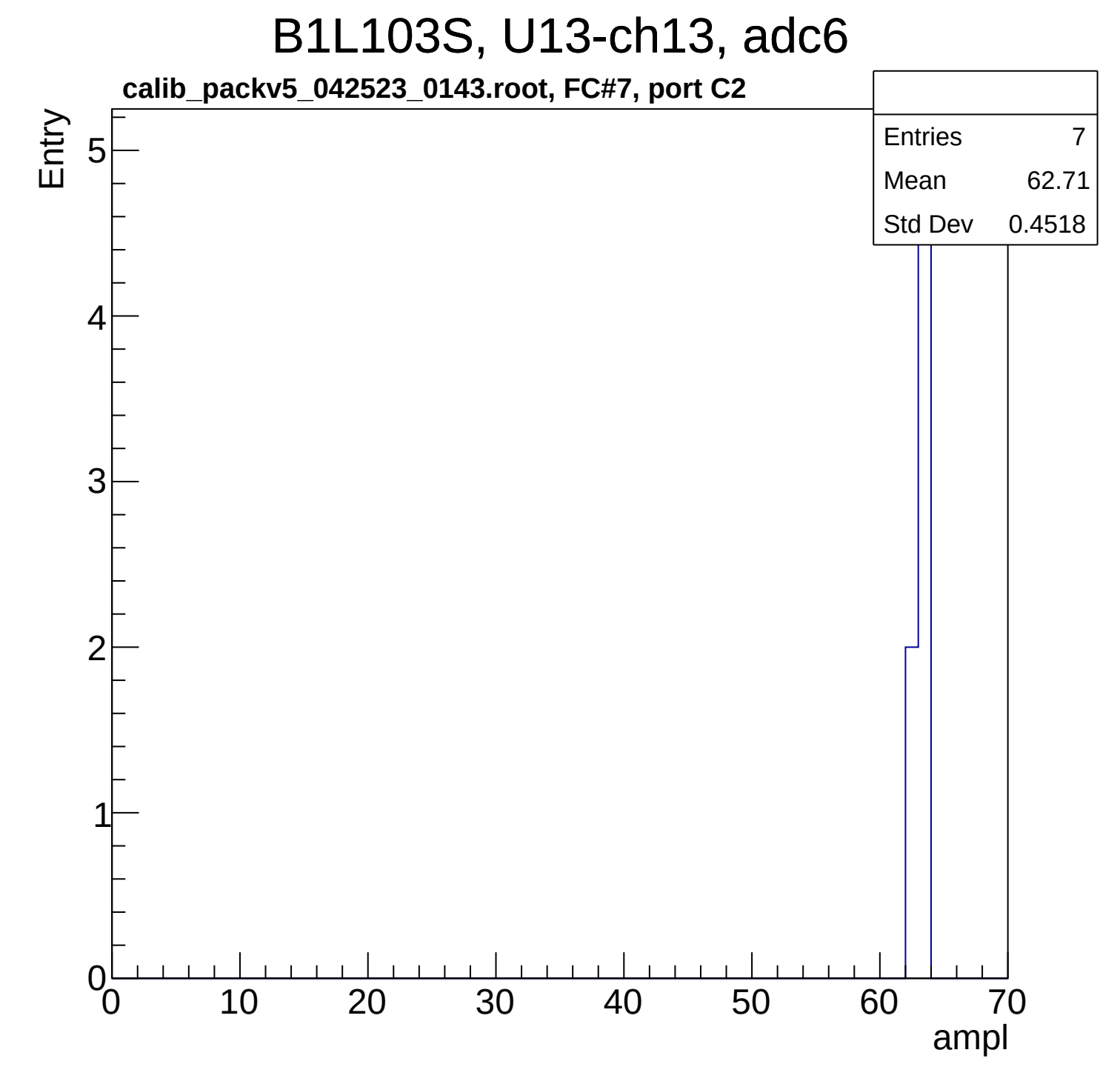
B1L103S, U13-ch13, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
ampl
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0
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20
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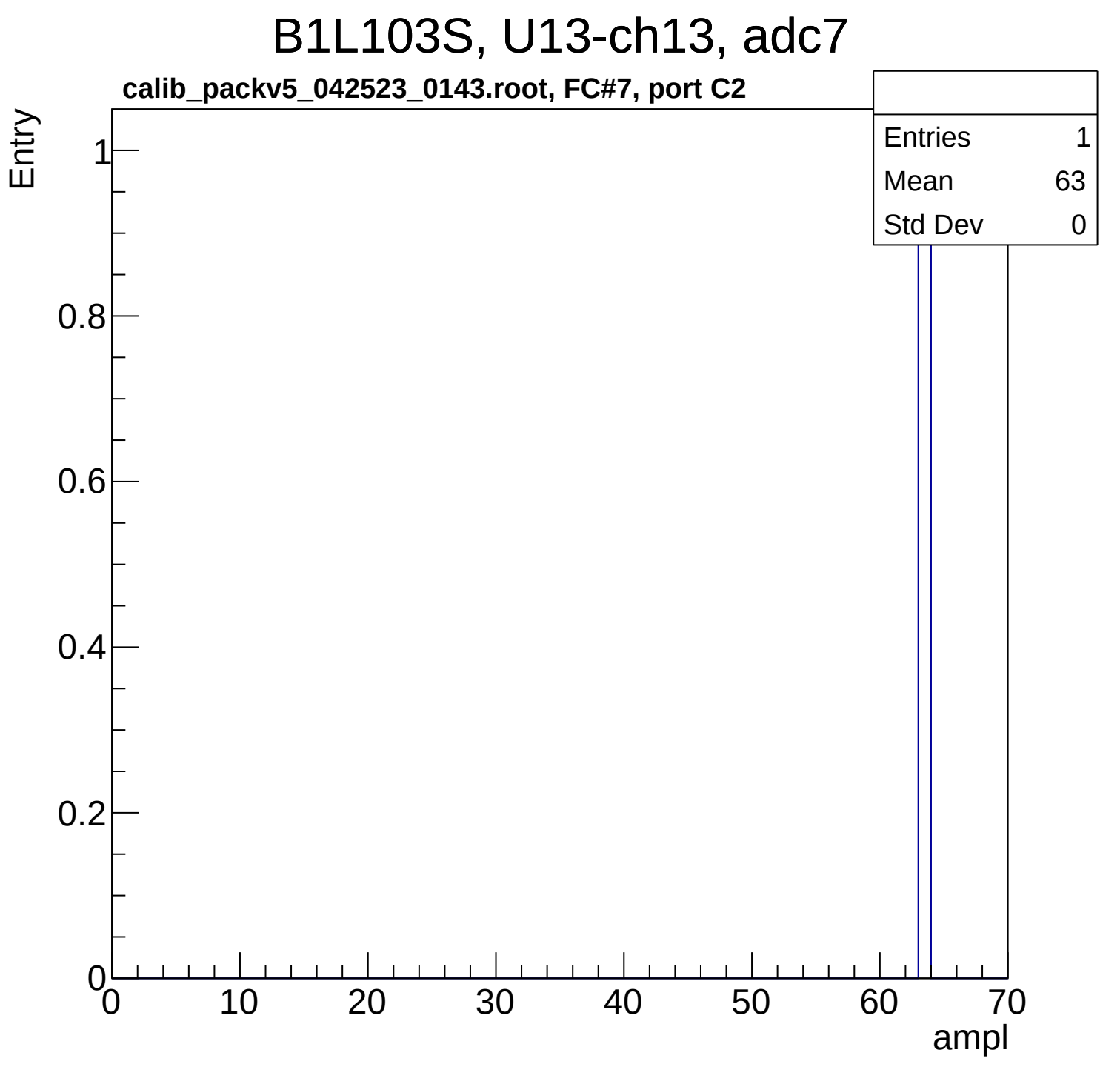
40
50
60
70


B1L103S, U13-ch13, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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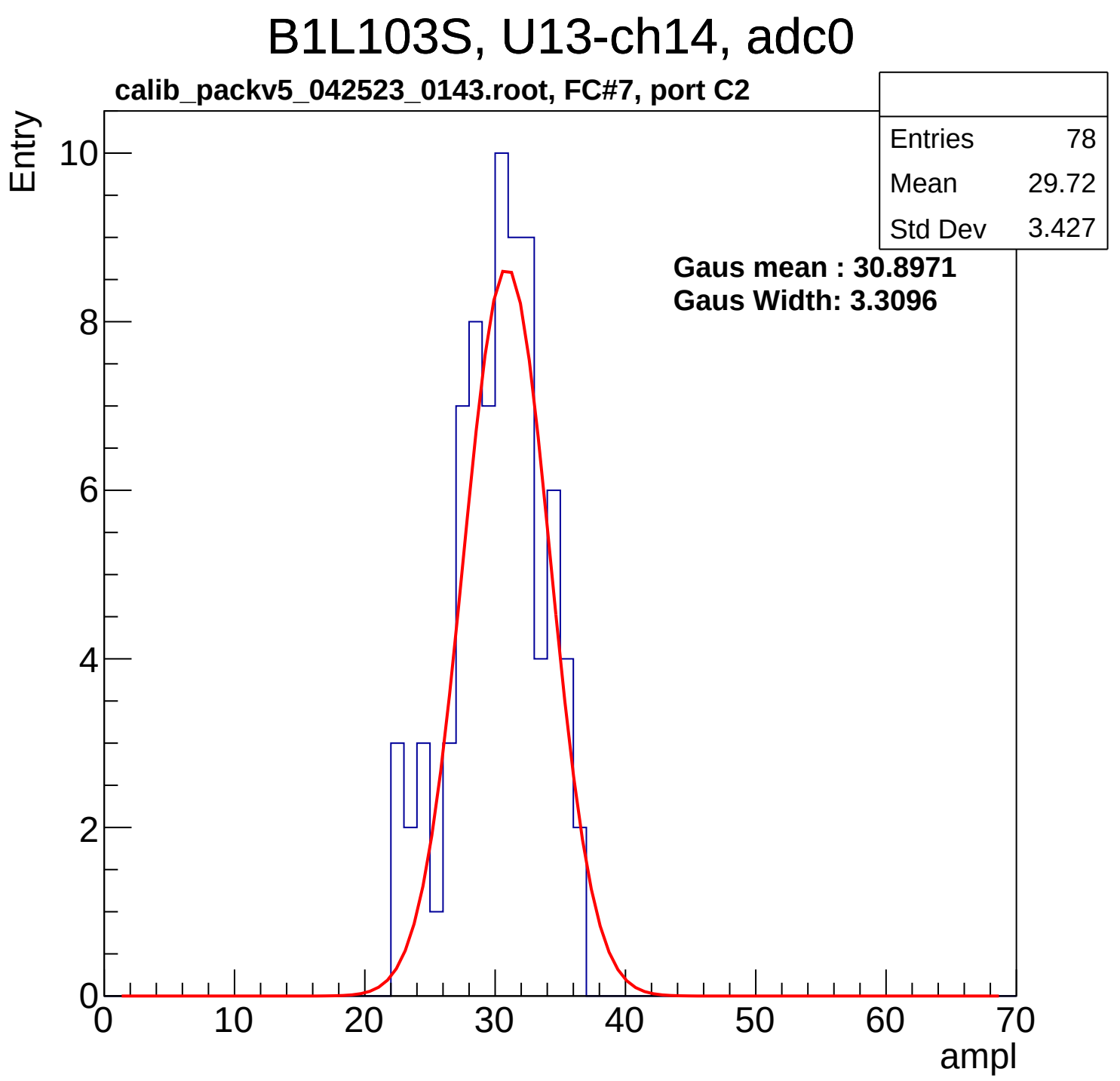
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch14, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 29.72
Std Dev 3.427
Gaus mean : 30.8971
Gaus Width: 3.3096
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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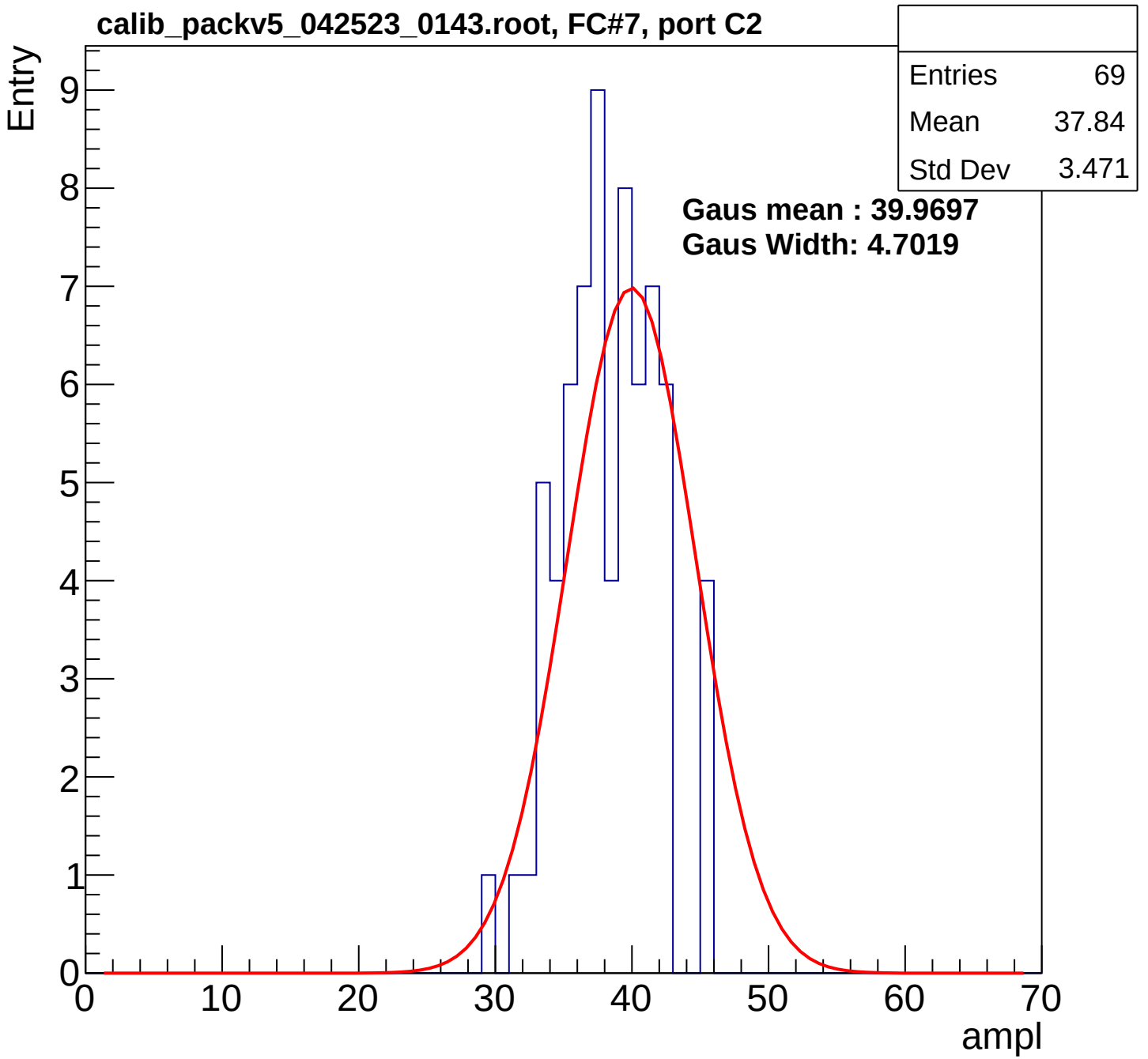
B1L103S, U13-ch14, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 37.84
Std Dev 3.471
Gaus mean : 39.9697
Gaus Width: 4.7019
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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60
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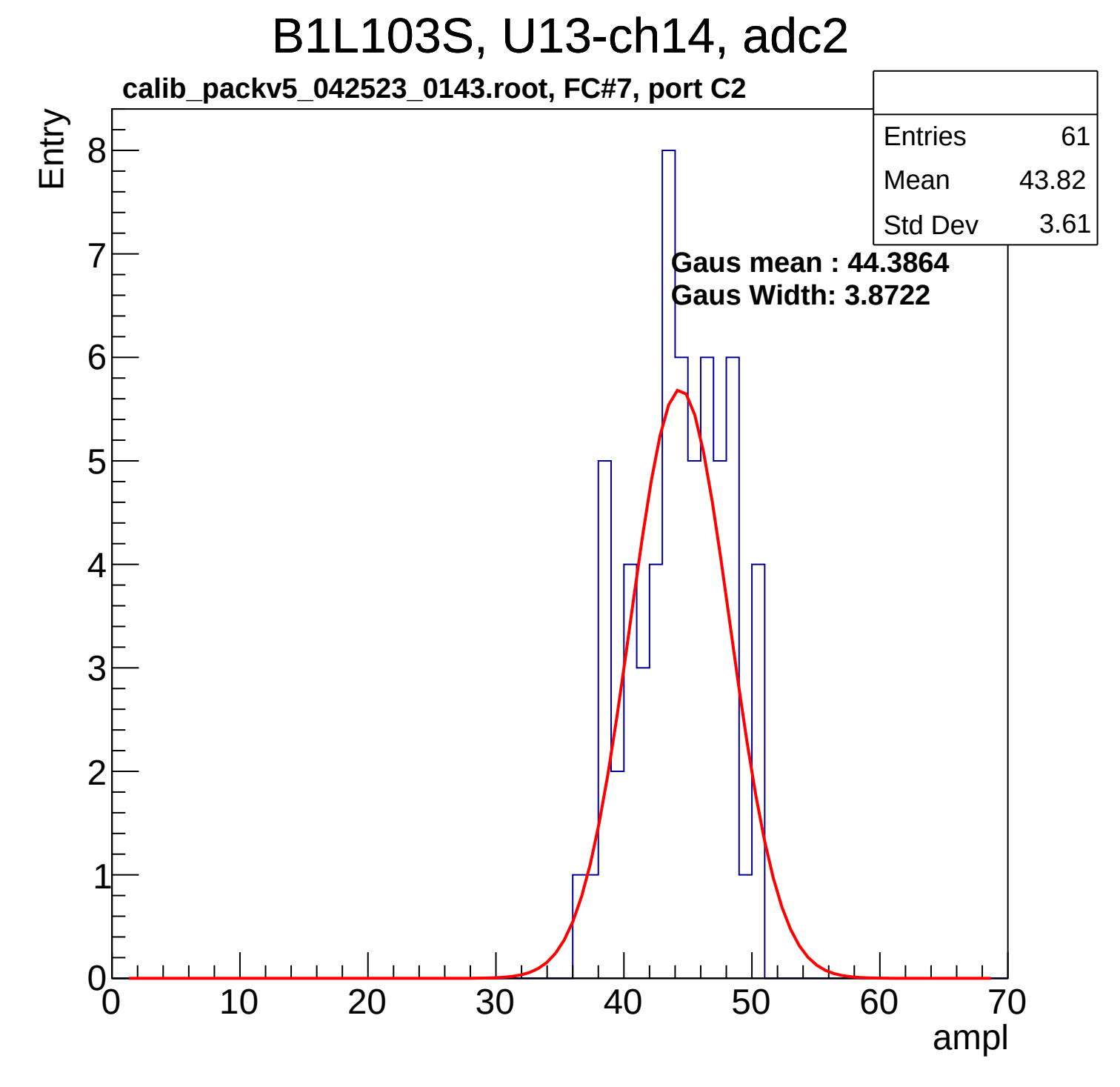
B1L103S, U13-ch14, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.82
Std Dev 3.61
Gaus mean : 44.3864
Gaus Width: 3.8722
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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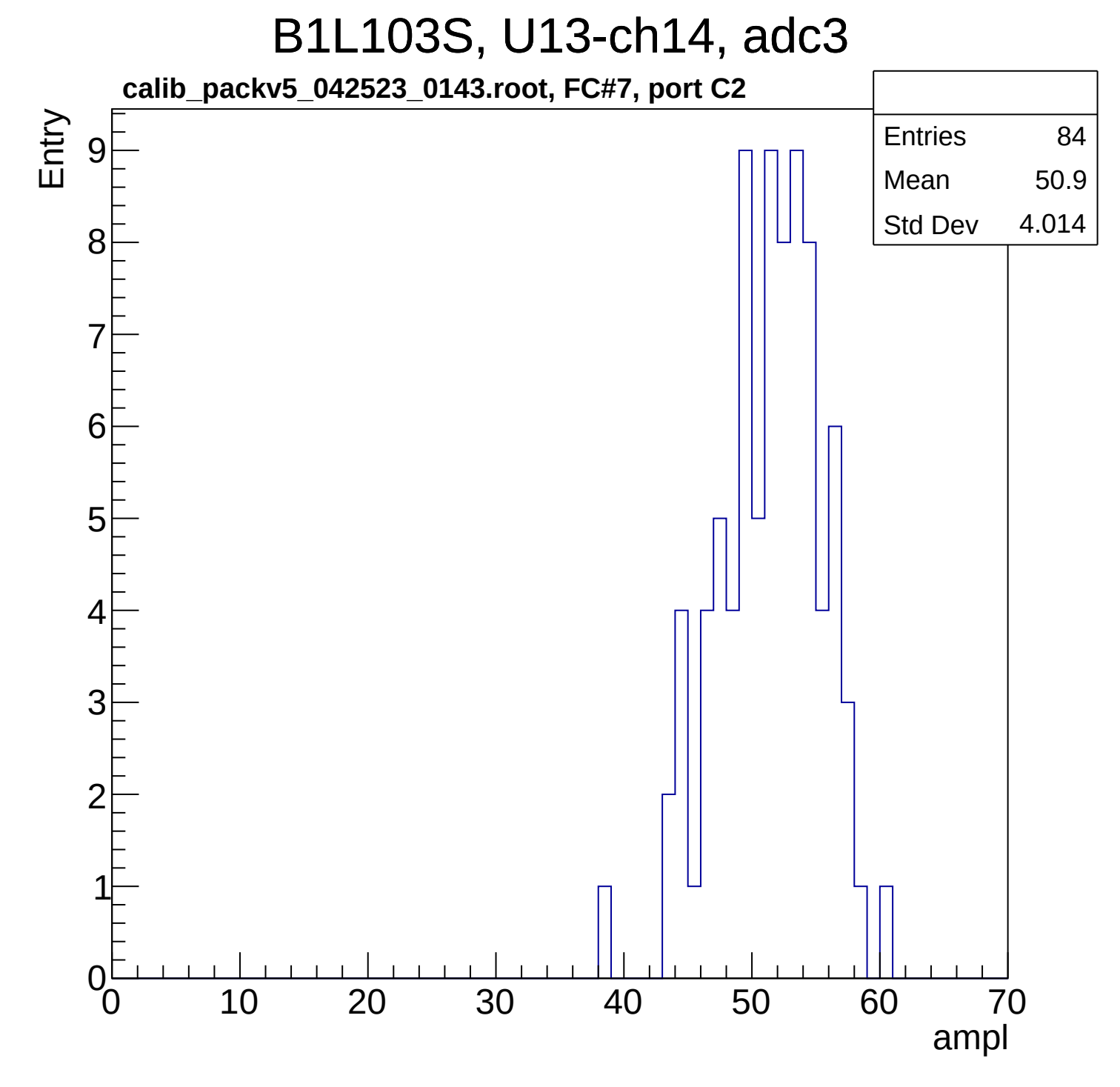
B1L103S, U13-ch14, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 50.9
Std Dev 4.014
Entry
ampl
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6
7
8
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0
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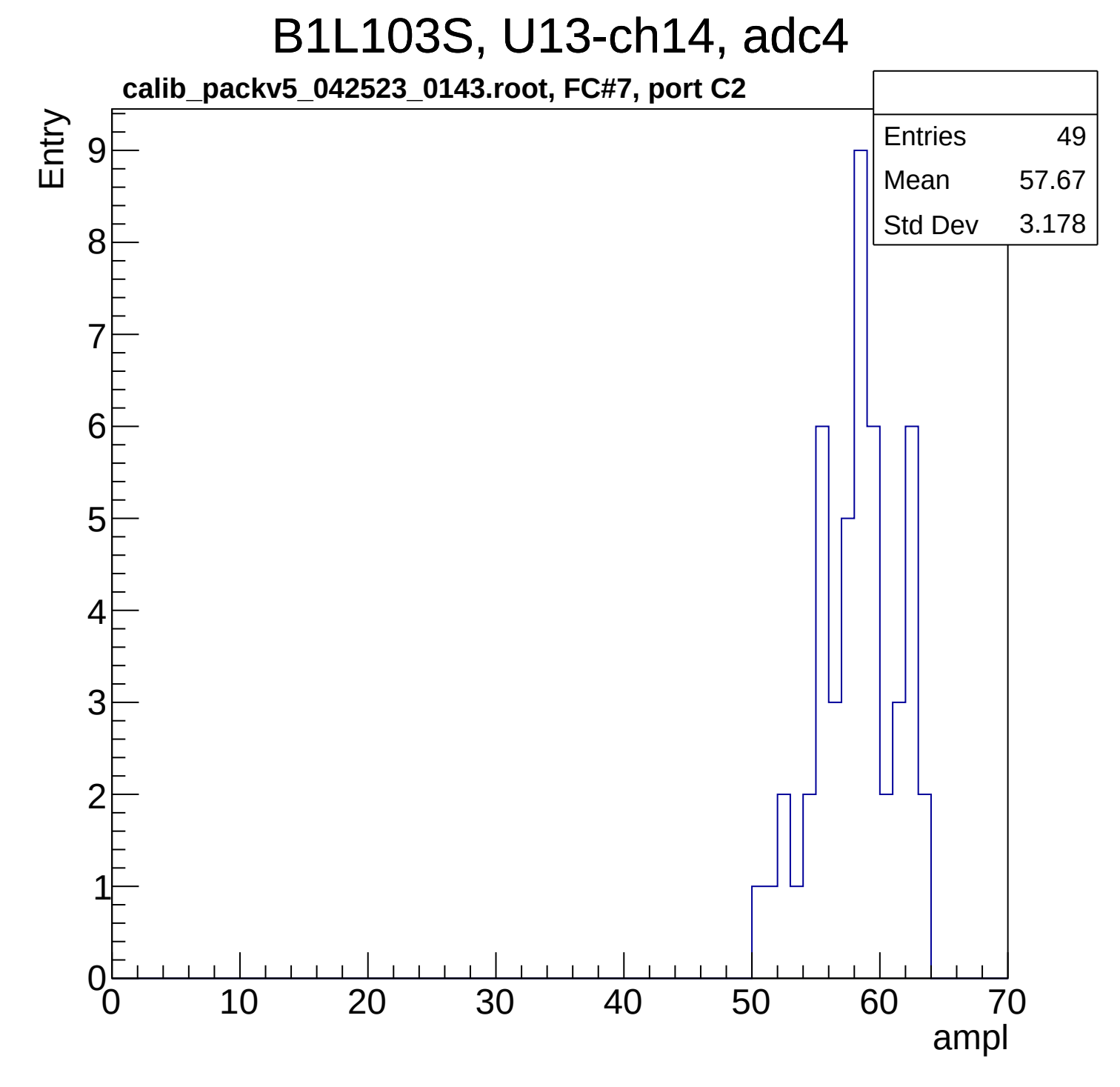
B1L103S, U13-ch14, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 57.67
Std Dev 3.178
Entry
ampl
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6
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0
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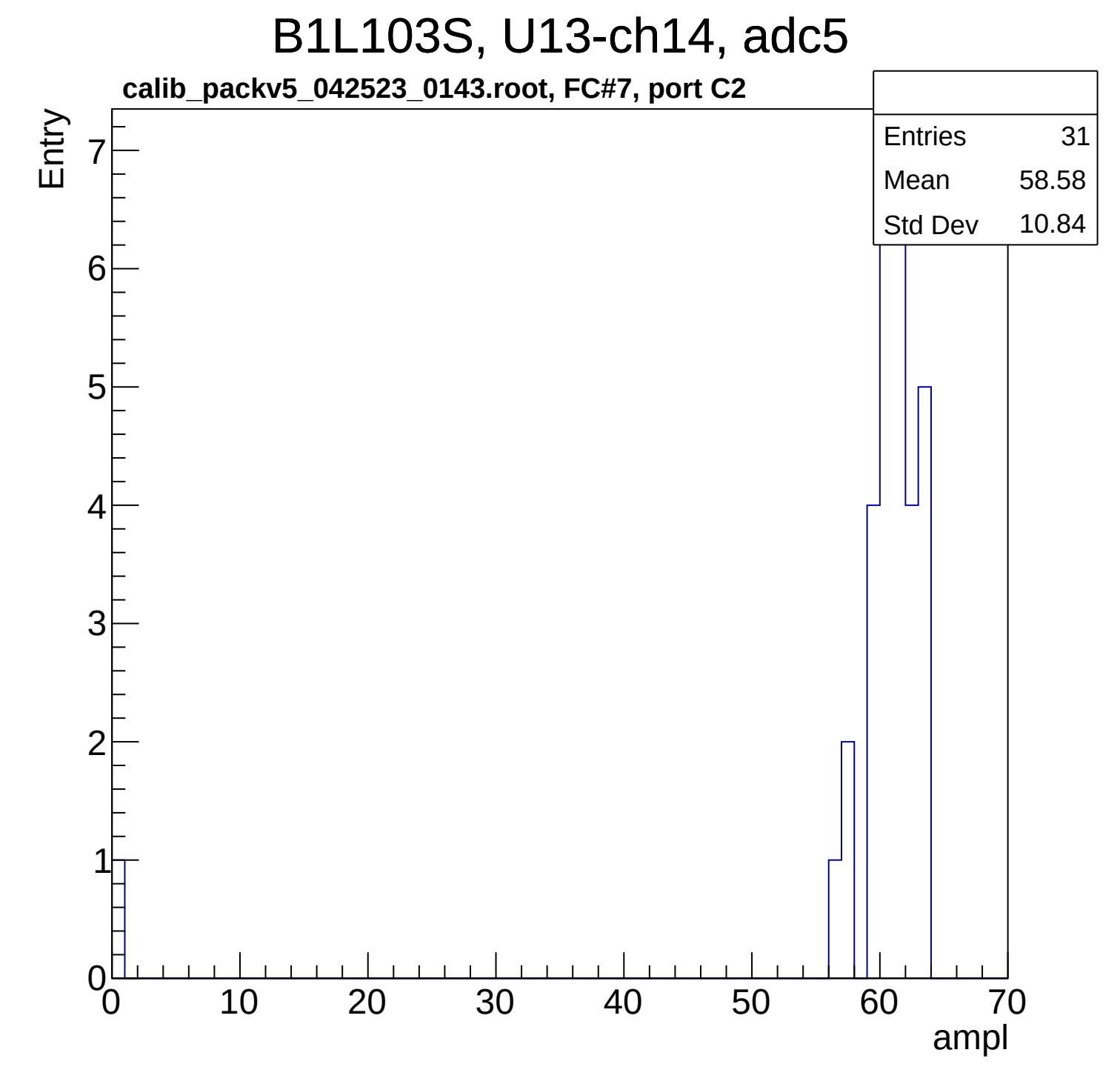
B1L103S, U13-ch14, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 58.58
Std Dev 10.84
Entry
ampl
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3
4
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0
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60
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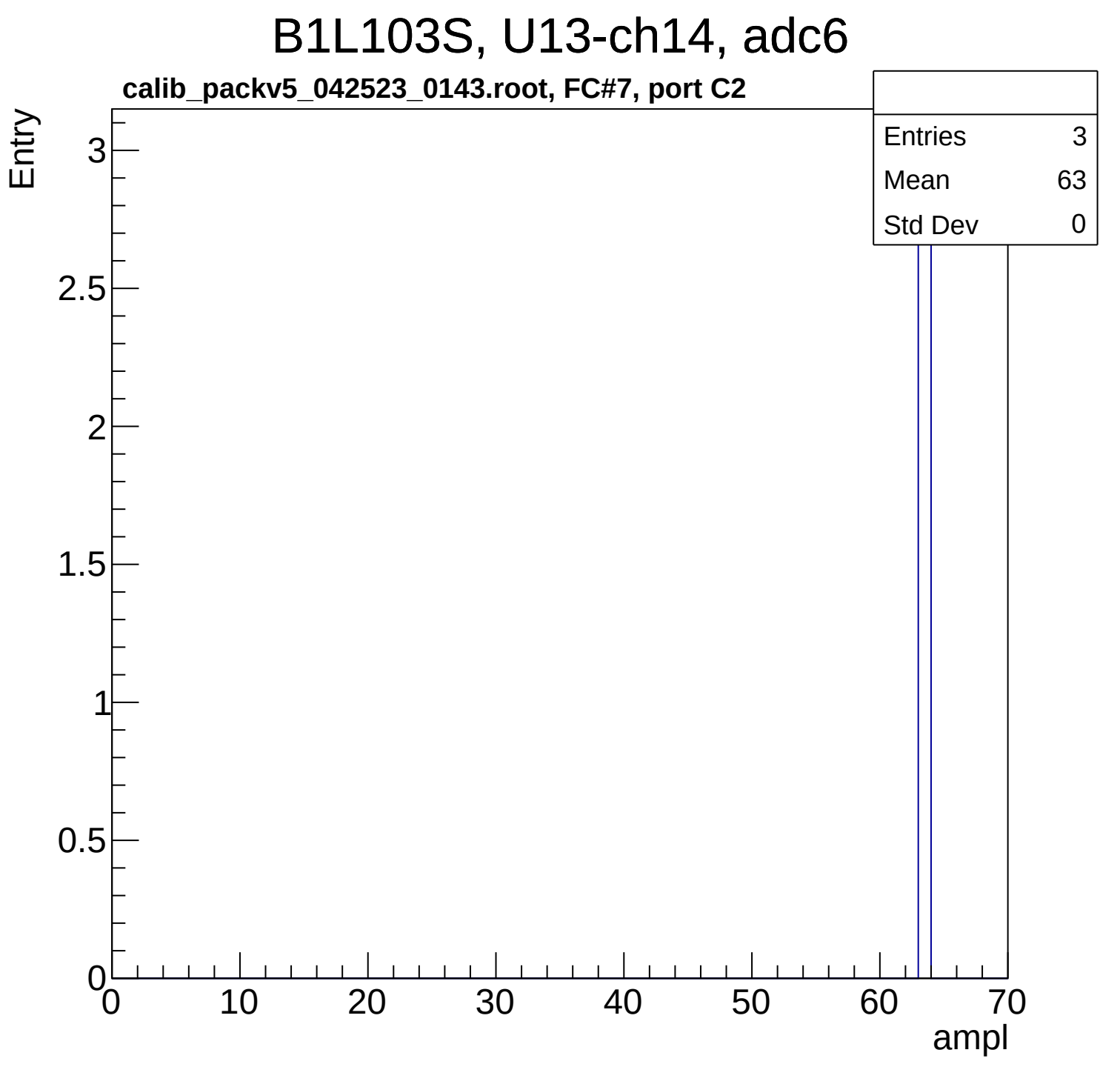
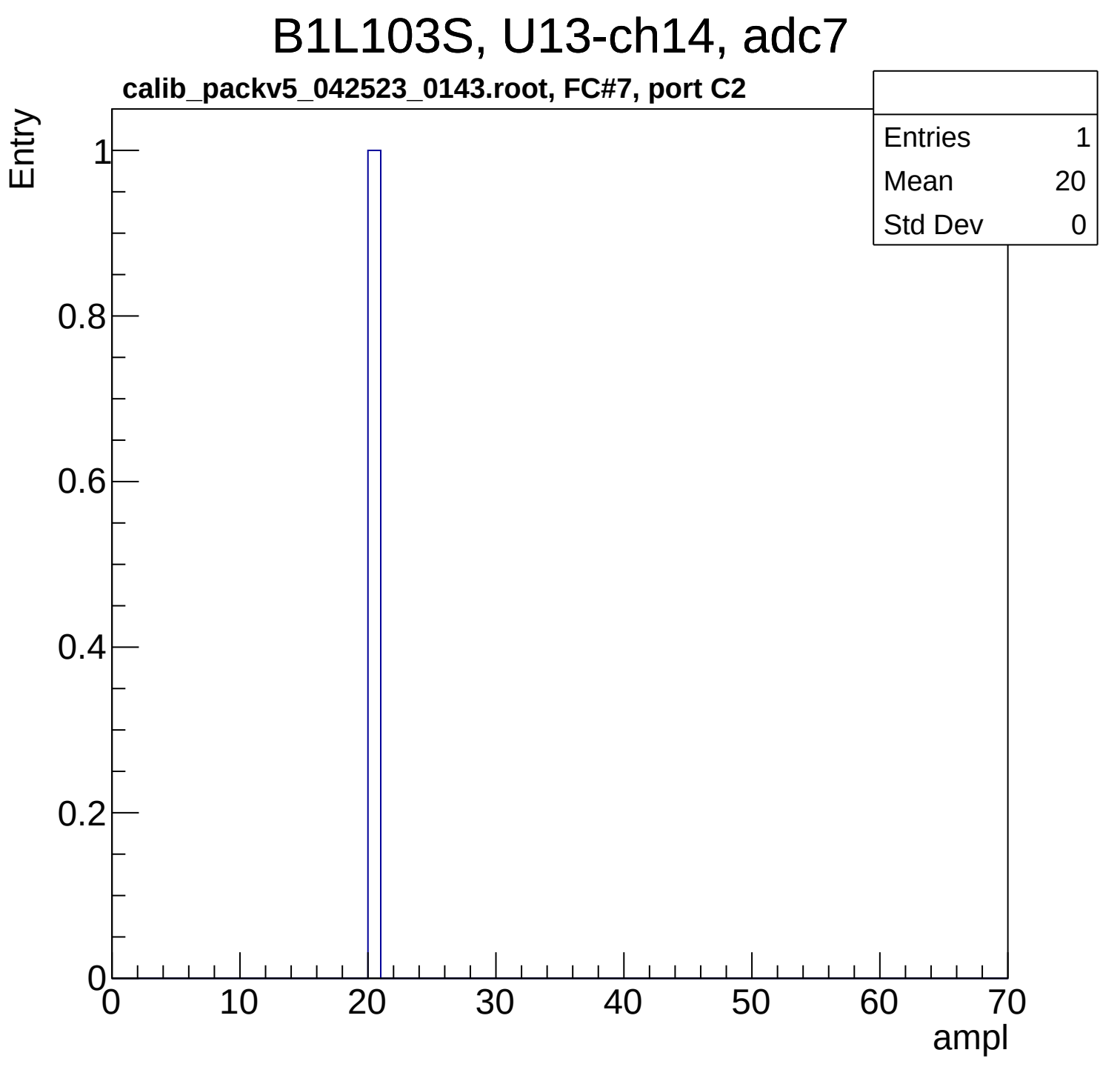

B1L103S, U13-ch14, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch14, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
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0
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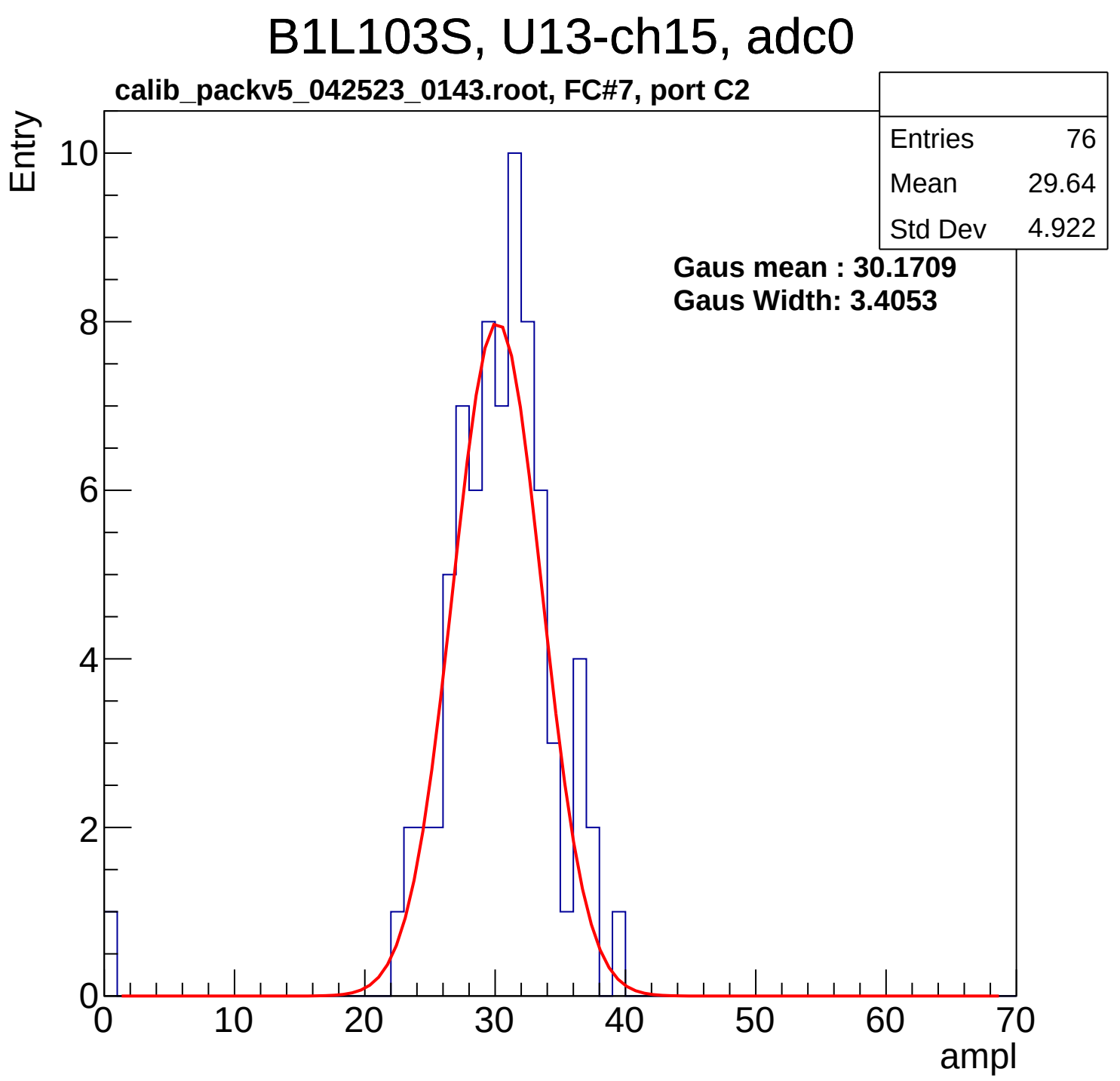
40
50
60
70


B1L103S, U13-ch15, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.64
Std Dev 4.922
Gaus mean : 30.1709
Gaus Width: 3.4053
Entry
ampl
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4
6
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10
0
10
20
30
40
50
60
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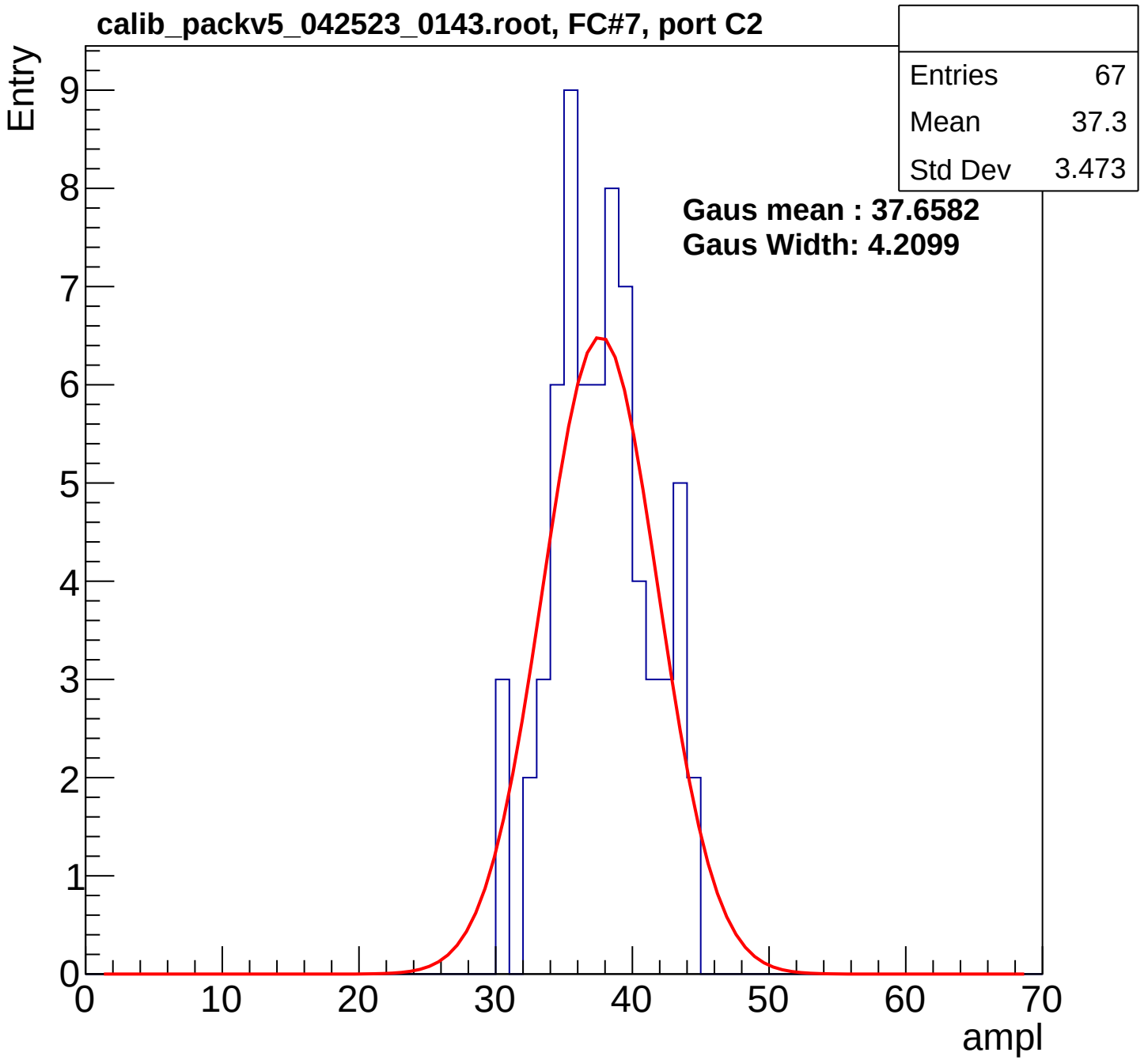
B1L103S, U13-ch15, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.3
Std Dev 3.473
Gaus mean : 37.6582
Gaus Width: 4.2099
Entry
ampl
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3
4
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6
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8
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0
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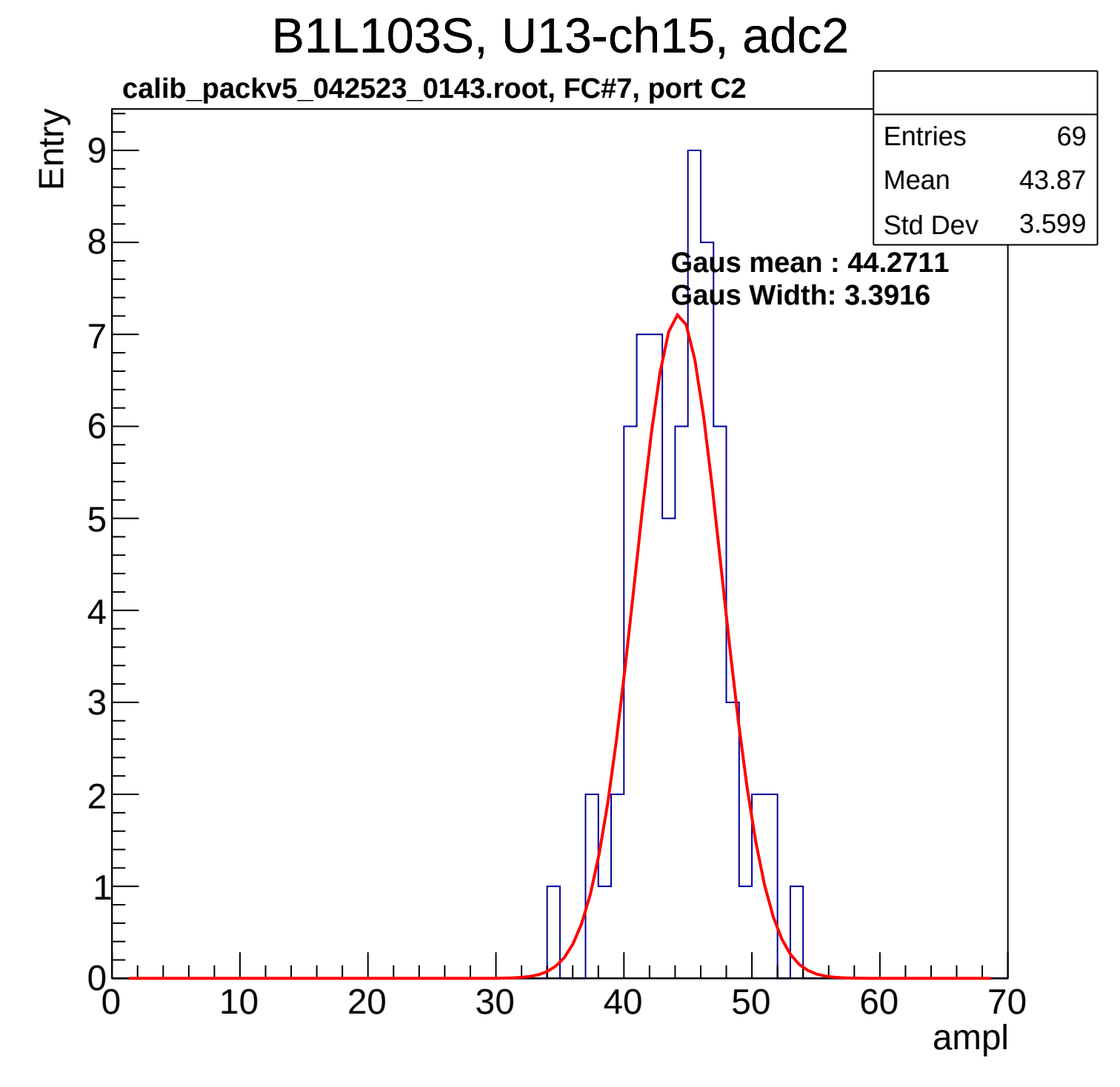
B1L103S, U13-ch15, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 43.87
Std Dev 3.599
Gaus mean : 44.2711
Gaus Width: 3.3916
Entry
ampl
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2
3
4
5
6
7
8
9
0
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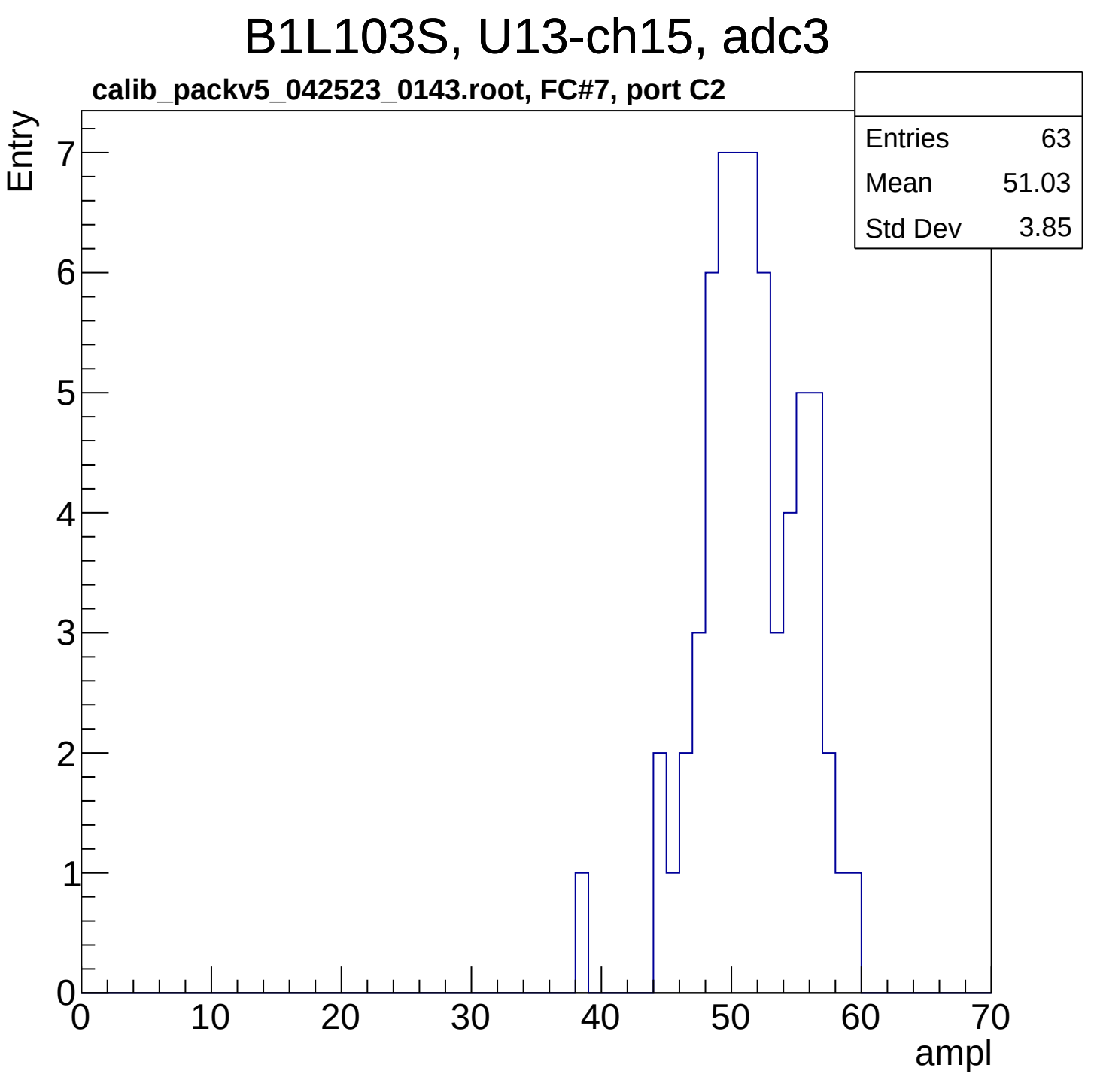
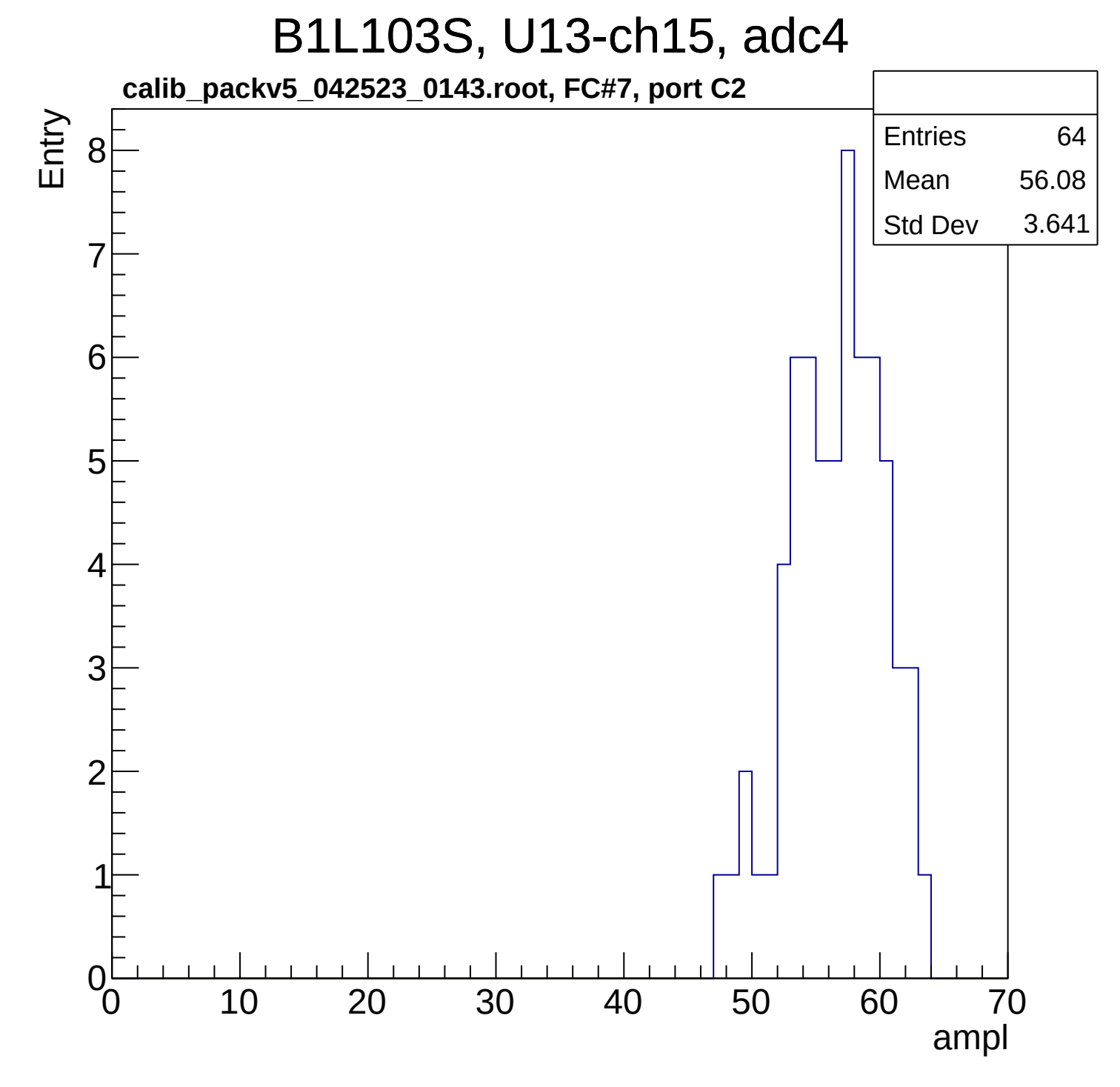

B1L103S, U13-ch15, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 51.03
Std Dev 3.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch15, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 56.08
Std Dev 3.641
Entry
ampl
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2
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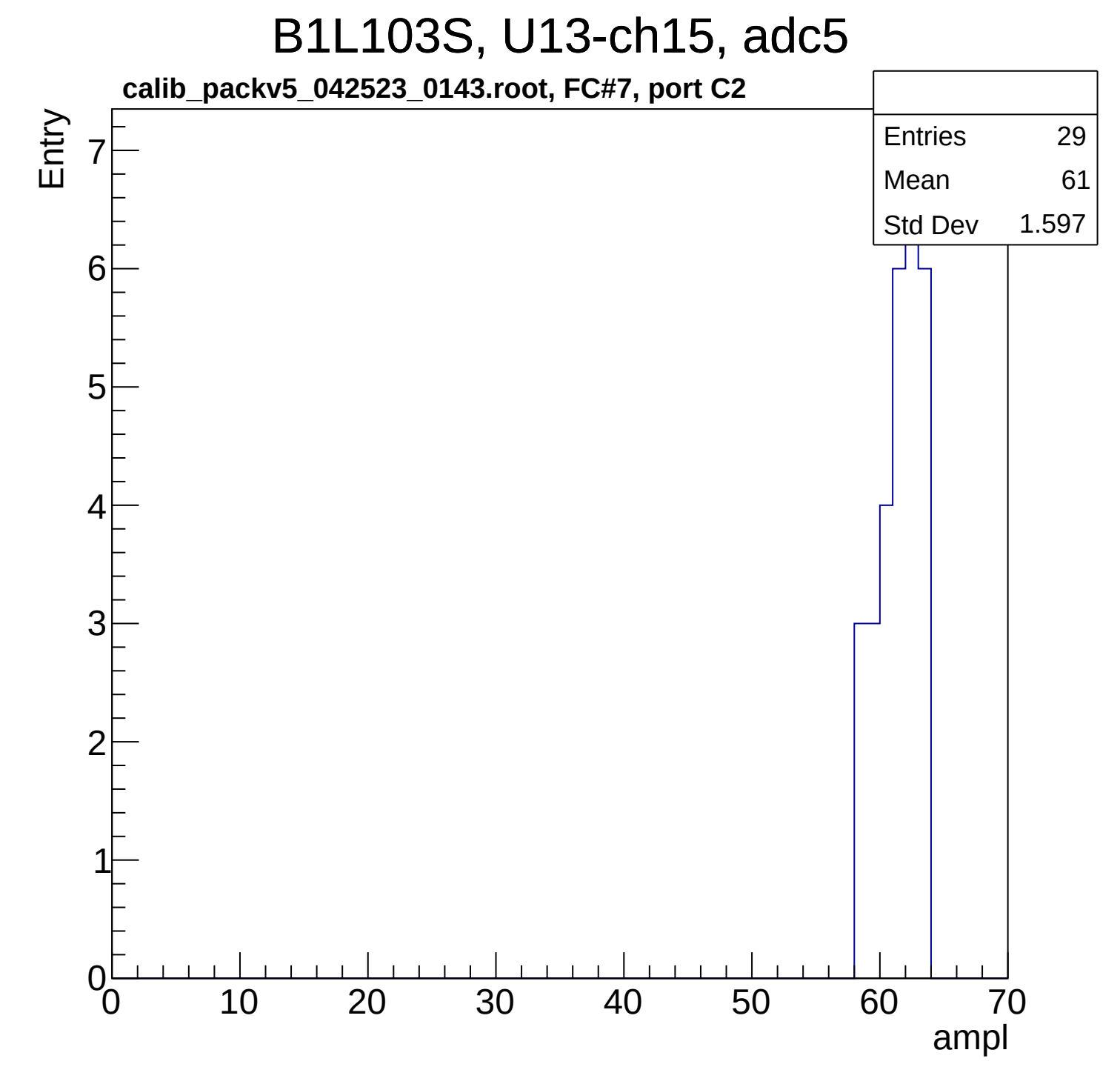
B1L103S, U13-ch15, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 61
Std Dev 1.597
Entry
ampl
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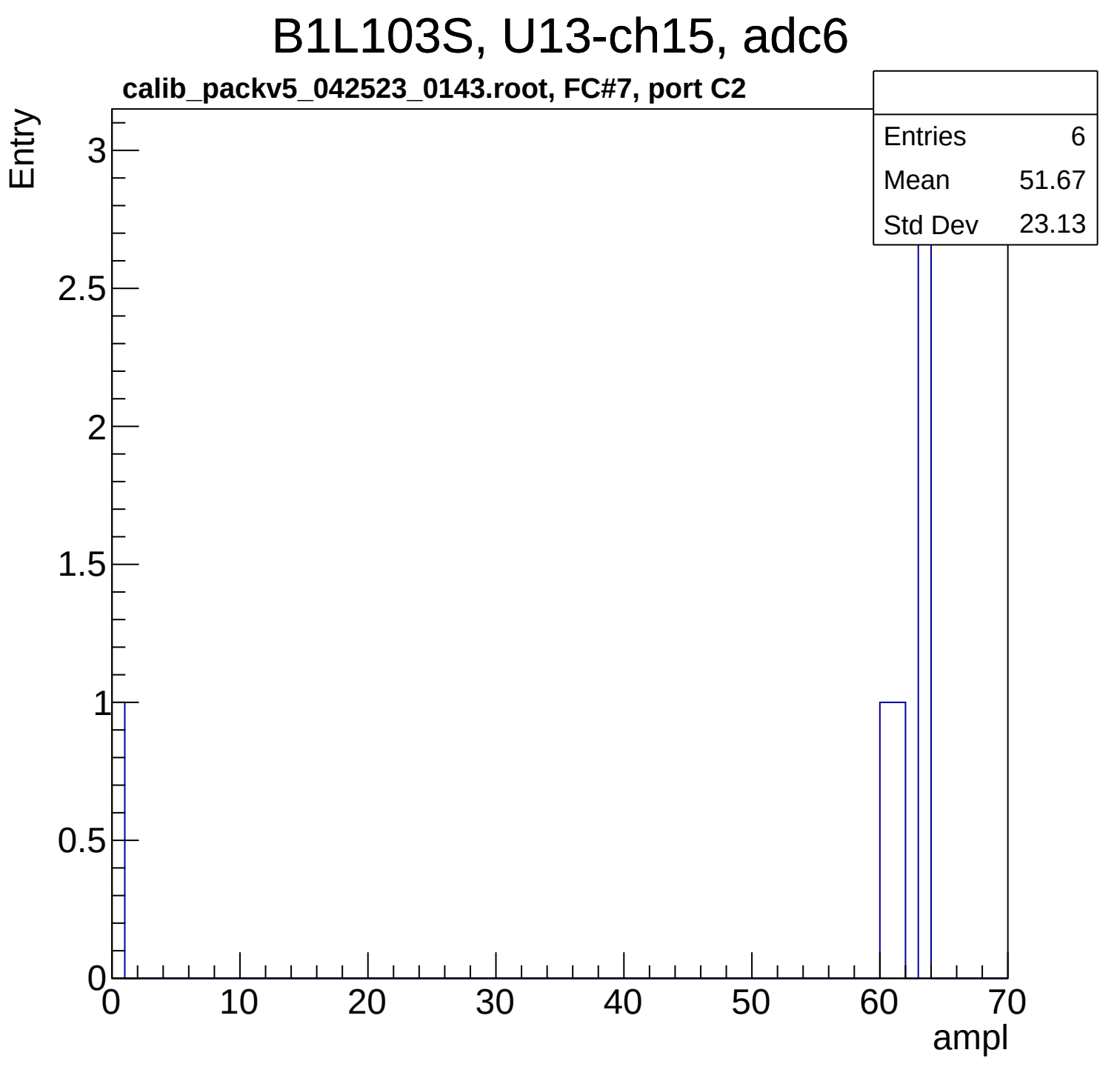
B1L103S, U13-ch15, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.67
Std Dev 23.13
Entry
0
0.5
1
1.5
2
2.5
3
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10
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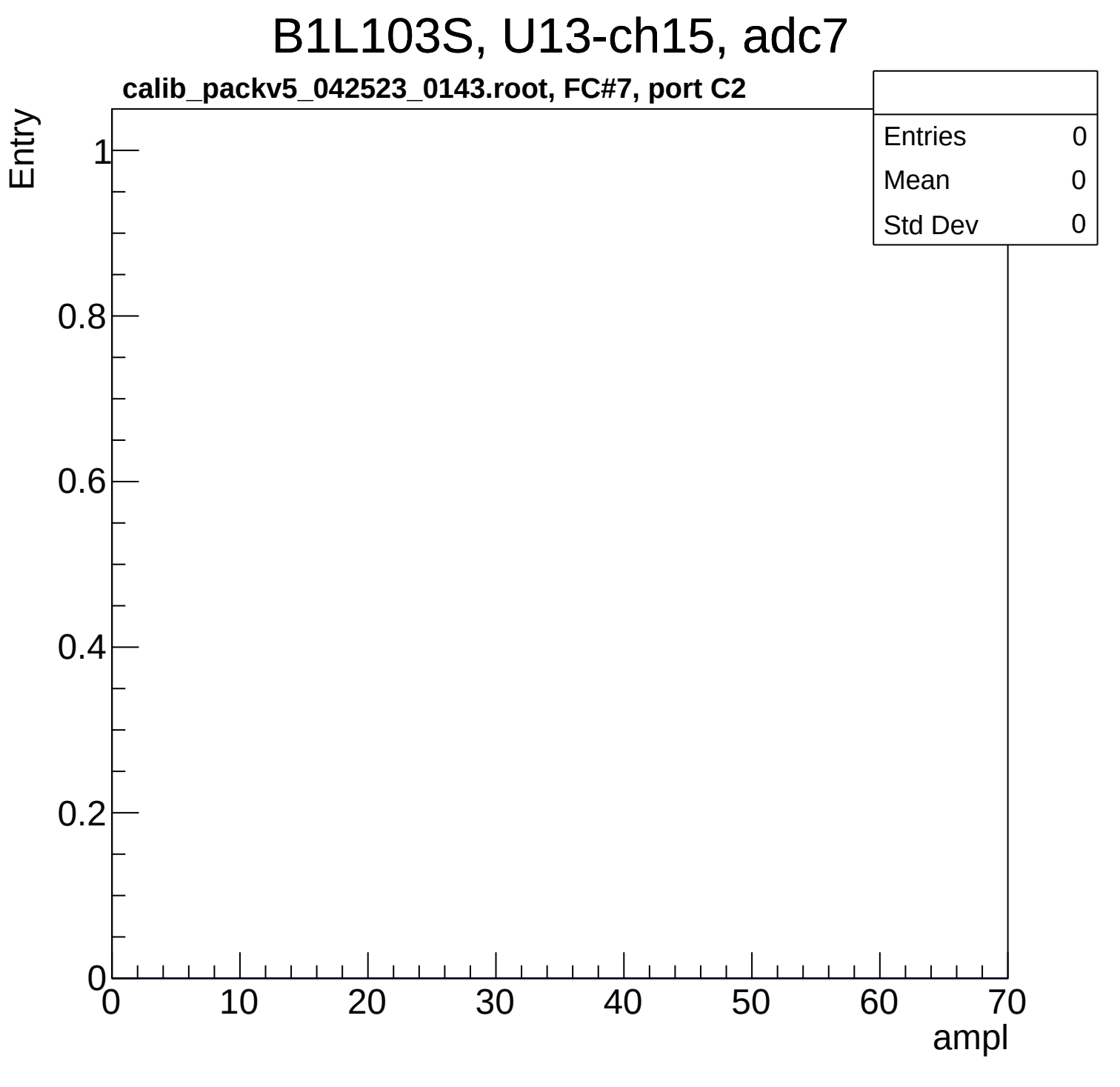
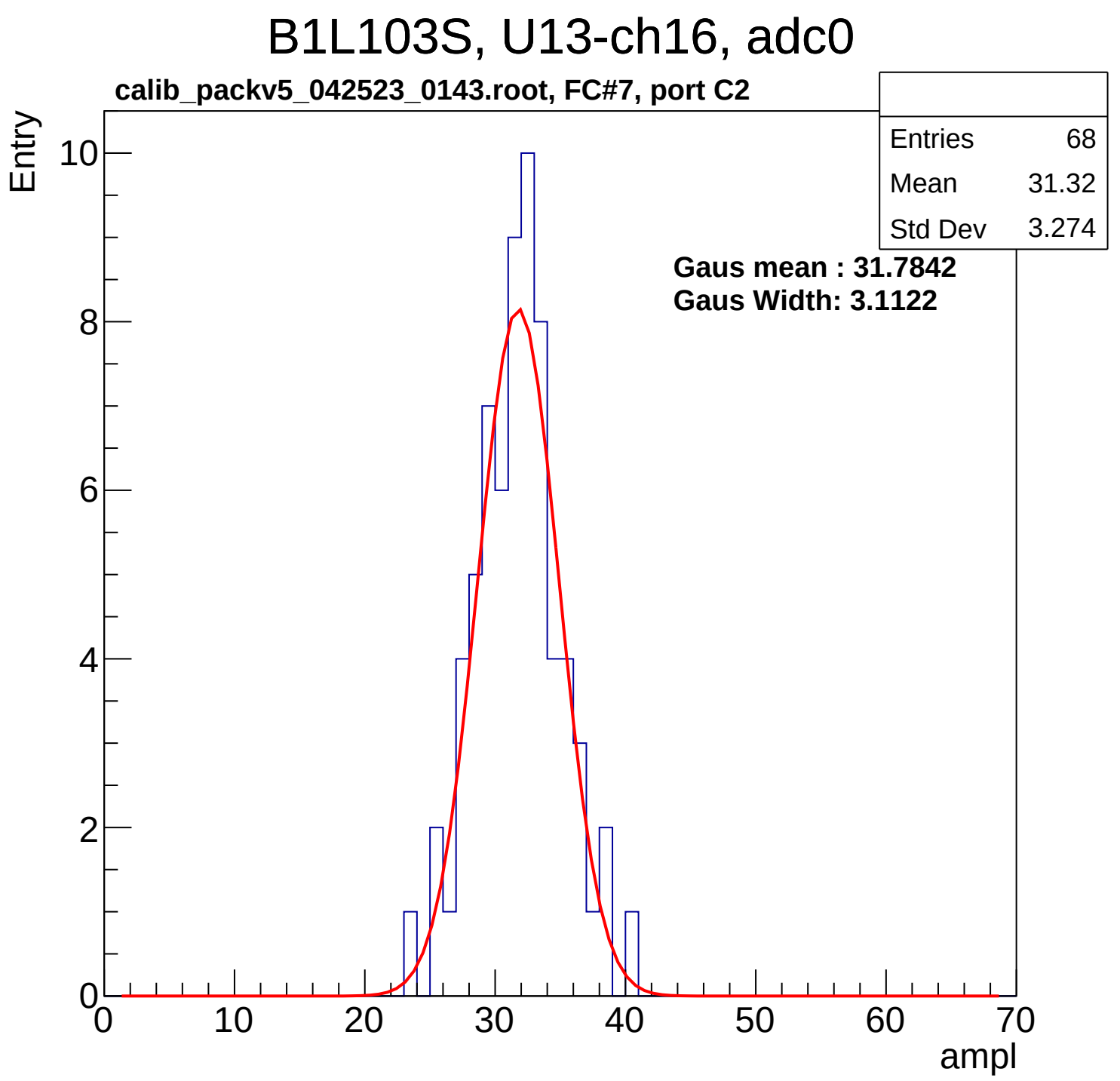
50
60
70
ampl


B1L103S, U13-ch15, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch16, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 31.32
Std Dev 3.274
Gaus mean : 31.7842
Gaus Width: 3.1122
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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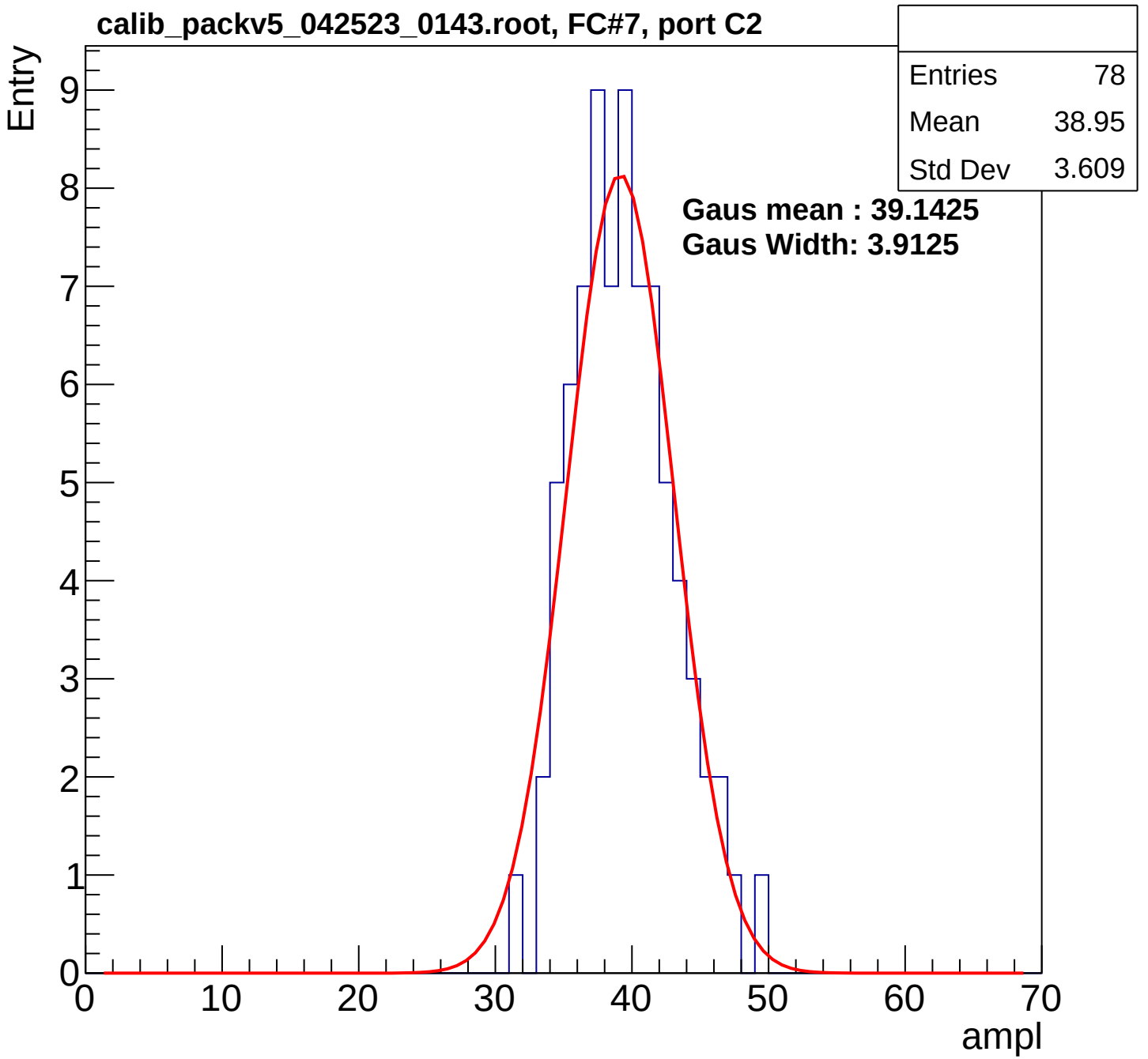
B1L103S, U13-ch16, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 38.95
Std Dev 3.609
Gaus mean : 39.1425
Gaus Width: 3.9125
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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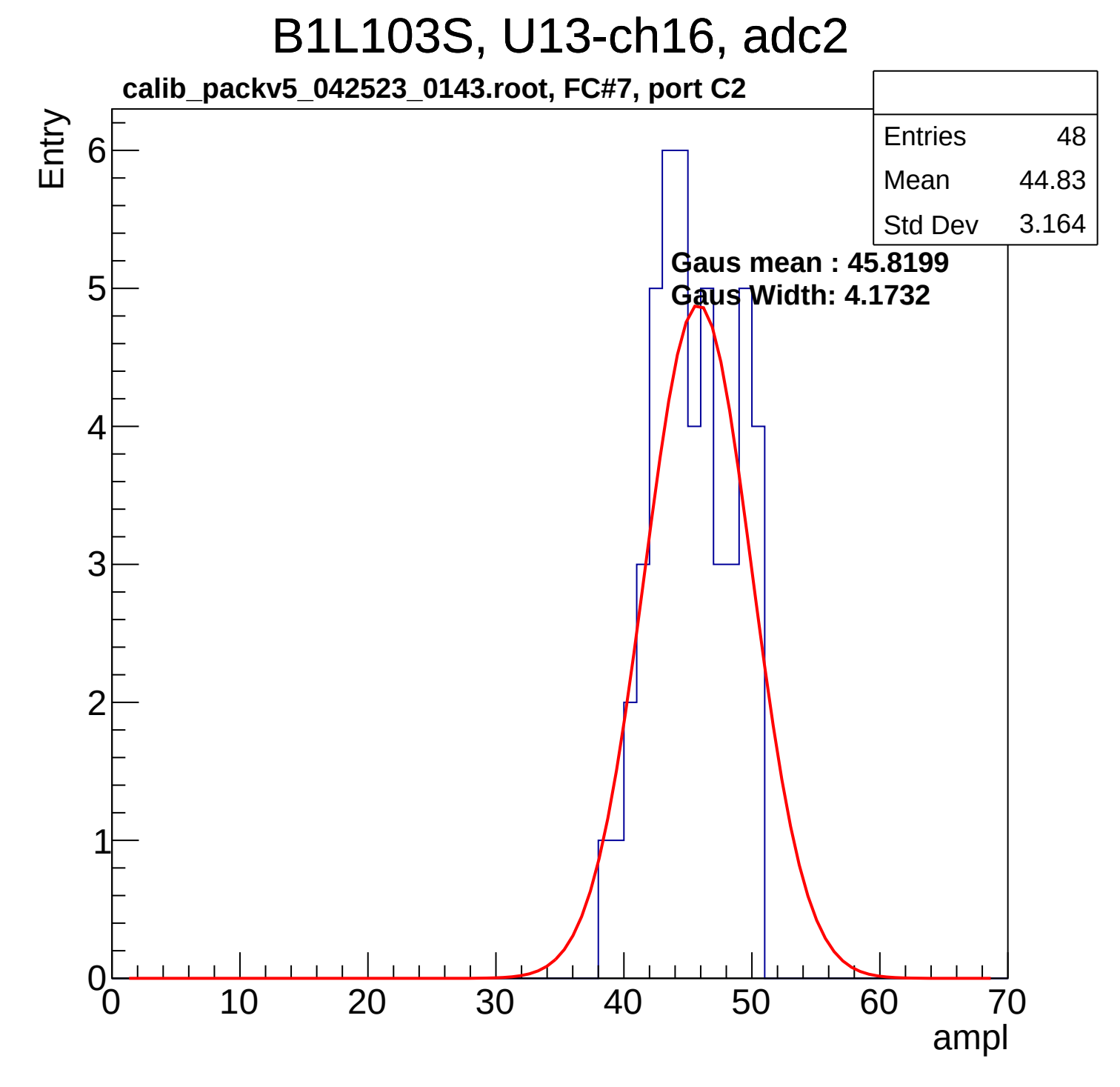
B1L103S, U13-ch16, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 44.83
Std Dev 3.164
Gaus mean : 45.8199
Gaus Width: 4.1732
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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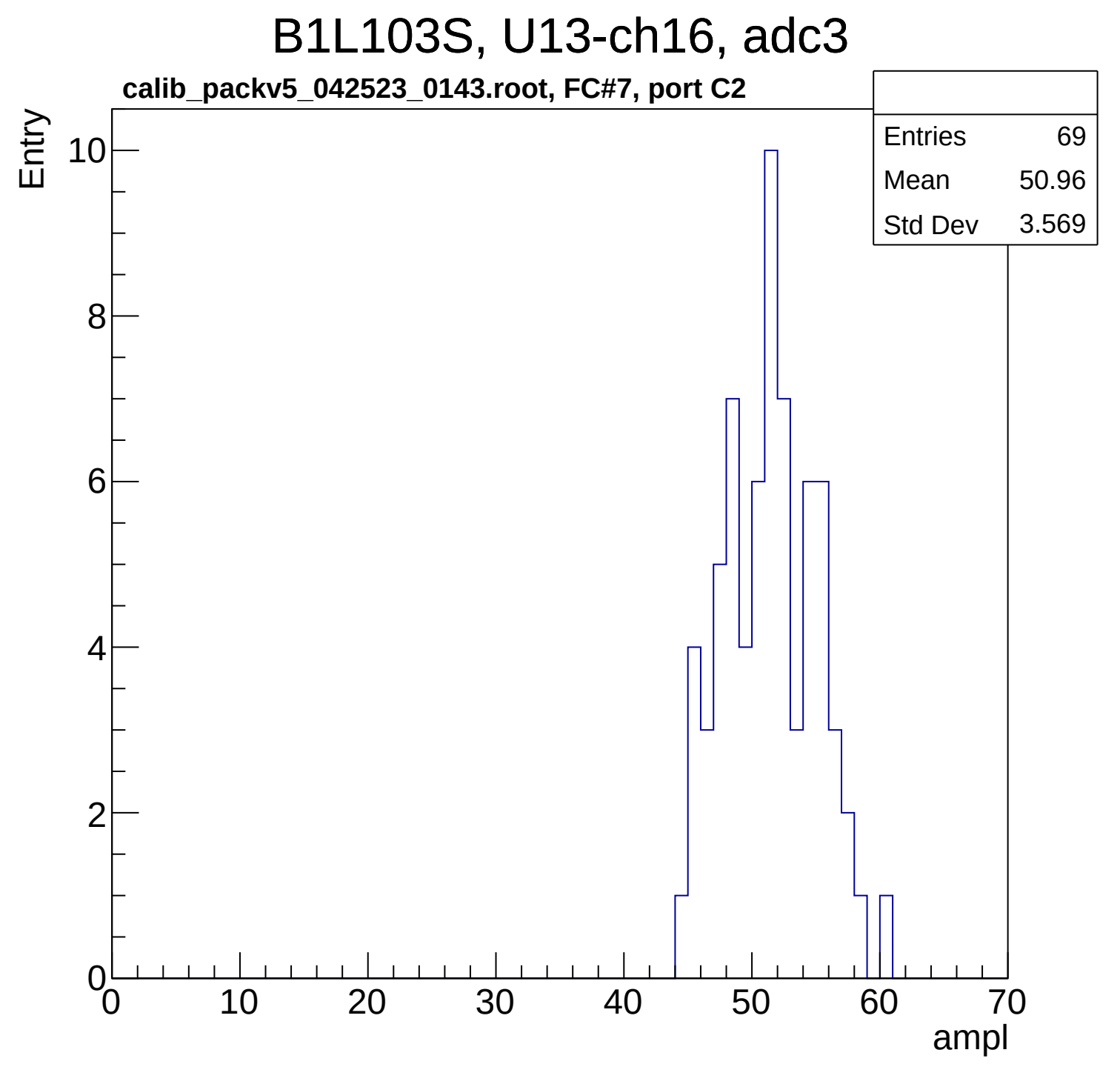
B1L103S, U13-ch16, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.96
Std Dev 3.569
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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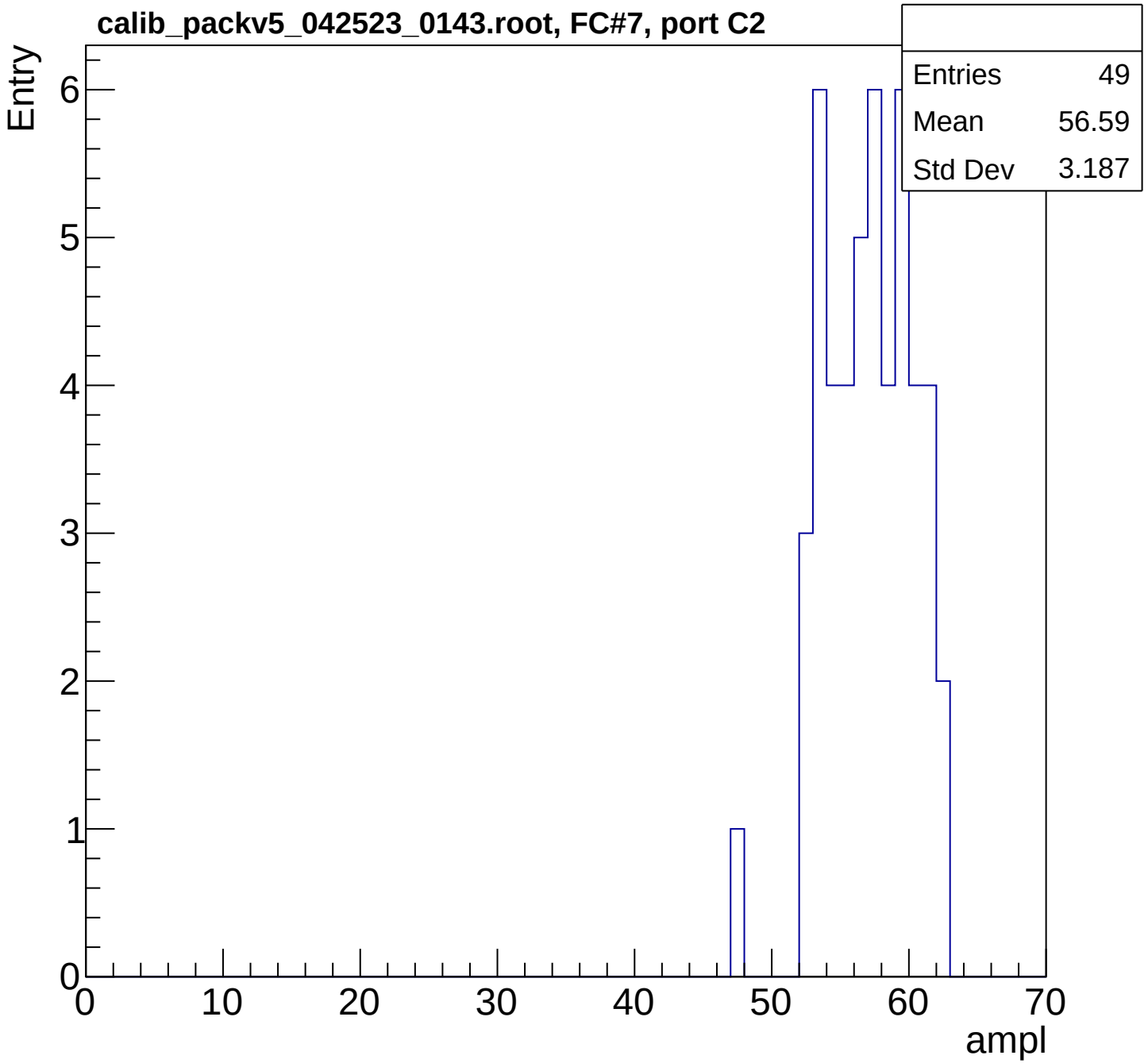
B1L103S, U13-ch16, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 56.59
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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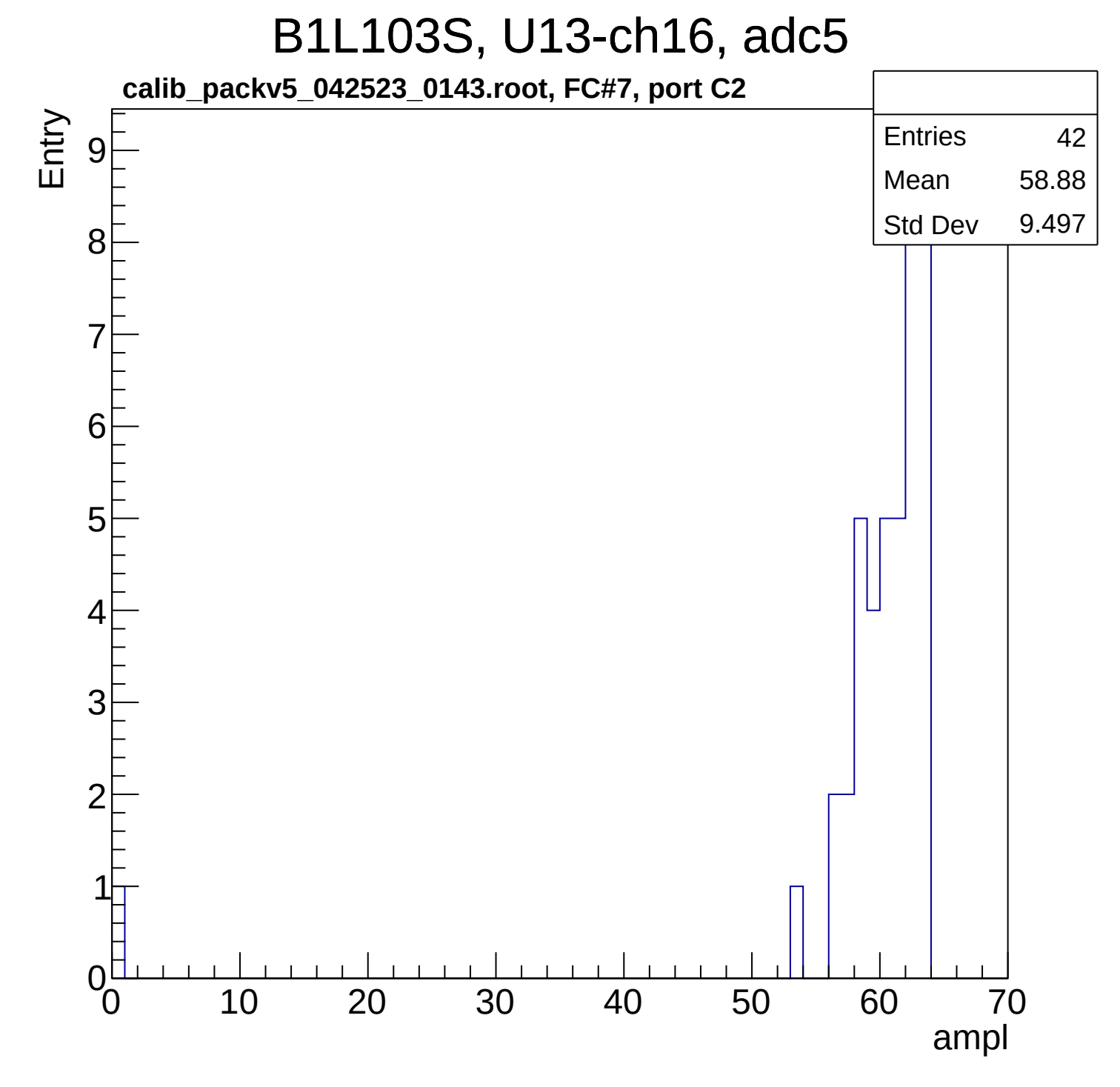
B1L103S, U13-ch16, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.88
Std Dev 9.497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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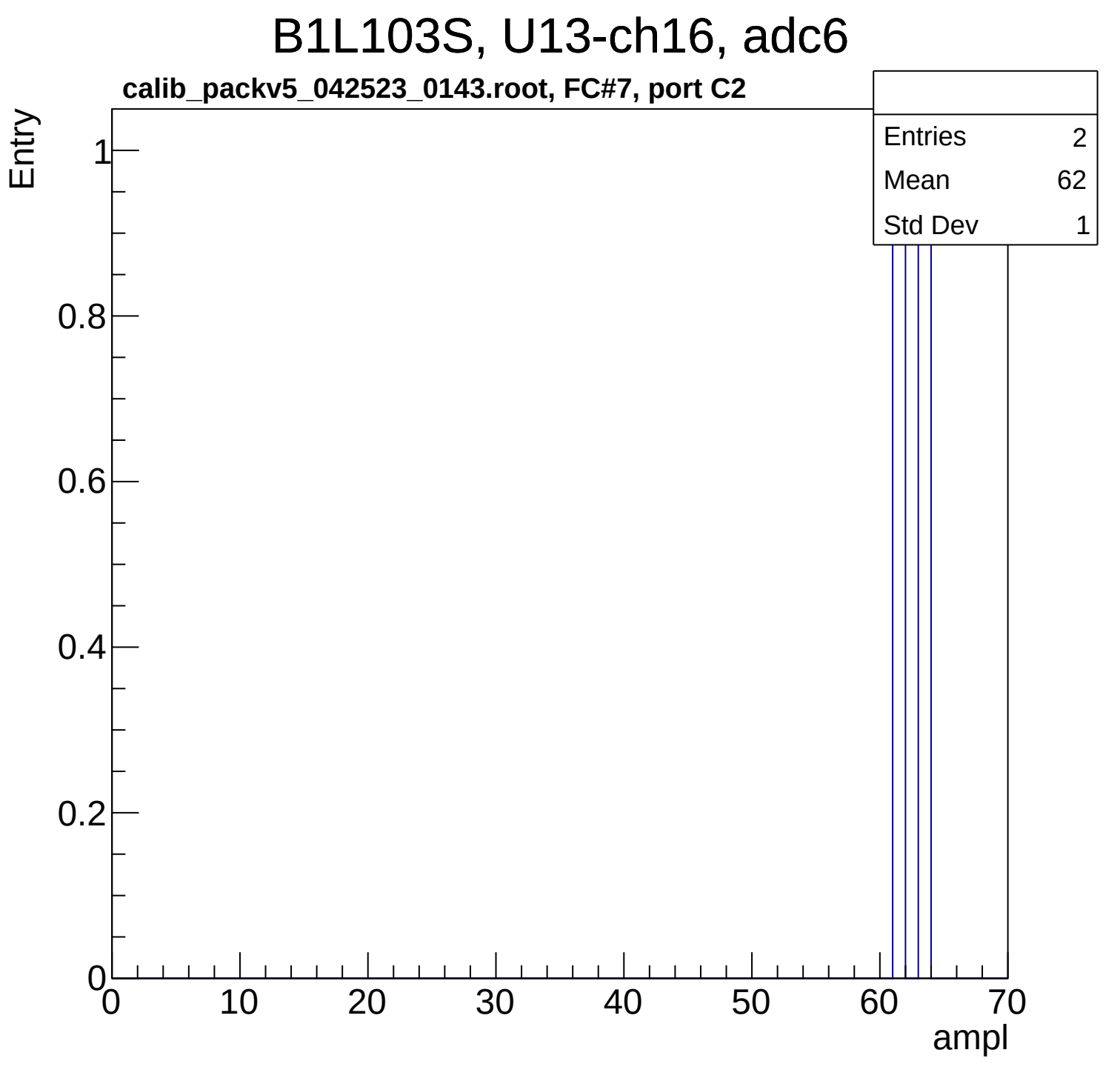
B1L103S, U13-ch16, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
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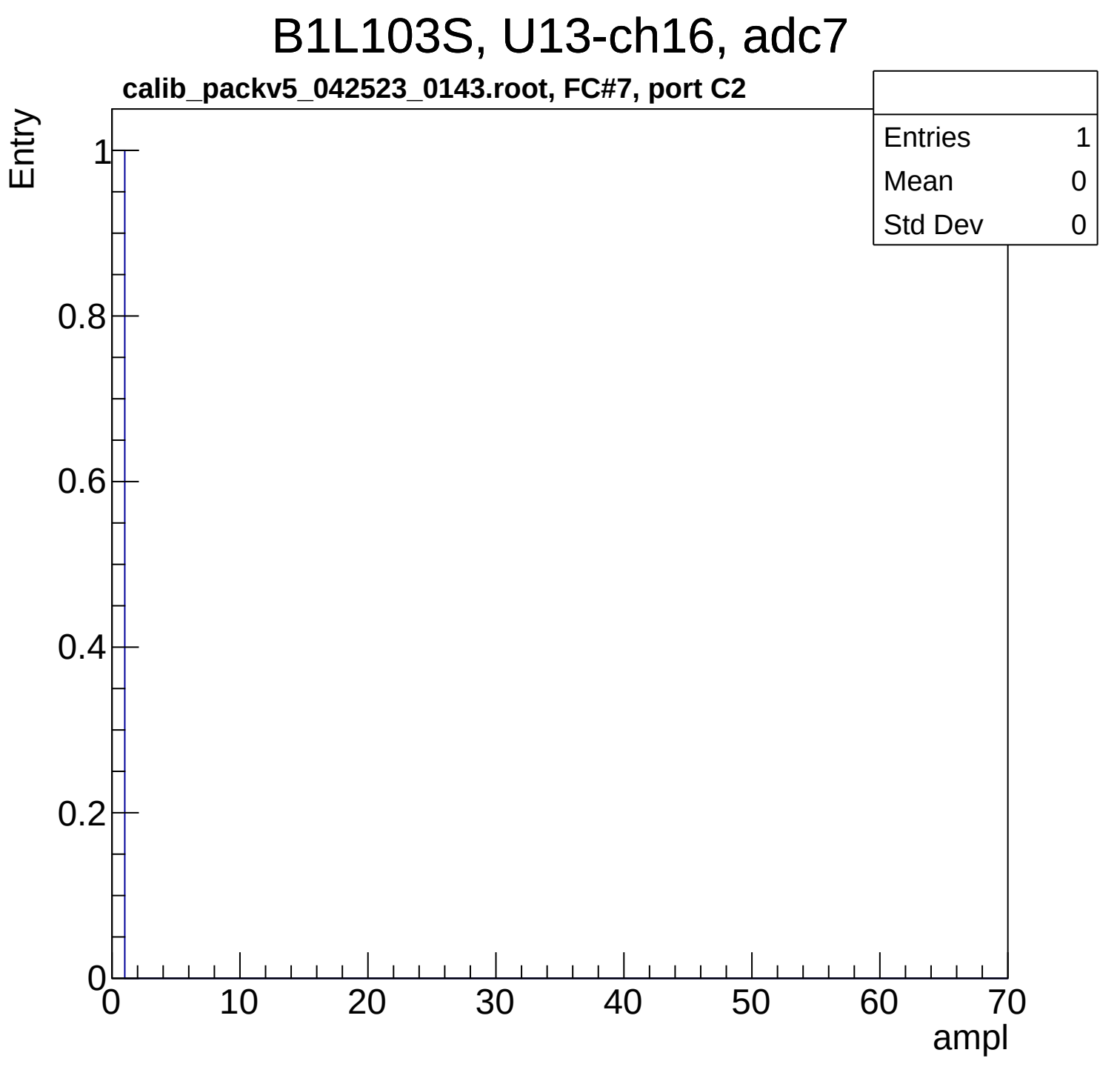
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch16, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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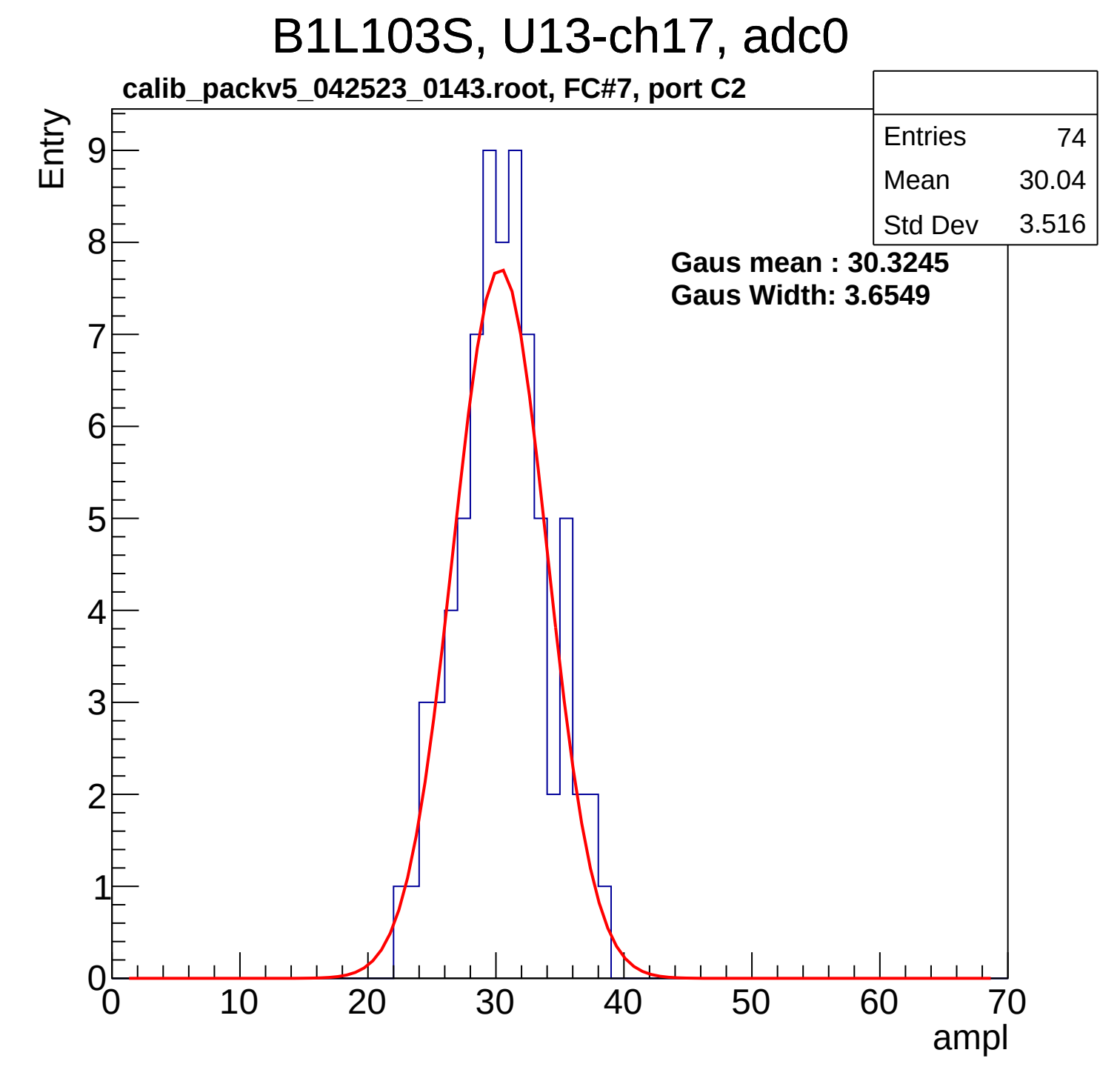
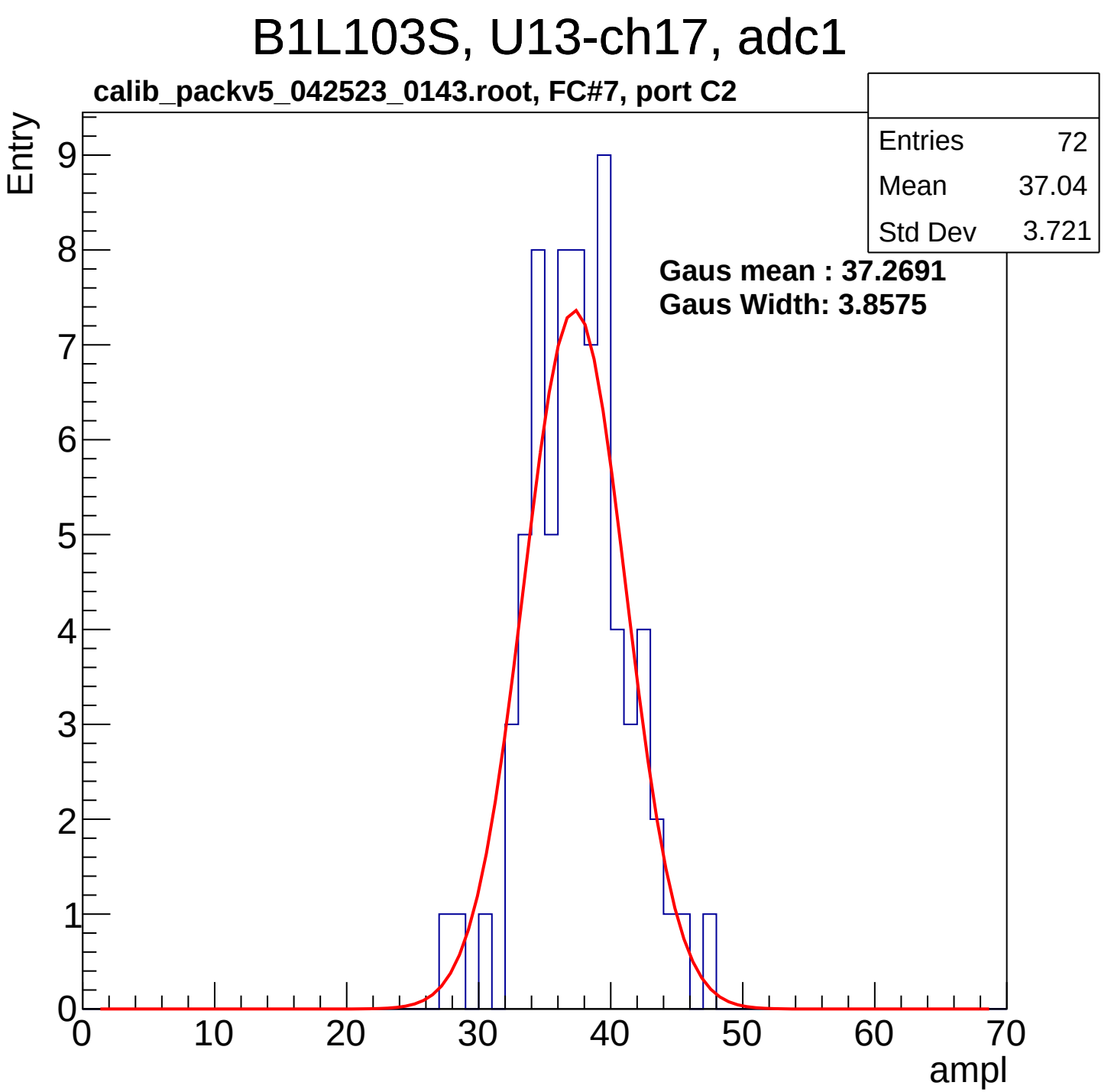
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch17, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.04
Std Dev 3.516
Gaus mean : 30.3245
Gaus Width: 3.6549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch17, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.04
Std Dev 3.721
Gaus mean : 37.2691
Gaus Width: 3.8575
Entry
ampl
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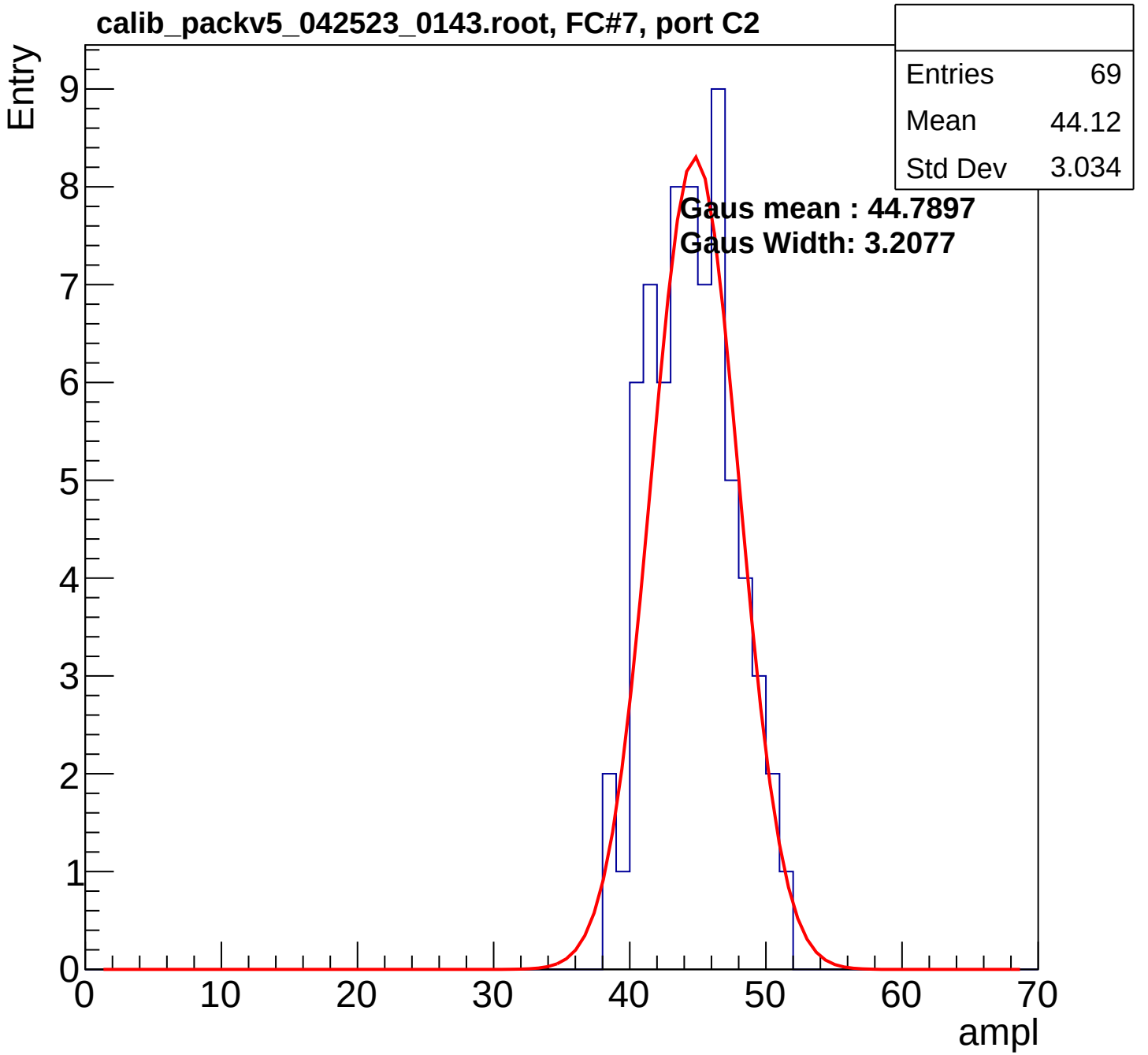
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch17, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 44.12
Std Dev 3.034
Gaus mean : 44.7897
Gaus Width: 3.2077
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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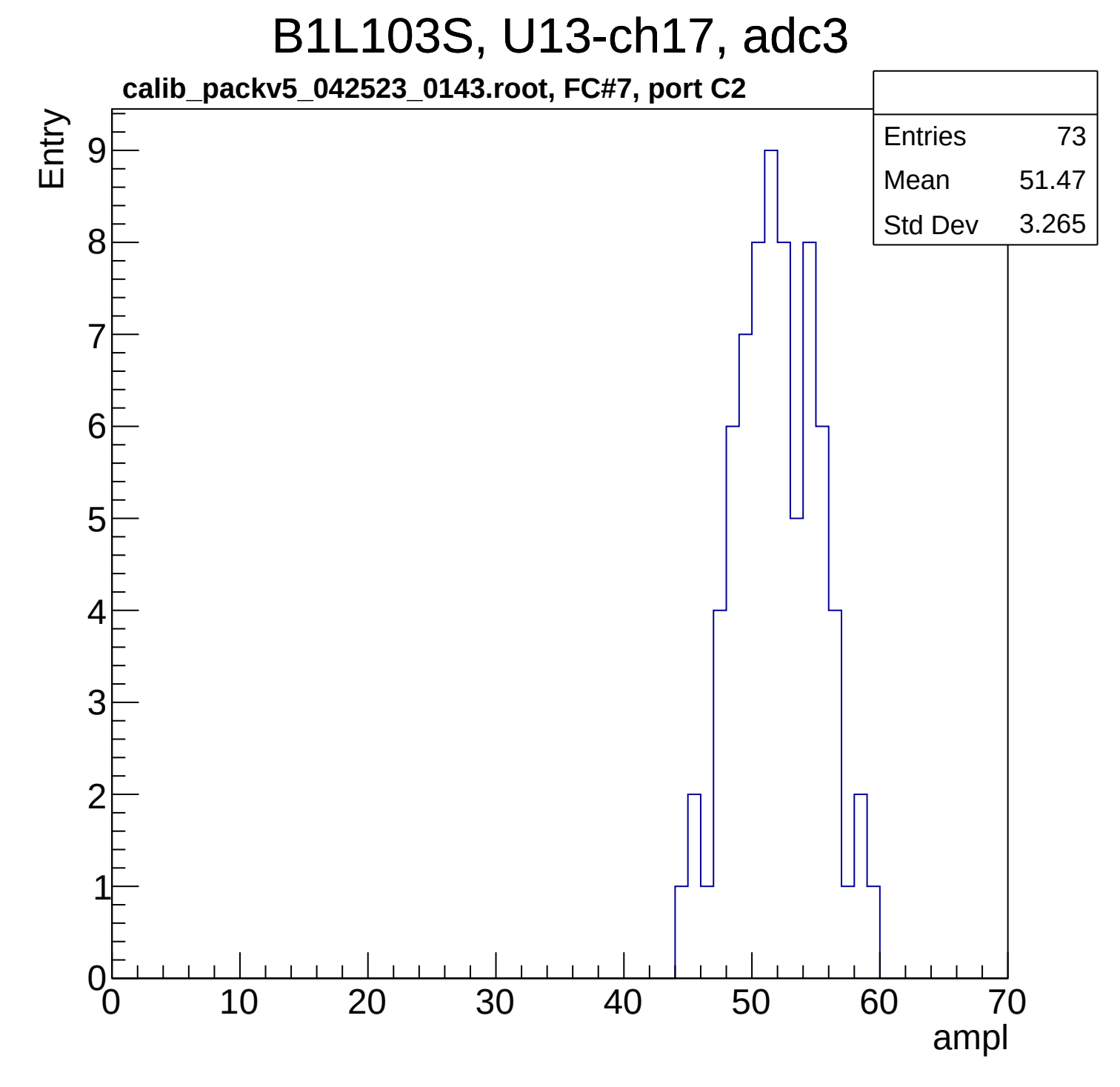
B1L103S, U13-ch17, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 51.47
Std Dev 3.265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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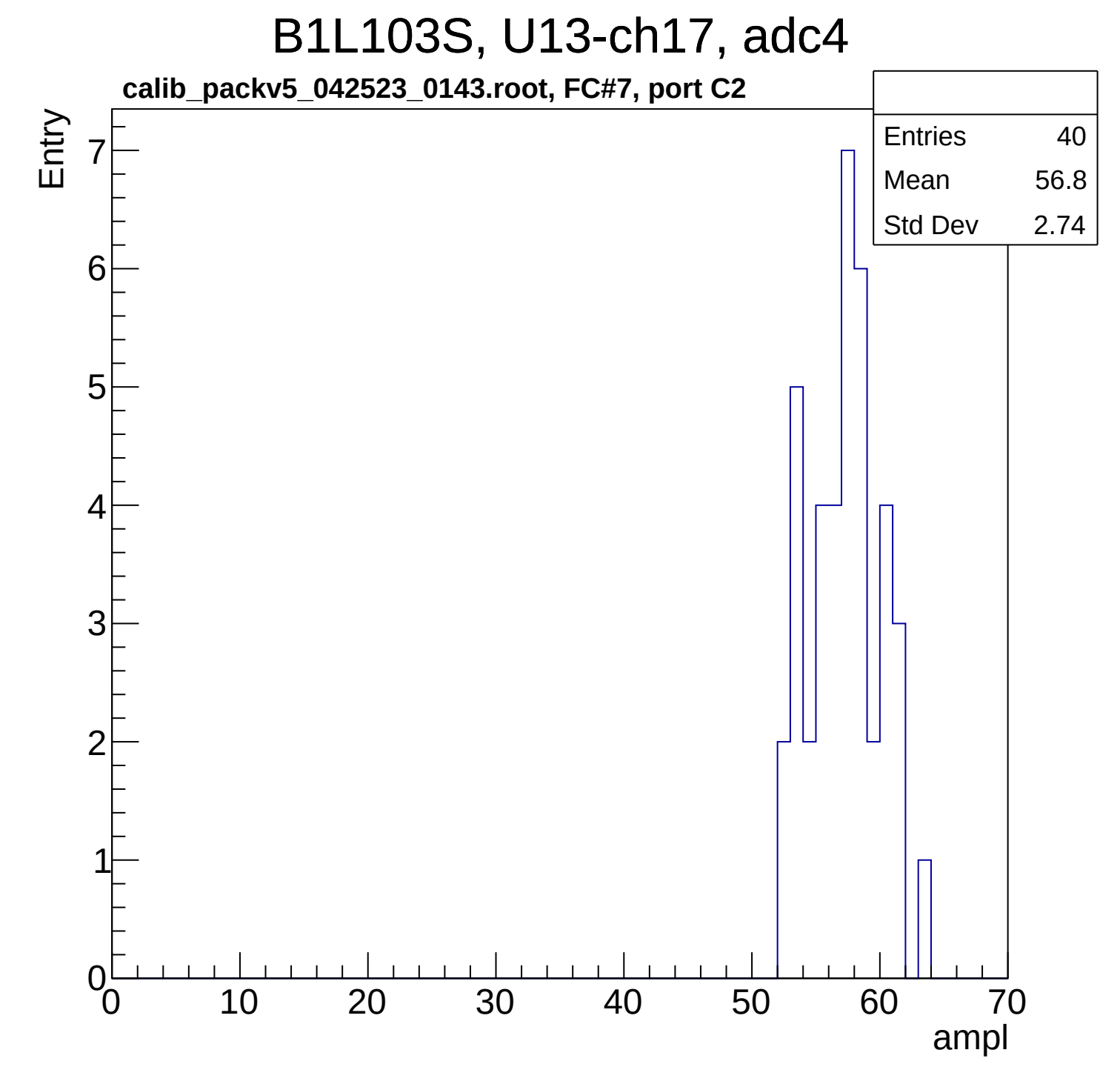
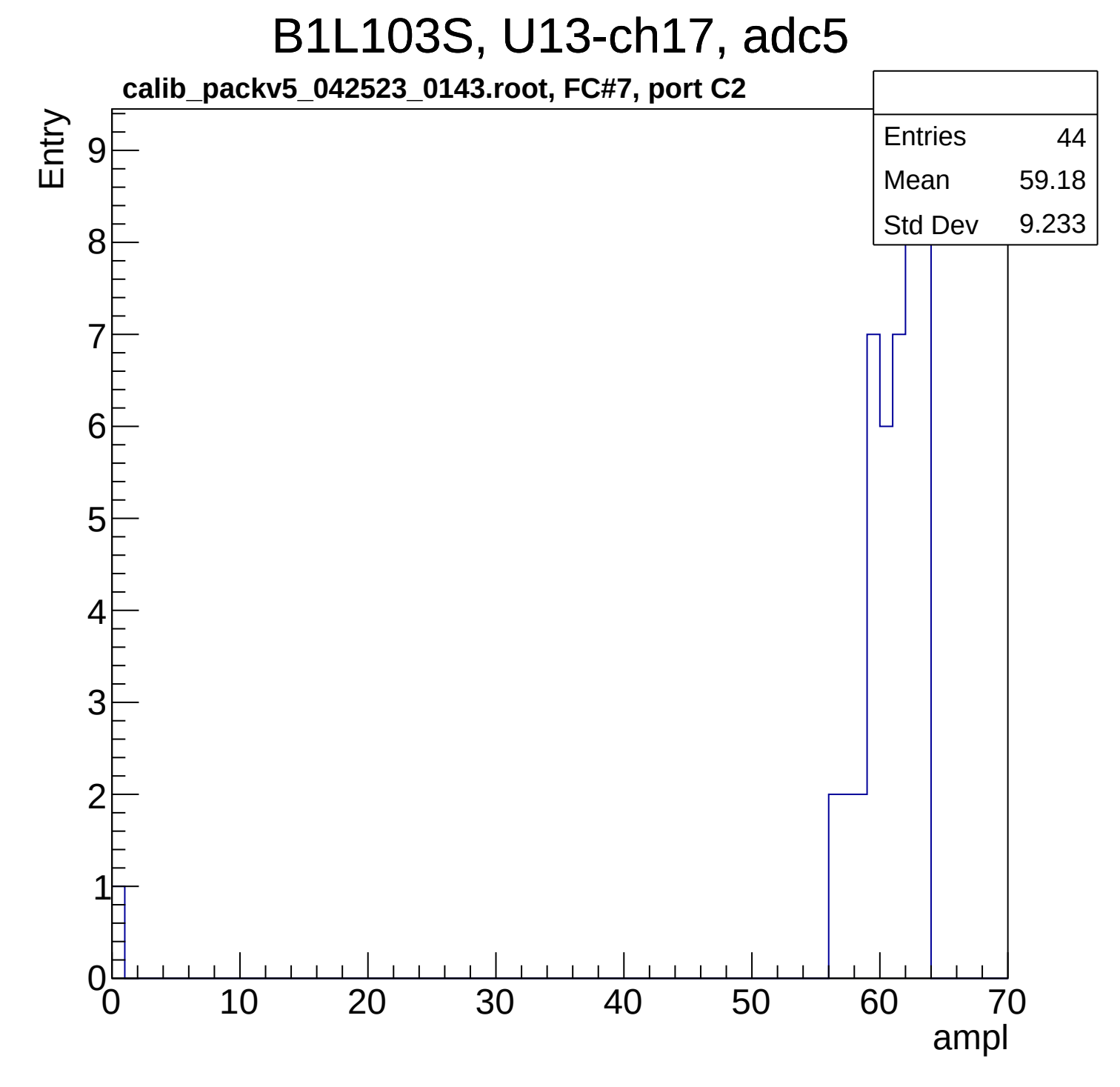

B1L103S, U13-ch17, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 56.8
Std Dev 2.74
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch17, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.18
Std Dev 9.233
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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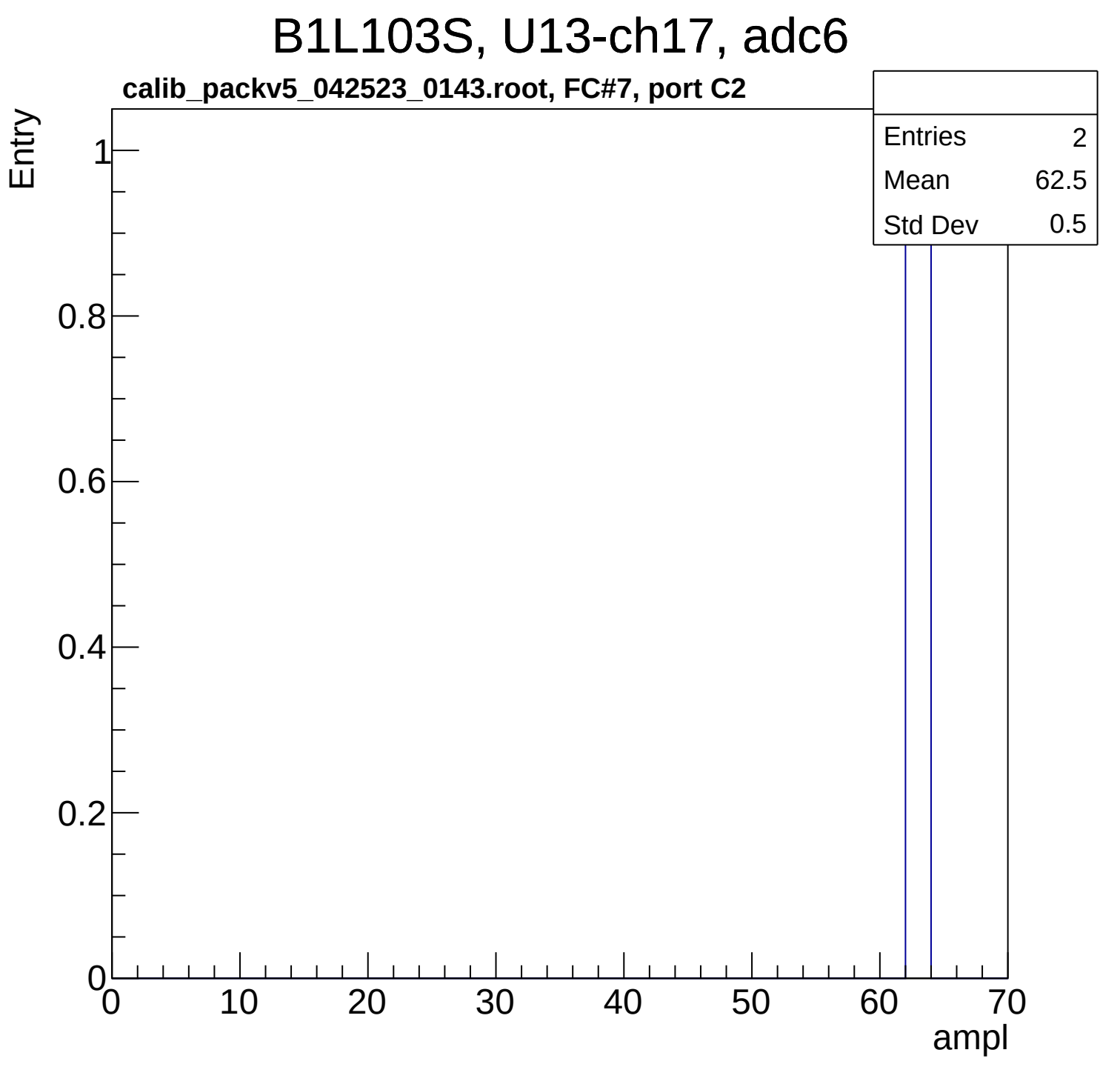
B1L103S, U13-ch17, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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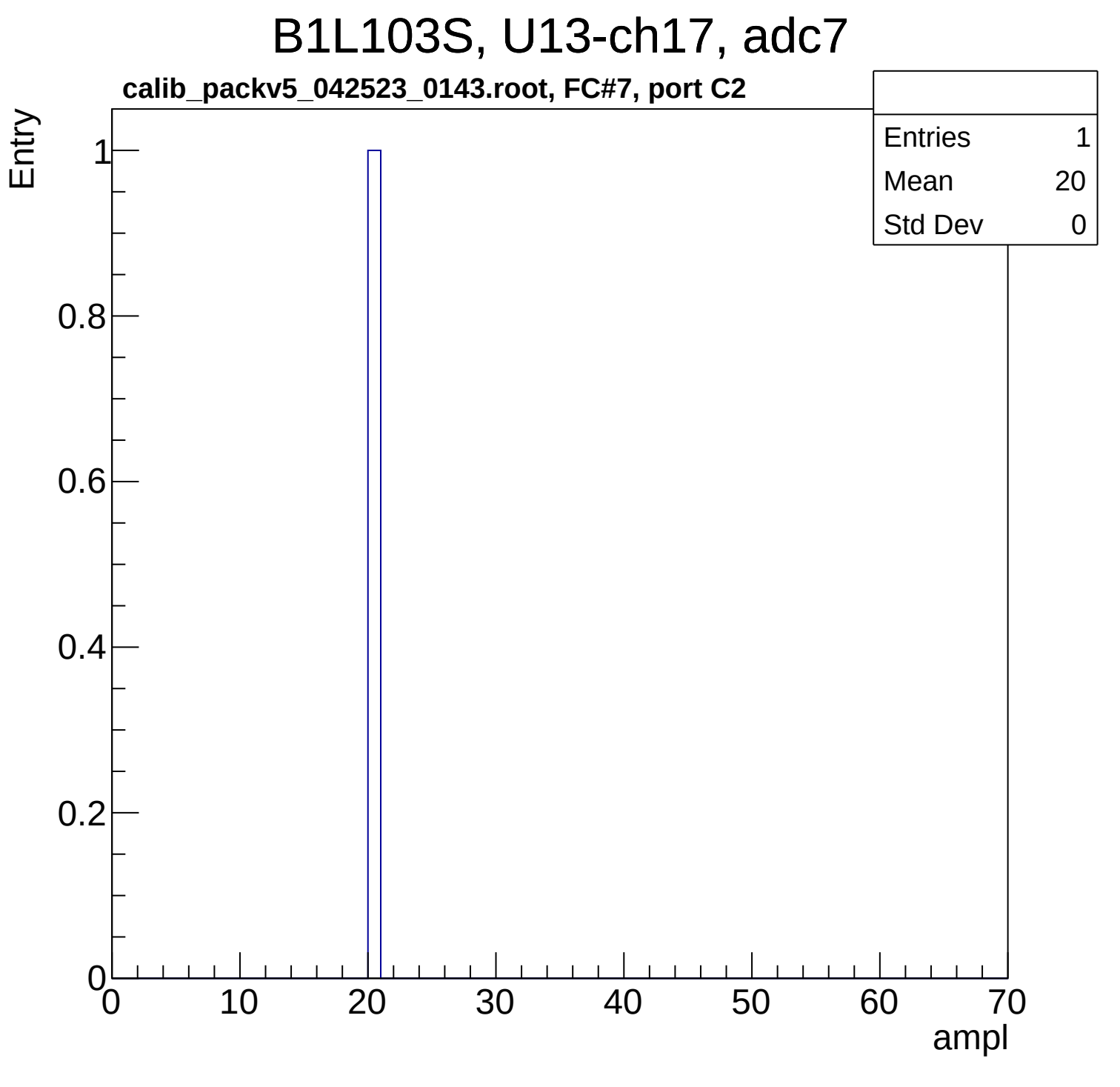
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch17, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
0.8
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0
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60
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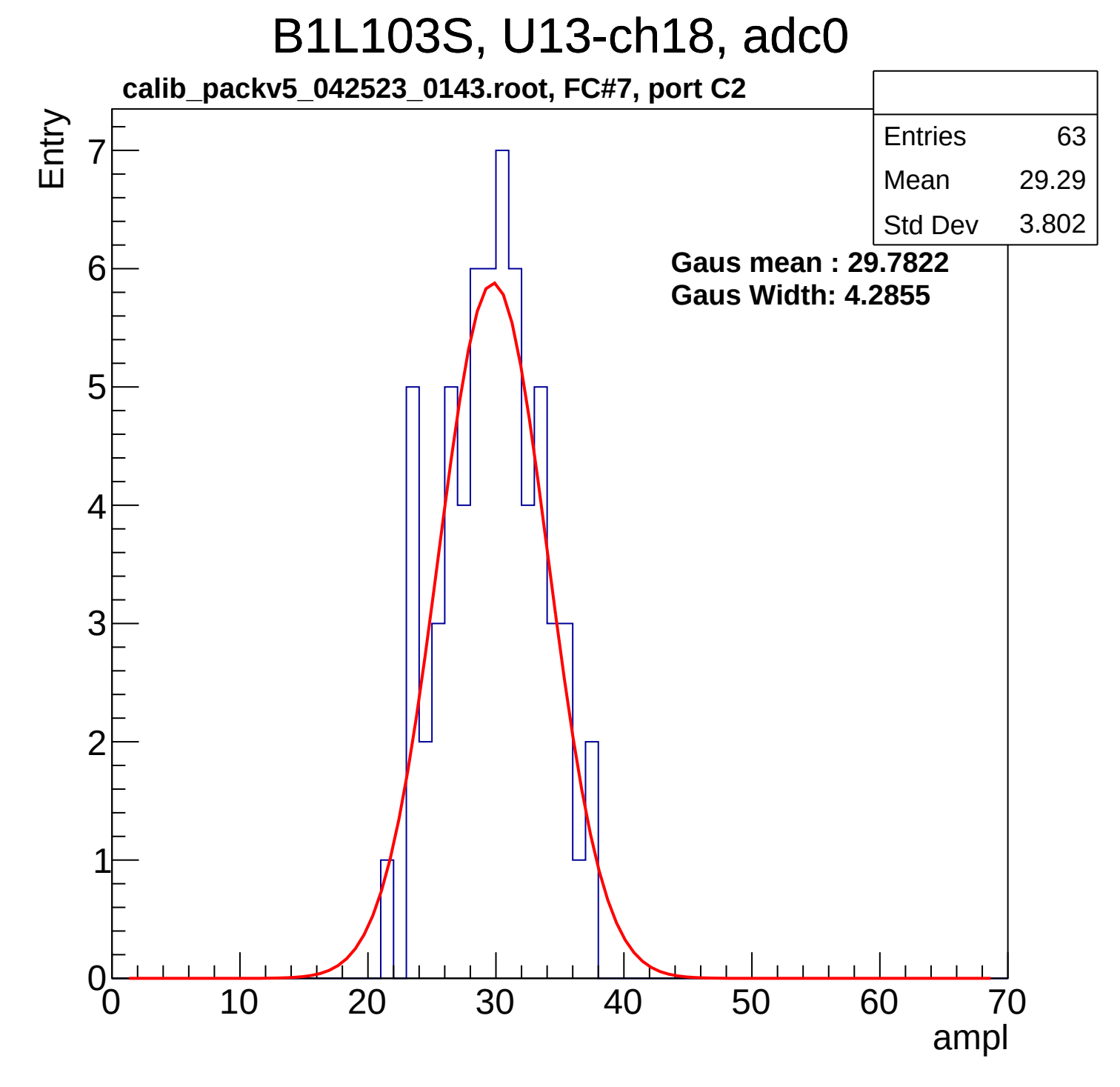
B1L103S, U13-ch18, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.29
Std Dev 3.802
Gaus mean : 29.7822
Gaus Width: 4.2855
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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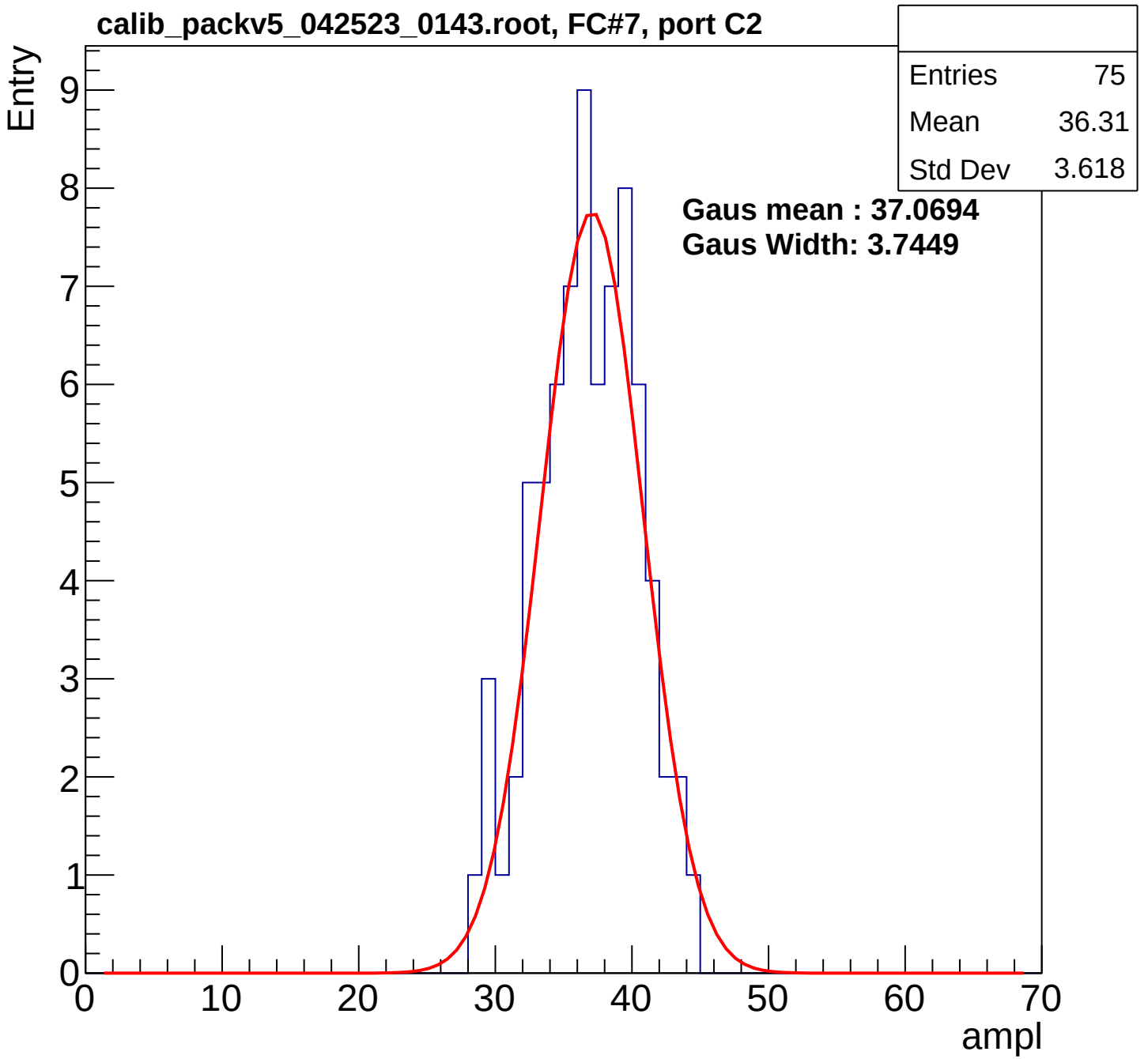
B1L103S, U13-ch18, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 36.31
Std Dev 3.618
Gaus mean : 37.0694
Gaus Width: 3.7449
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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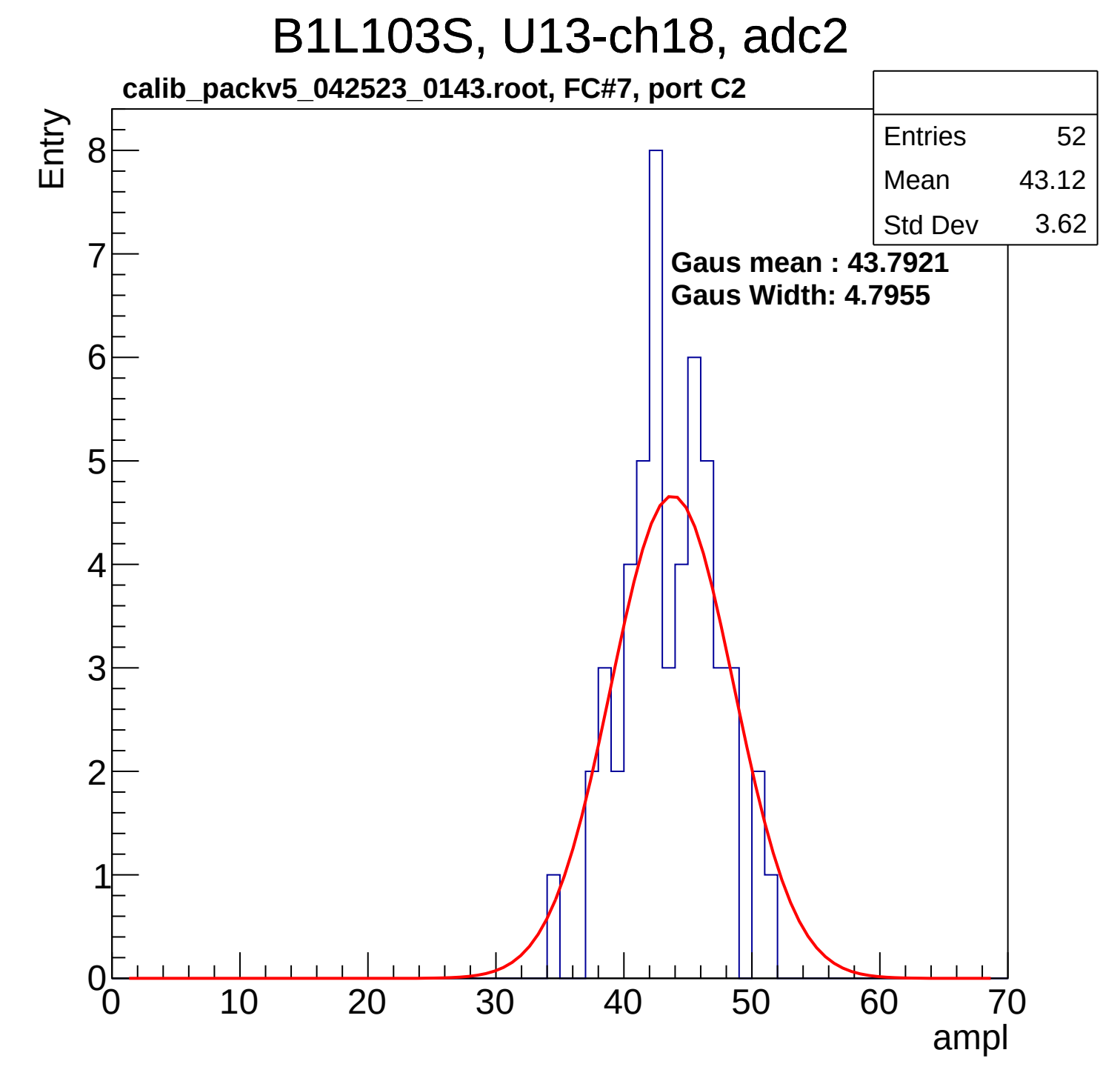
B1L103S, U13-ch18, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 43.12
Std Dev 3.62
Gaus mean : 43.7921
Gaus Width: 4.7955
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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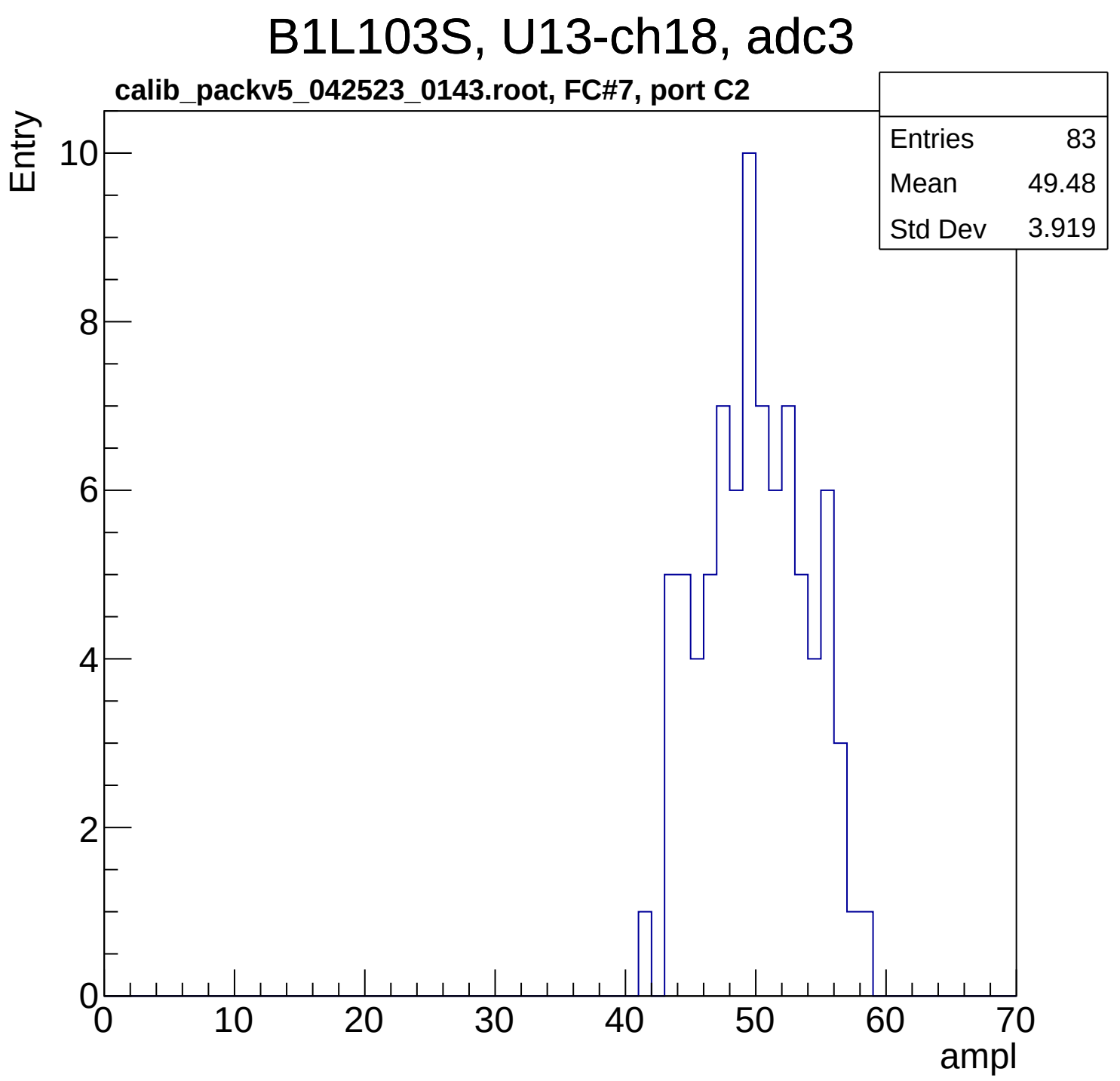
B1L103S, U13-ch18, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 49.48
Std Dev 3.919
Entry
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6
8
10
0
10
20
30
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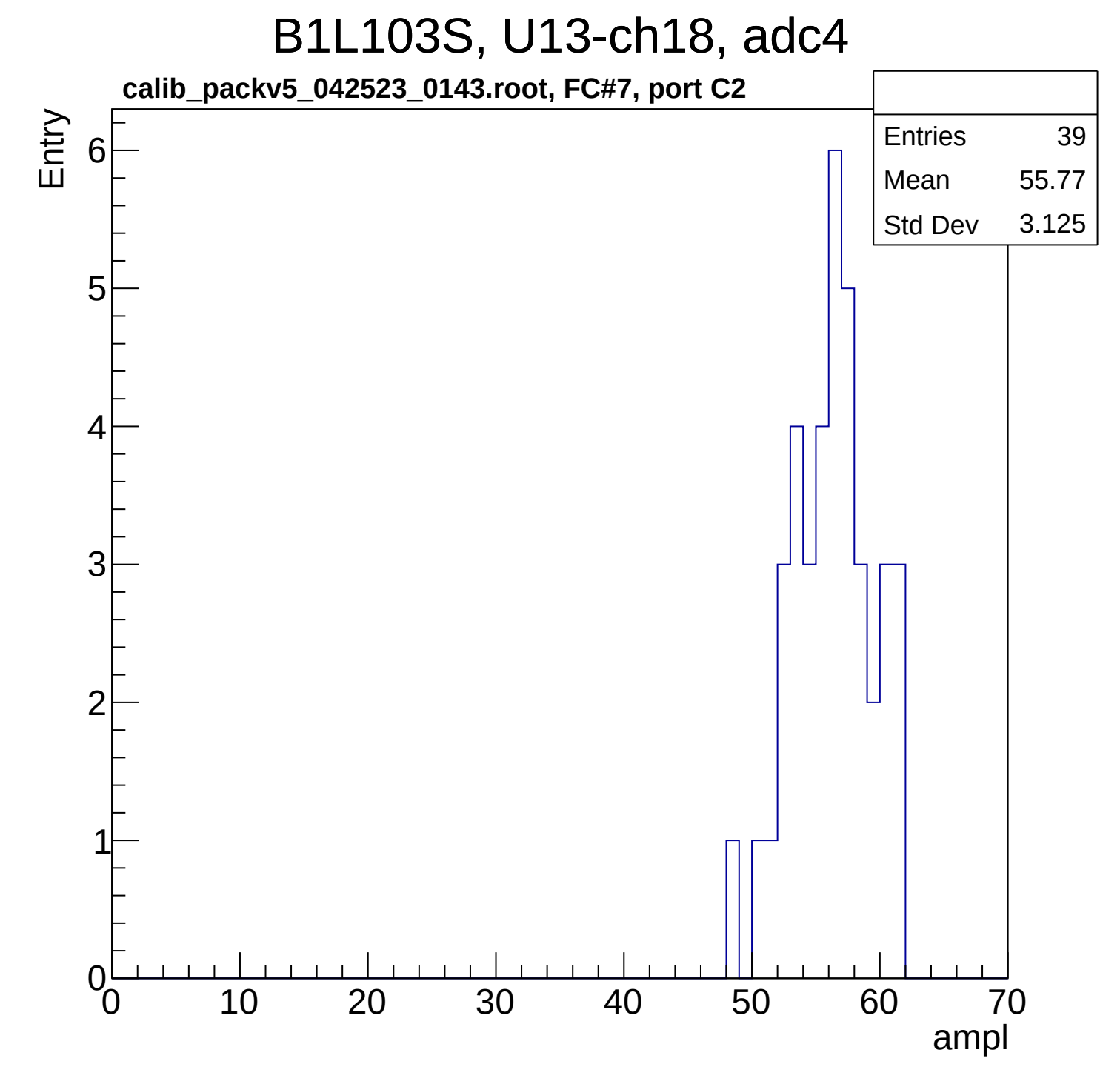
50
60
70
ampl


B1L103S, U13-ch18, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 55.77
Std Dev 3.125
Entry
ampl
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2
3
4
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6
0
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40
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60
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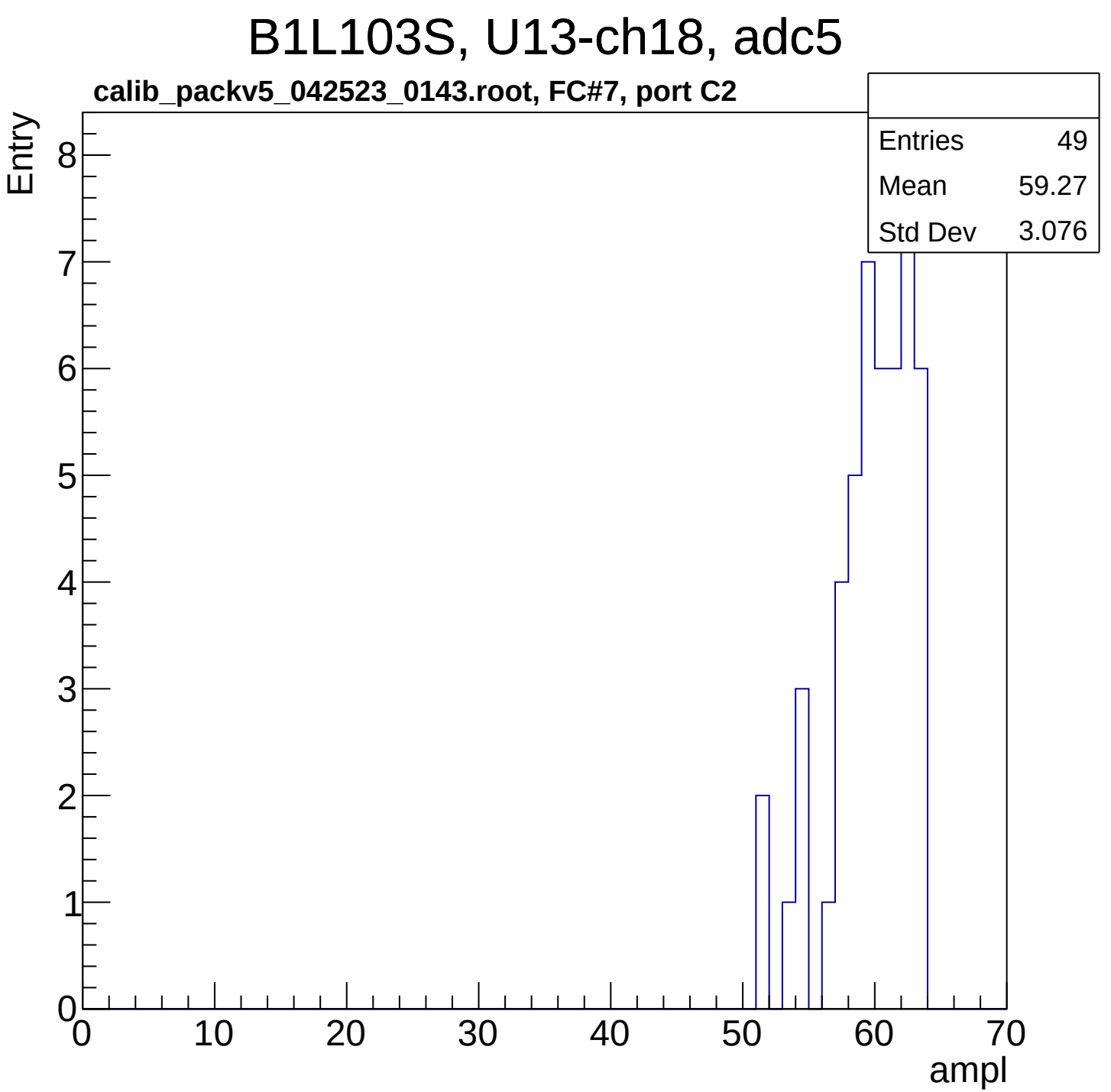
B1L103S, U13-ch18, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.27
Std Dev 3.076
Entry
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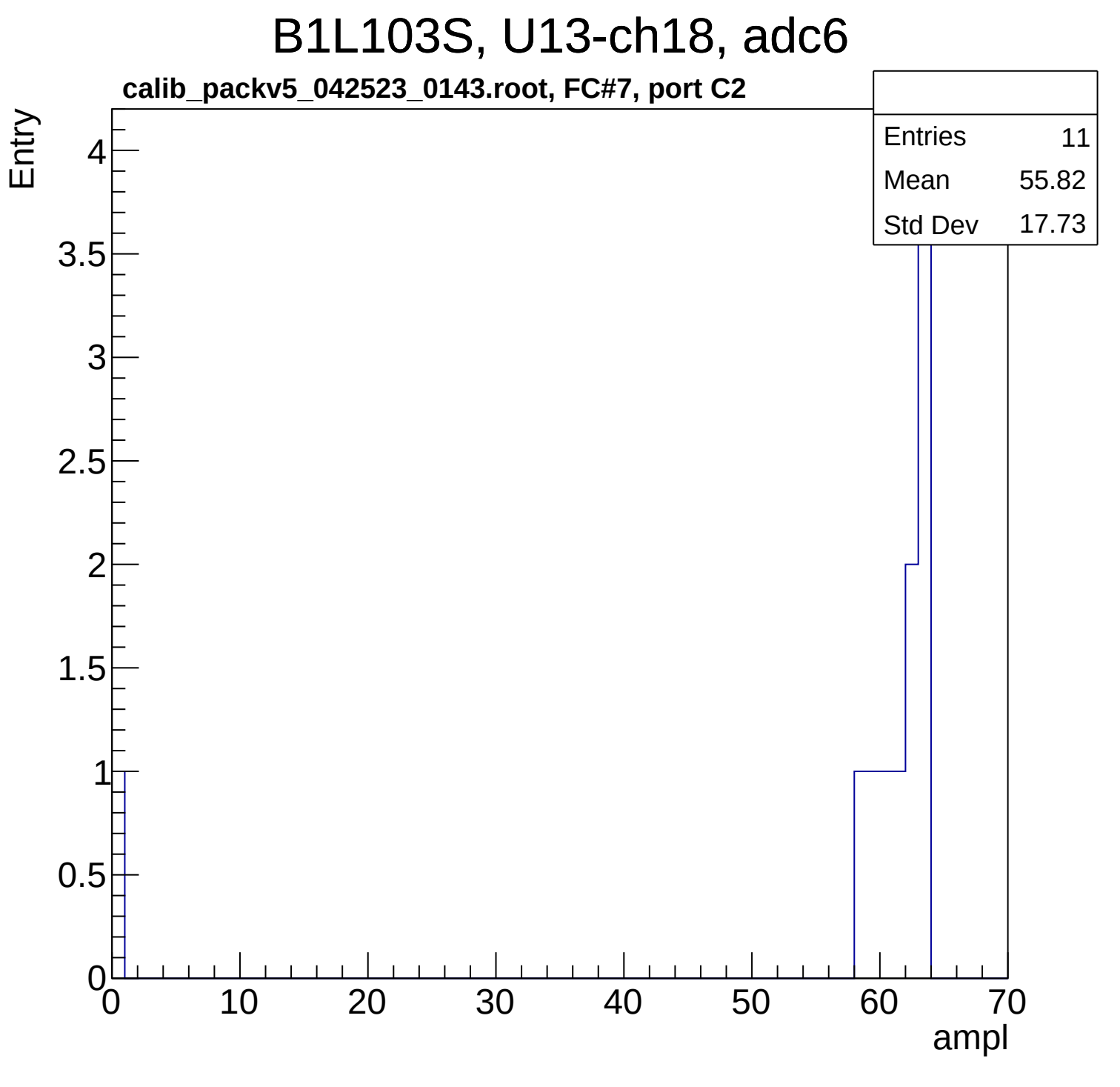
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch18, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 55.82
Std Dev 17.73
Entry
ampl
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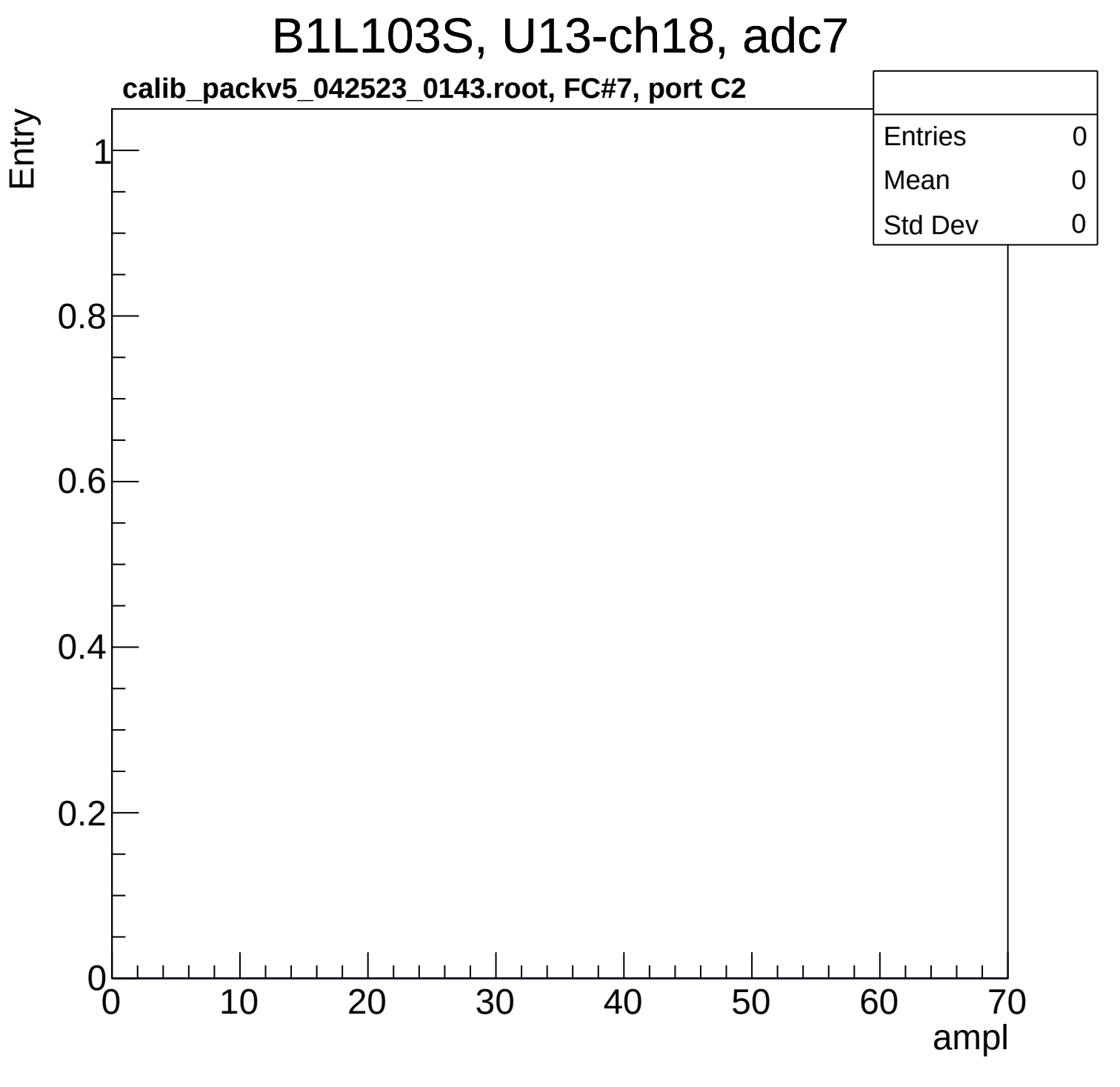
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U13-ch18, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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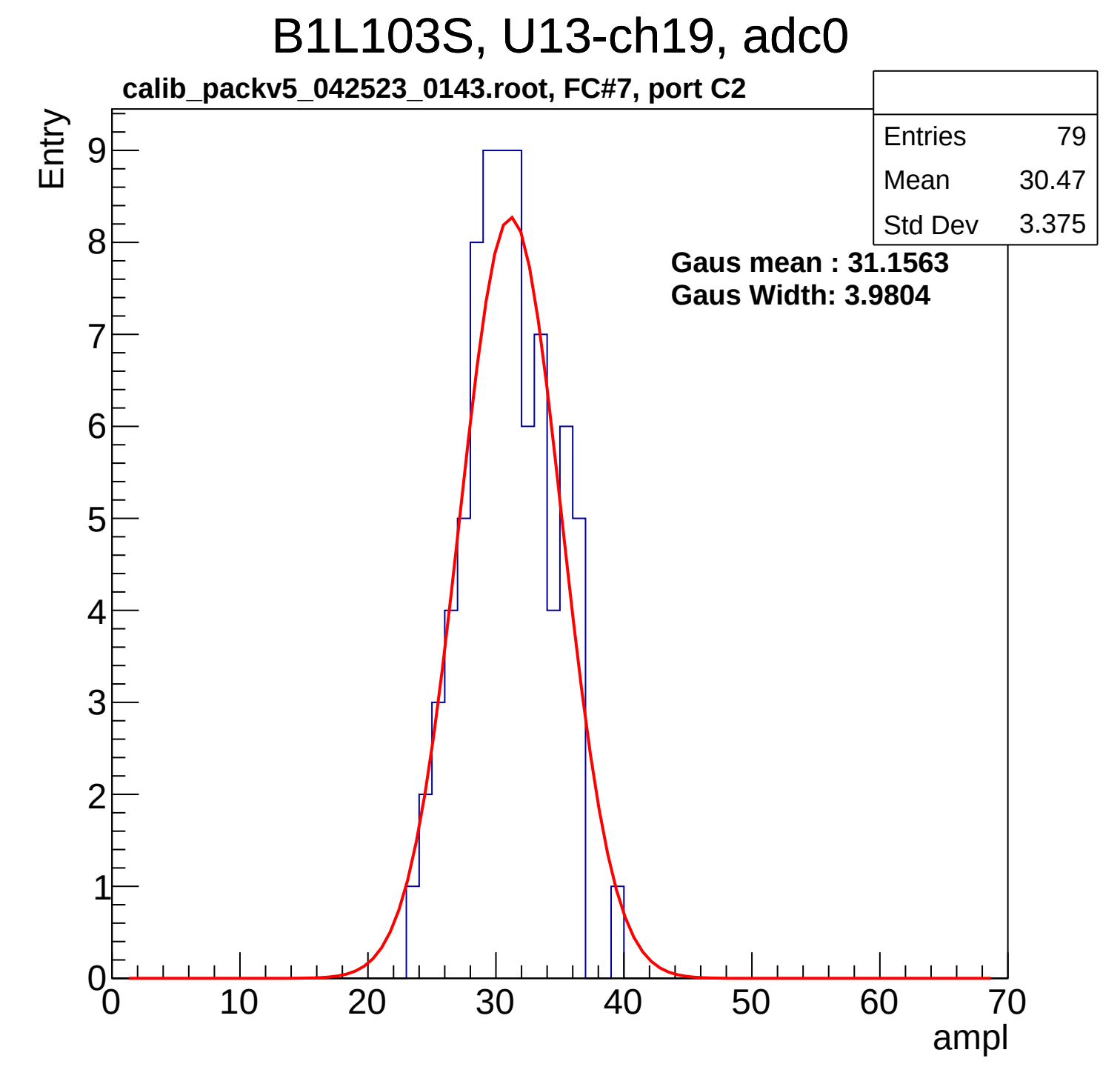
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch19, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 30.47
Std Dev 3.375
Gaus mean : 31.1563
Gaus Width: 3.9804
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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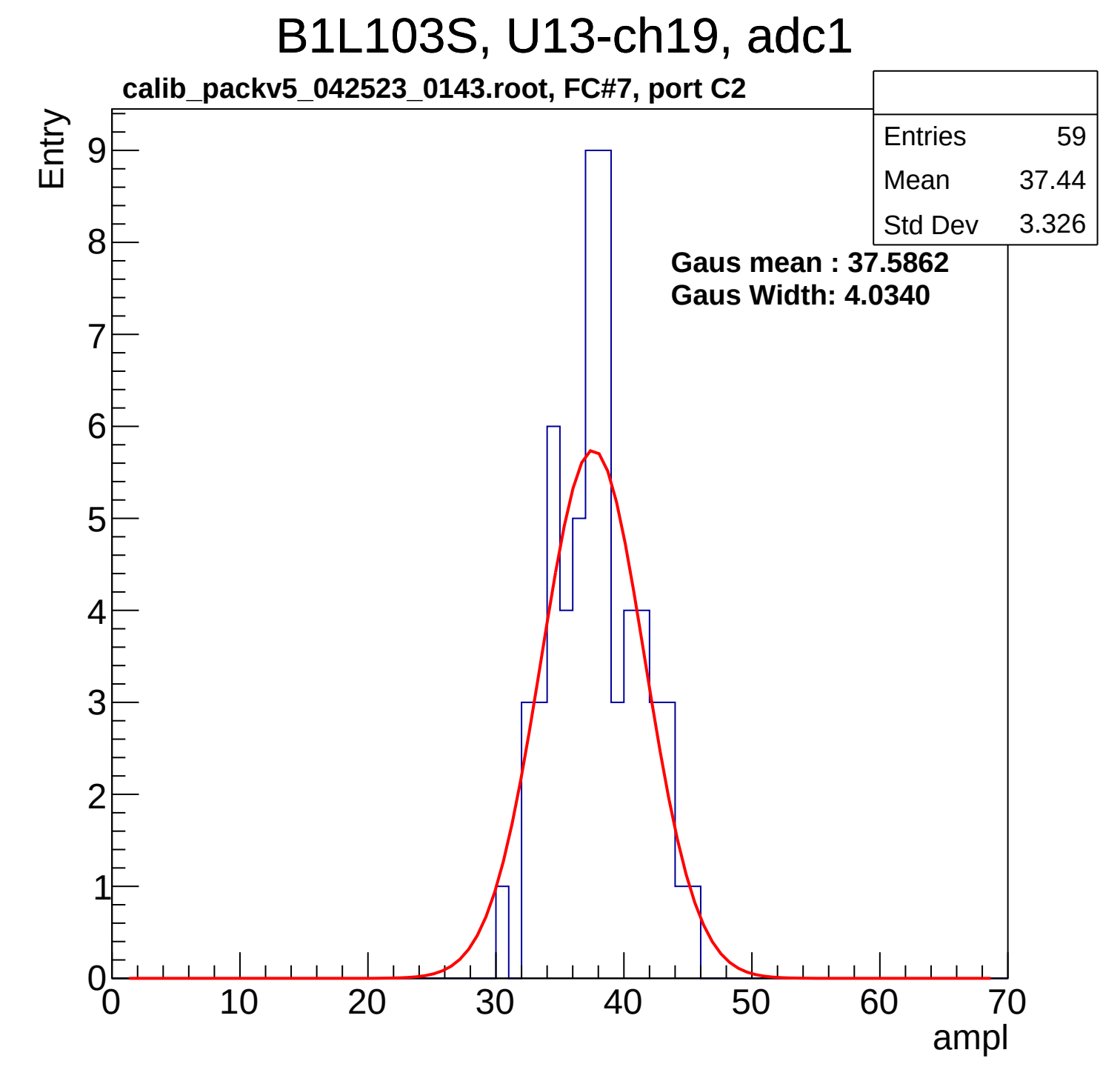
B1L103S, U13-ch19, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 37.44
Std Dev 3.326
Gaus mean : 37.5862
Gaus Width: 4.0340
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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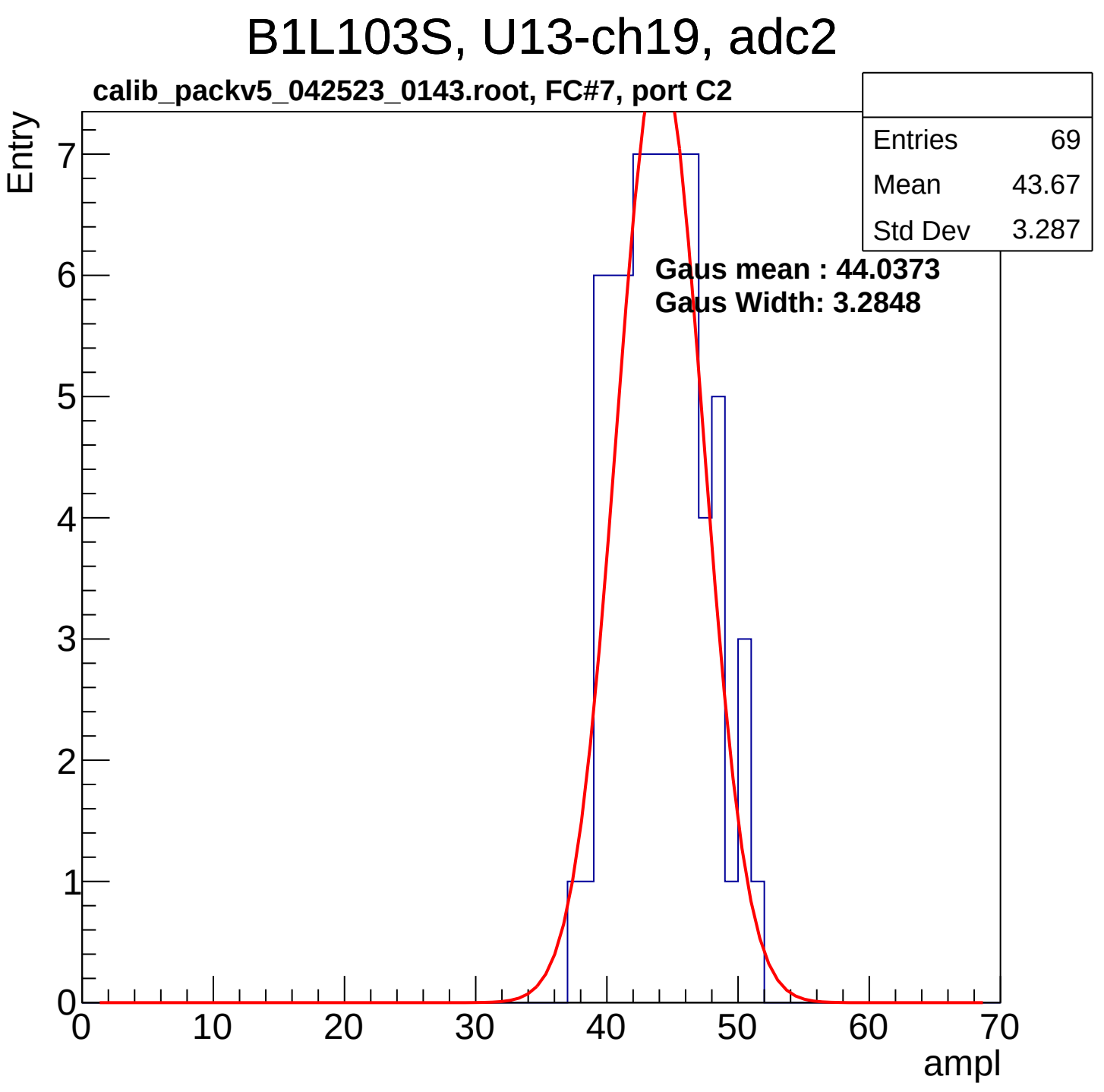
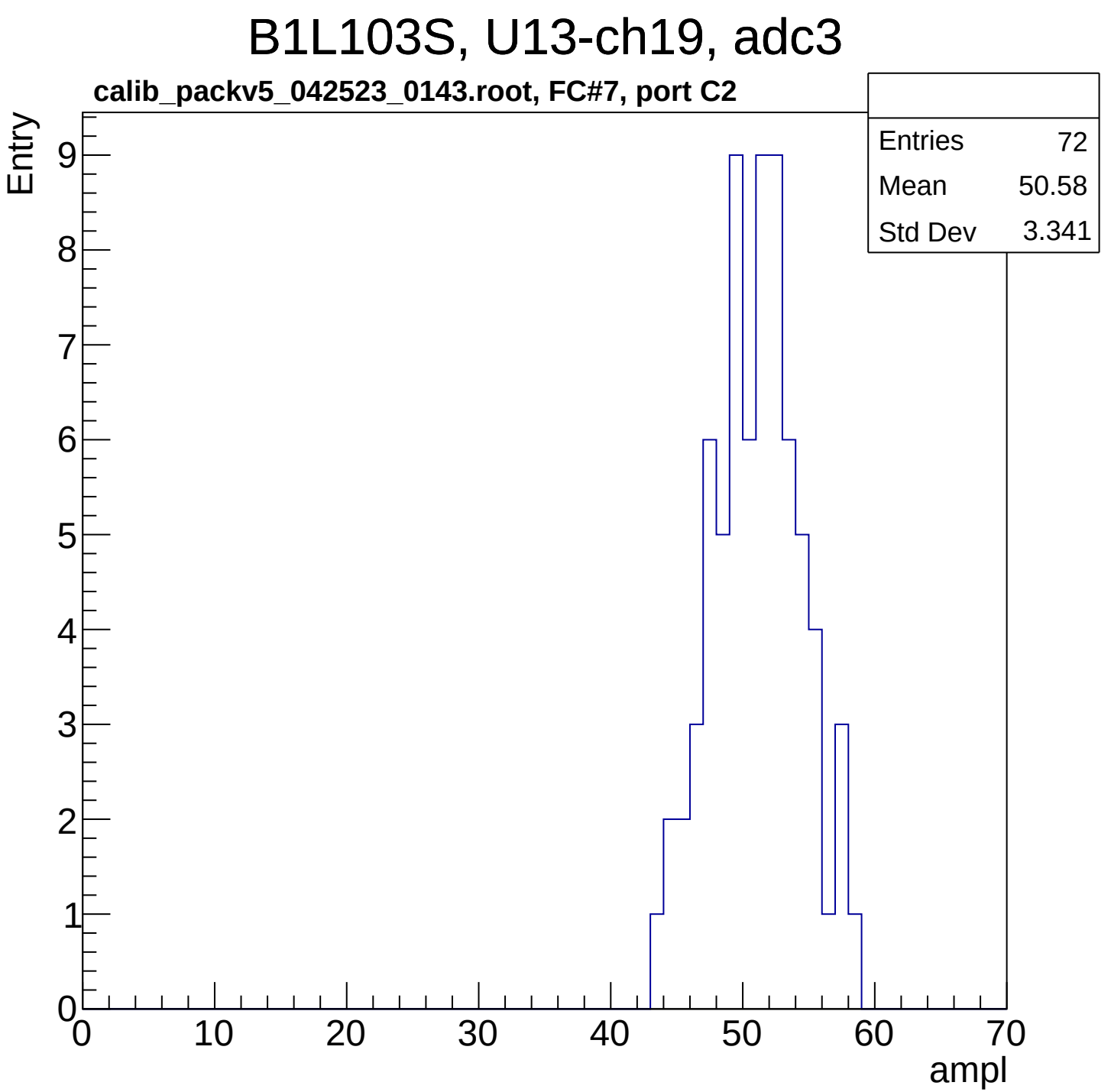

B1L103S, U13-ch19, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 43.67
Std Dev 3.287
Gaus mean : 44.0373
Gaus Width: 3.2848
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch19, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.58
Std Dev 3.341
Entry
ampl
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3
4
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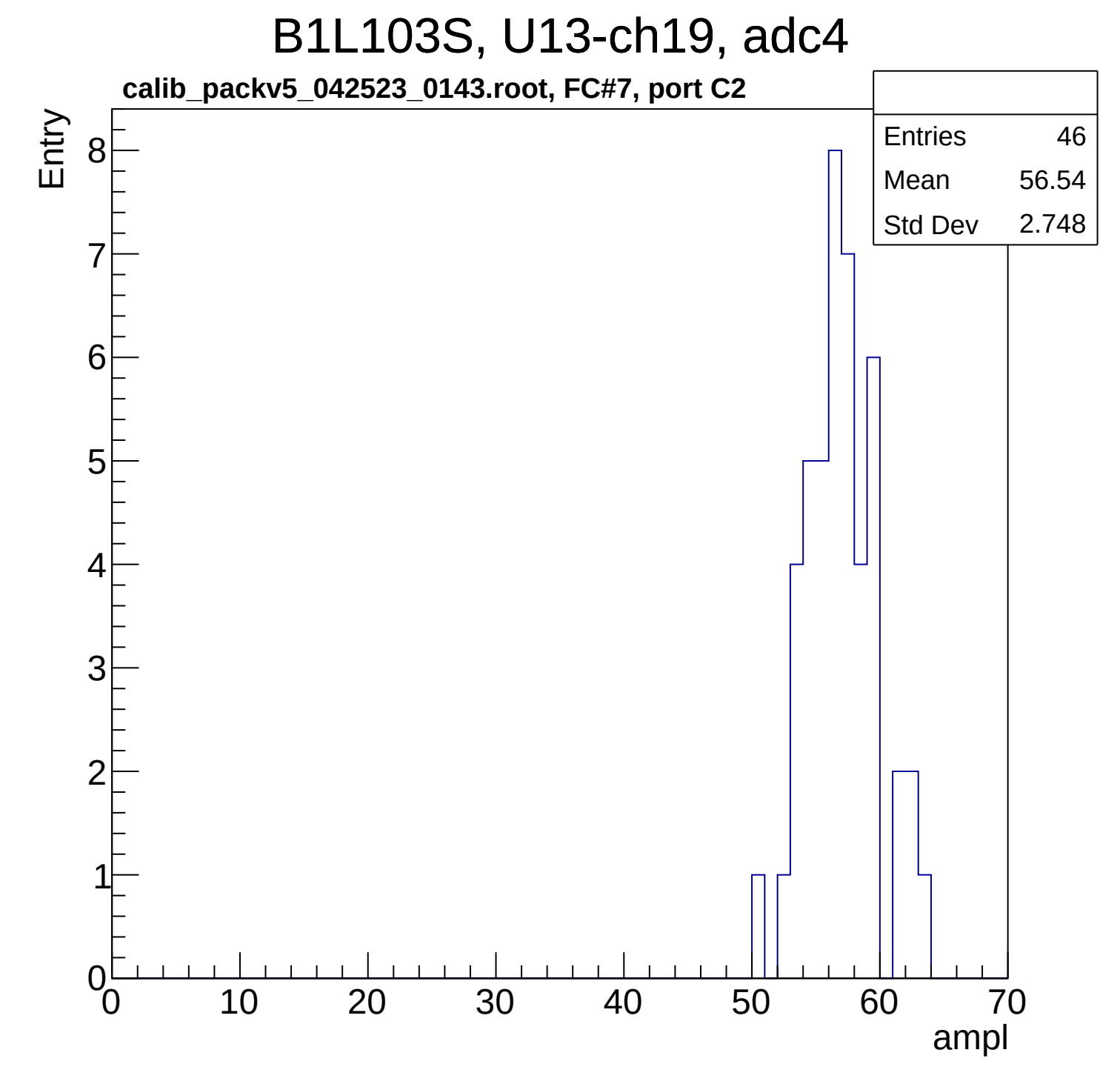
B1L103S, U13-ch19, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.54
Std Dev 2.748
Entry
ampl
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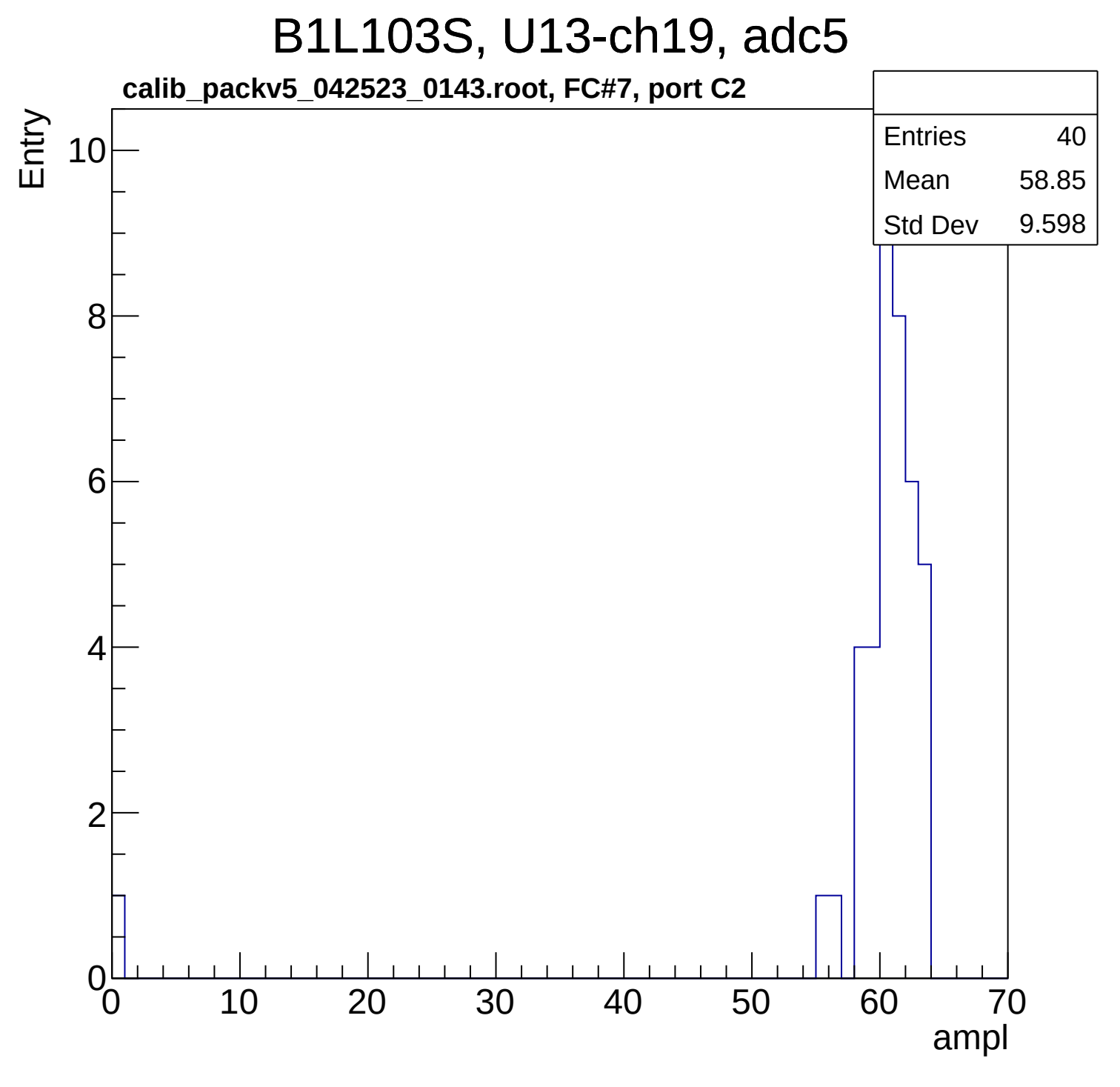
B1L103S, U13-ch19, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.85
Std Dev 9.598
Entry
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2
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10
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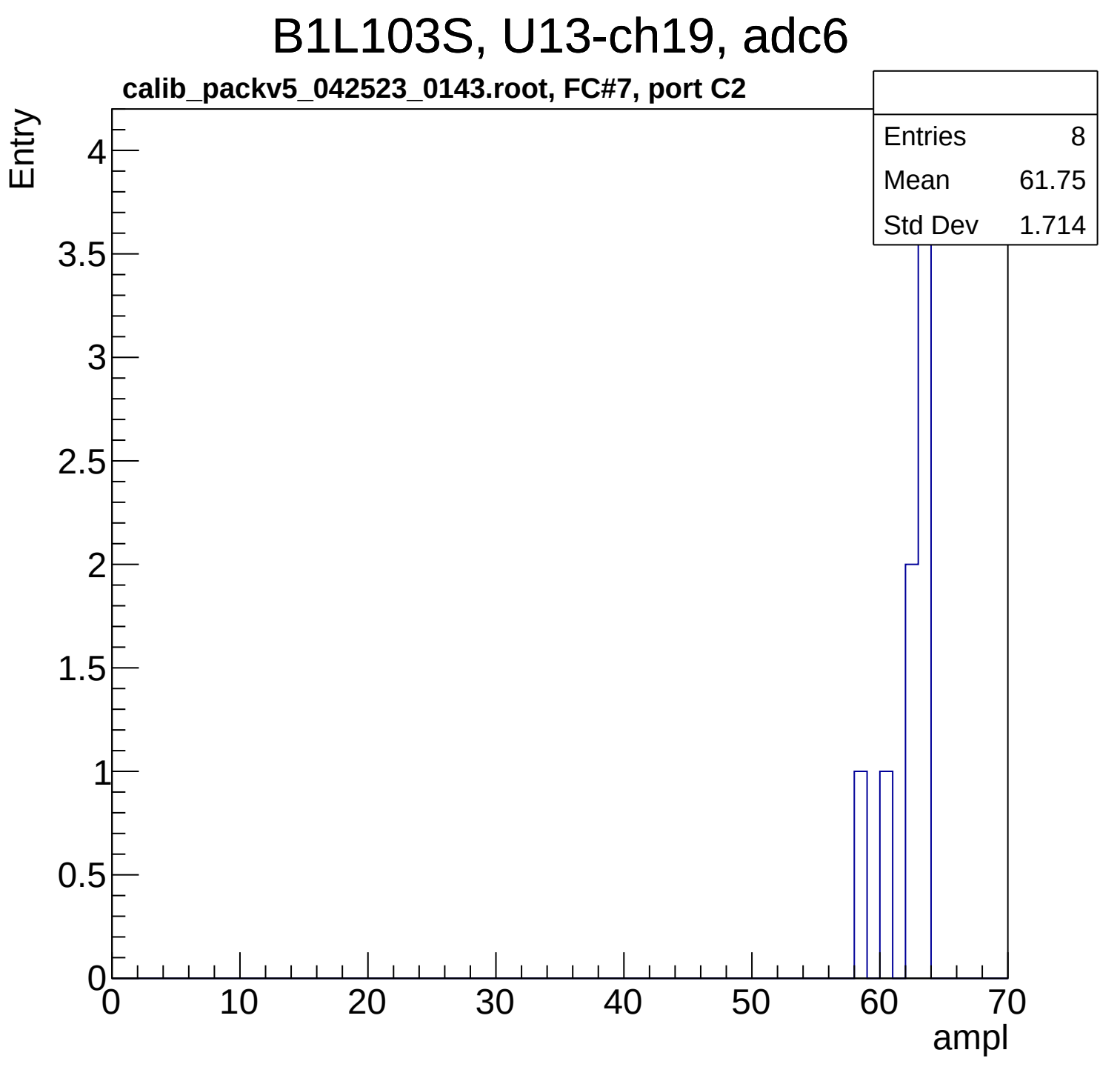
60
70
ampl


B1L103S, U13-ch19, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.75
Std Dev 1.714
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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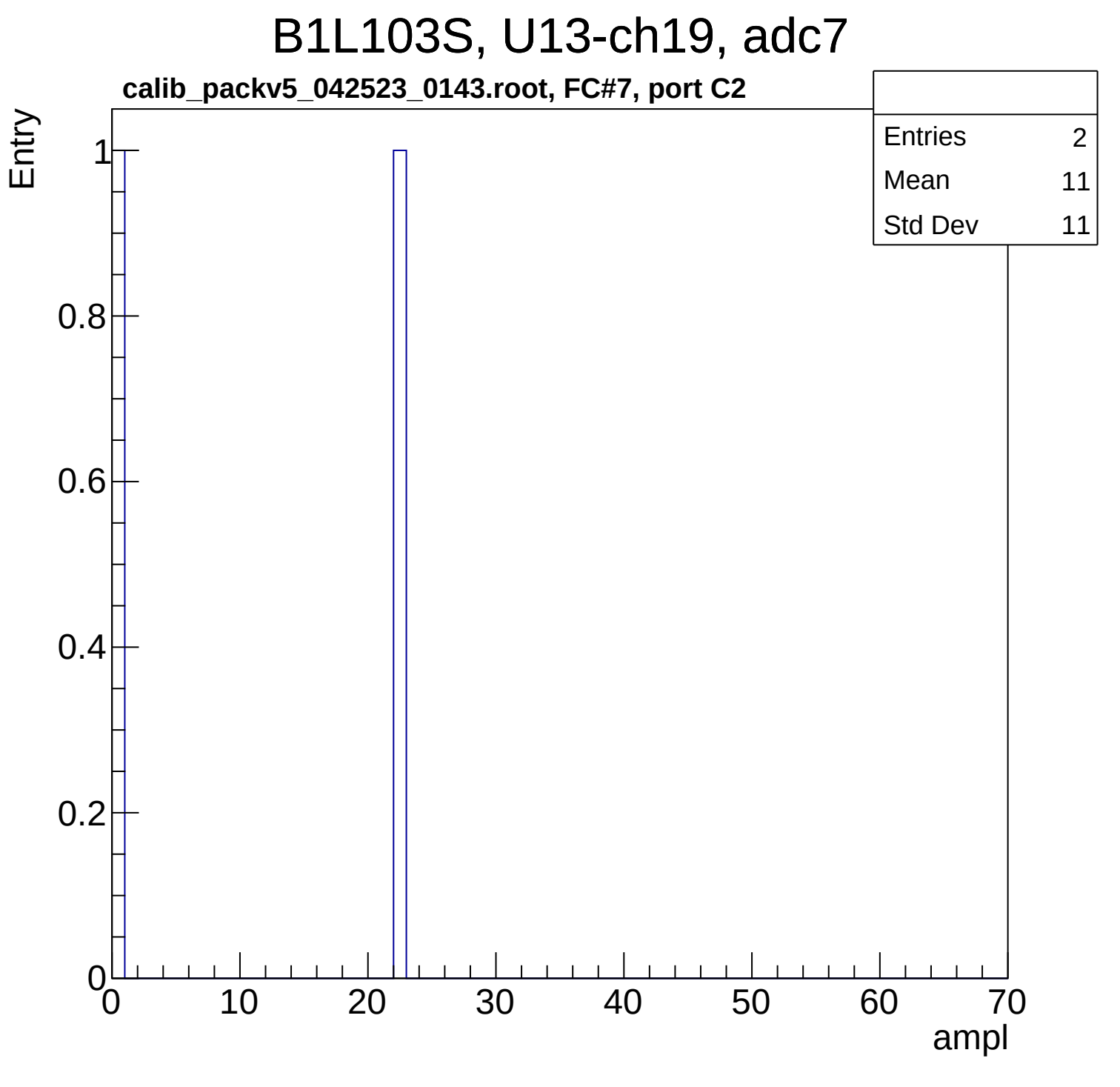
B1L103S, U13-ch19, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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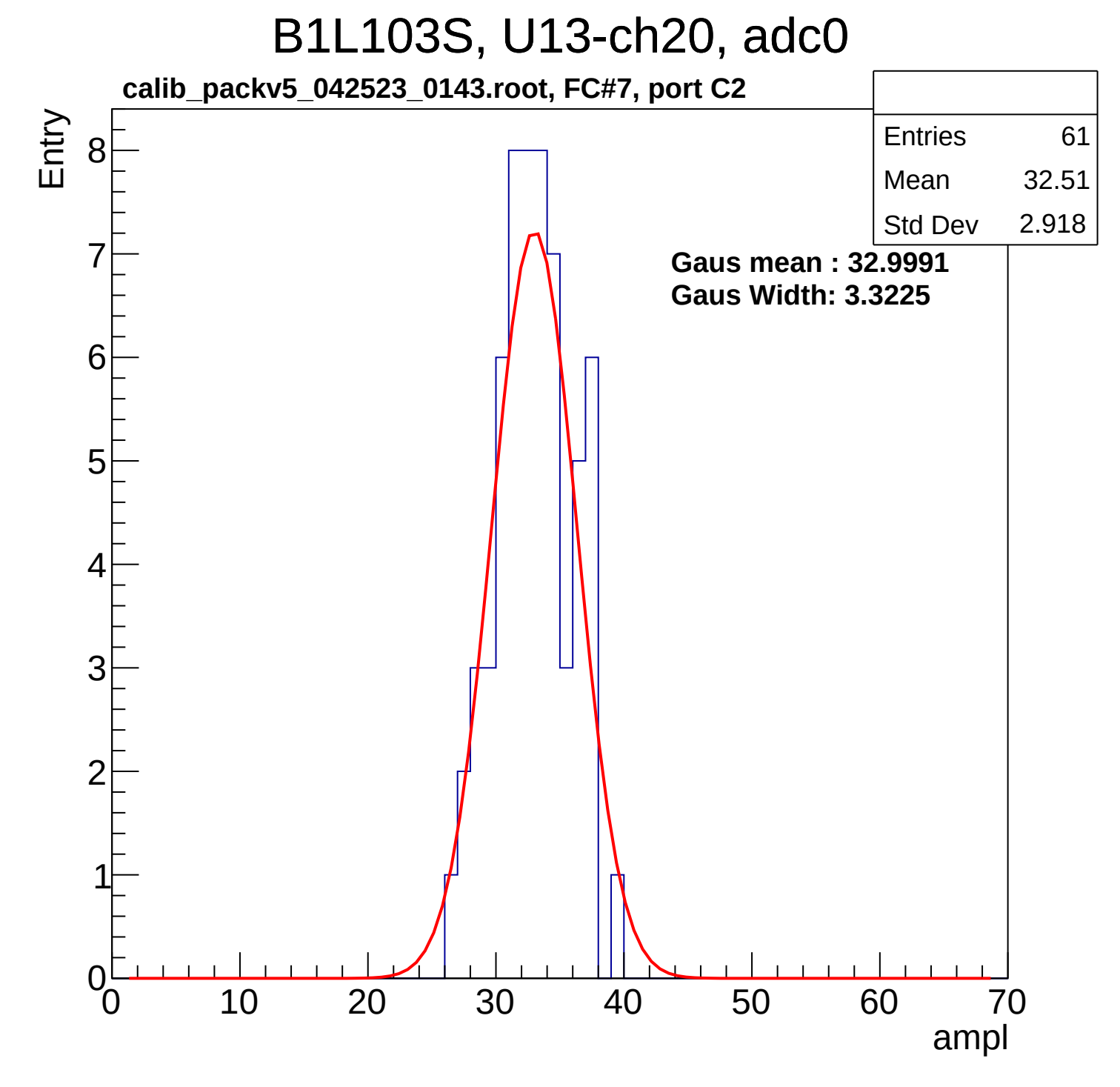
50
60
70
ampl


B1L103S, U13-ch20, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 32.51
Std Dev 2.918
Gaus mean : 32.9991
Gaus Width: 3.3225
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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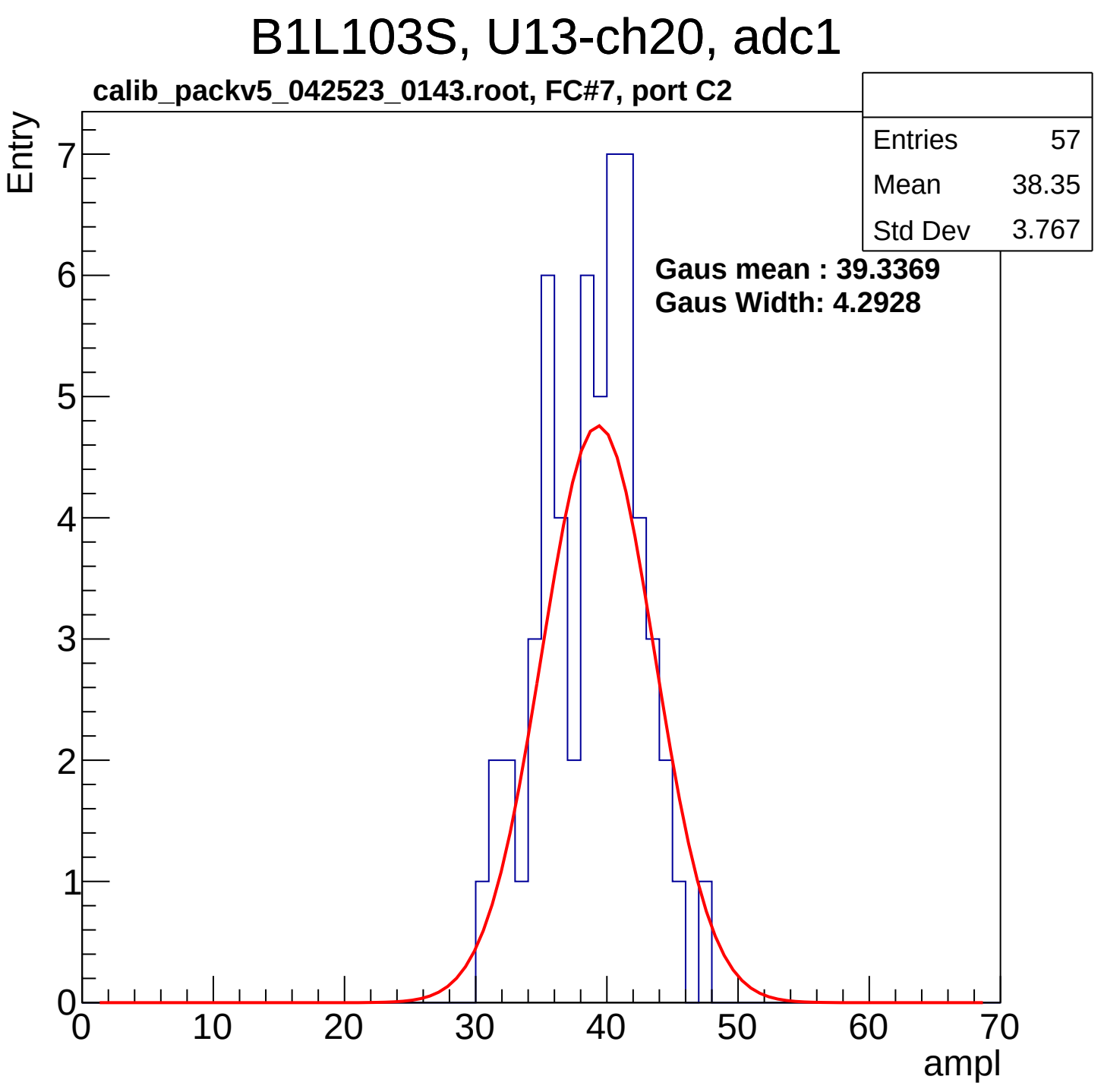
B1L103S, U13-ch20, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 38.35
Std Dev 3.767
Gaus mean : 39.3369
Gaus Width: 4.2928
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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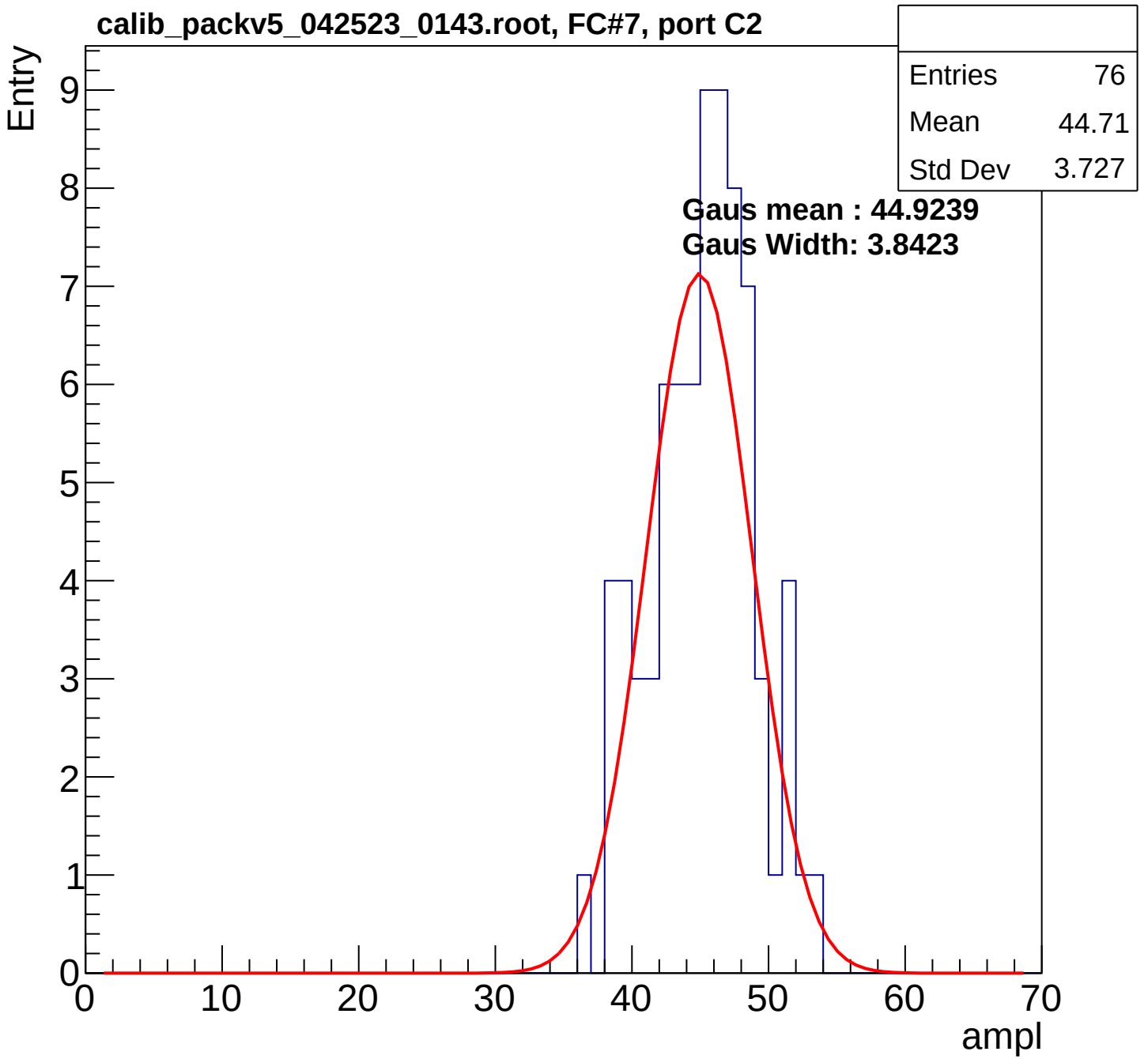
B1L103S, U13-ch20, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 44.71
Std Dev 3.727
Gaus mean : 44.9239
Gaus Width: 3.8423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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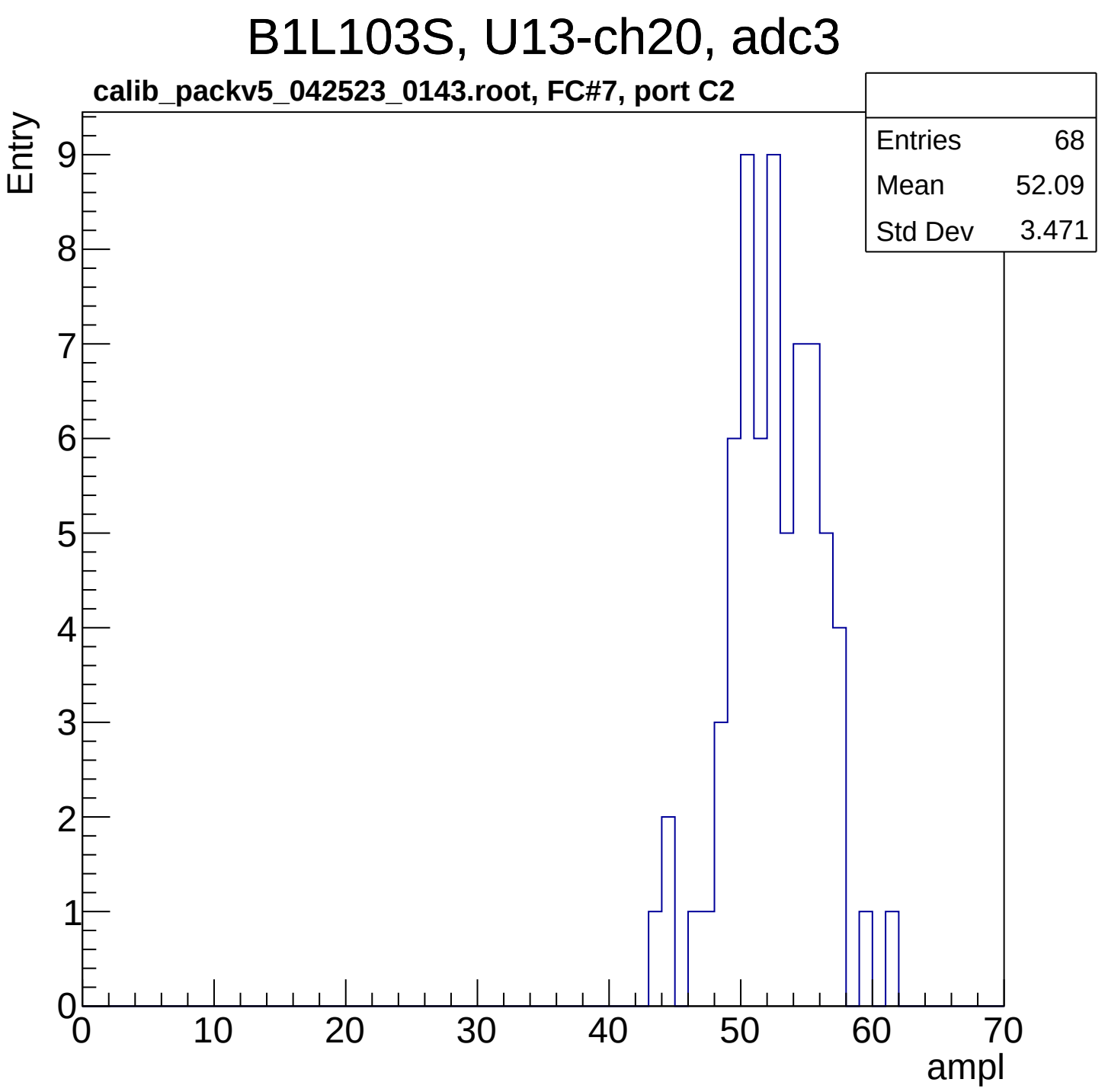
B1L103S, U13-ch20, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 52.09
Std Dev 3.471
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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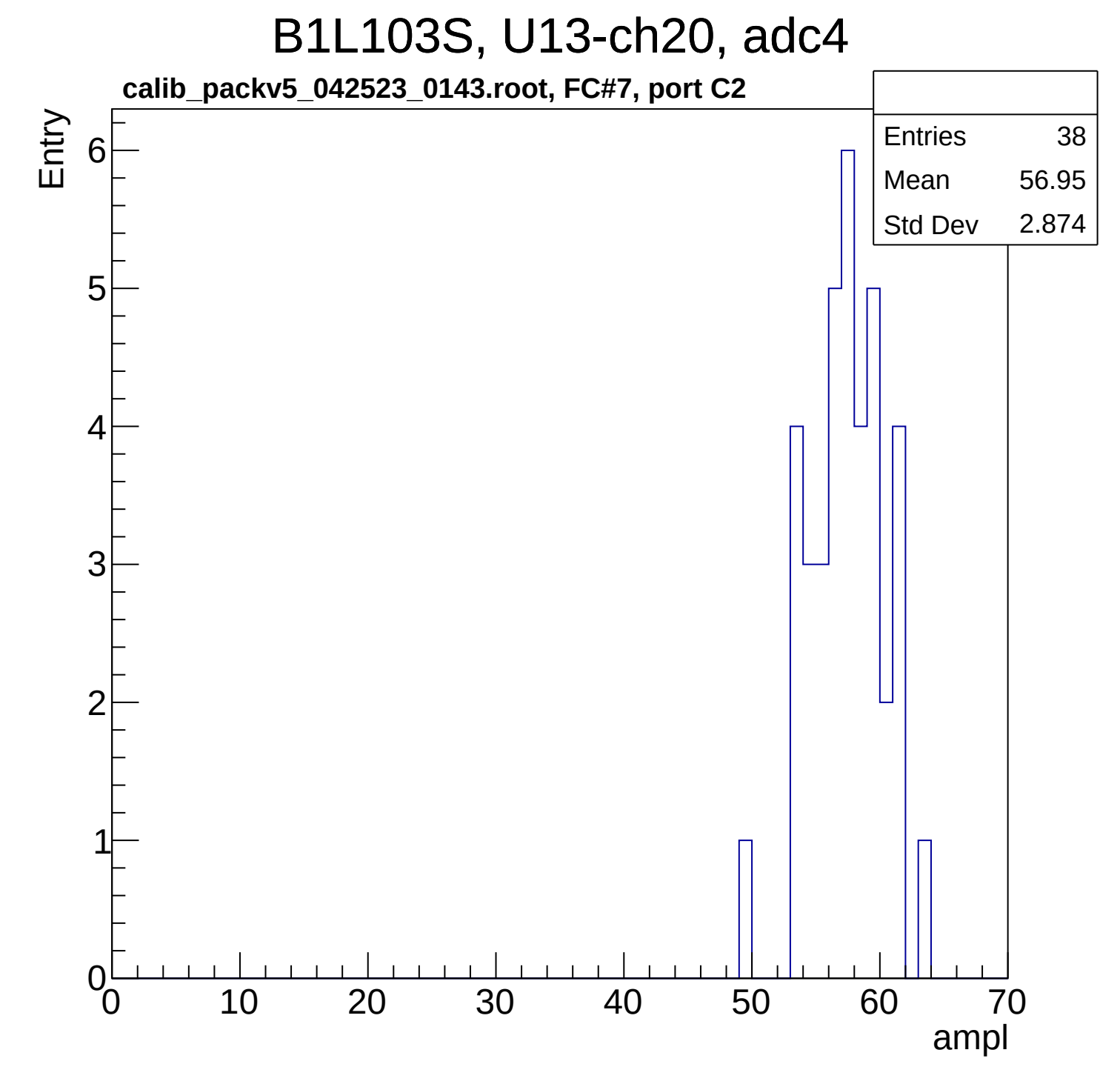
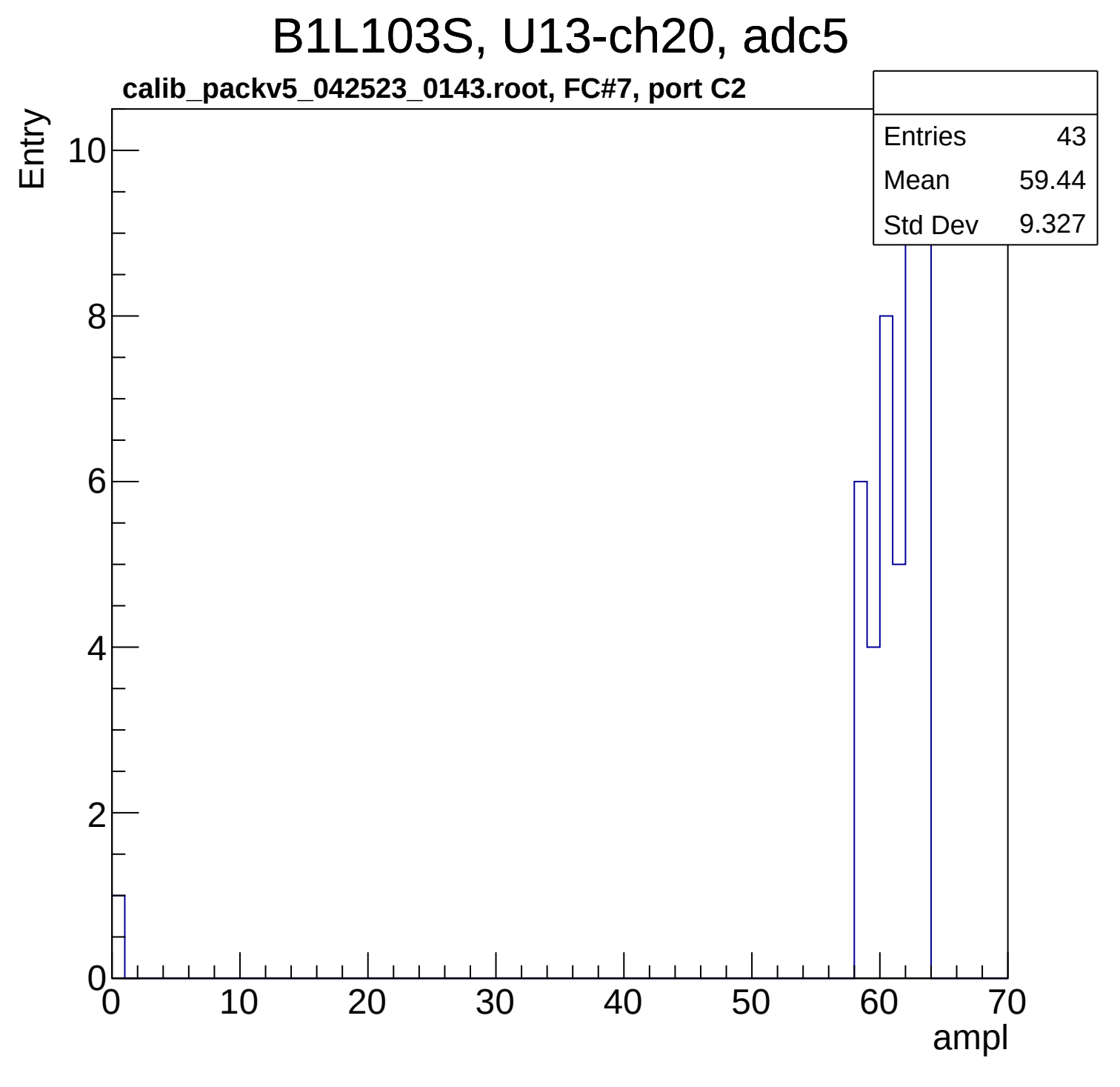

B1L103S, U13-ch20, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 56.95
Std Dev 2.874
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch20, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.44
Std Dev 9.327
Entry
0
2
4
6
8
10
0
10
20
30
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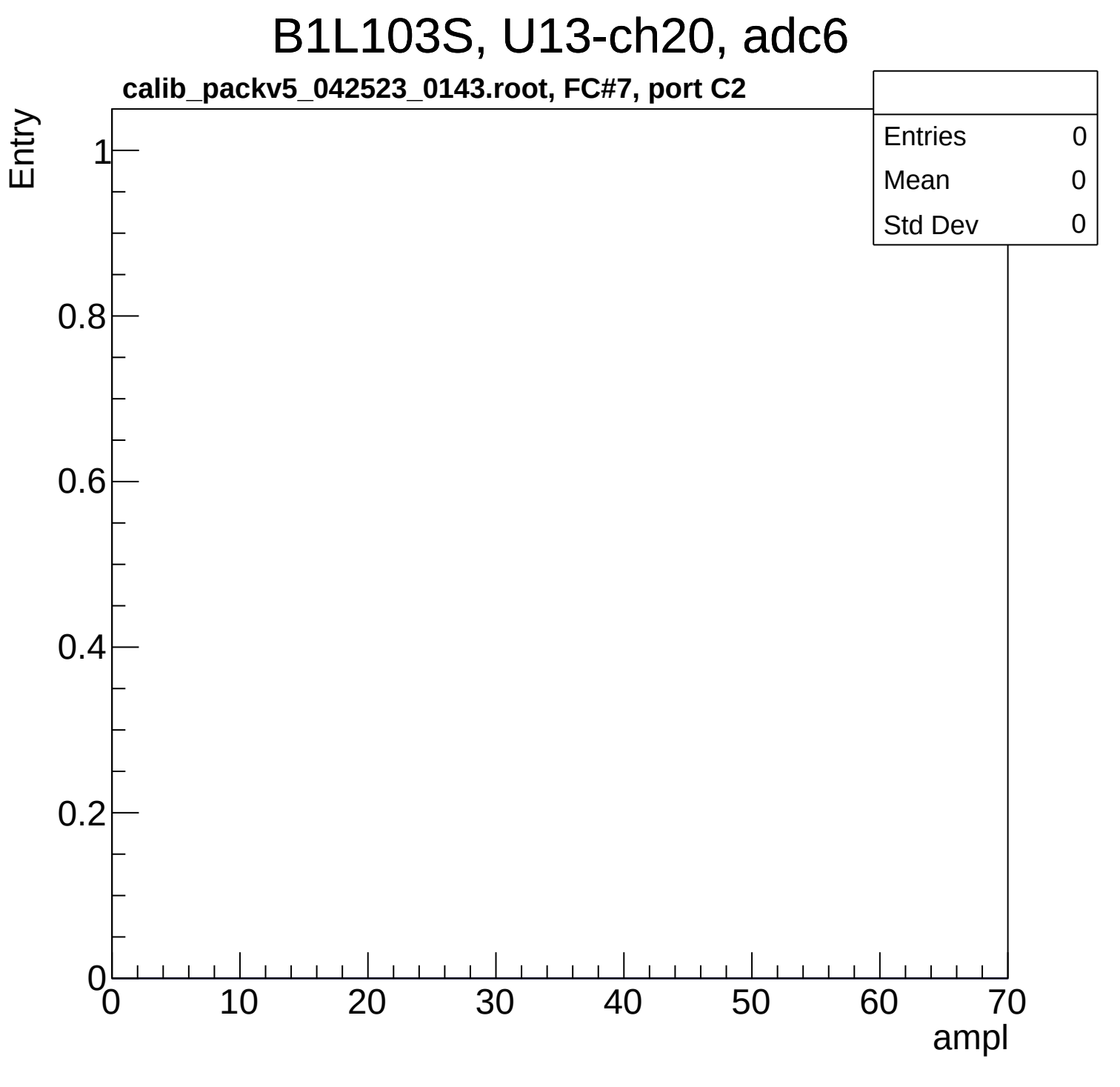
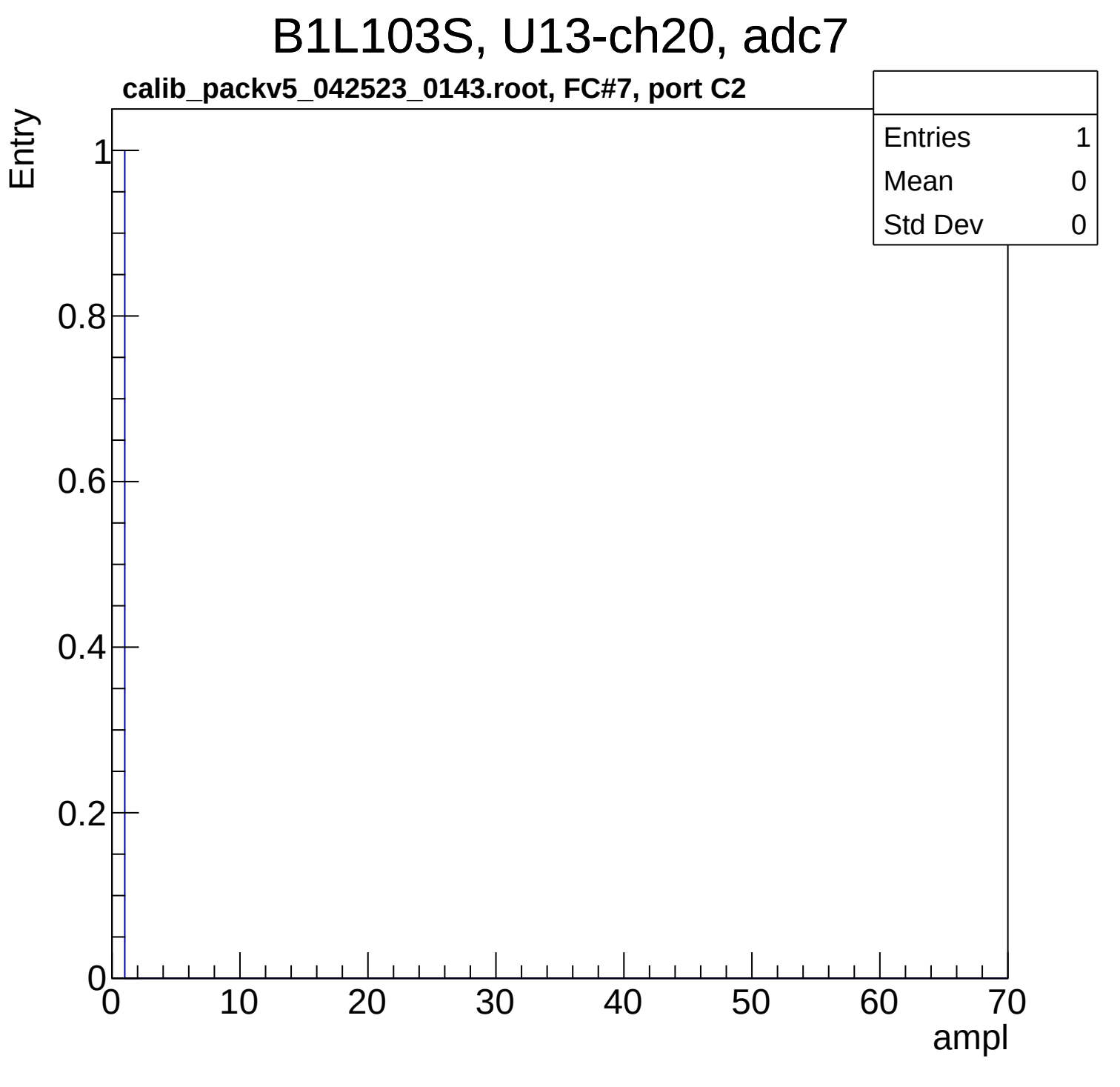
50
60
70
ampl


B1L103S, U13-ch20, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch20, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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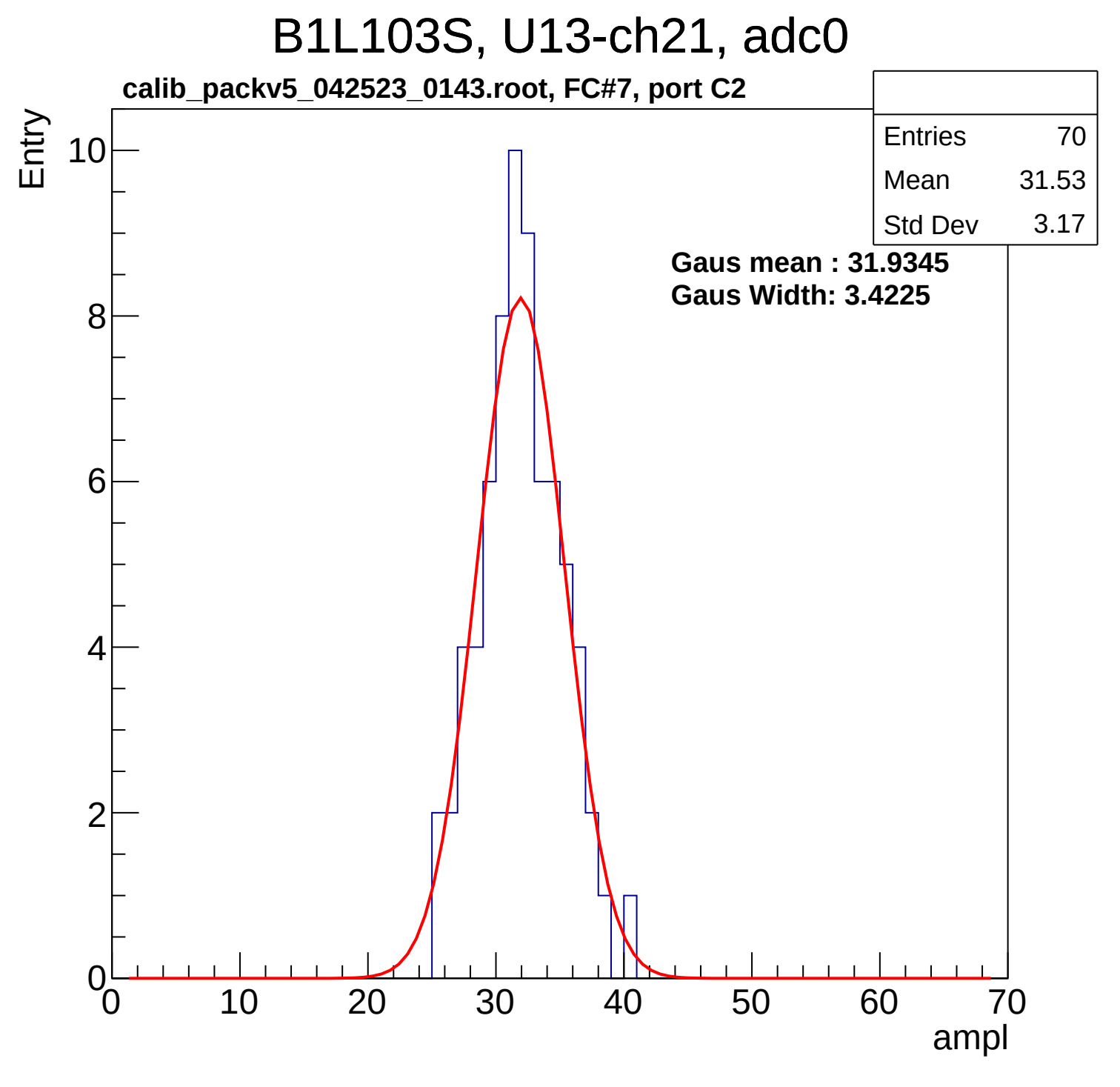
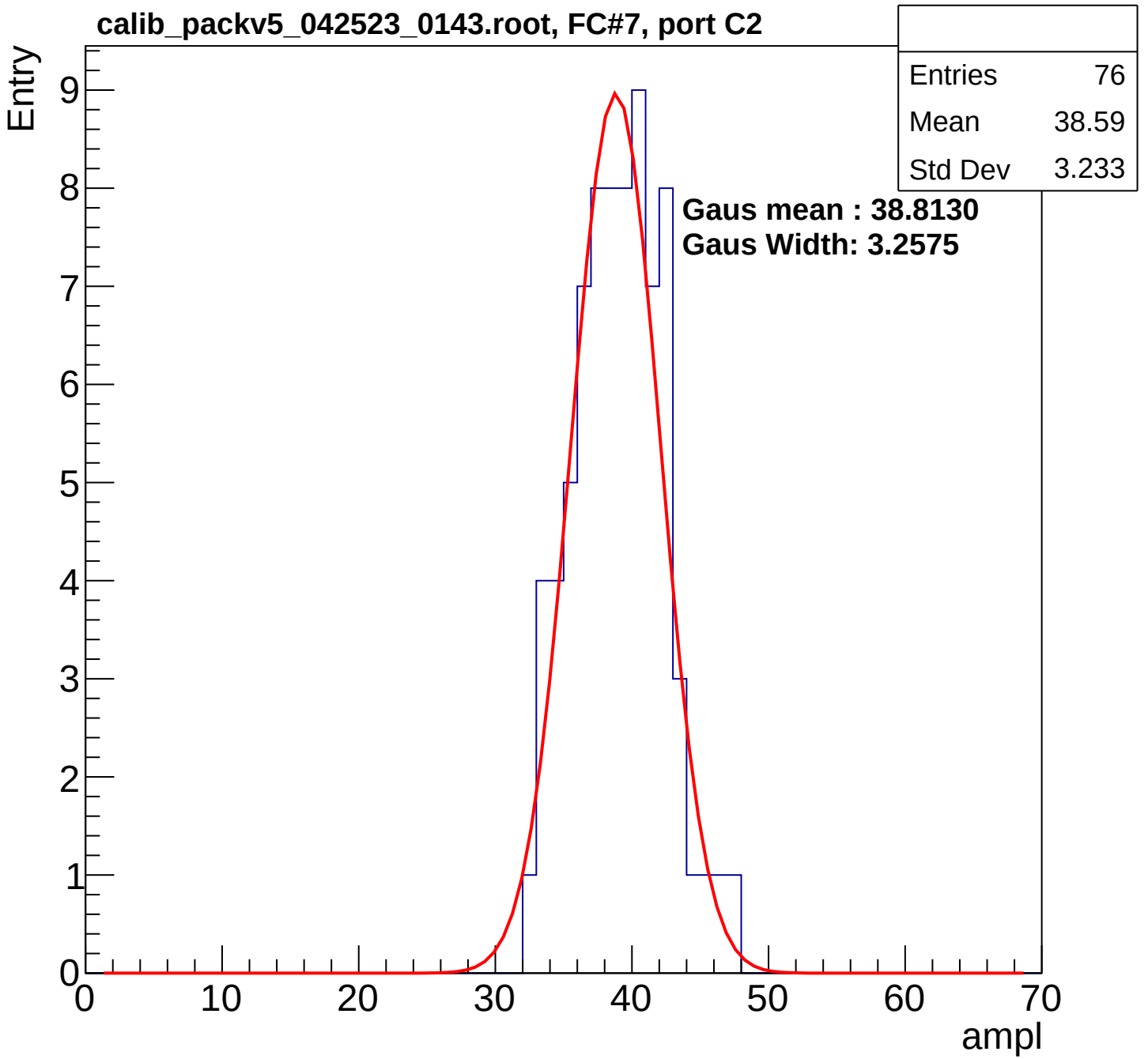
50
60
70
ampl


B1L103S, U13-ch21, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 31.53
Std Dev 3.17
Gaus mean : 31.9345
Gaus Width: 3.4225
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch21, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 38.59
Std Dev 3.233
Gaus mean : 38.8130
Gaus Width: 3.2575
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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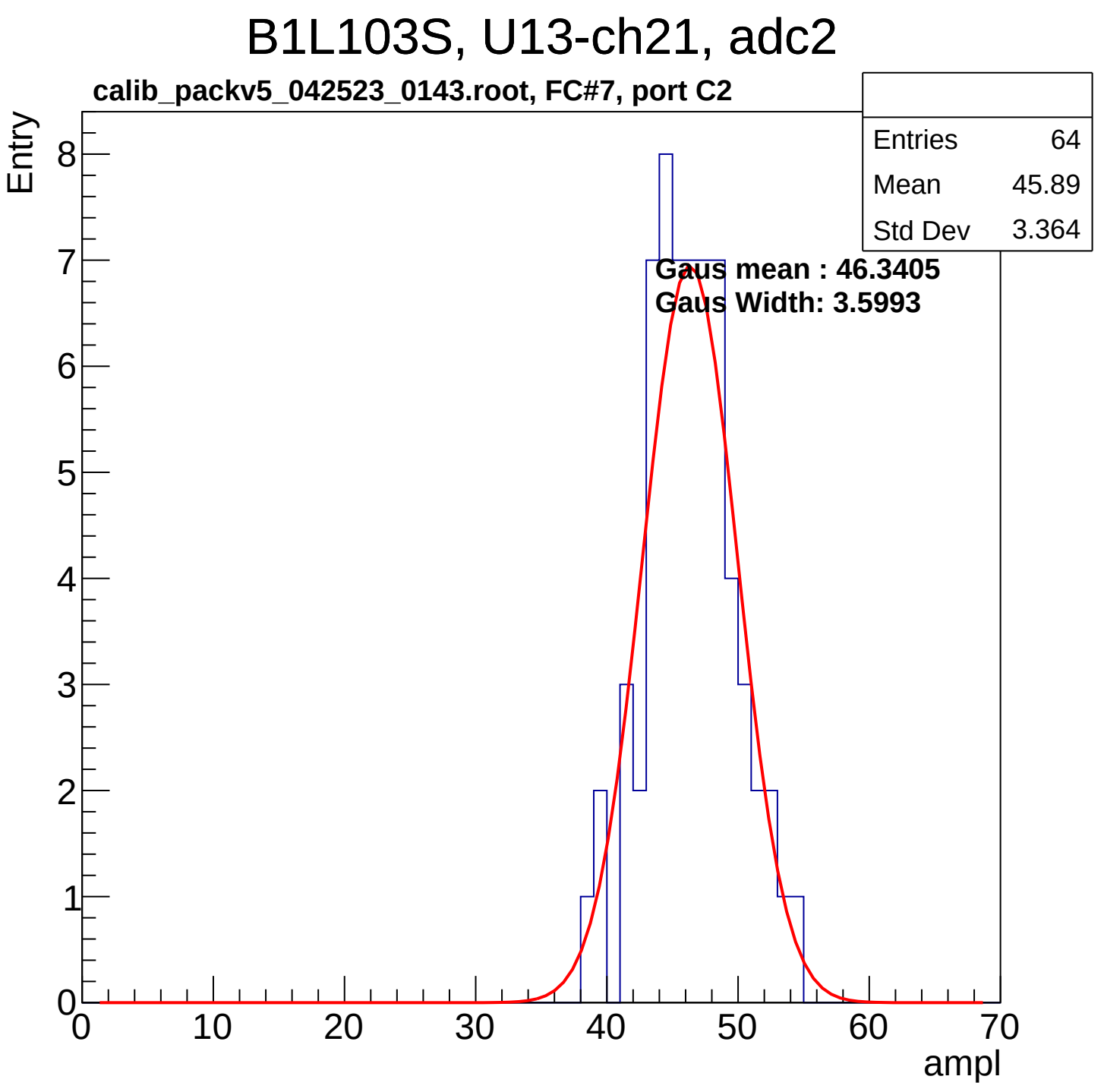
B1L103S, U13-ch21, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 45.89
Std Dev 3.364
Gaus mean : 46.3405
Gaus Width: 3.5993
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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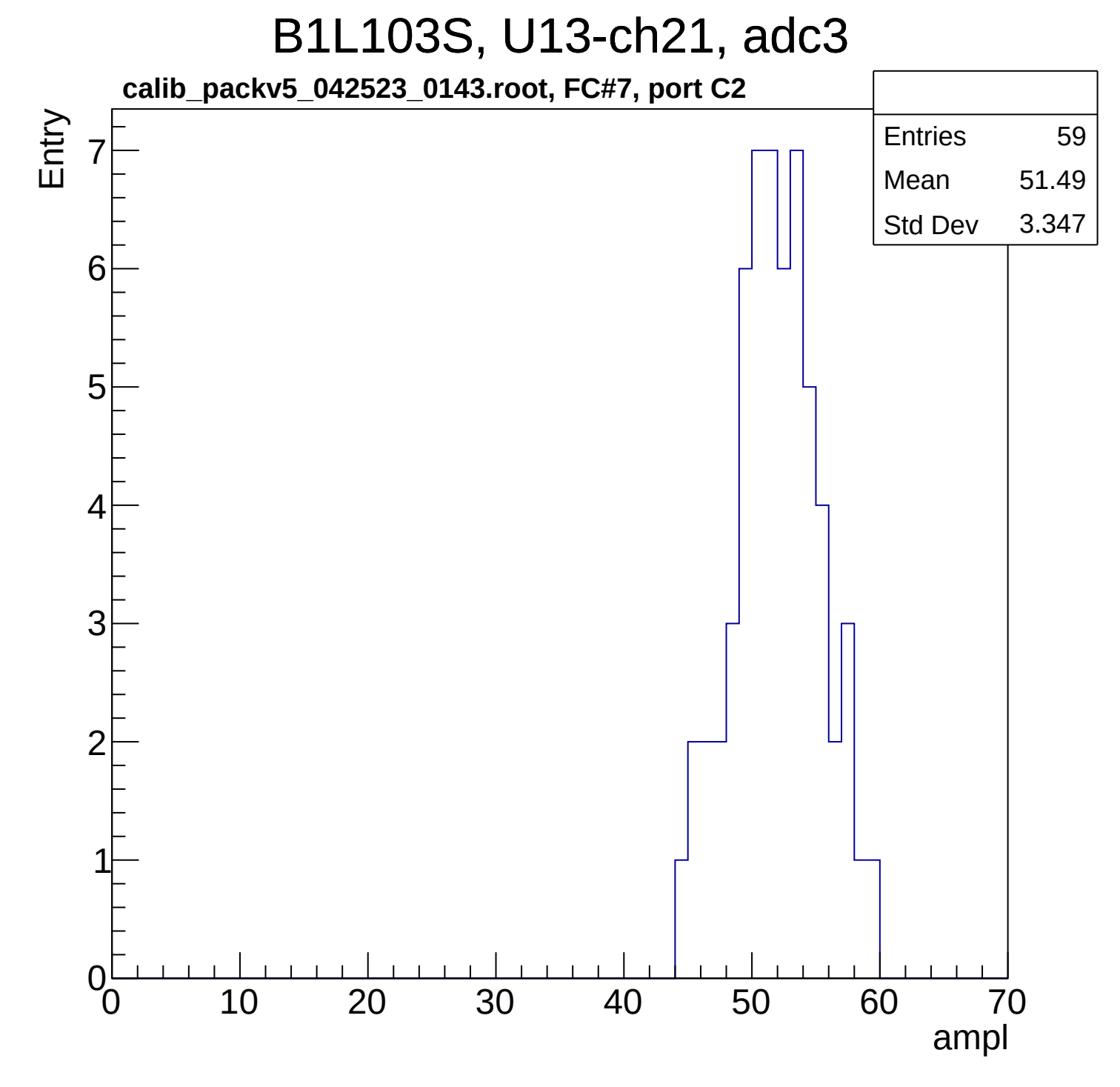
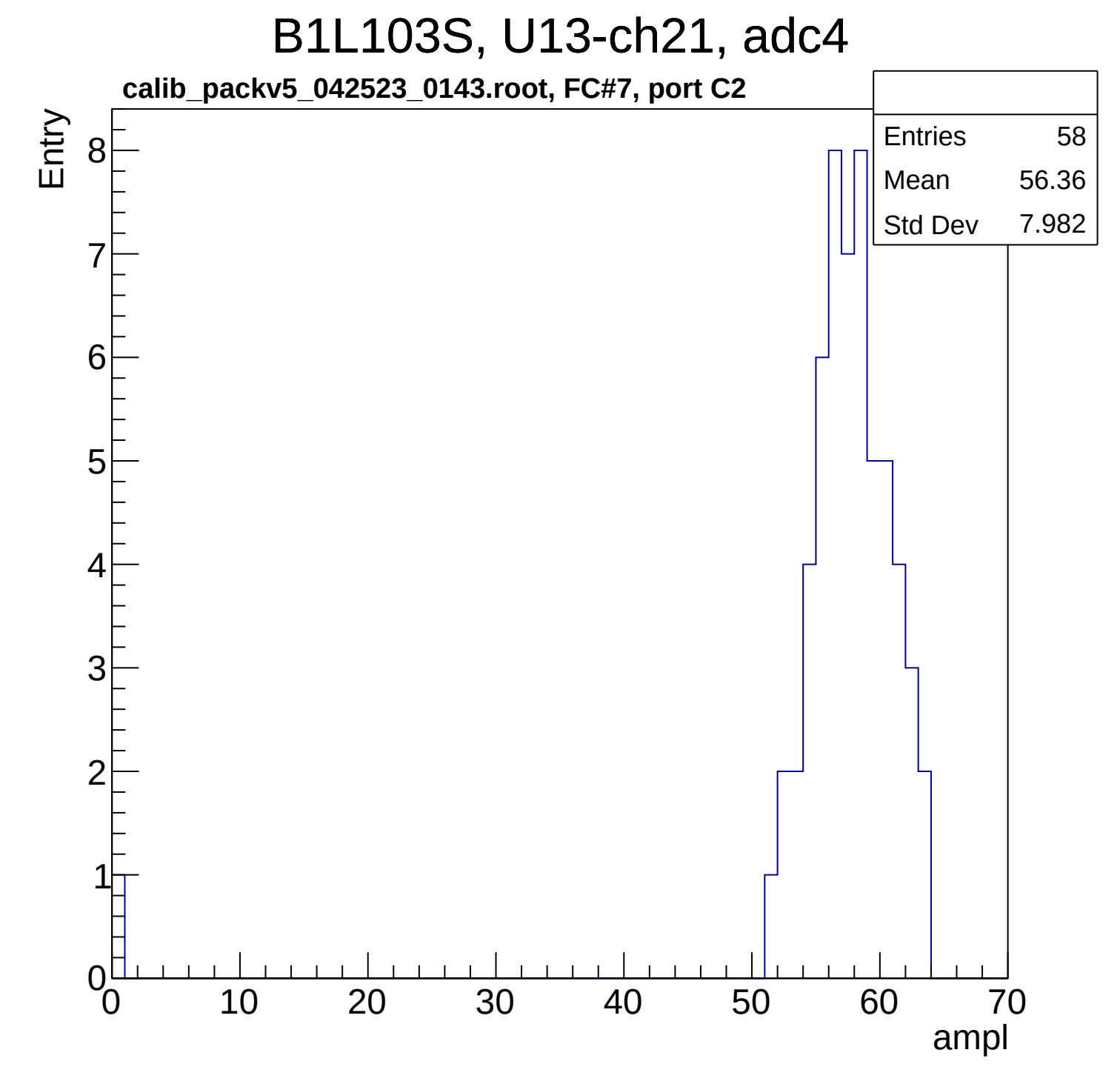

B1L103S, U13-ch21, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 51.49
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch21, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.36
Std Dev 7.982
Entry
ampl
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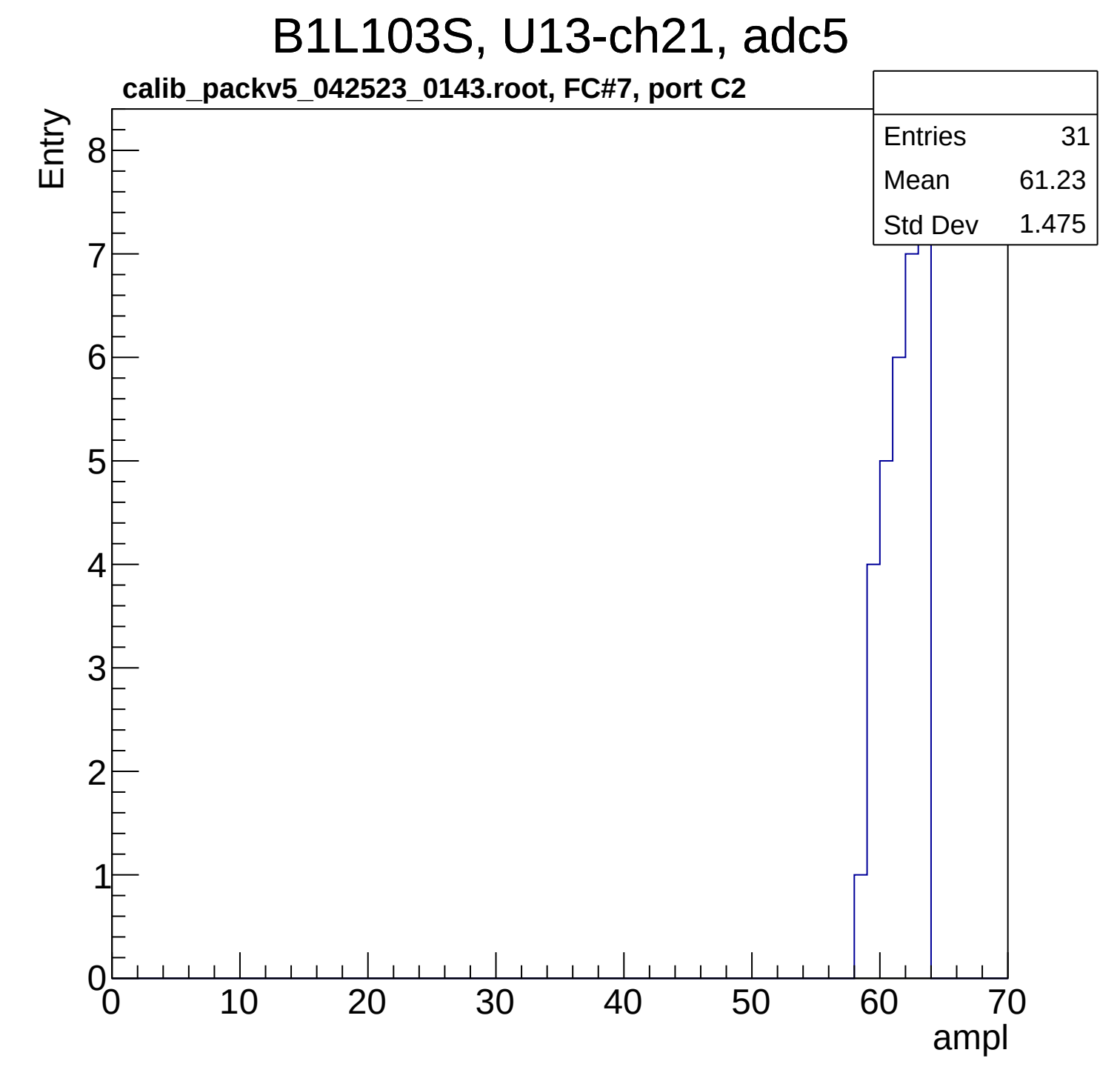
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch21, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 61.23
Std Dev 1.475
Entry
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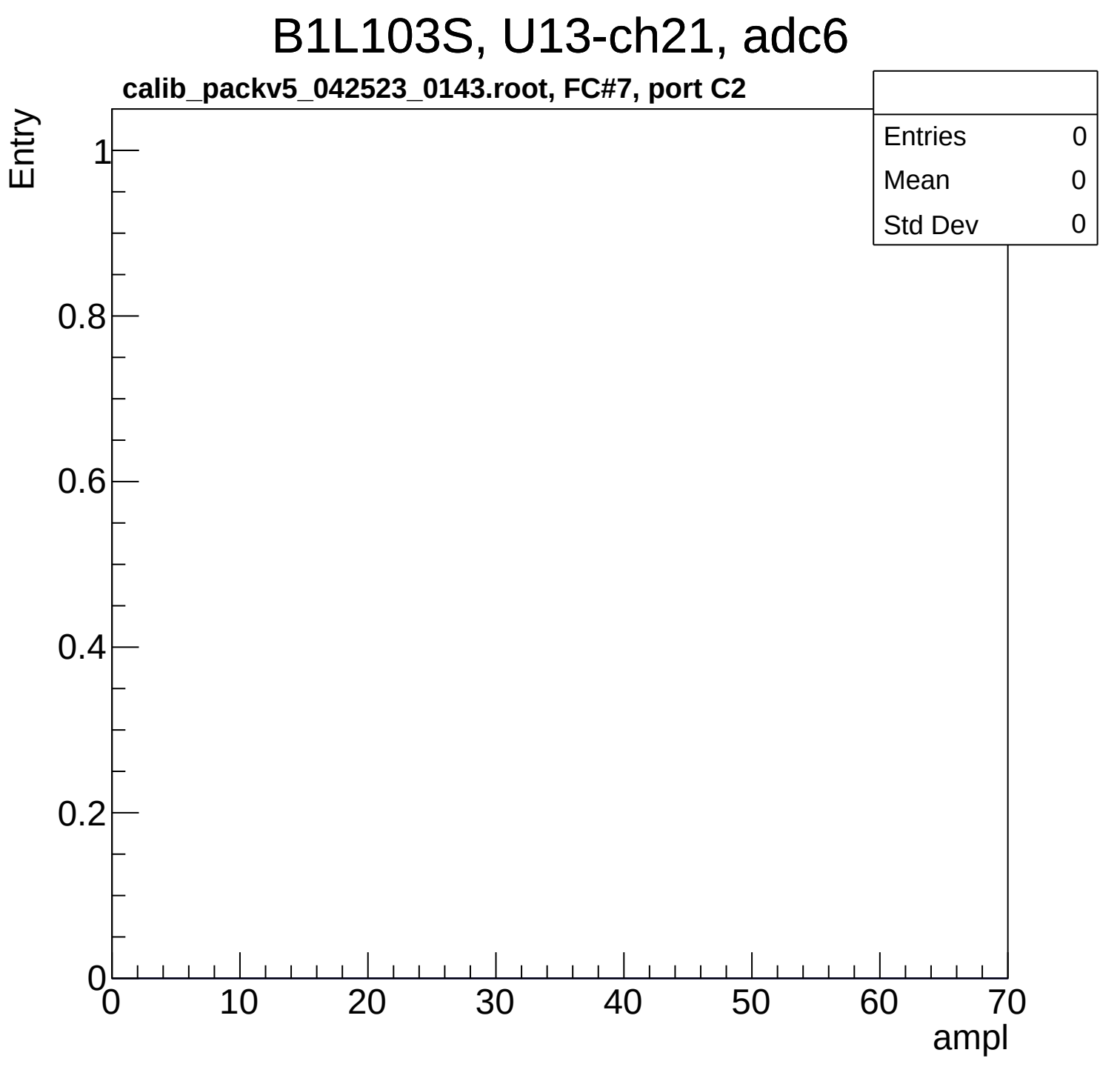
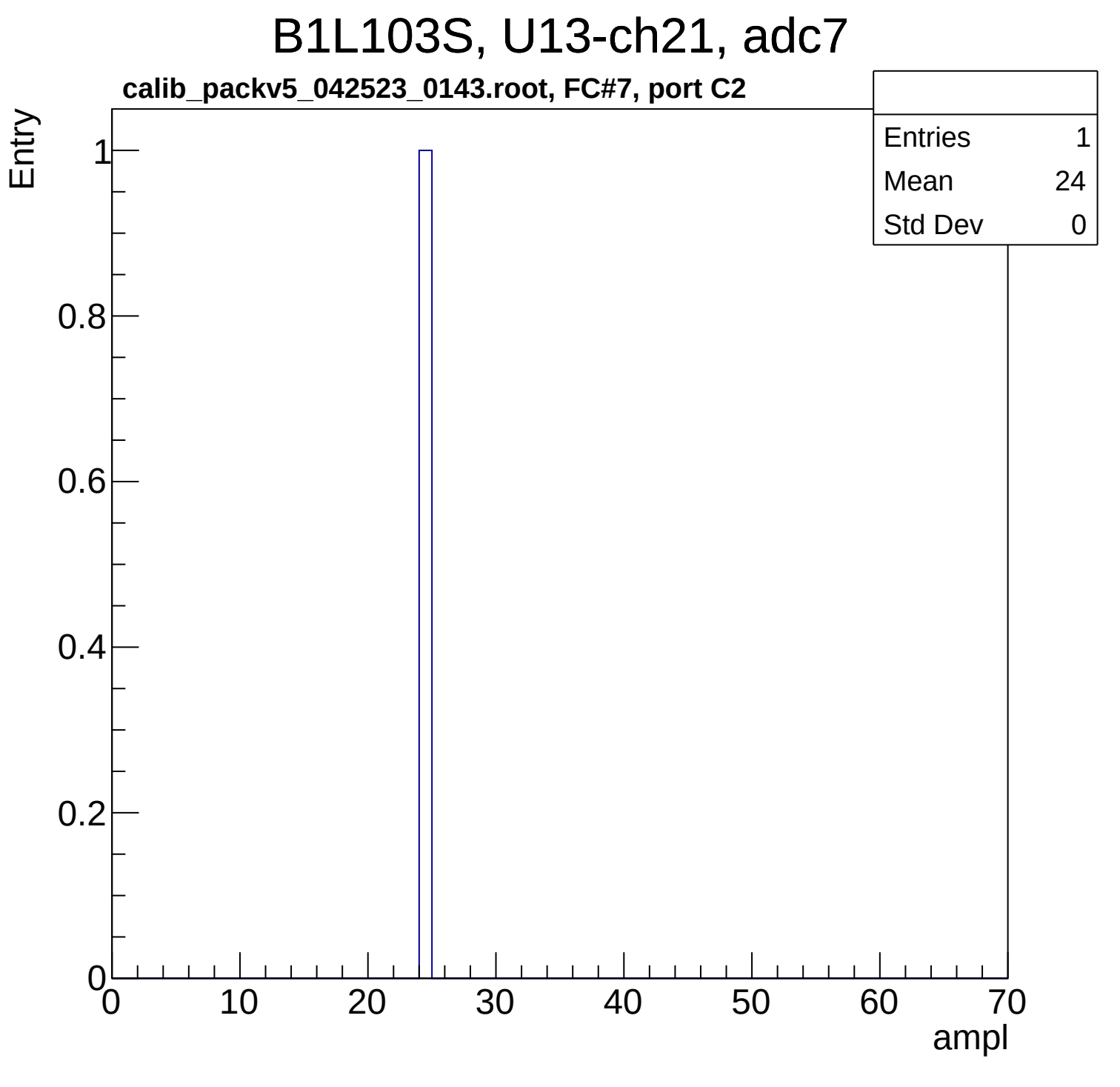
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch21, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch21, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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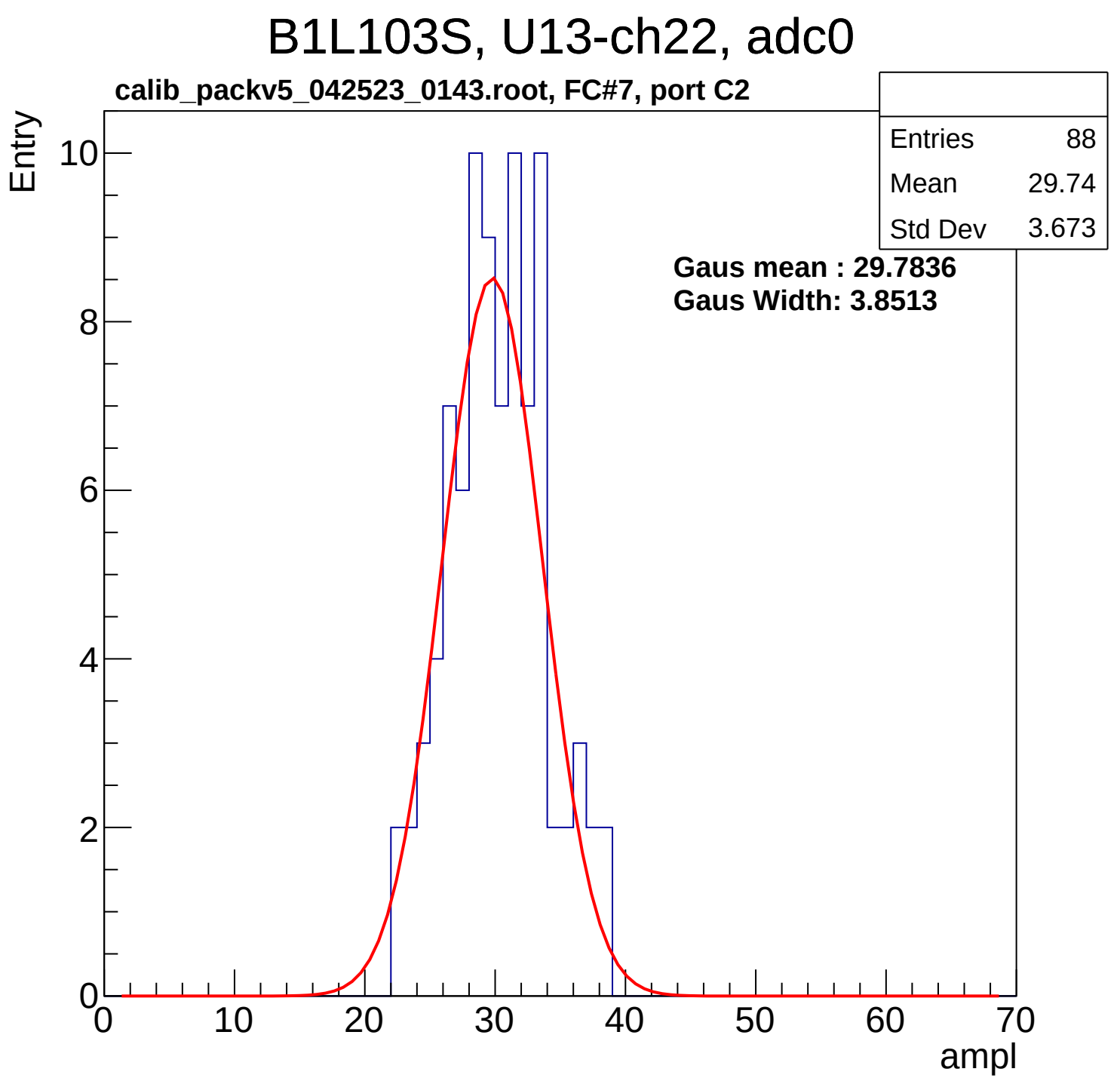
30
40
50
60
70
ampl


B1L103S, U13-ch22, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 29.74
Std Dev 3.673
Gaus mean : 29.7836
Gaus Width: 3.8513
Entry
ampl
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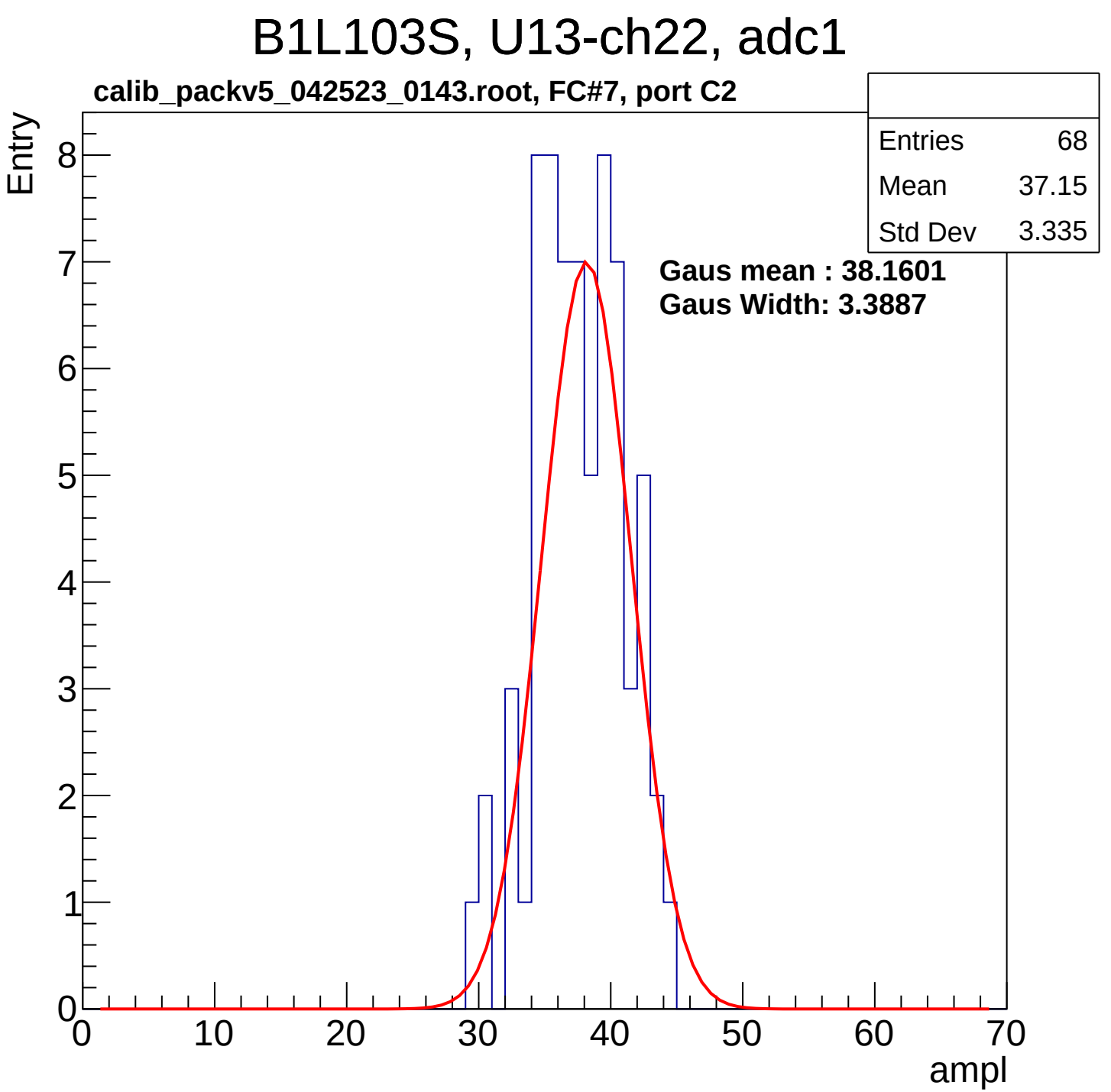
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch22, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 37.15
Std Dev 3.335
Gaus mean : 38.1601
Gaus Width: 3.3887
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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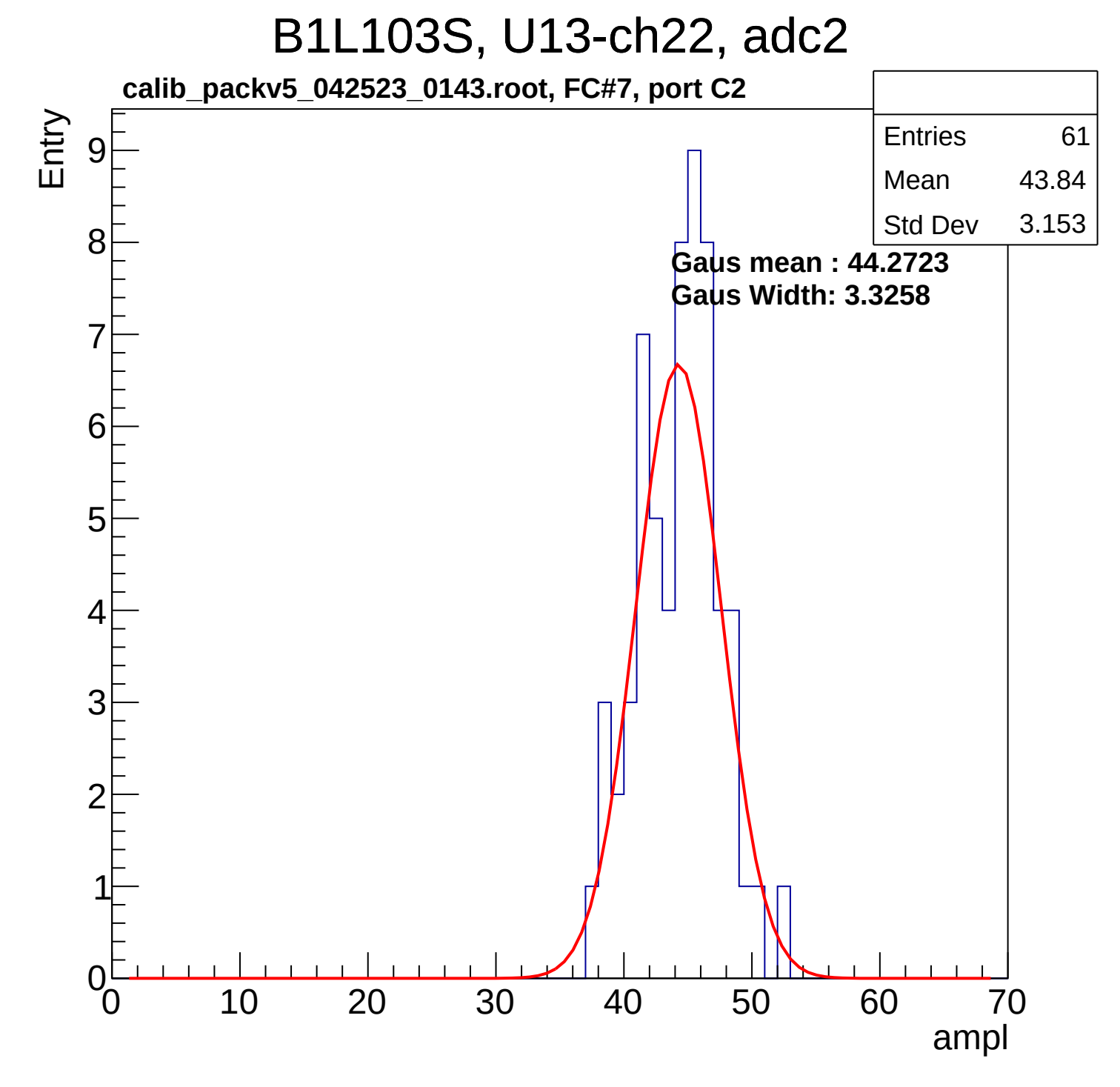
40
50
60
70


B1L103S, U13-ch22, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.84
Std Dev 3.153
Gaus mean : 44.2723
Gaus Width: 3.3258
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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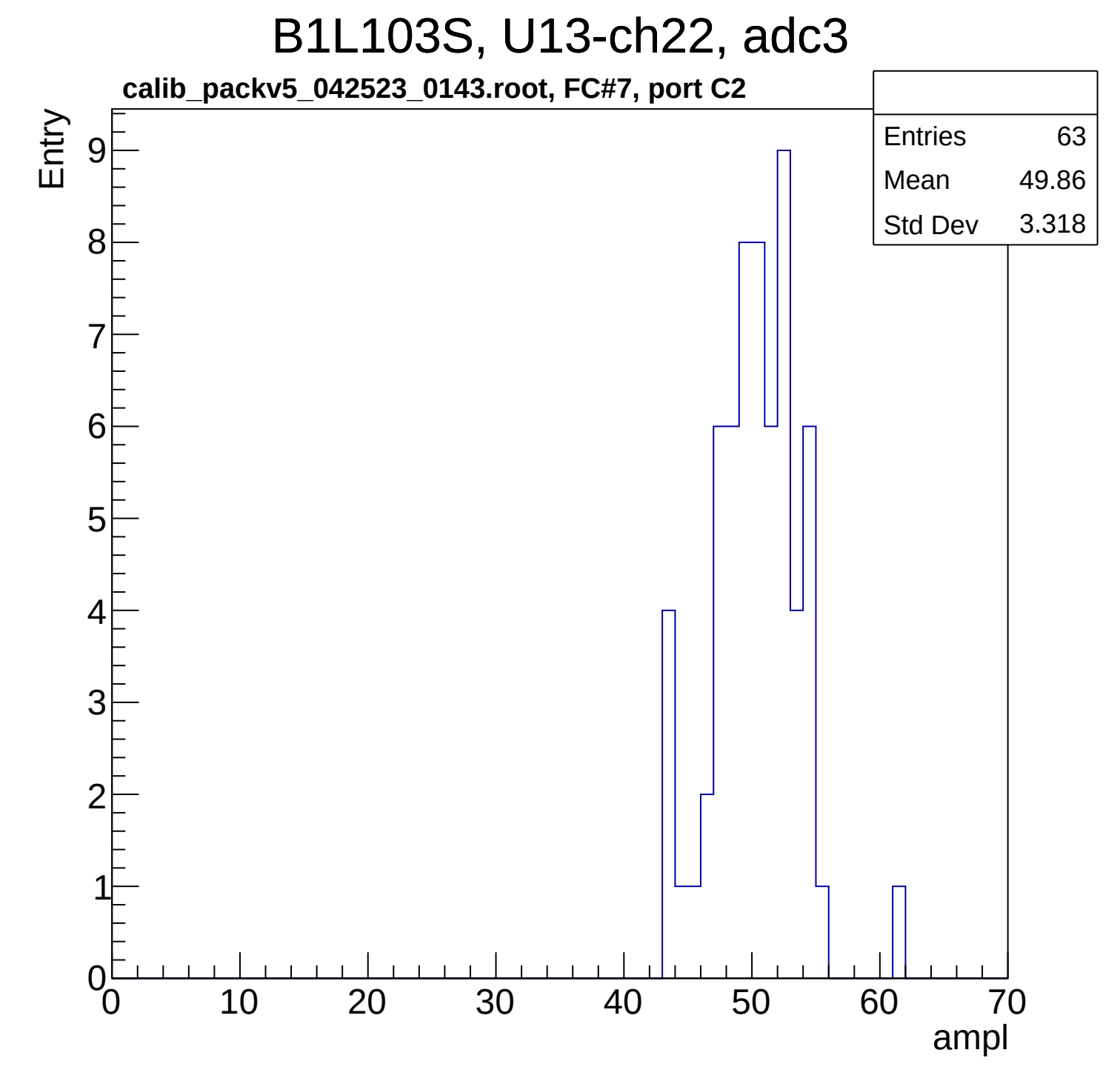
40
50
60
70


B1L103S, U13-ch22, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.86
Std Dev 3.318
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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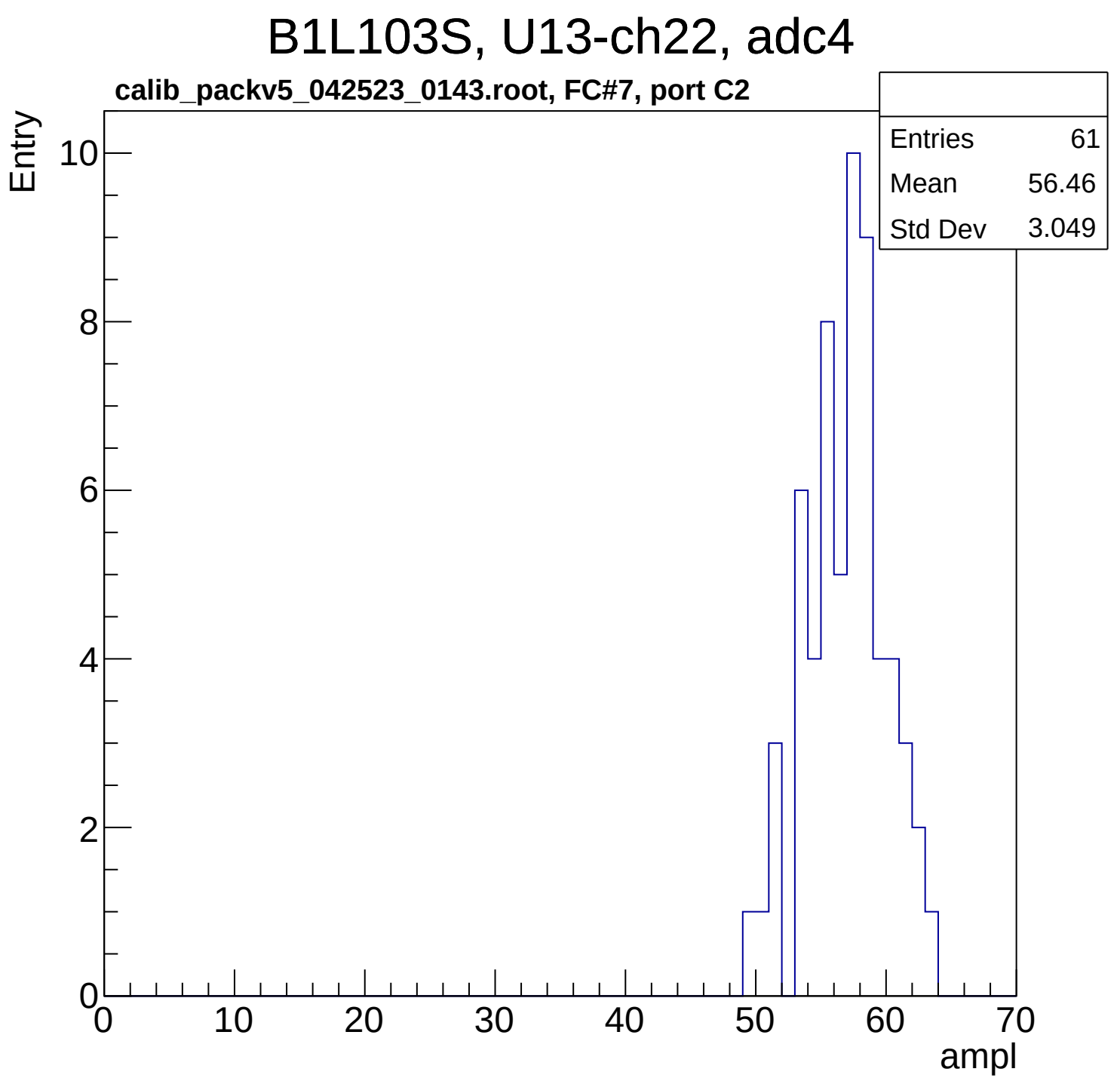
B1L103S, U13-ch22, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.46
Std Dev 3.049
Entry
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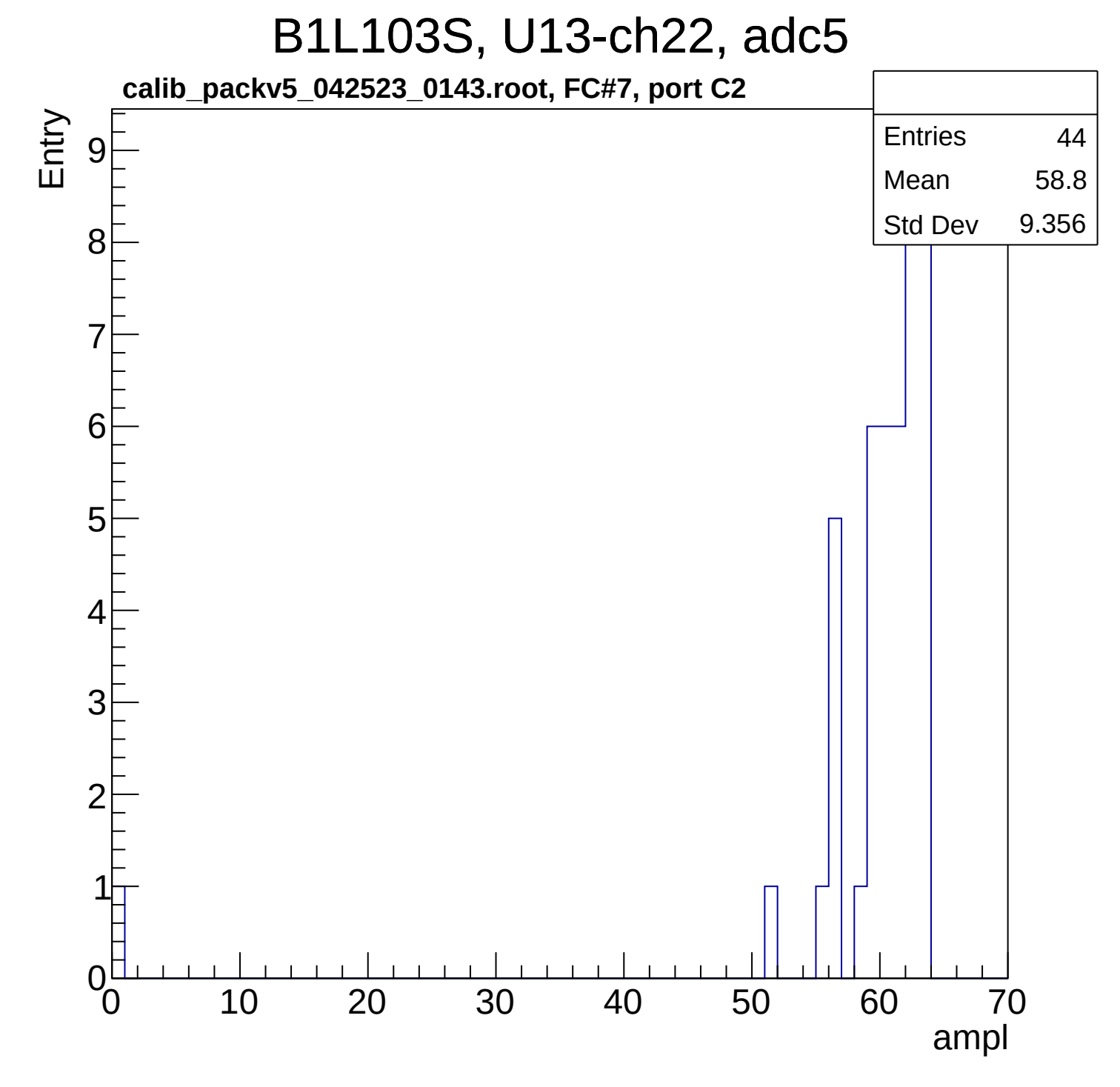
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch22, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.8
Std Dev 9.356
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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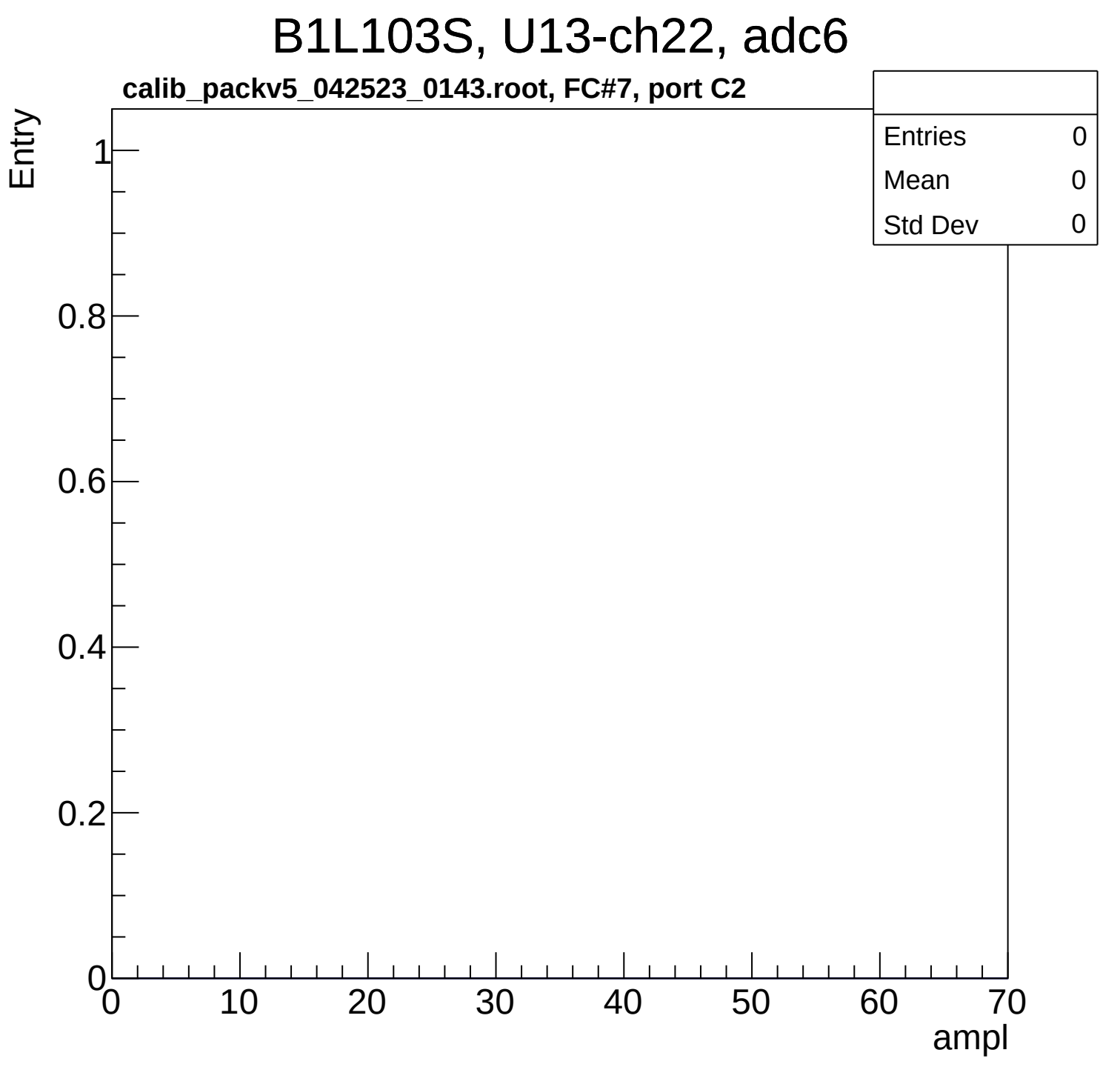
B1L103S, U13-ch22, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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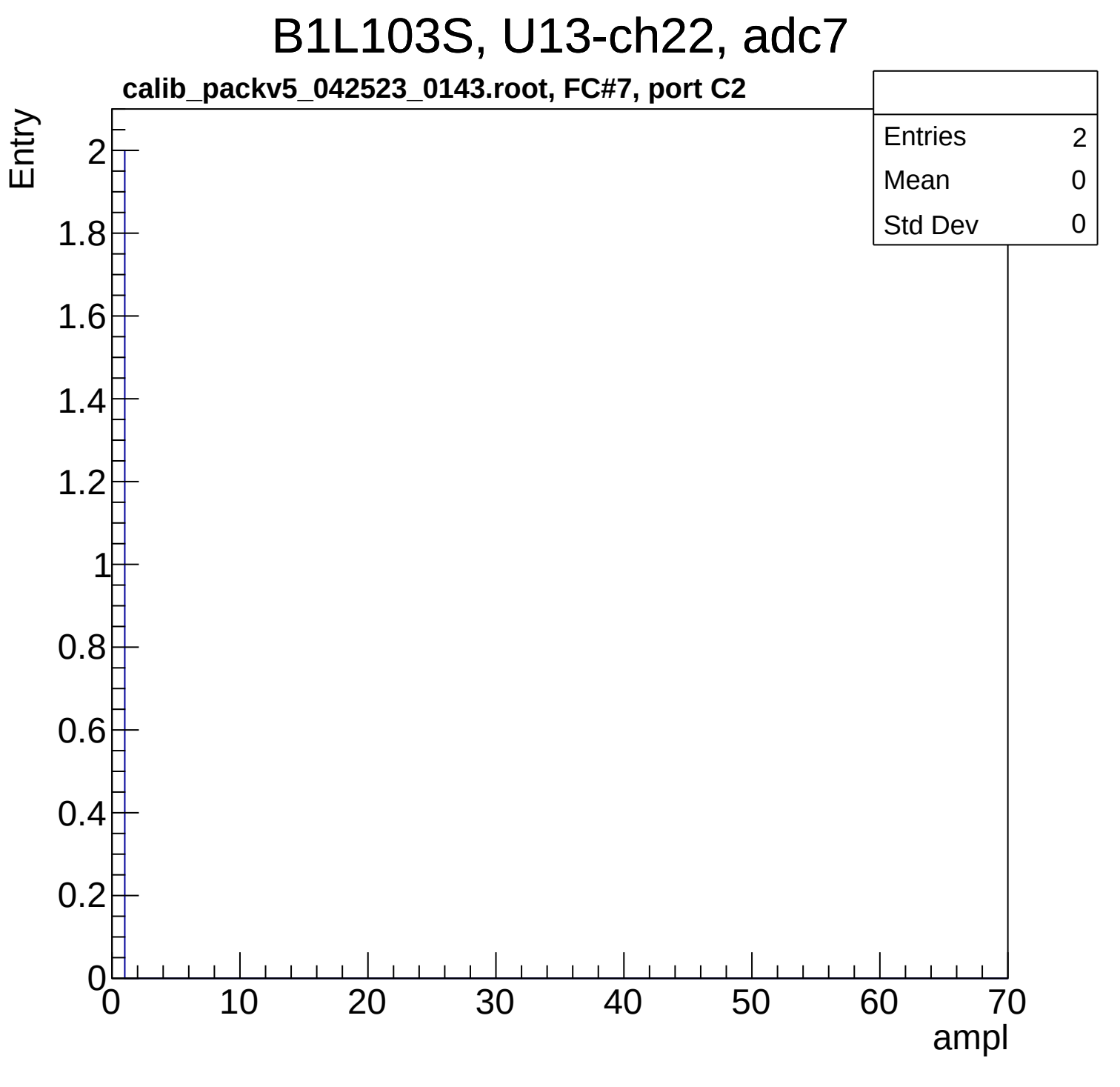
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch22, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
20
30
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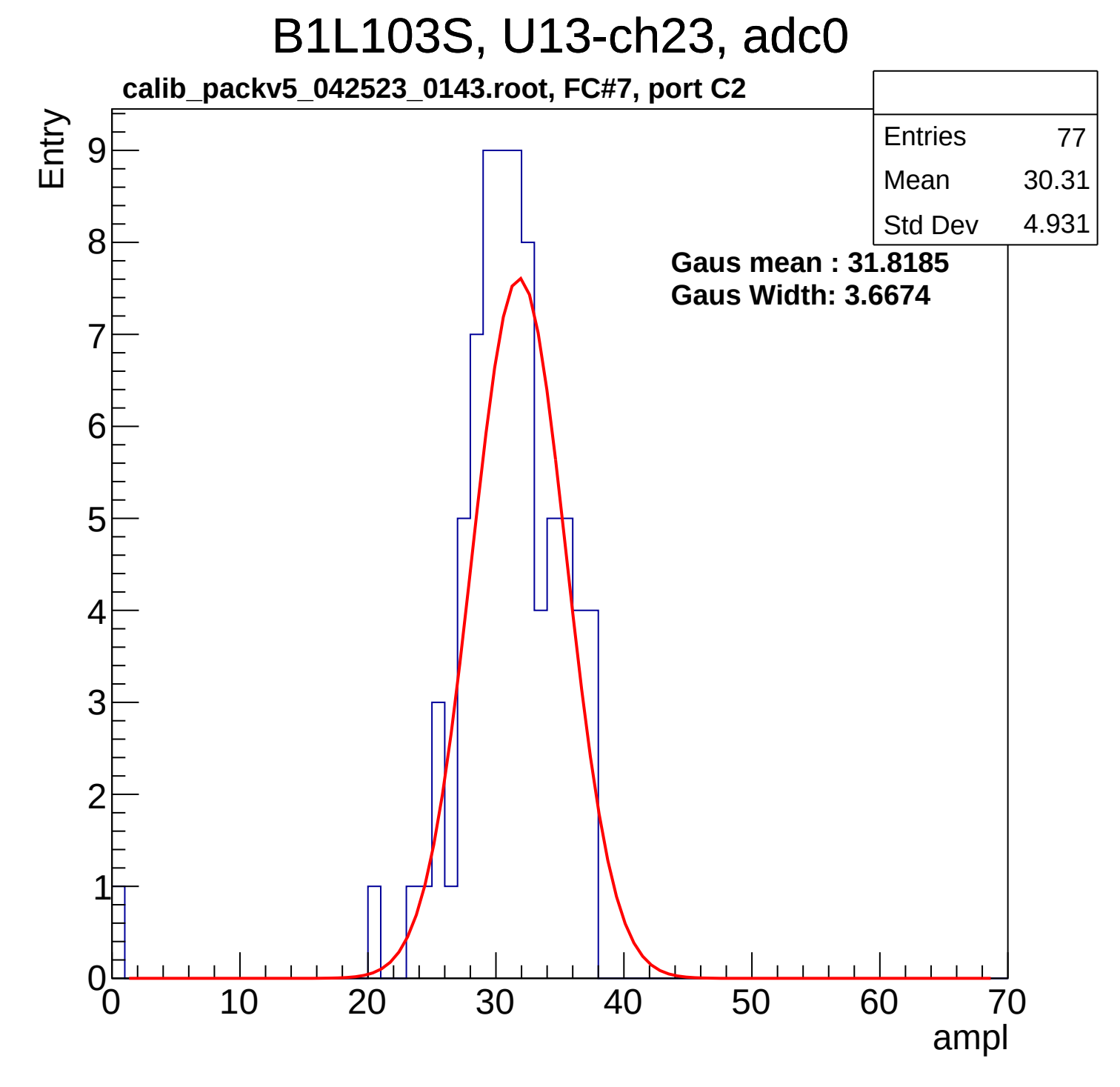
50
60
70
ampl


B1L103S, U13-ch23, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.31
Std Dev 4.931
Gaus mean : 31.8185
Gaus Width: 3.6674
Entry
ampl
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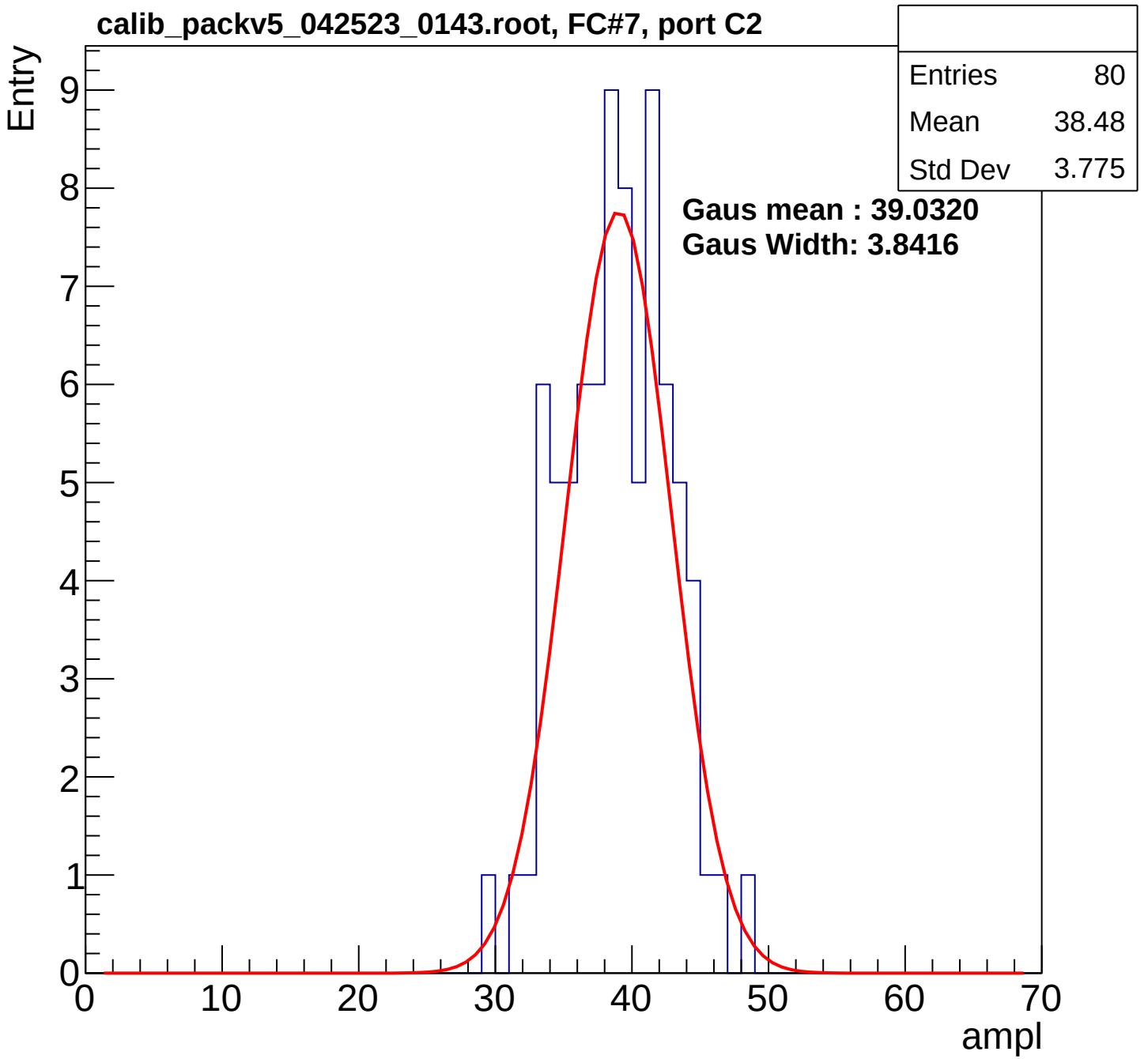
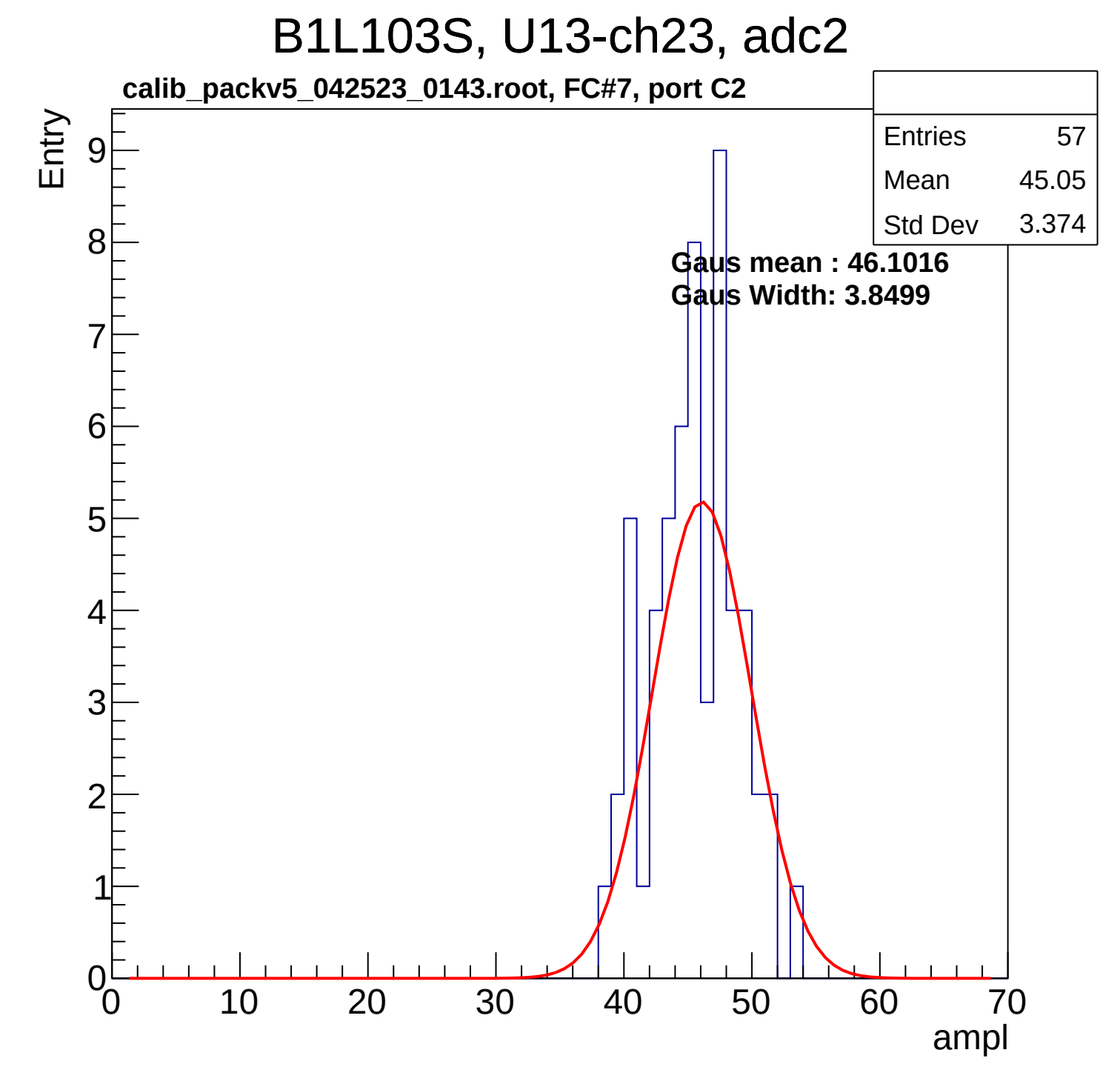
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch23, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 38.48
Std Dev 3.775
Gaus mean : 39.0320
Gaus Width: 3.8416
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch23, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 45.05
Std Dev 3.374
Gaus mean : 46.1016
Gaus Width: 3.8499
Entry
ampl
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2
3
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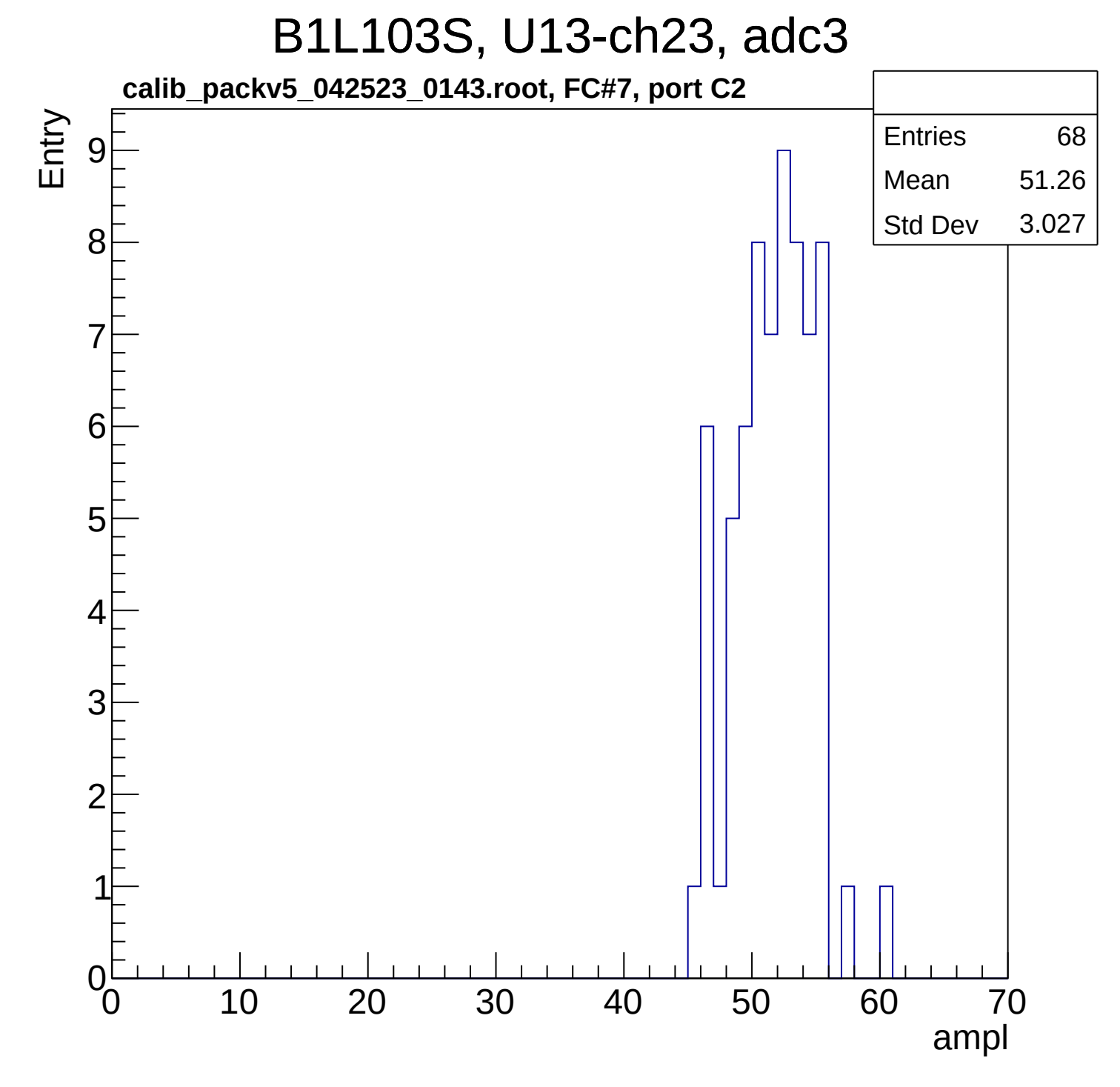
B1L103S, U13-ch23, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 51.26
Std Dev 3.027
Entry
ampl
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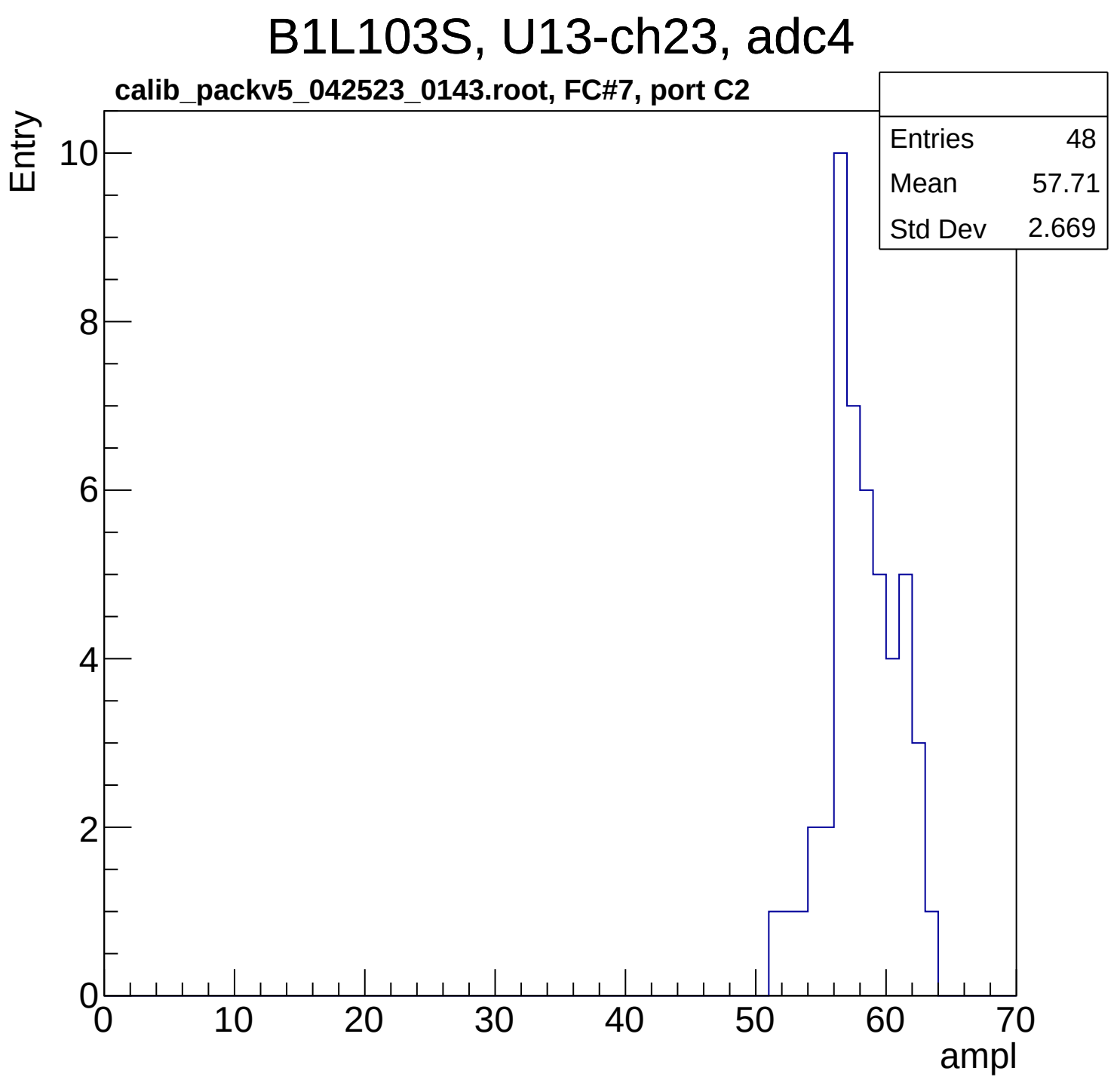
B1L103S, U13-ch23, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 57.71
Std Dev 2.669
Entry
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2
4
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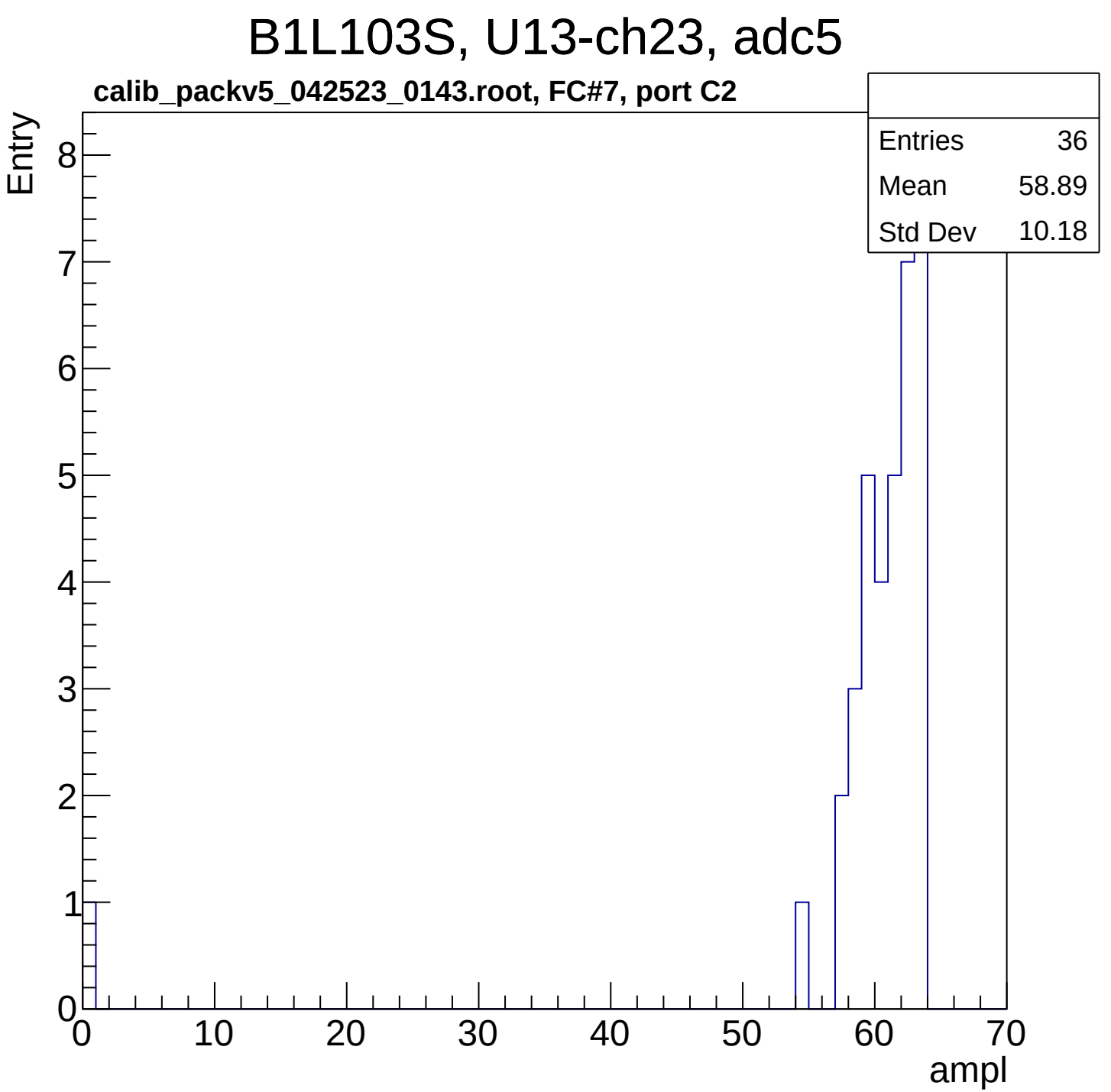
70
ampl


B1L103S, U13-ch23, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.89
Std Dev 10.18
Entry
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3
4
5
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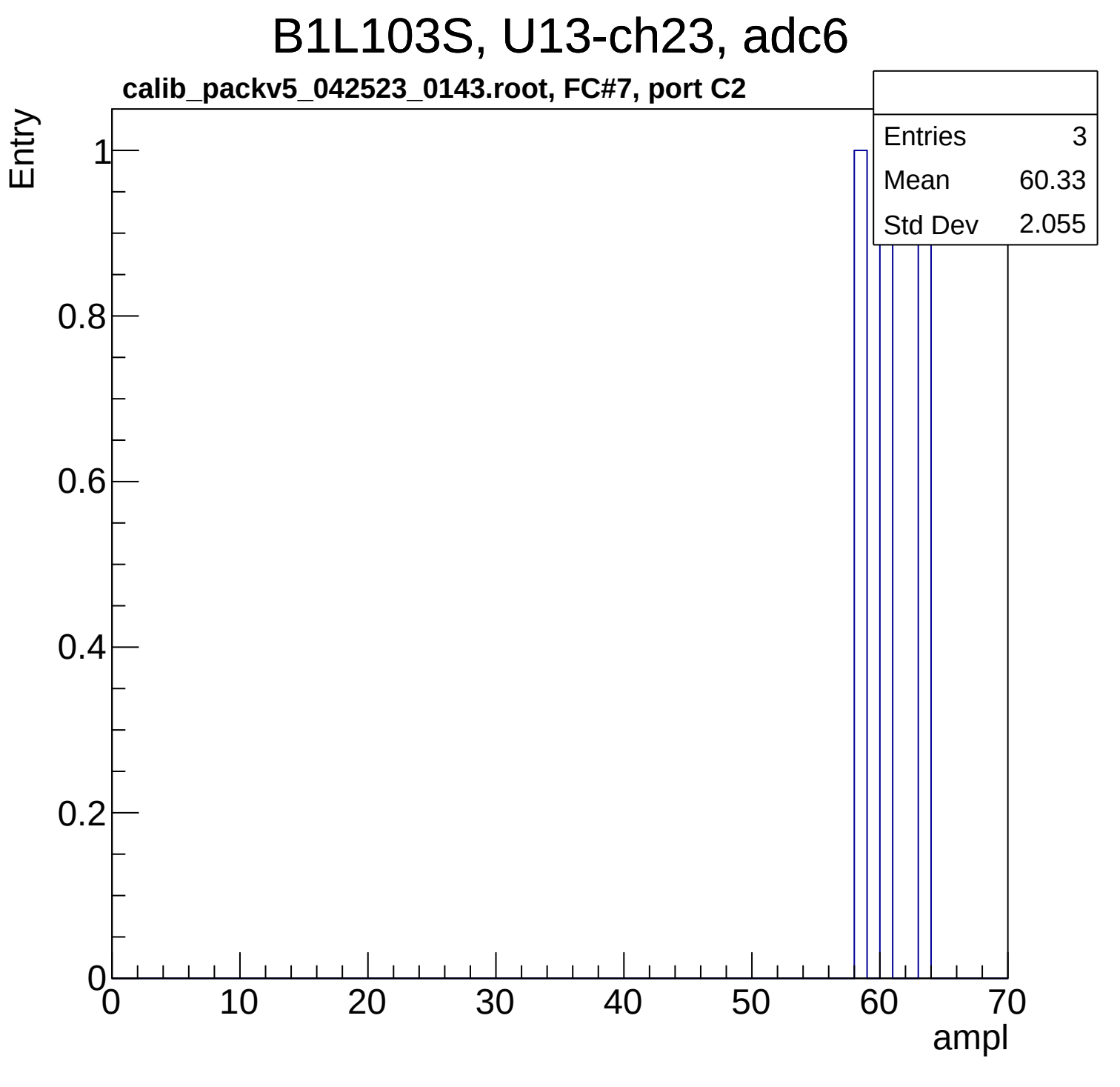
ampl


B1L103S, U13-ch23, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 60.33
Std Dev 2.055
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
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60
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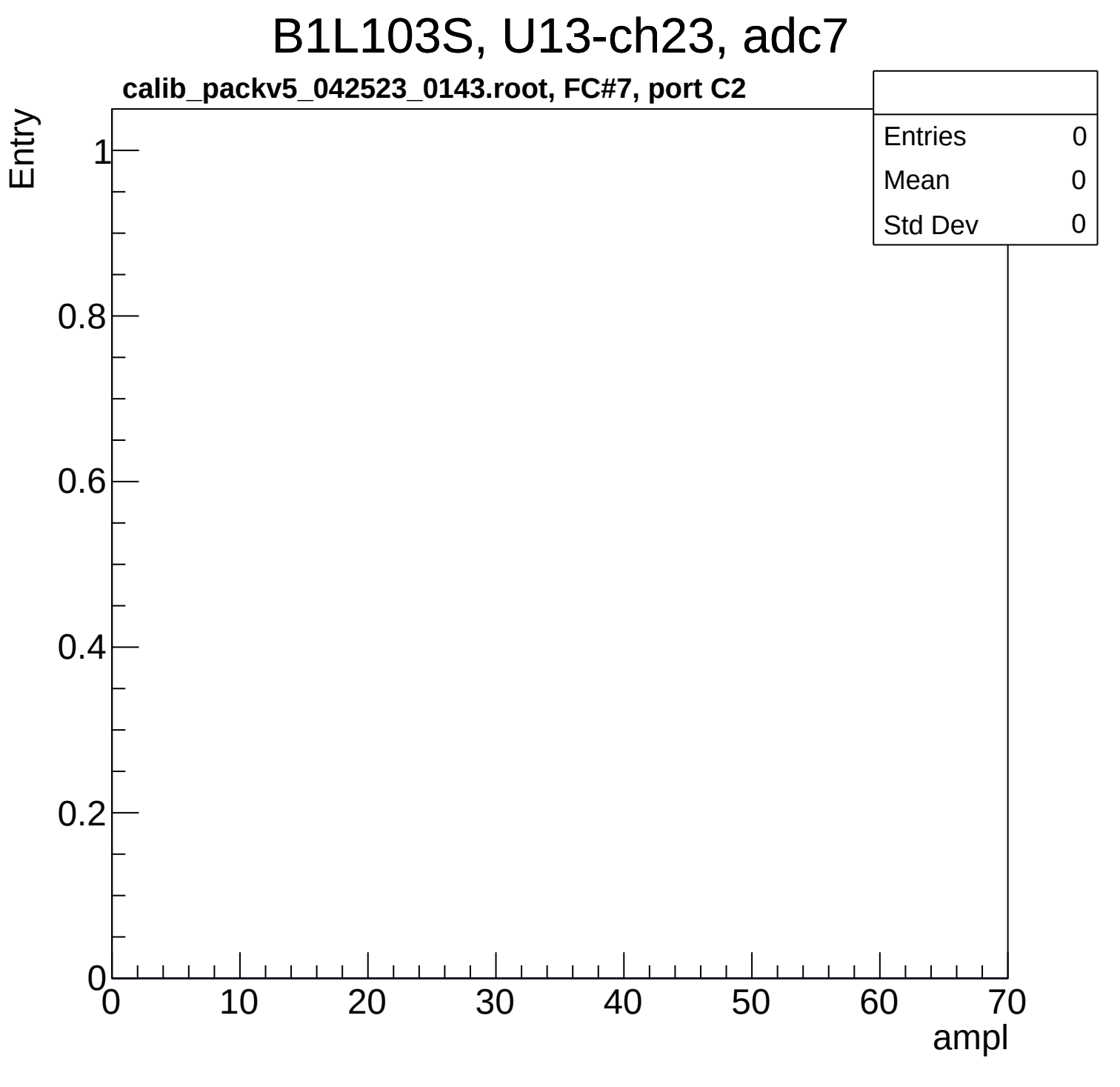
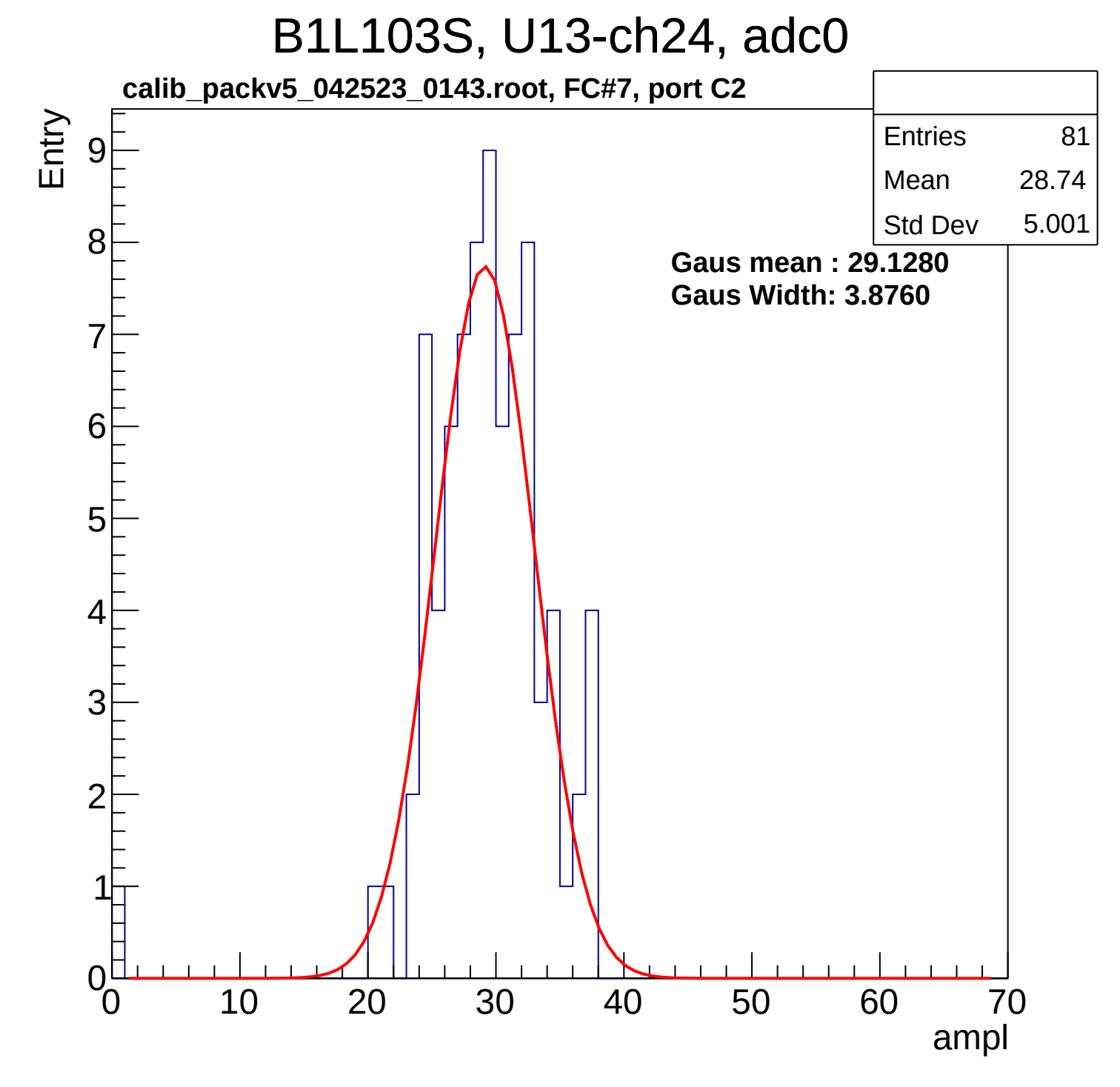

B1L103S, U13-ch23, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch24, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 28.74
Std Dev 5.001
Gaus mean : 29.1280
Gaus Width: 3.8760
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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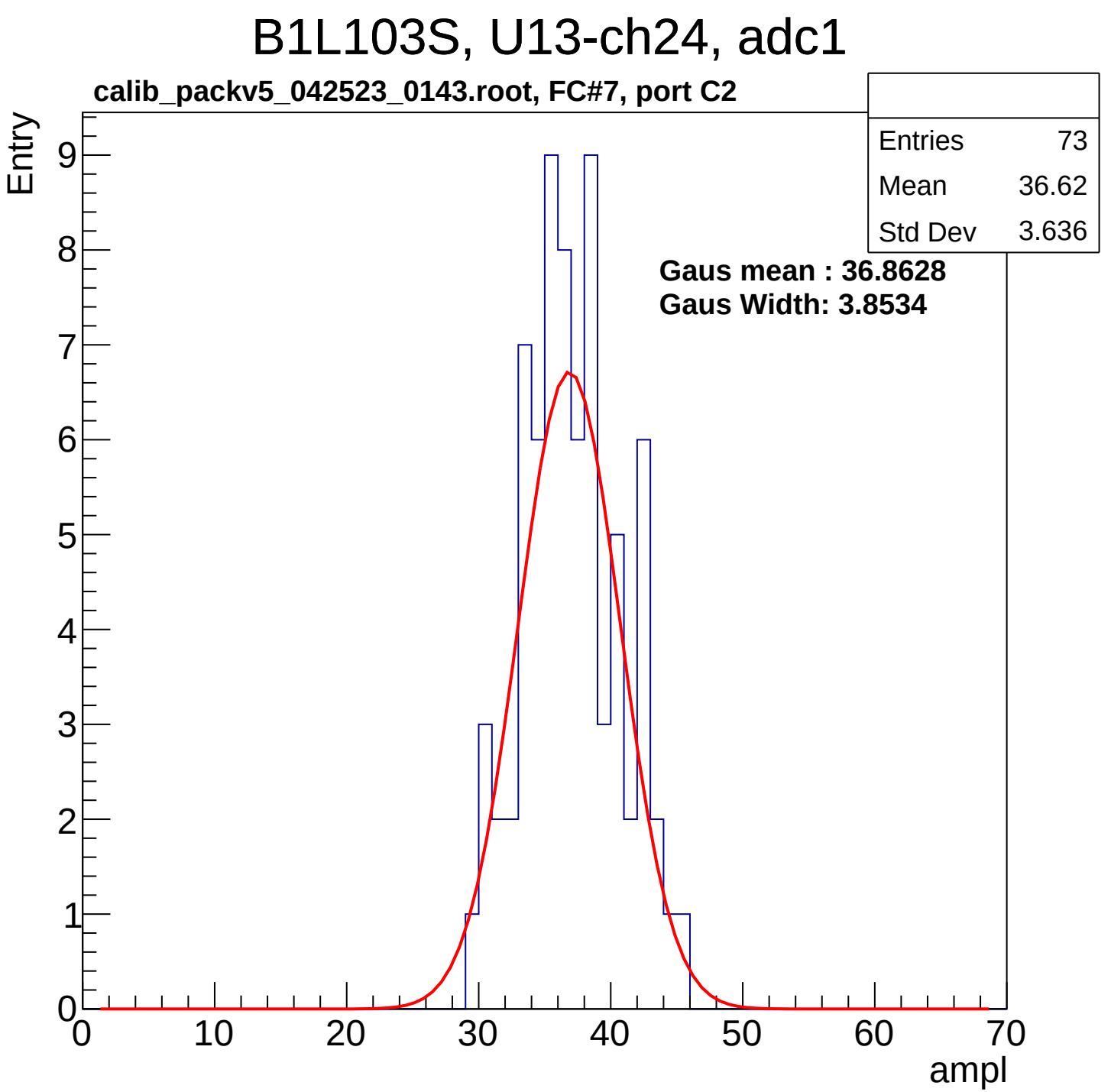
B1L103S, U13-ch24, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.62
Std Dev 3.636
Gaus mean : 36.8628
Gaus Width: 3.8534
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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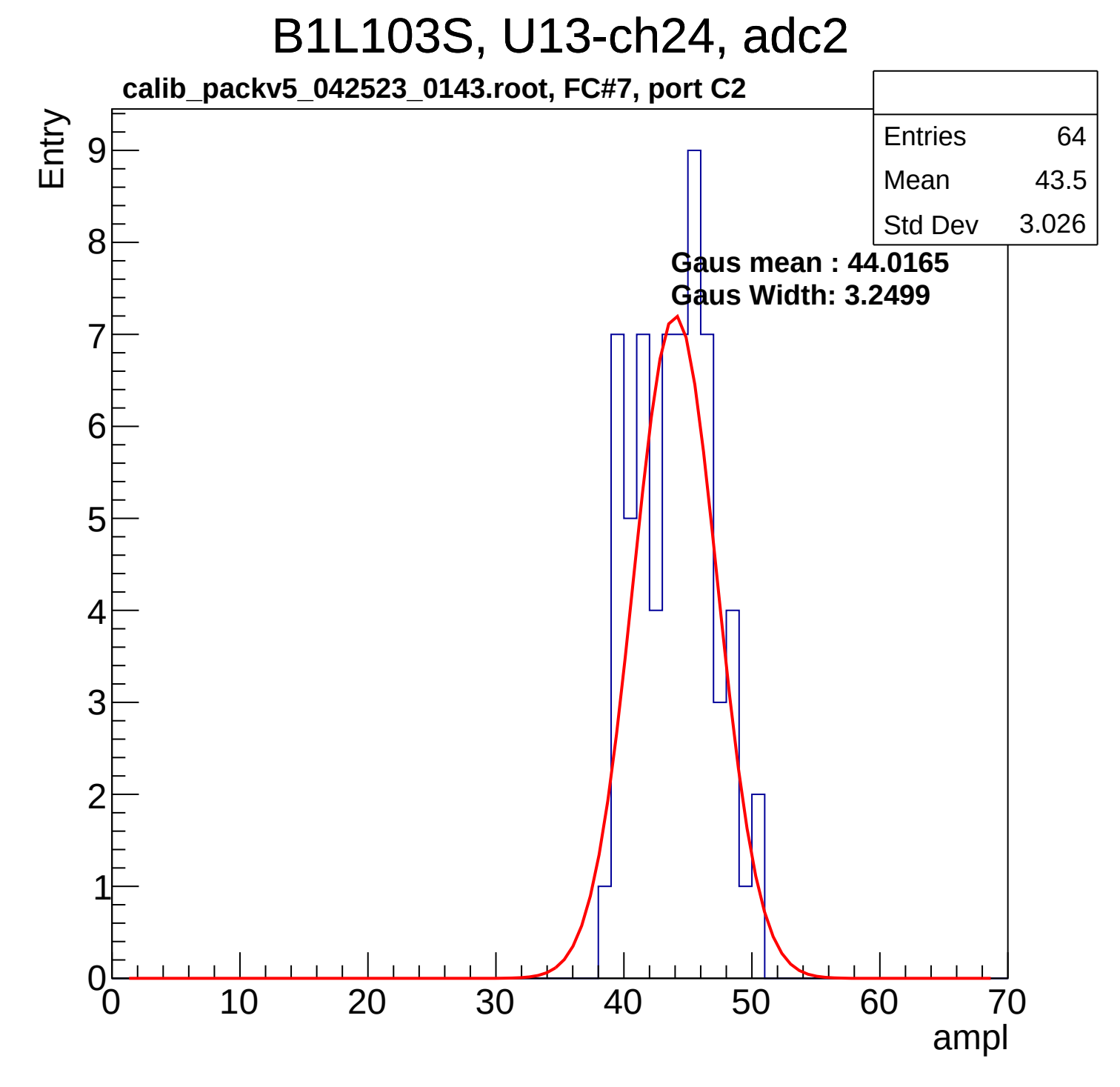
B1L103S, U13-ch24, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 43.5
Std Dev 3.026
Gaus mean : 44.0165
Gaus Width: 3.2499
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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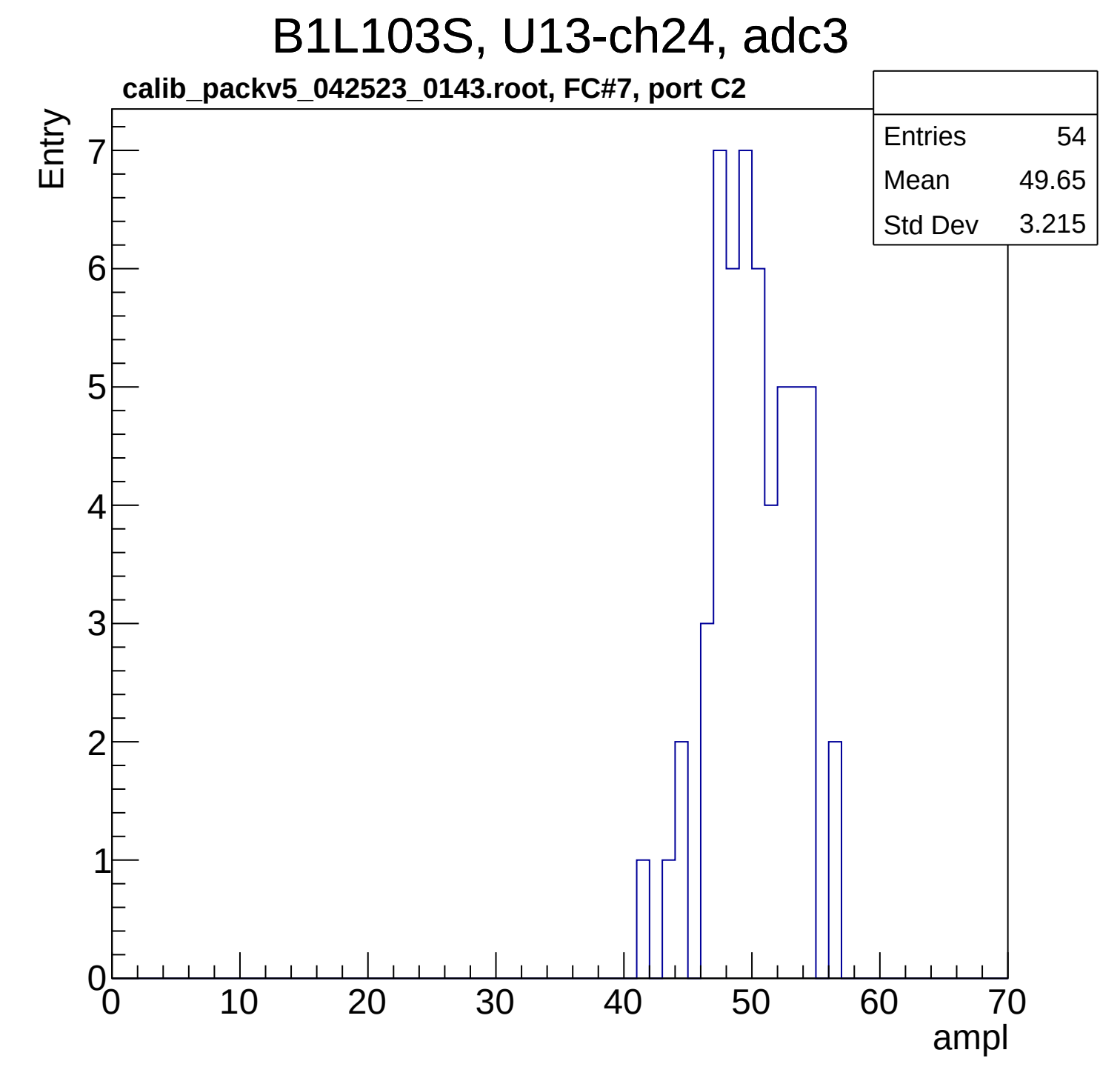
B1L103S, U13-ch24, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.65
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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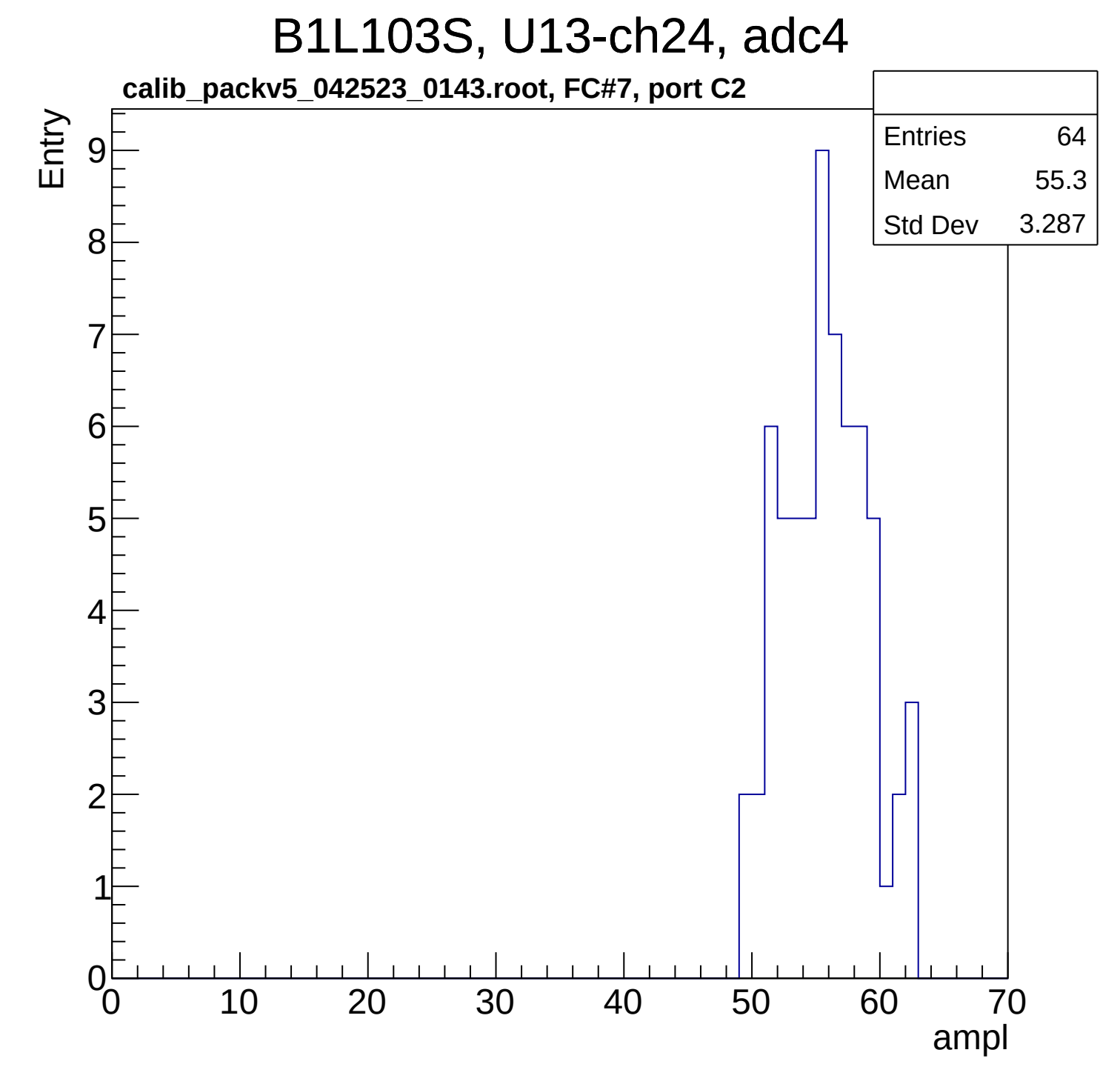
B1L103S, U13-ch24, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.3
Std Dev 3.287
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
50
60
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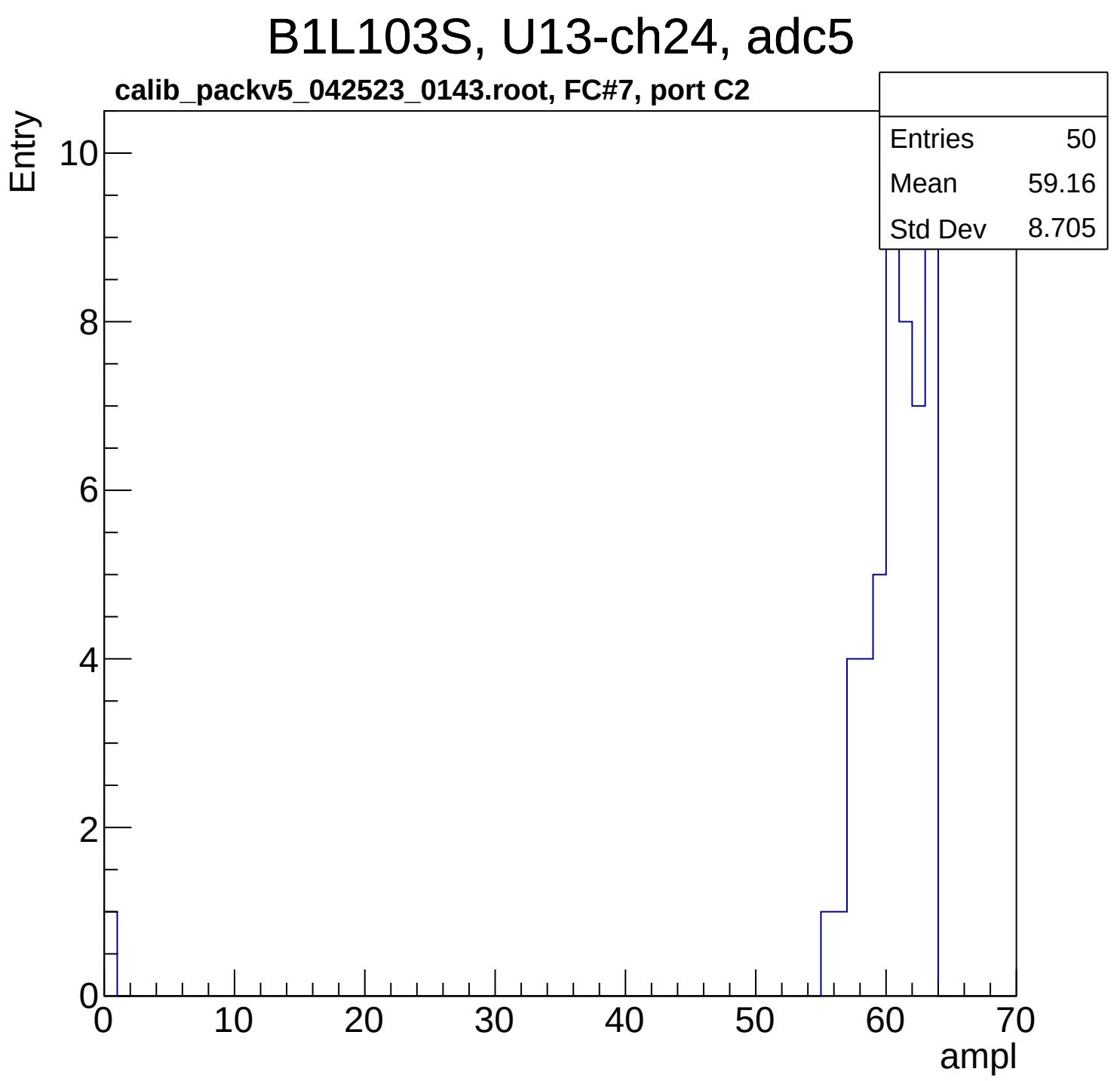
B1L103S, U13-ch24, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.16
Std Dev 8.705
Entry
ampl
0
2
4
6
8
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0
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20
30
40
50
60
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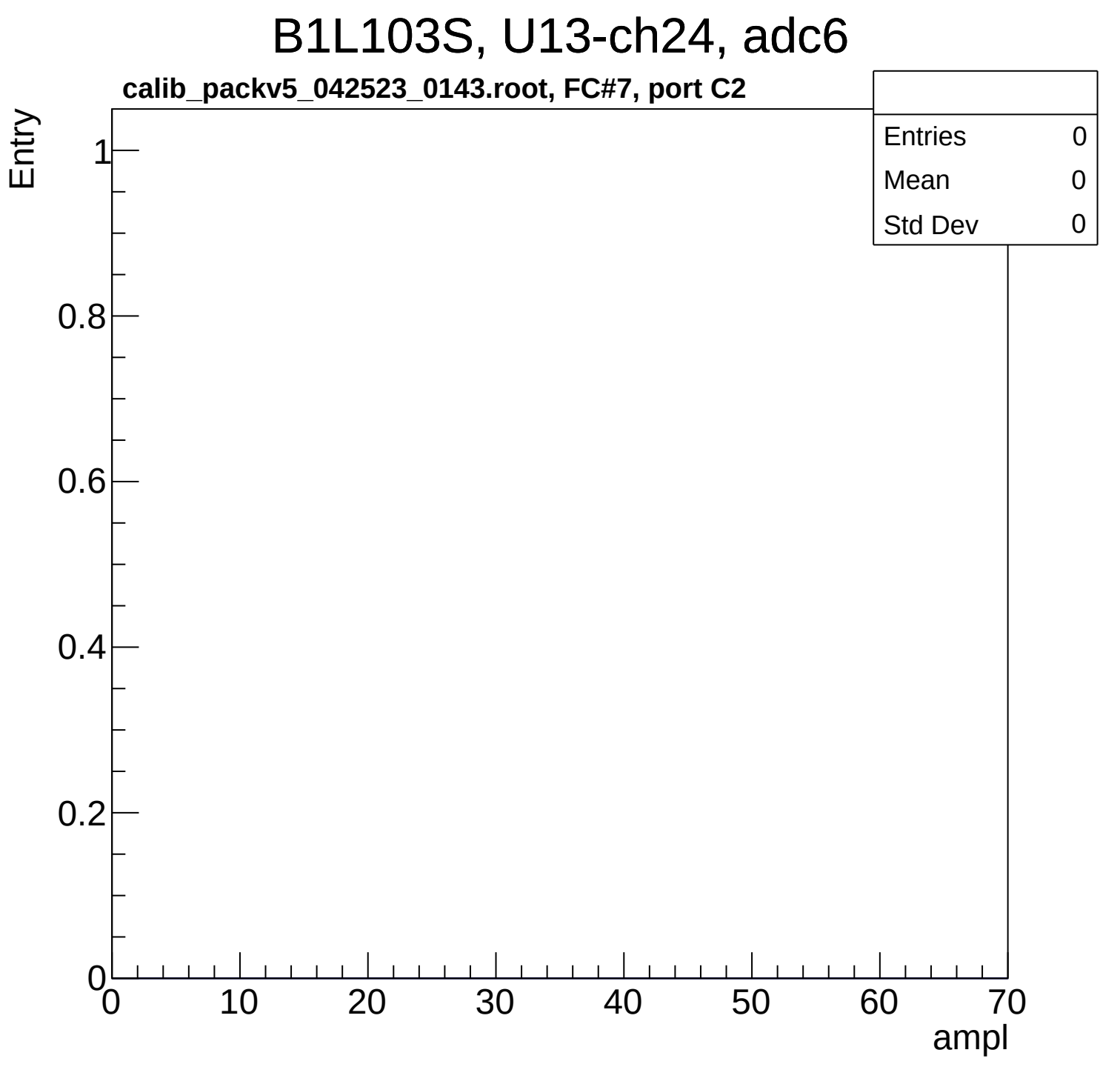
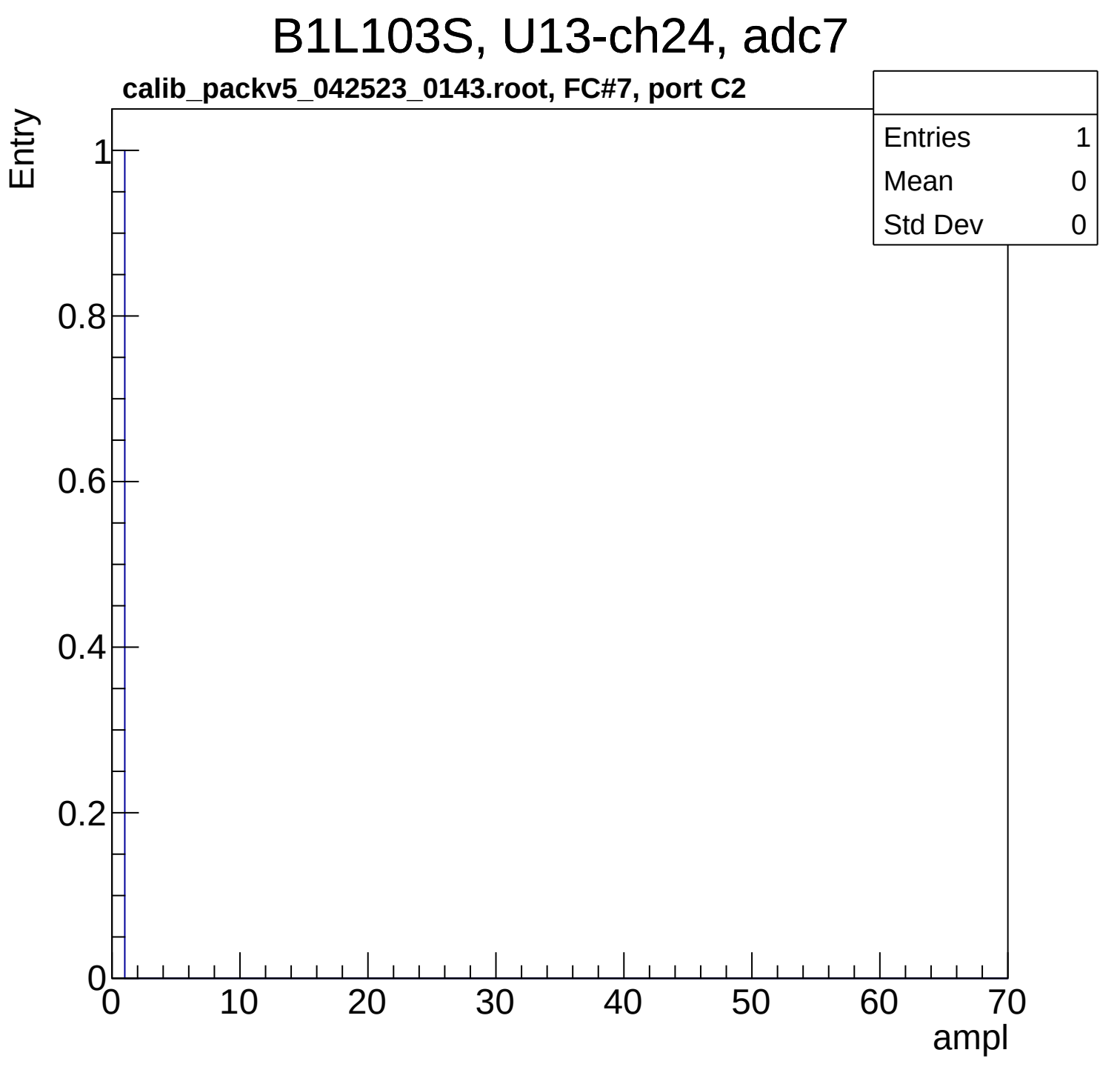

B1L103S, U13-ch24, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch24, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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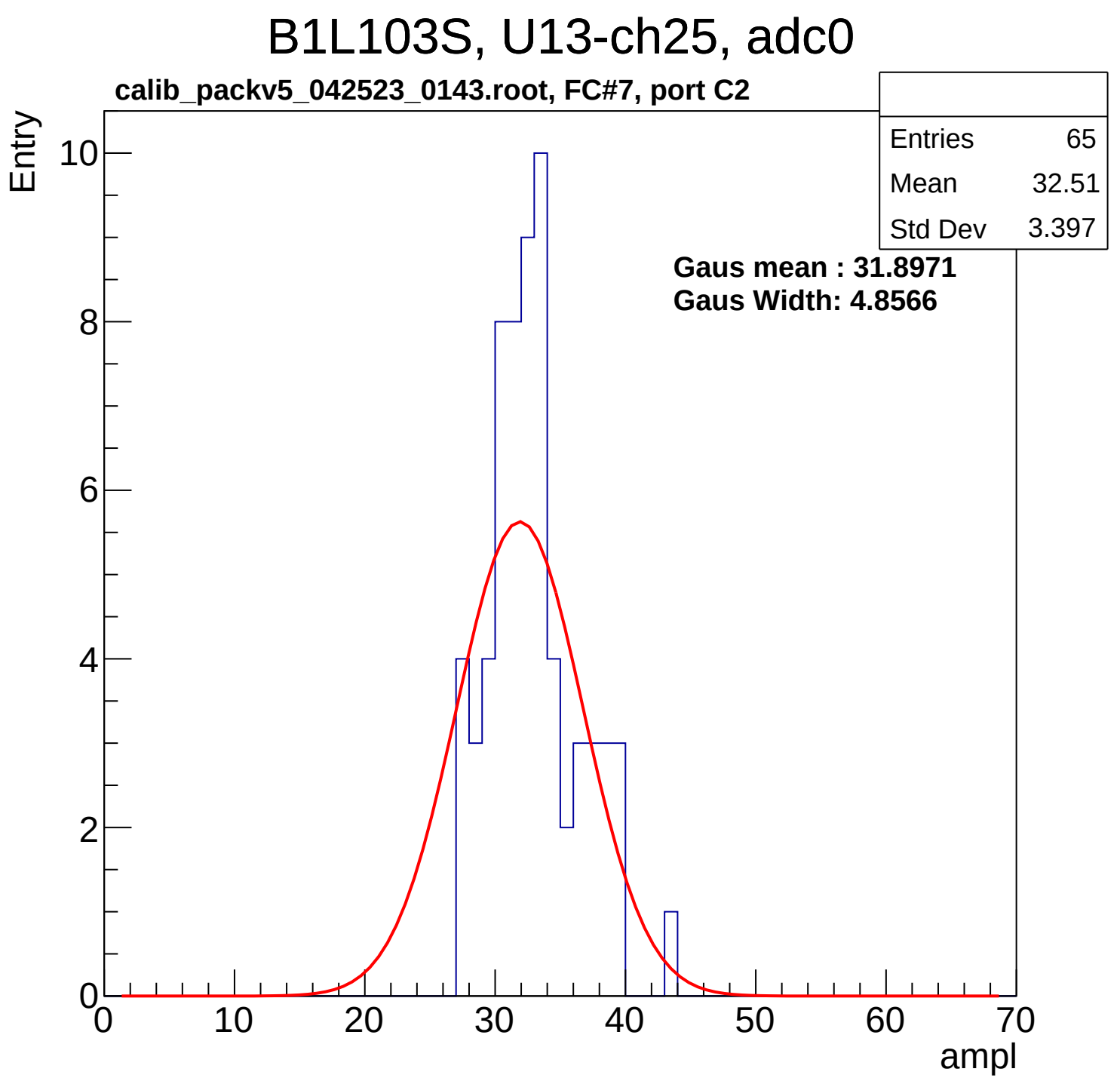
50
60
70
ampl


B1L103S, U13-ch25, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 32.51
Std Dev 3.397
Gaus mean : 31.8971
Gaus Width: 4.8566
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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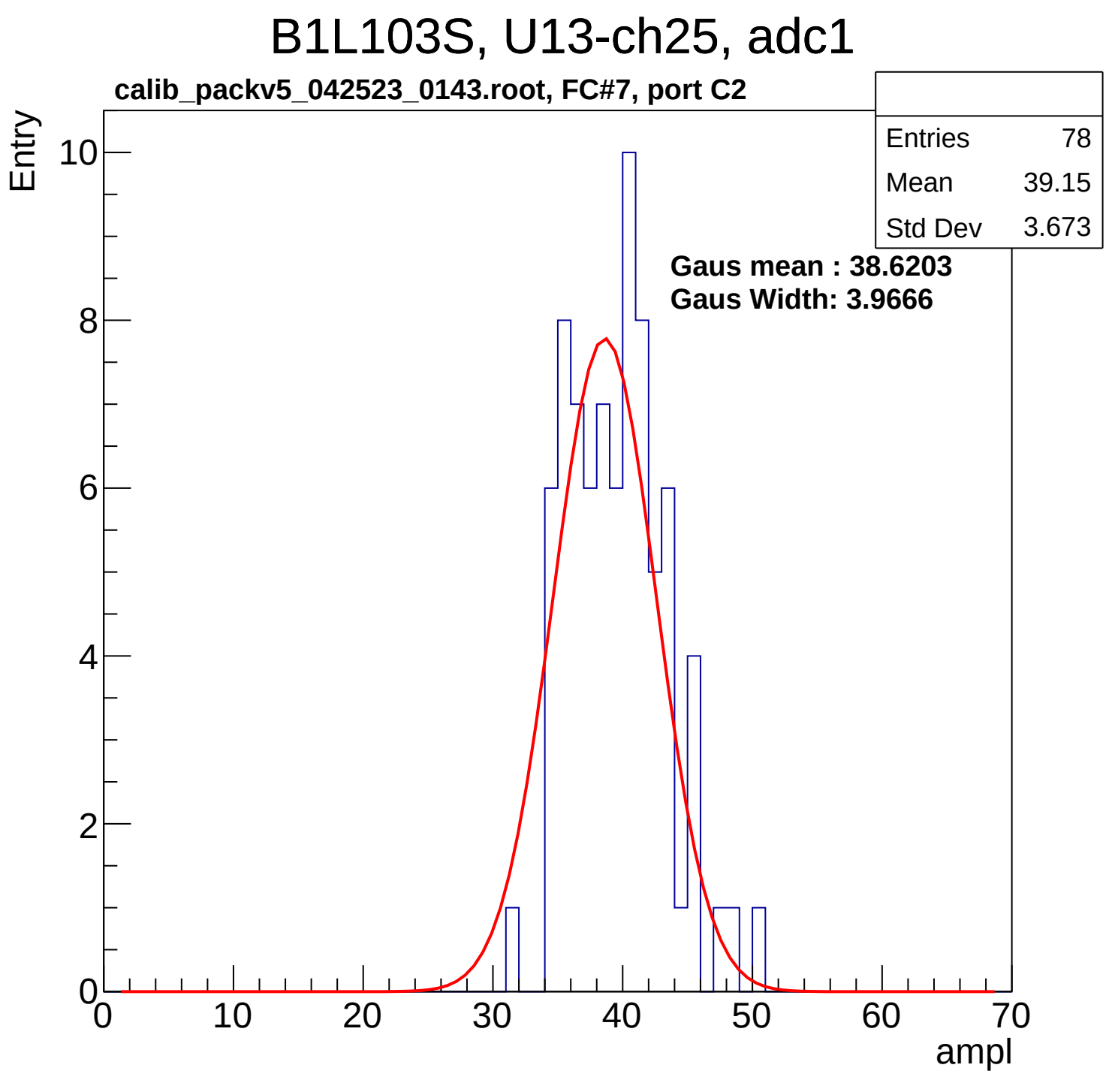
B1L103S, U13-ch25, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 39.15
Std Dev 3.673
Gaus mean : 38.6203
Gaus Width: 3.9666
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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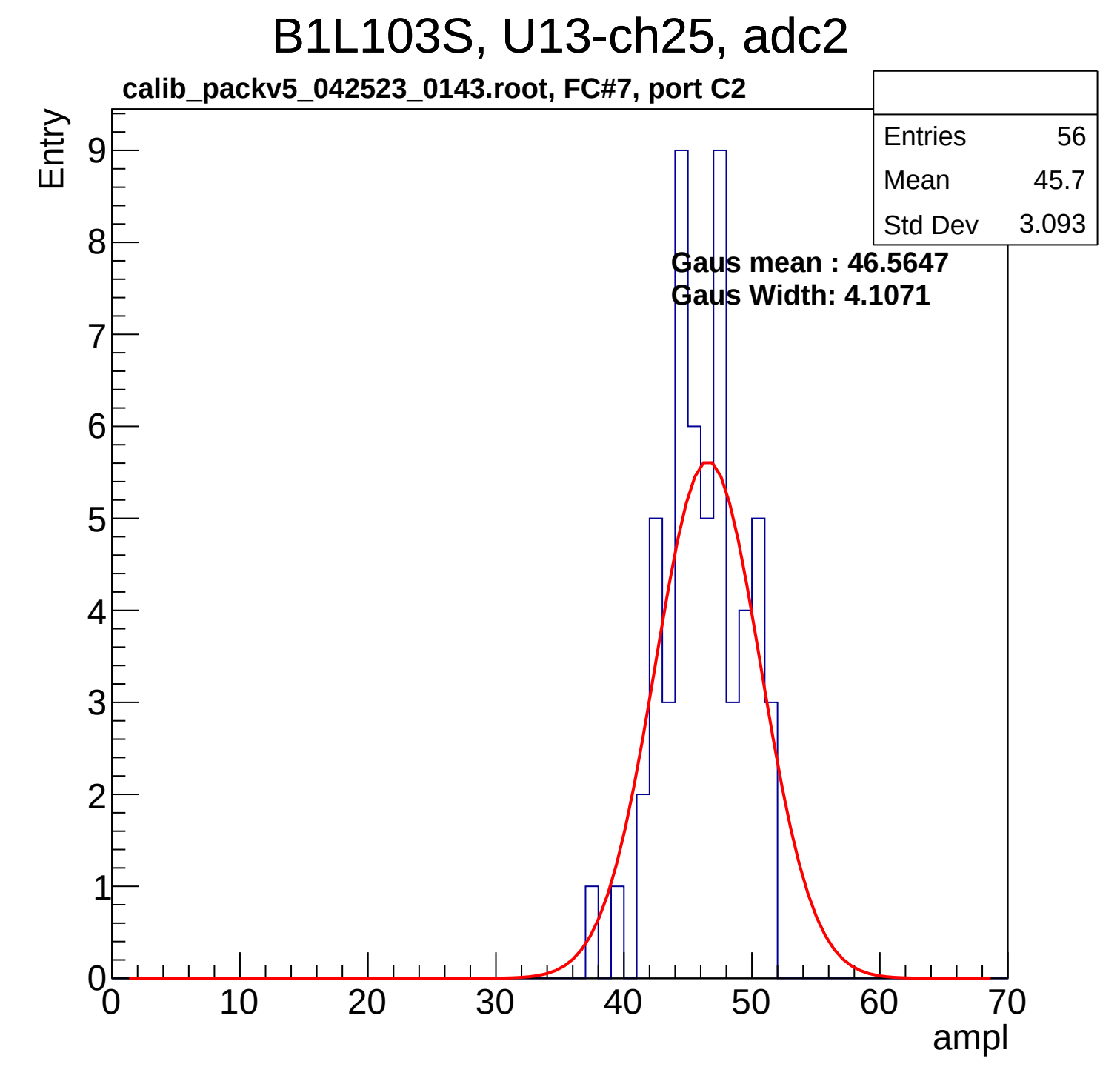
B1L103S, U13-ch25, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 45.7
Std Dev 3.093
Gaus mean : 46.5647
Gaus Width: 4.1071
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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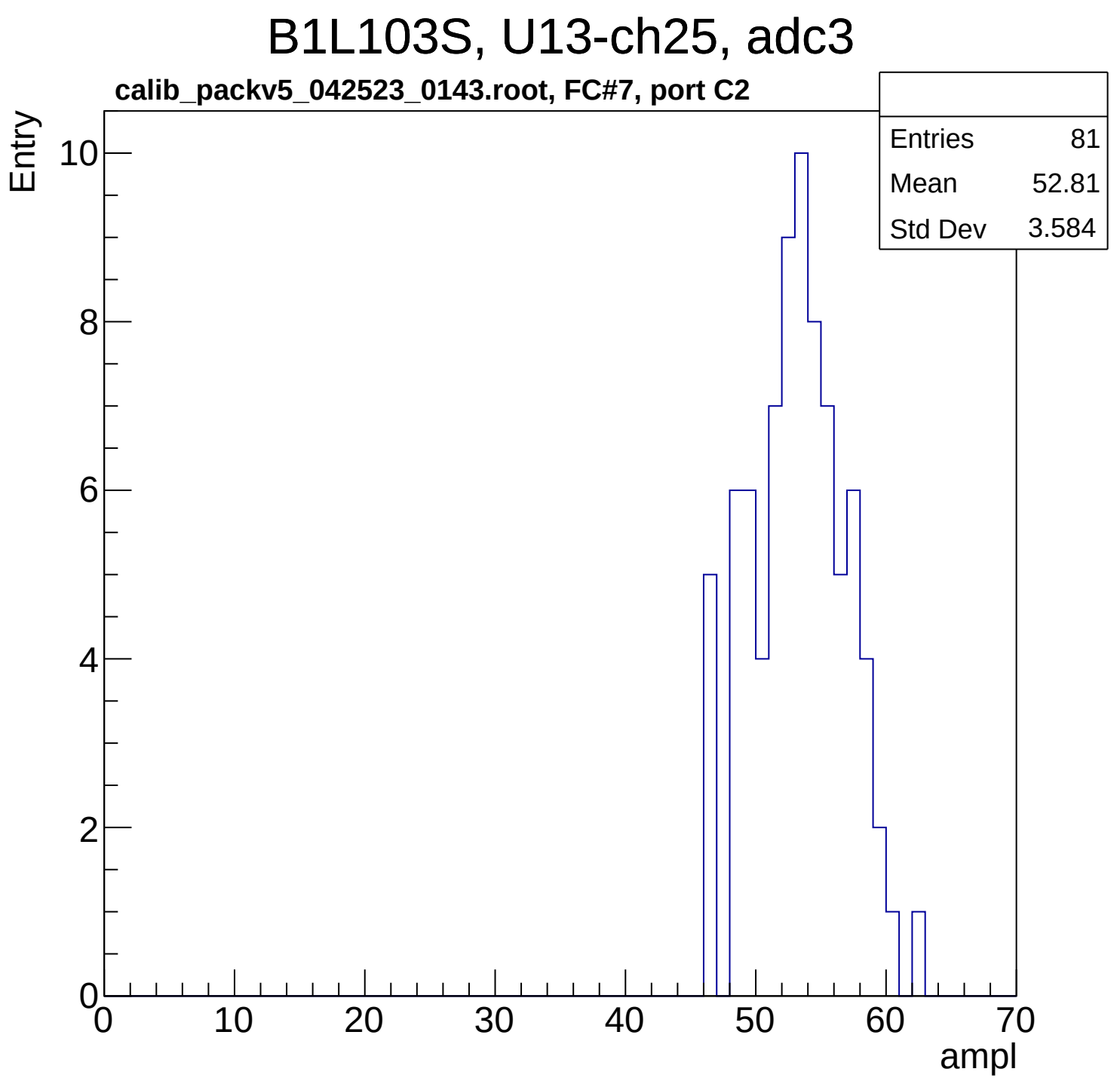
40
50
60
70


B1L103S, U13-ch25, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 52.81
Std Dev 3.584
Entry
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4
6
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10
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10
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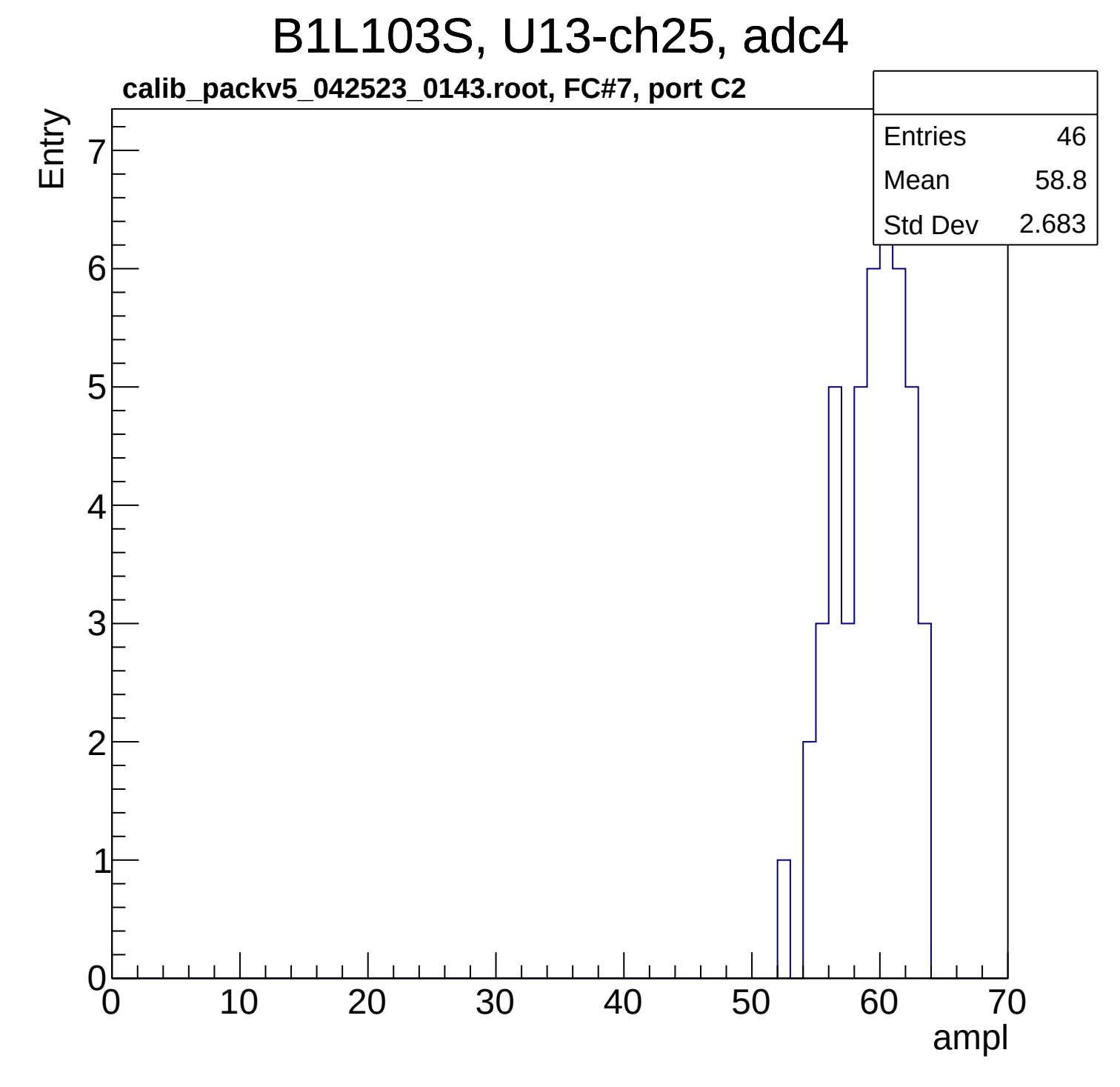
50
60
70
ampl


B1L103S, U13-ch25, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.8
Std Dev 2.683
Entry
ampl
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3
4
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6
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0
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60
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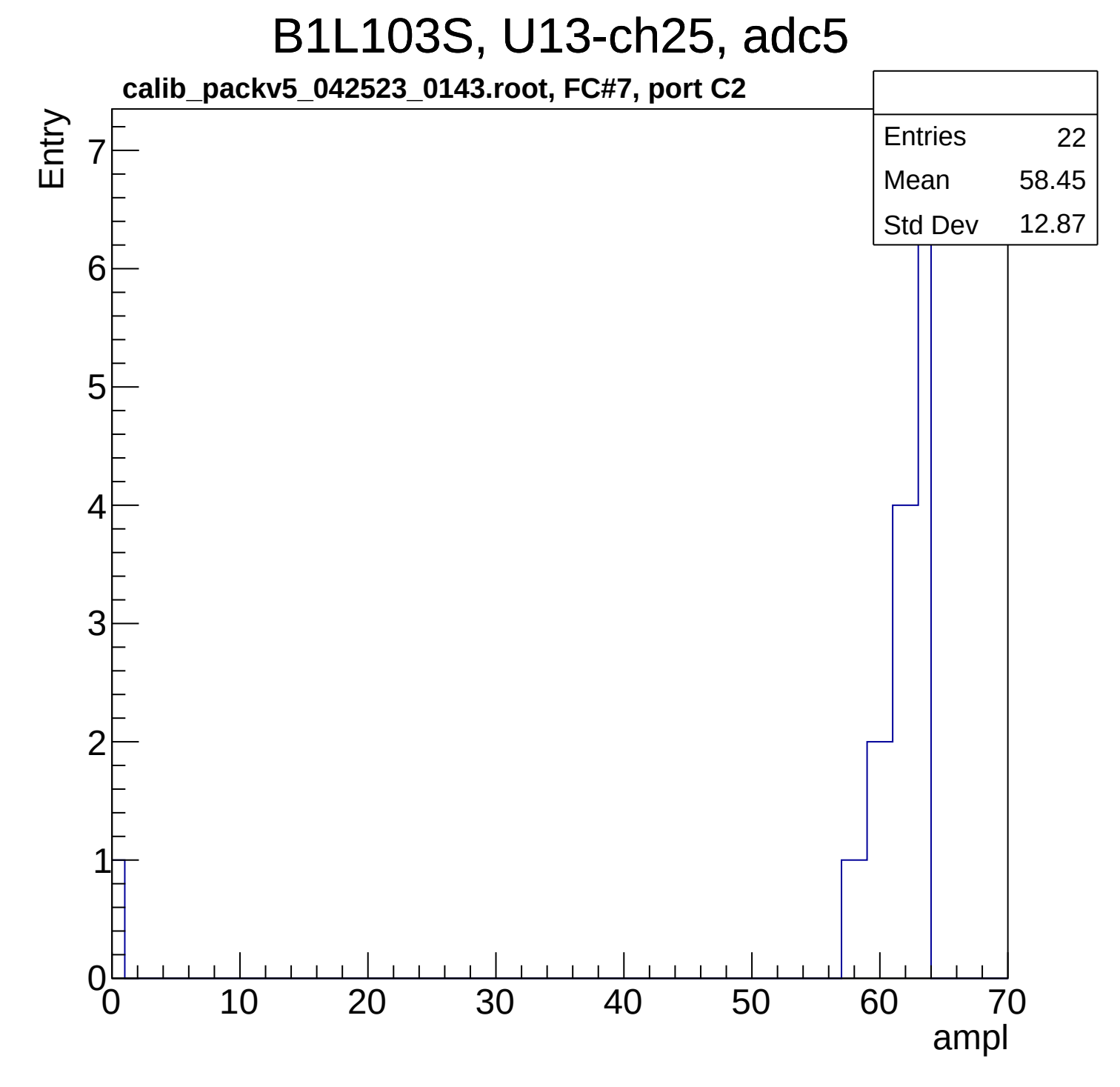
B1L103S, U13-ch25, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.45
Std Dev 12.87
Entry
ampl
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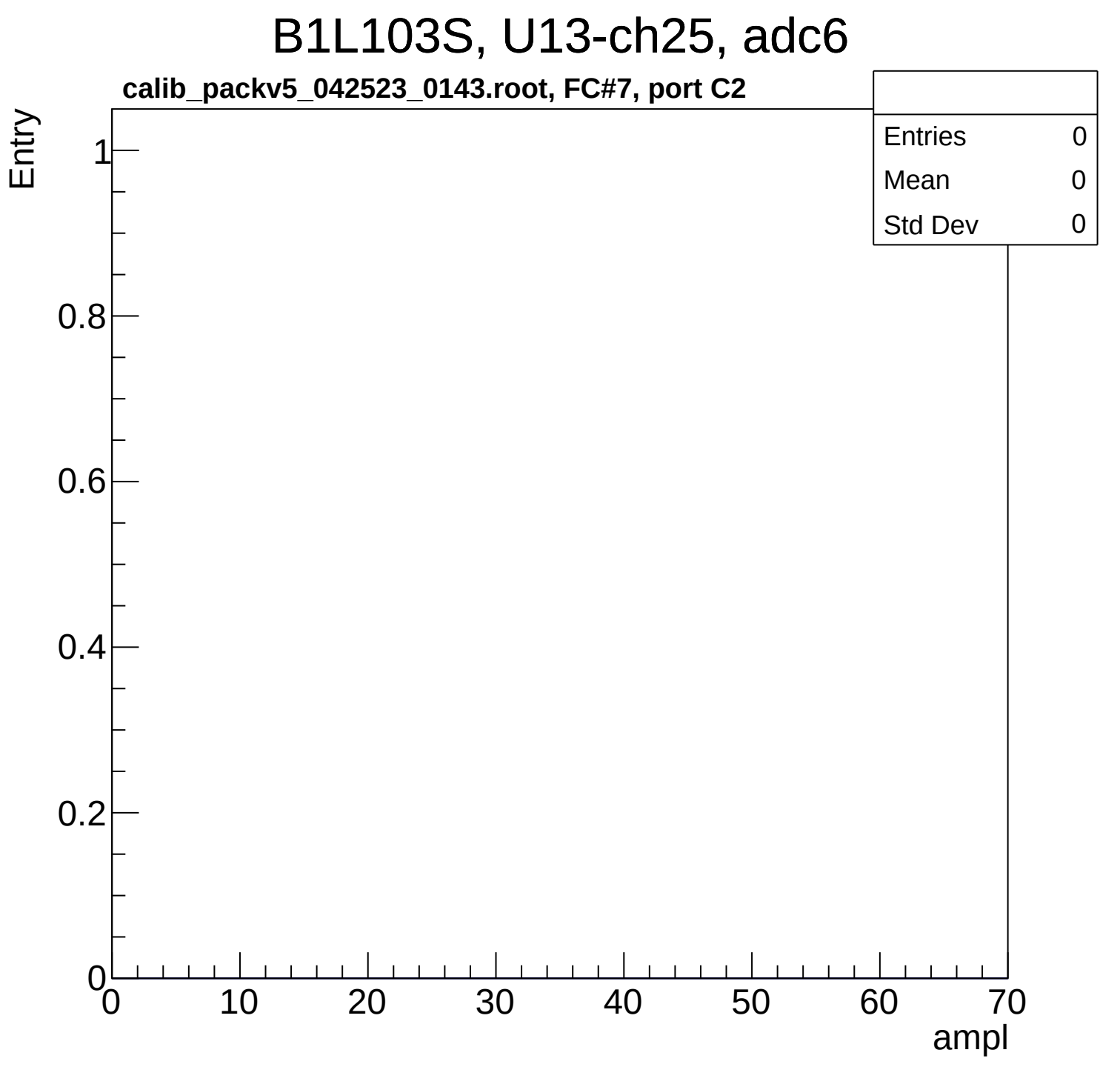
B1L103S, U13-ch25, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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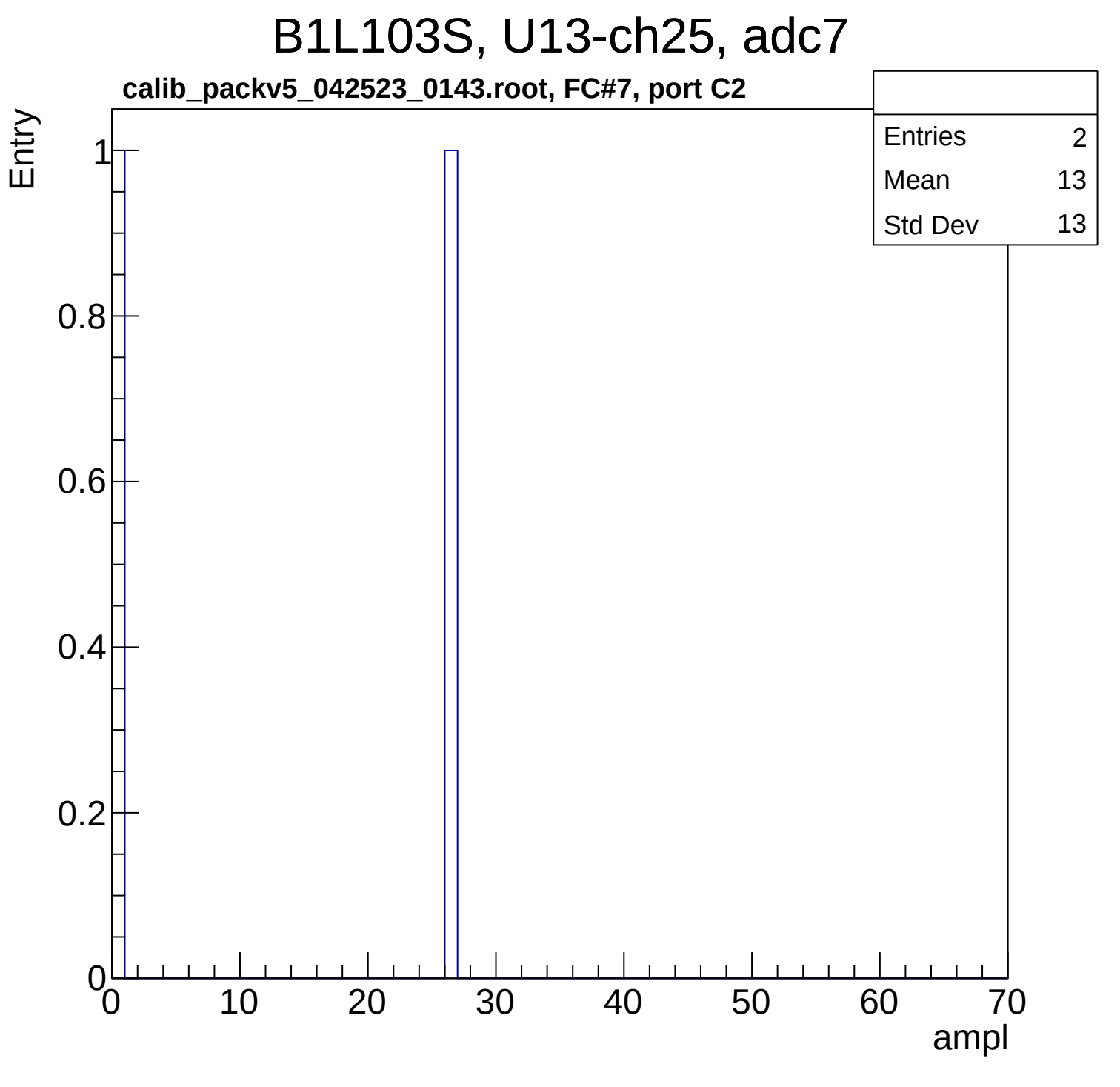
60
70
ampl


B1L103S, U13-ch25, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 13
Std Dev 13
Entry
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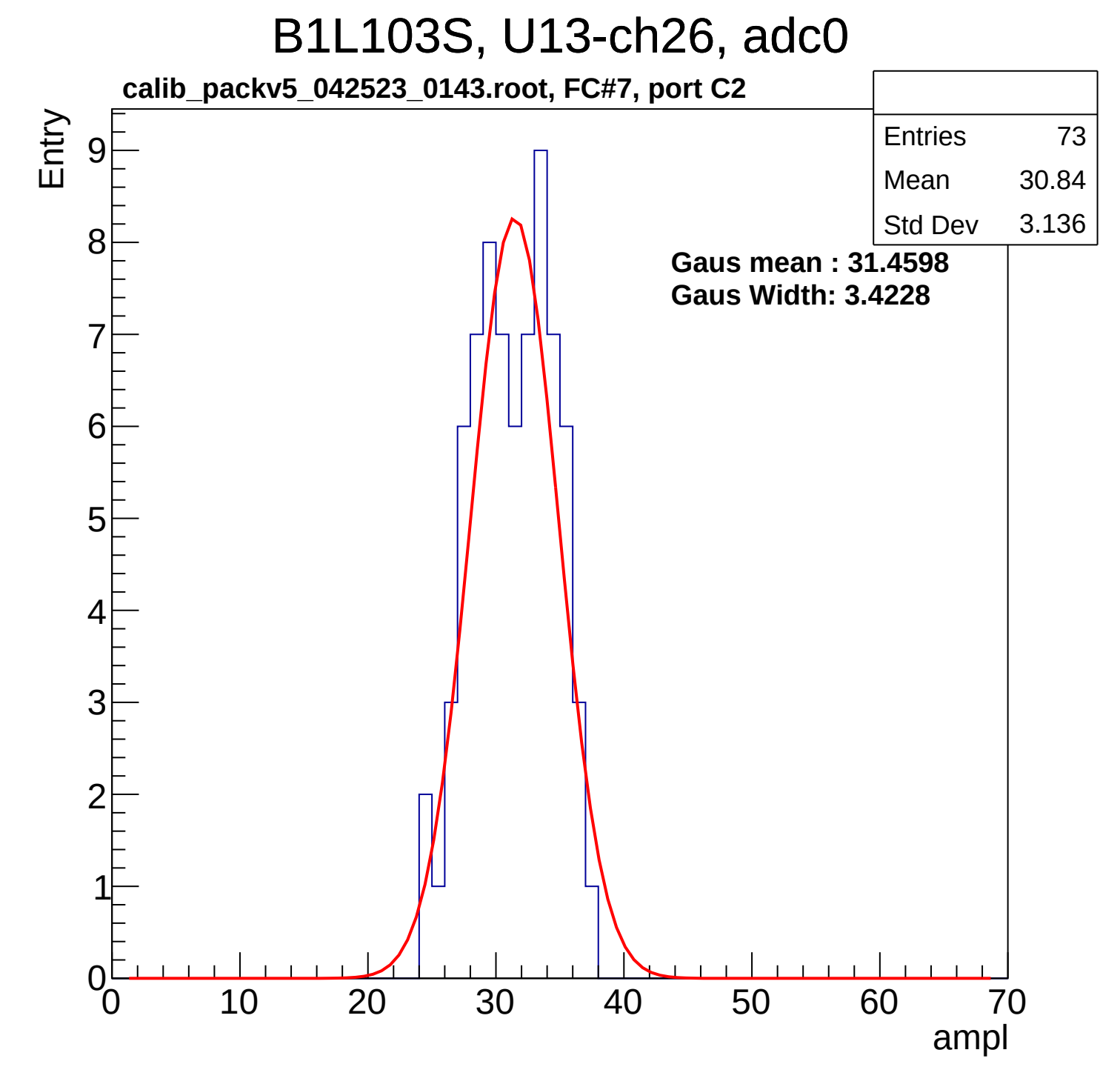
70
ampl


B1L103S, U13-ch26, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 30.84
Std Dev 3.136
Gaus mean : 31.4598
Gaus Width: 3.4228
Entry
ampl
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3
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6
7
8
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0
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60
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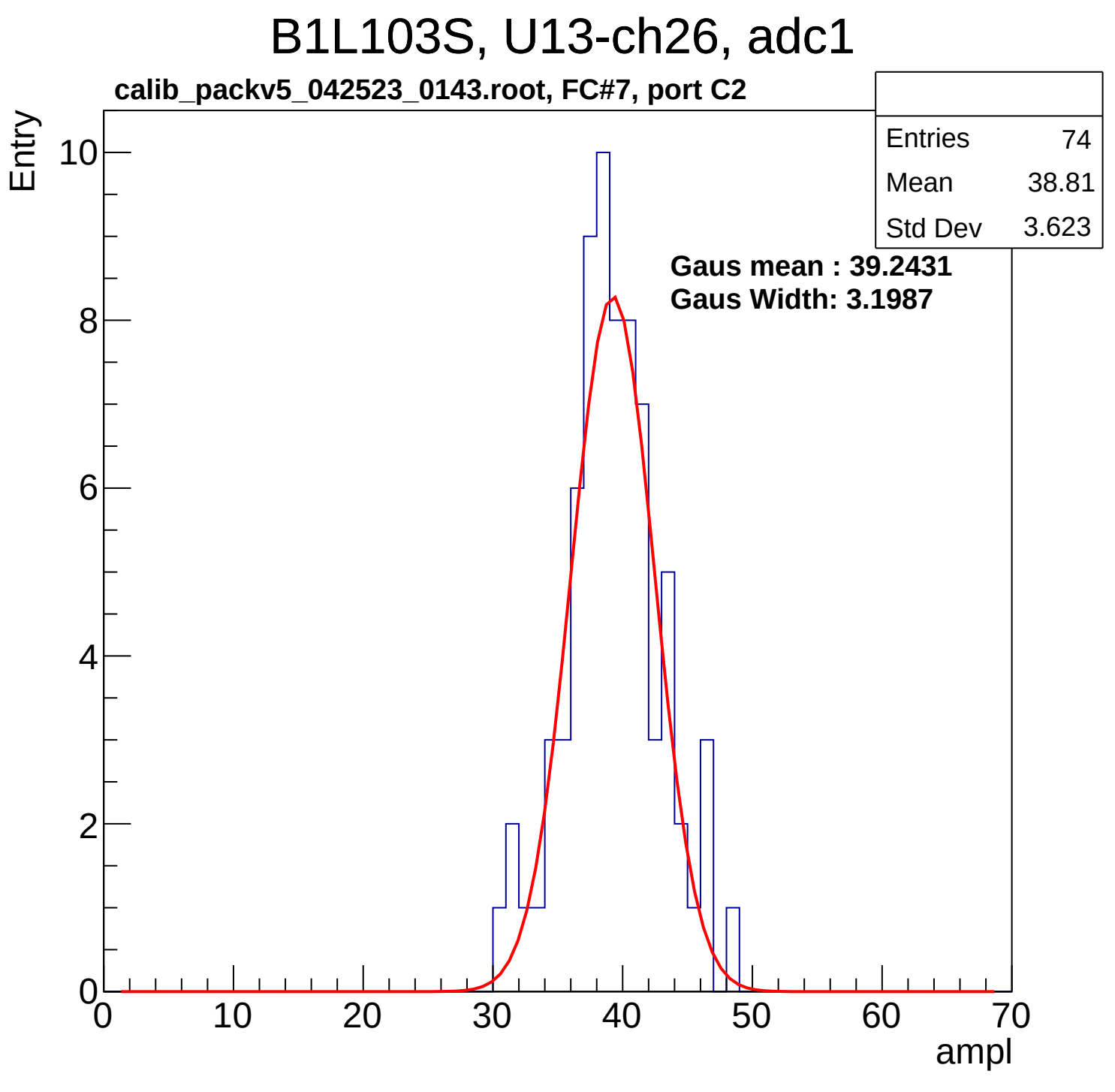
B1L103S, U13-ch26, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 38.81
Std Dev 3.623
Gaus mean : 39.2431
Gaus Width: 3.1987
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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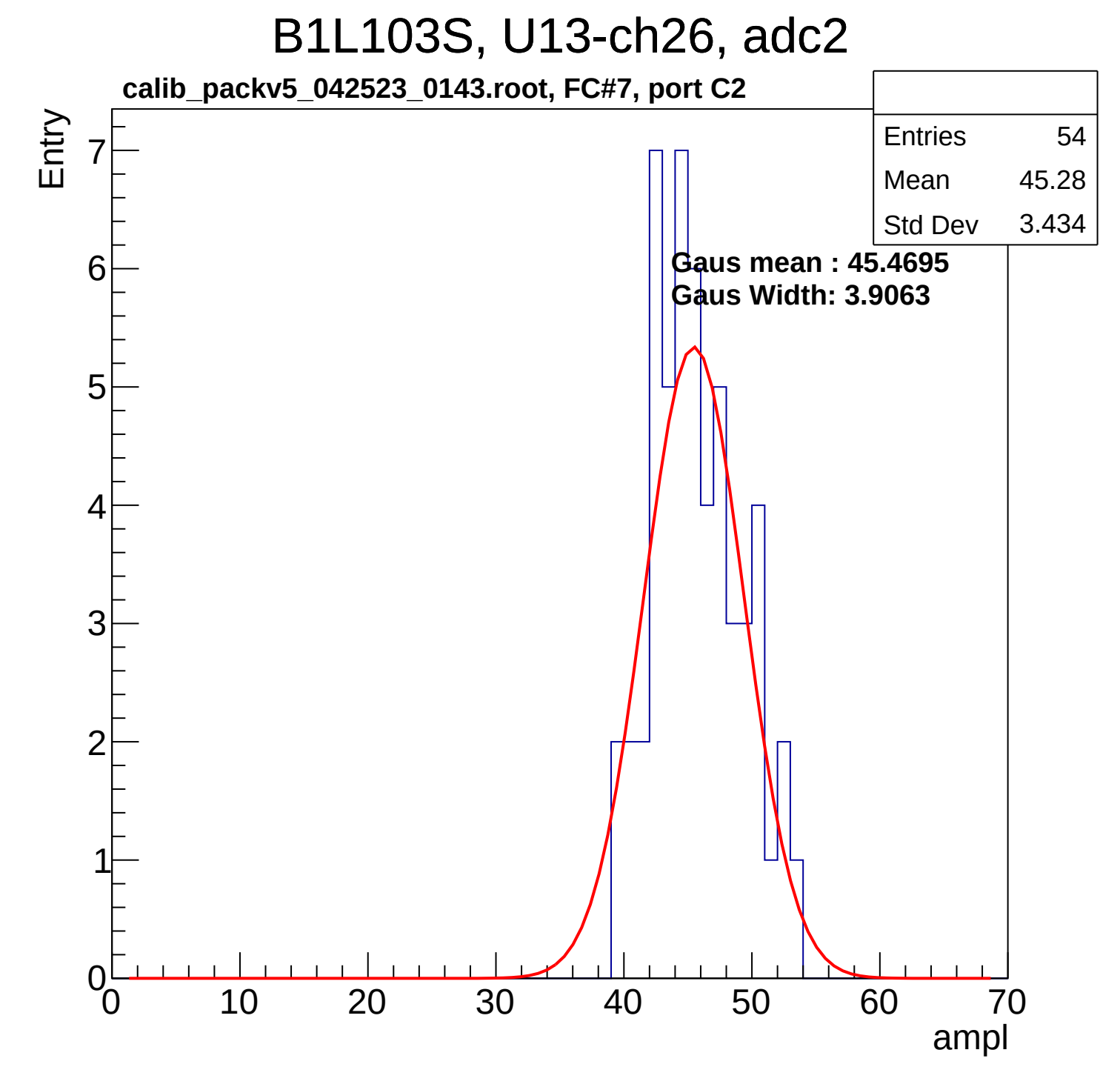
B1L103S, U13-ch26, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 45.28
Std Dev 3.434
Gaus mean : 45.4695
Gaus Width: 3.9063
Entry
ampl
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3
4
5
6
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0
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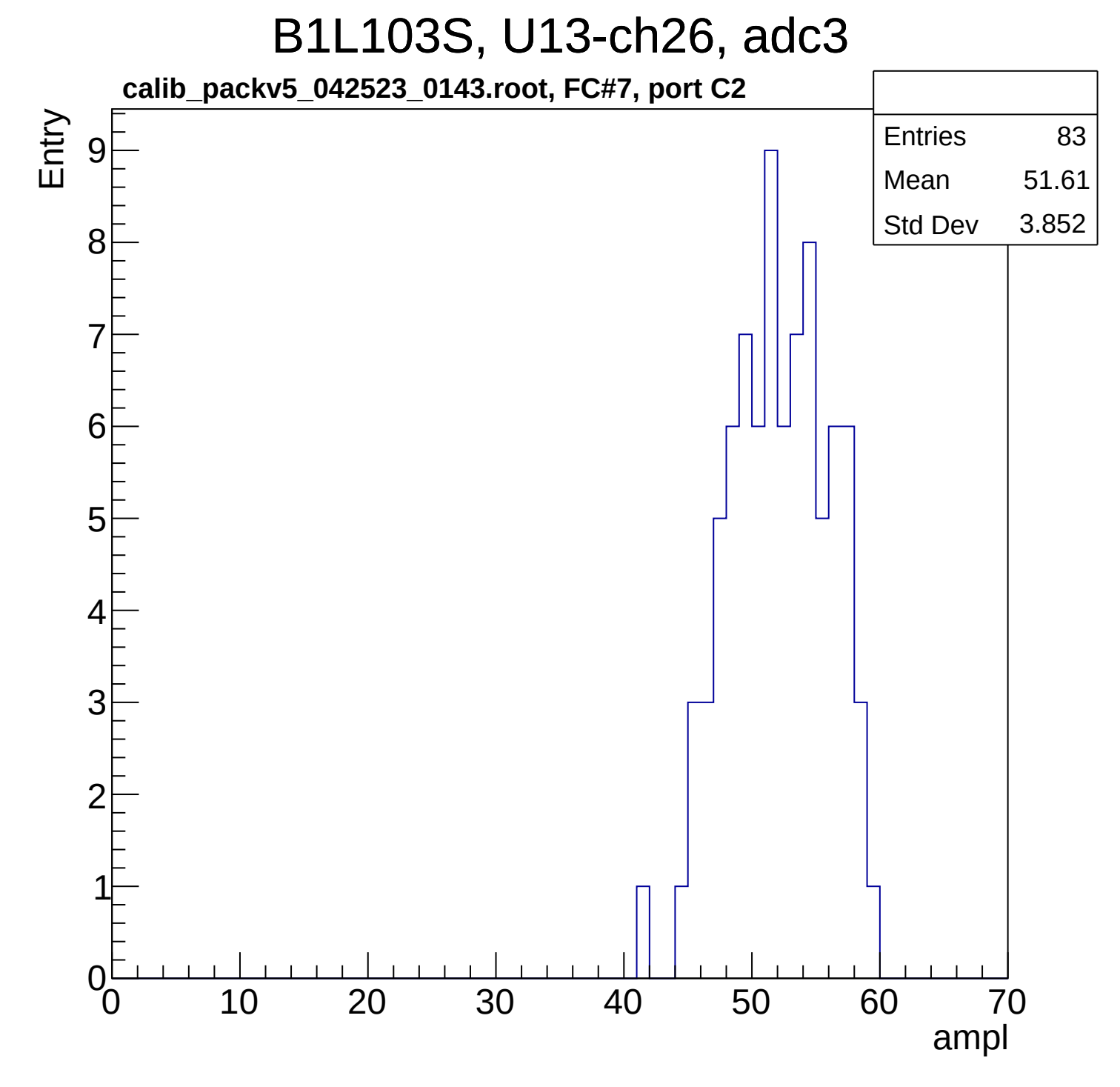
B1L103S, U13-ch26, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 51.61
Std Dev 3.852
Entry
ampl
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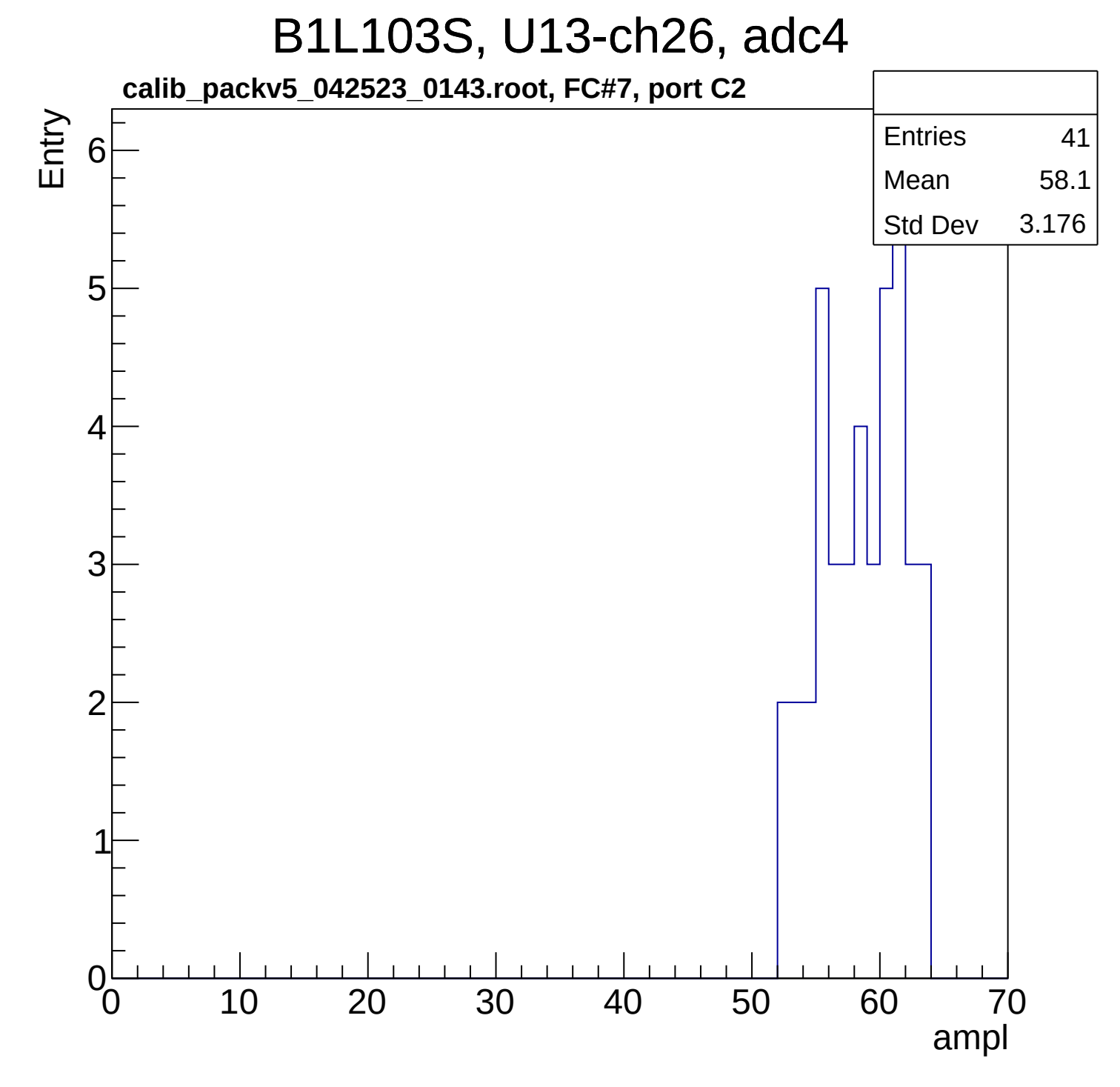
B1L103S, U13-ch26, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 58.1
Std Dev 3.176
Entry
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2
3
4
5
6
0
10
20
30
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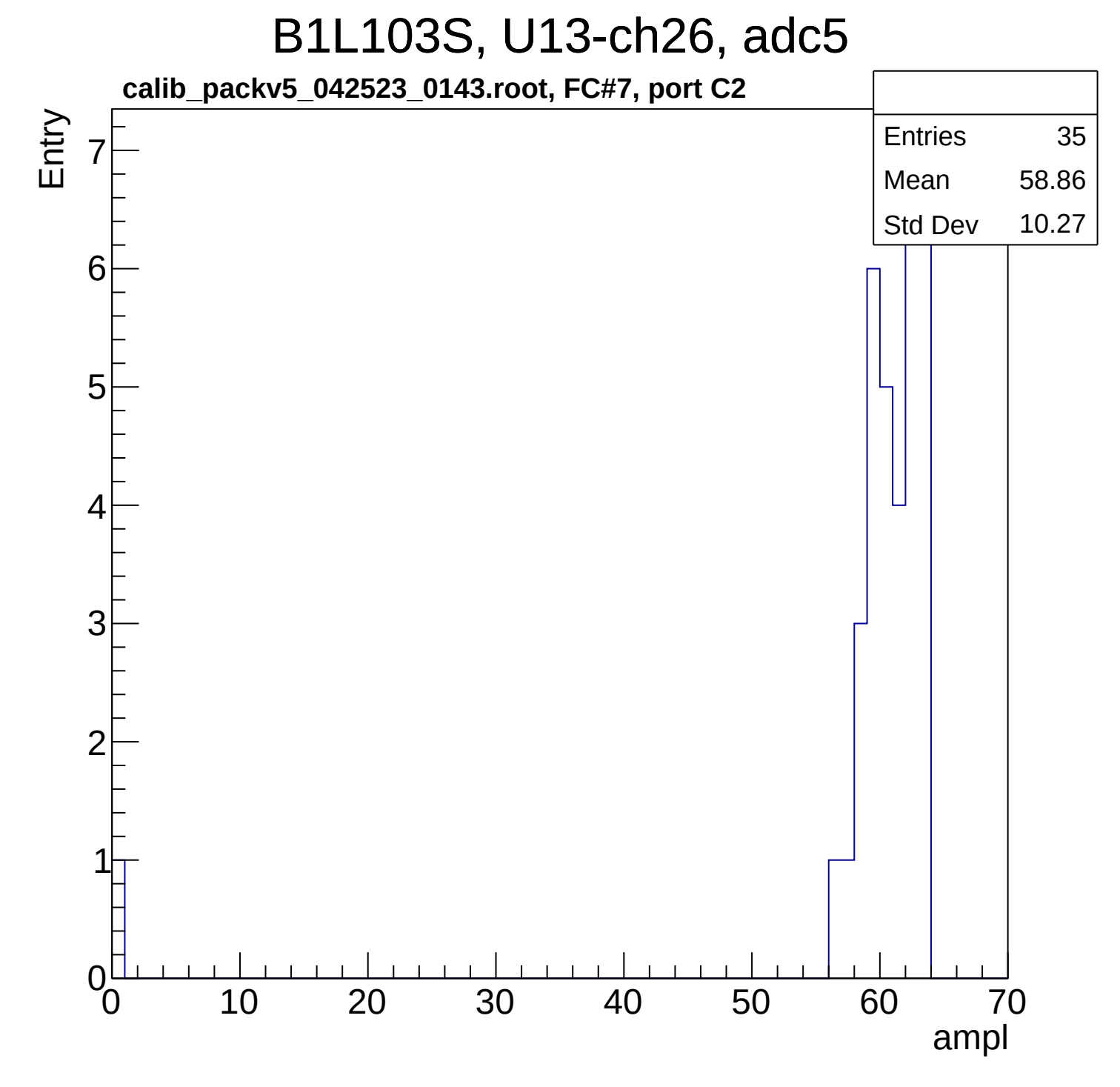
50
60
70
ampl


B1L103S, U13-ch26, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 58.86
Std Dev 10.27
Entry
ampl
0
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2
3
4
5
6
7
0
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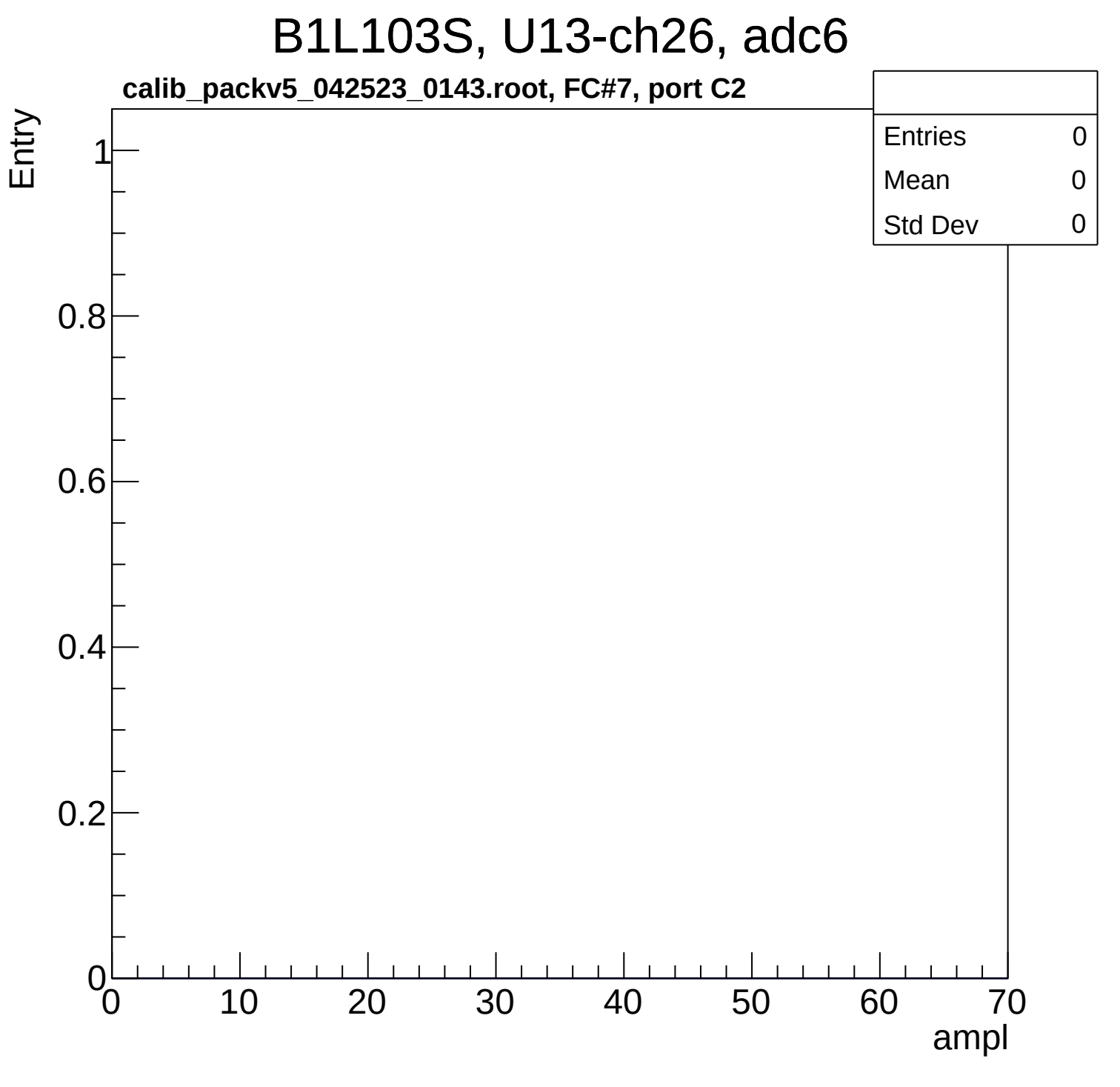
20
30
40
50
60
70


B1L103S, U13-ch26, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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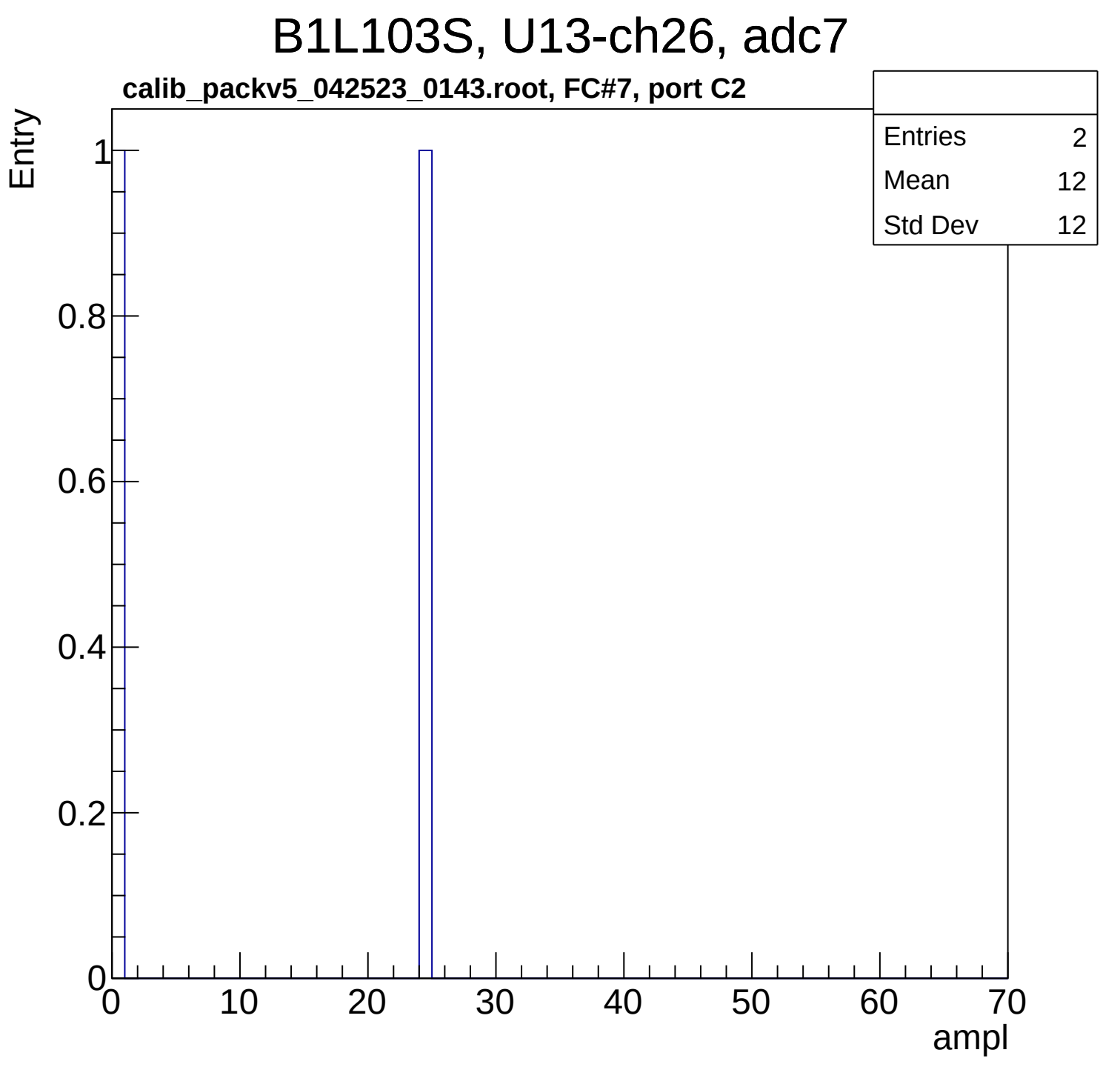
50
60
70
ampl


B1L103S, U13-ch26, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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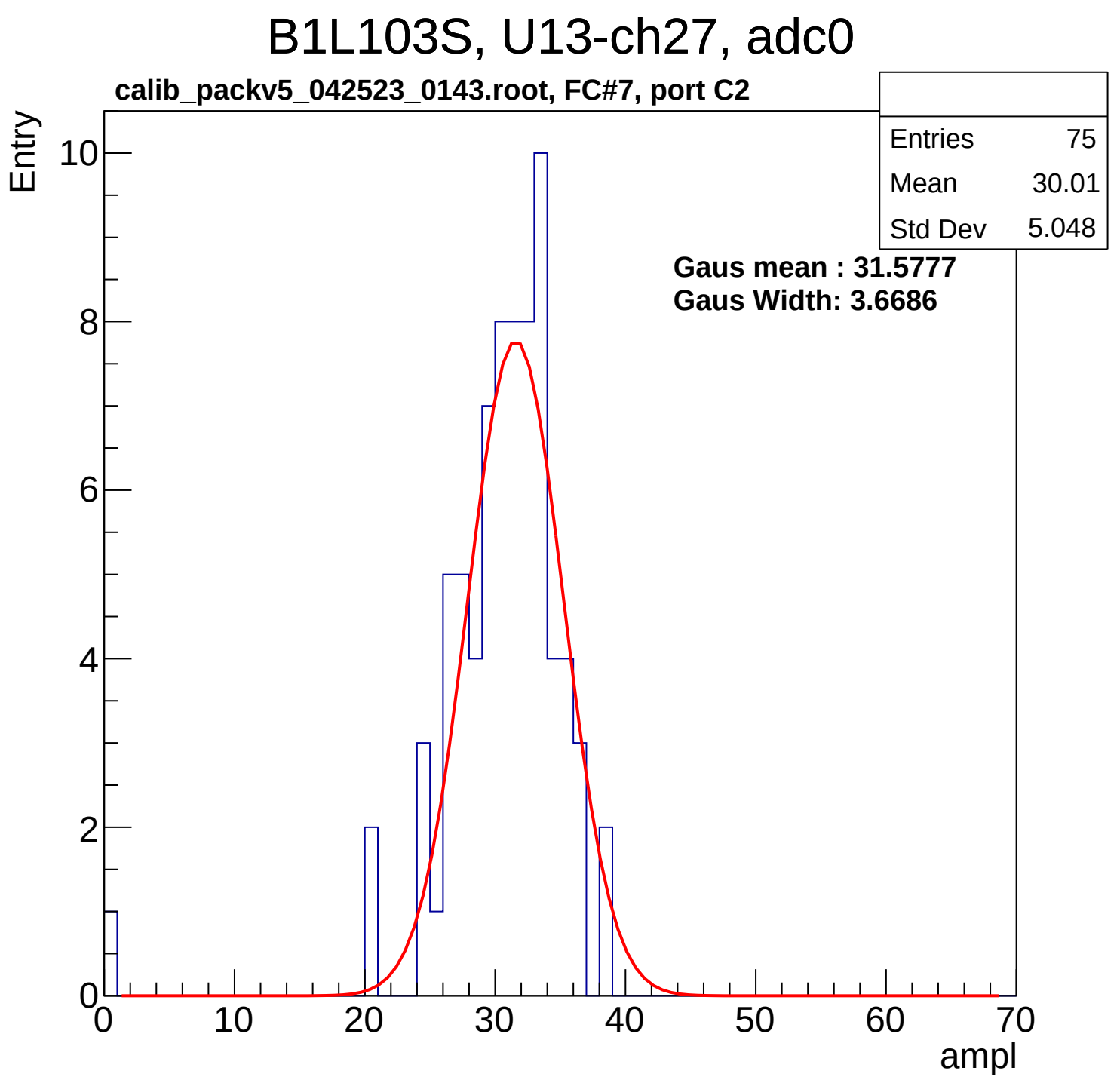
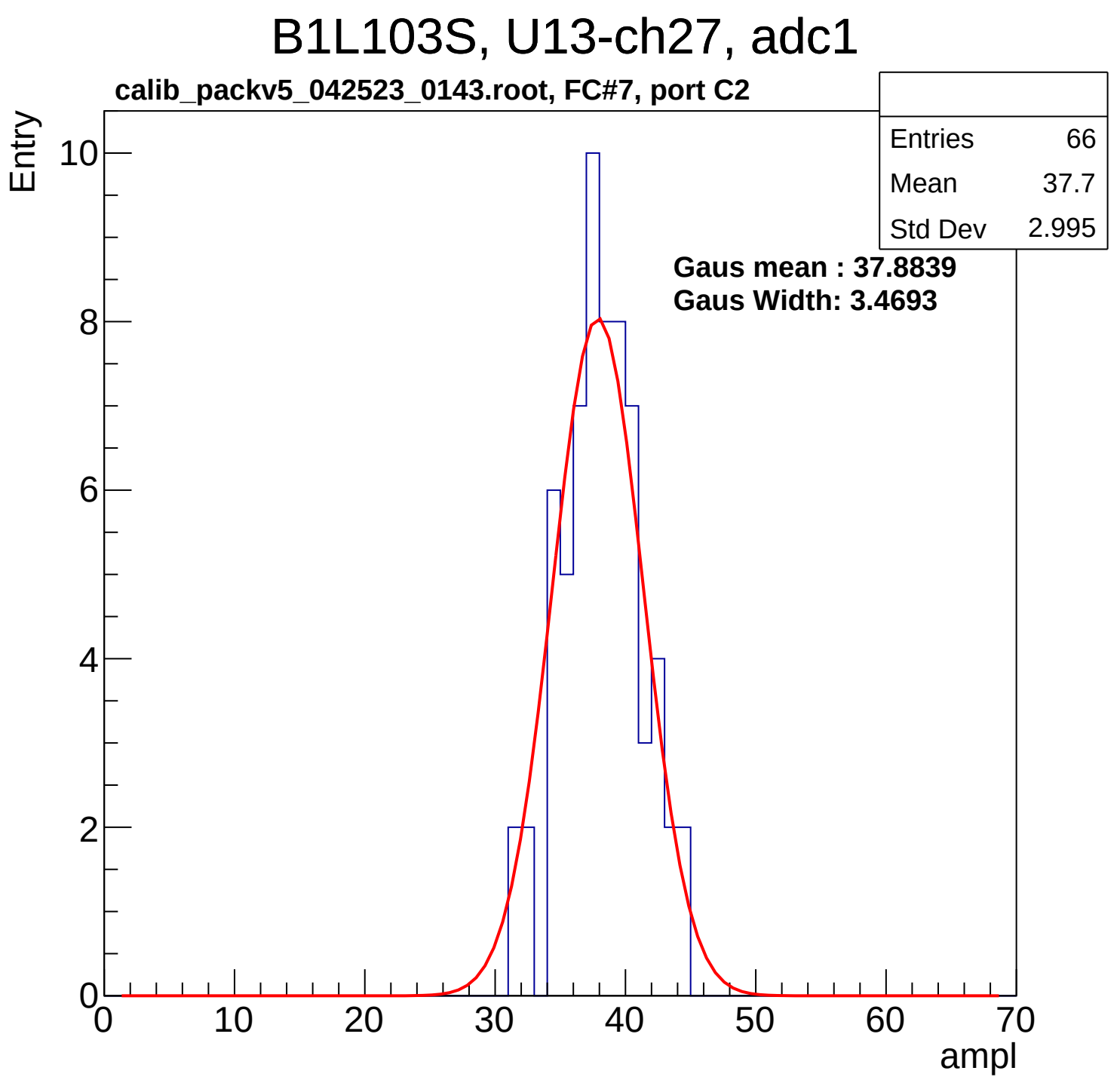
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch27, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.01
Std Dev 5.048
Gaus mean : 31.5777
Gaus Width: 3.6686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch27, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 37.7
Std Dev 2.995
Gaus mean : 37.8839
Gaus Width: 3.4693
Entry
ampl
0
2
4
6
8
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0
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40
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60
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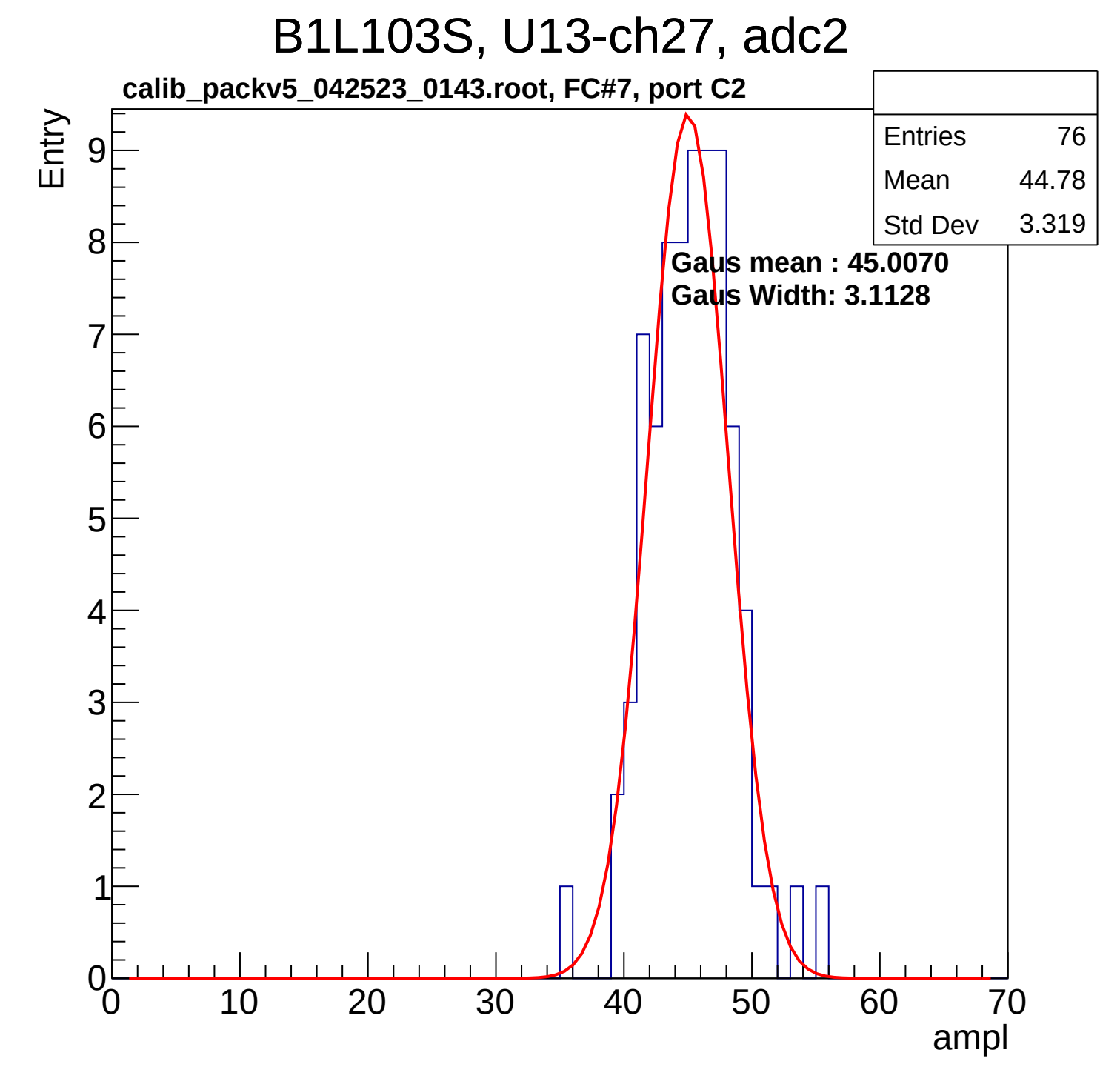
B1L103S, U13-ch27, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 44.78
Std Dev 3.319
Gaus mean : 45.0070
Gaus Width: 3.1128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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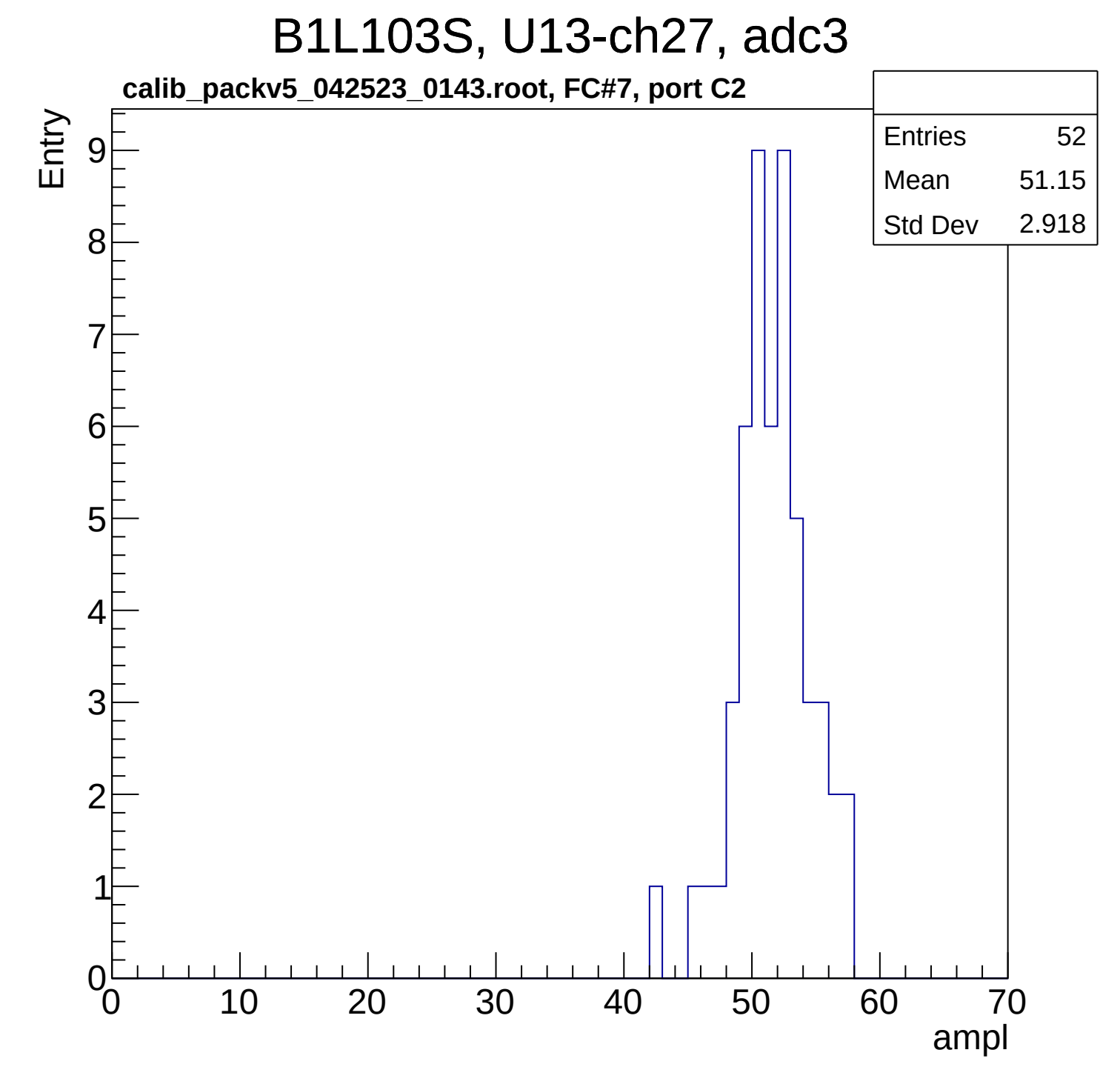
B1L103S, U13-ch27, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 51.15
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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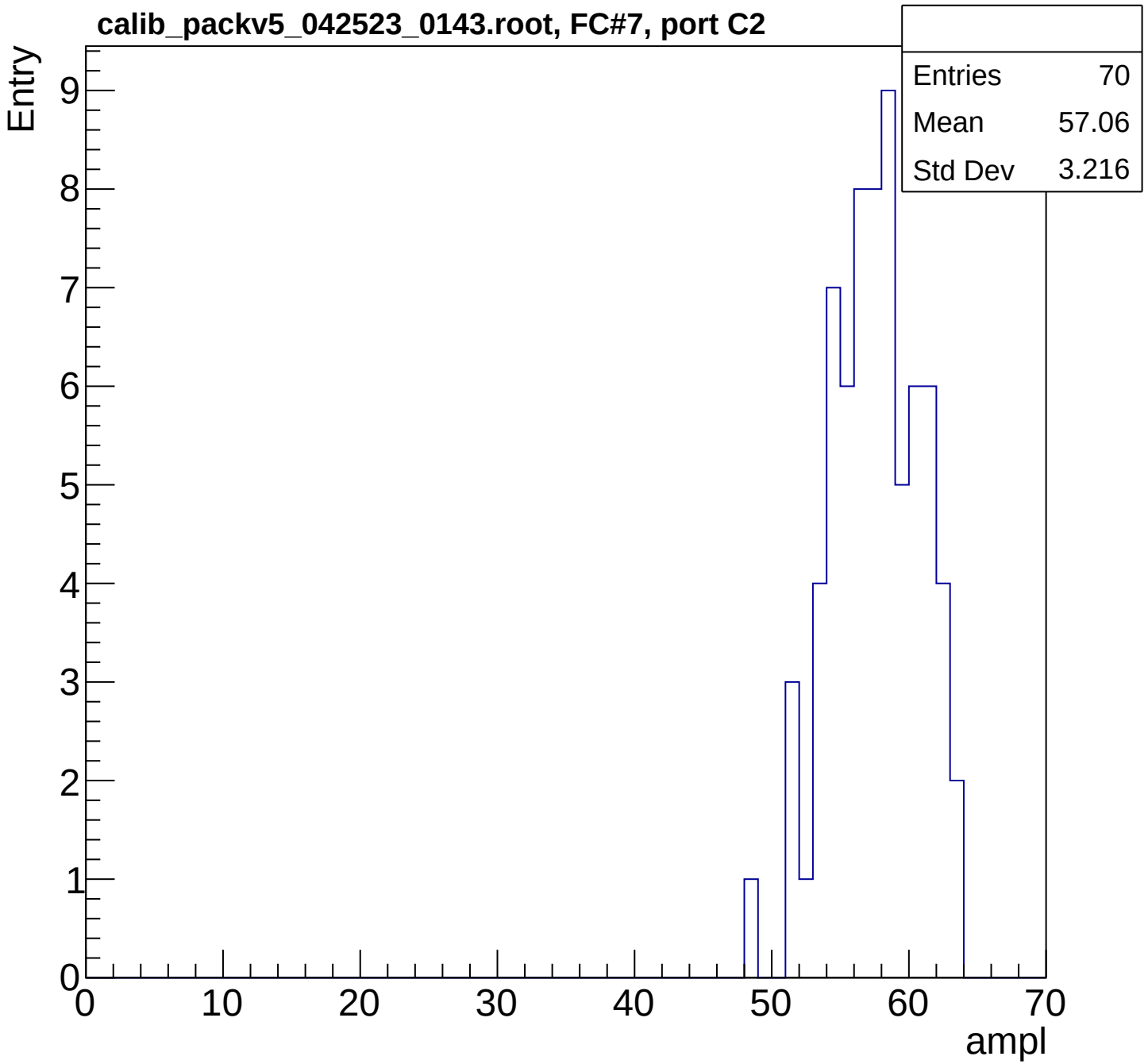
B1L103S, U13-ch27, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 57.06
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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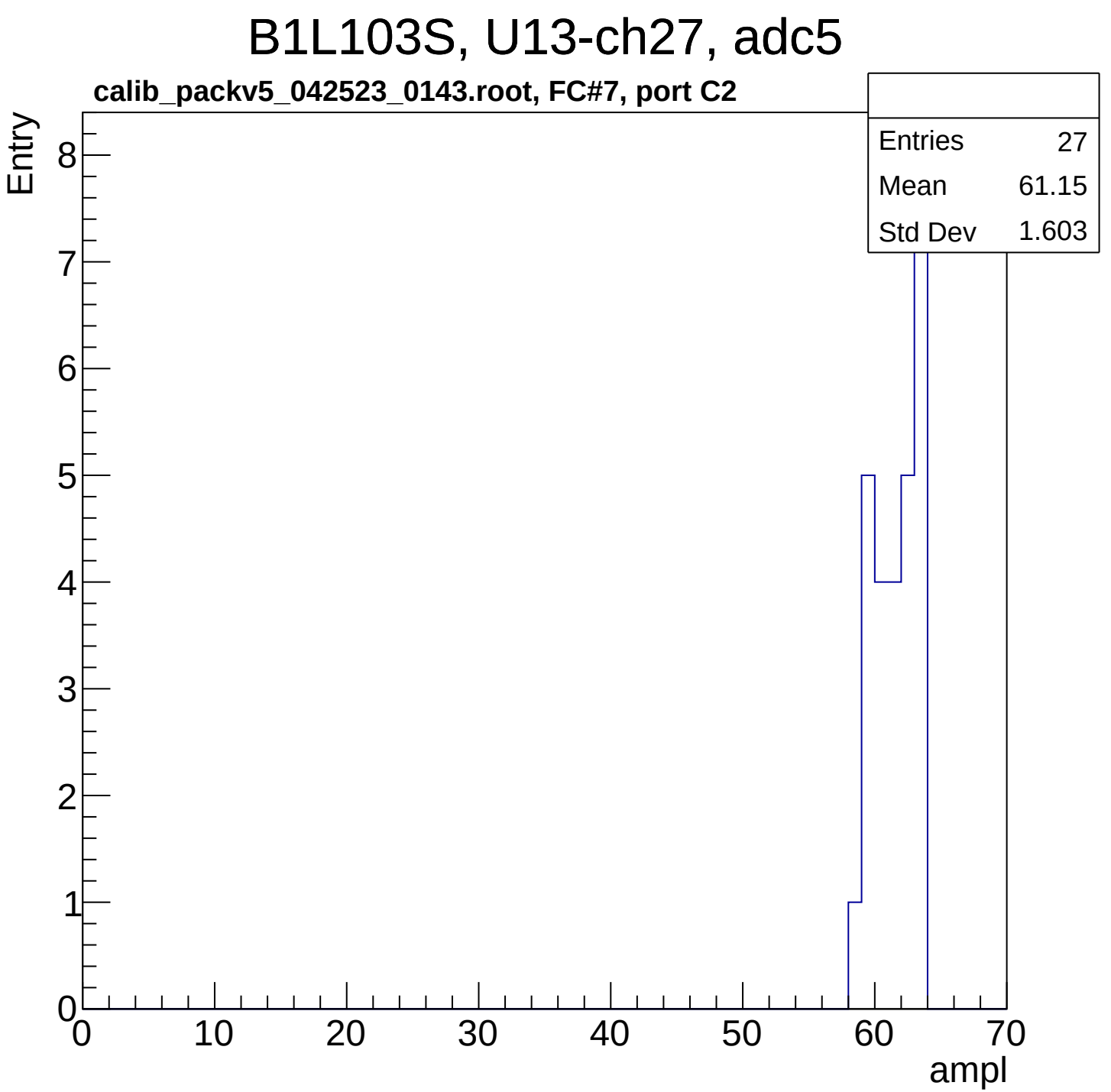
B1L103S, U13-ch27, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 27
Mean 61.15
Std Dev 1.603
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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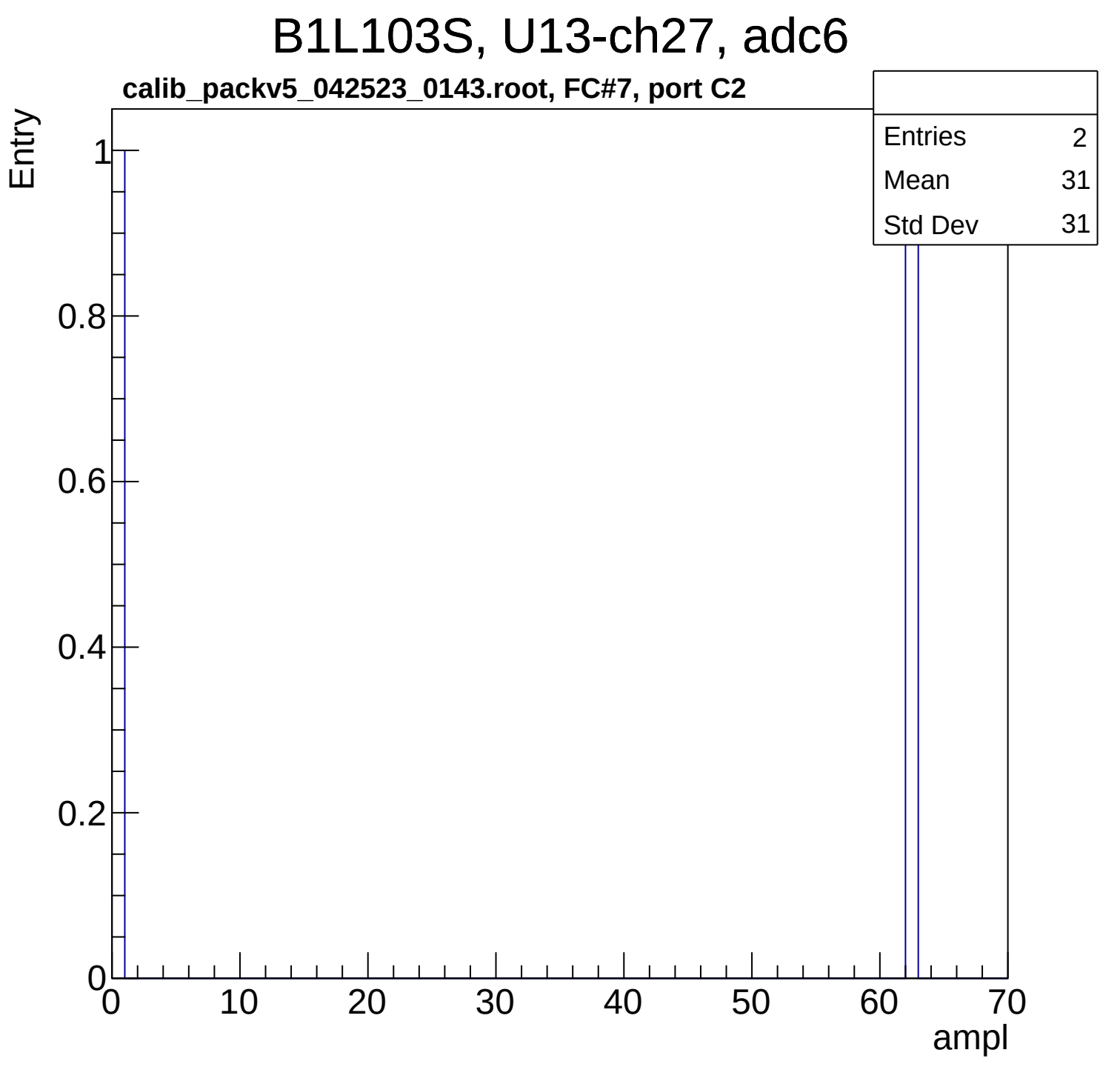
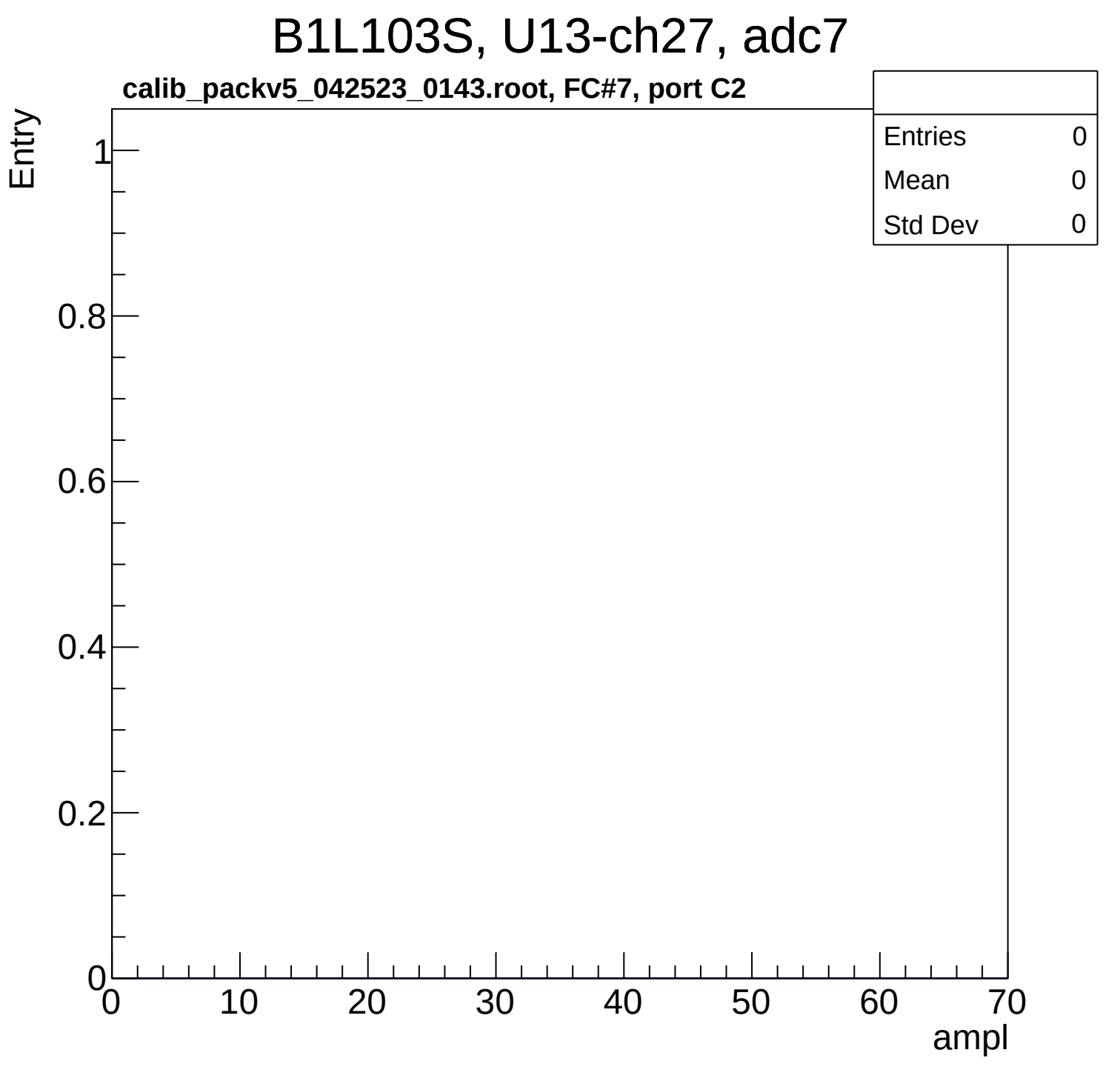
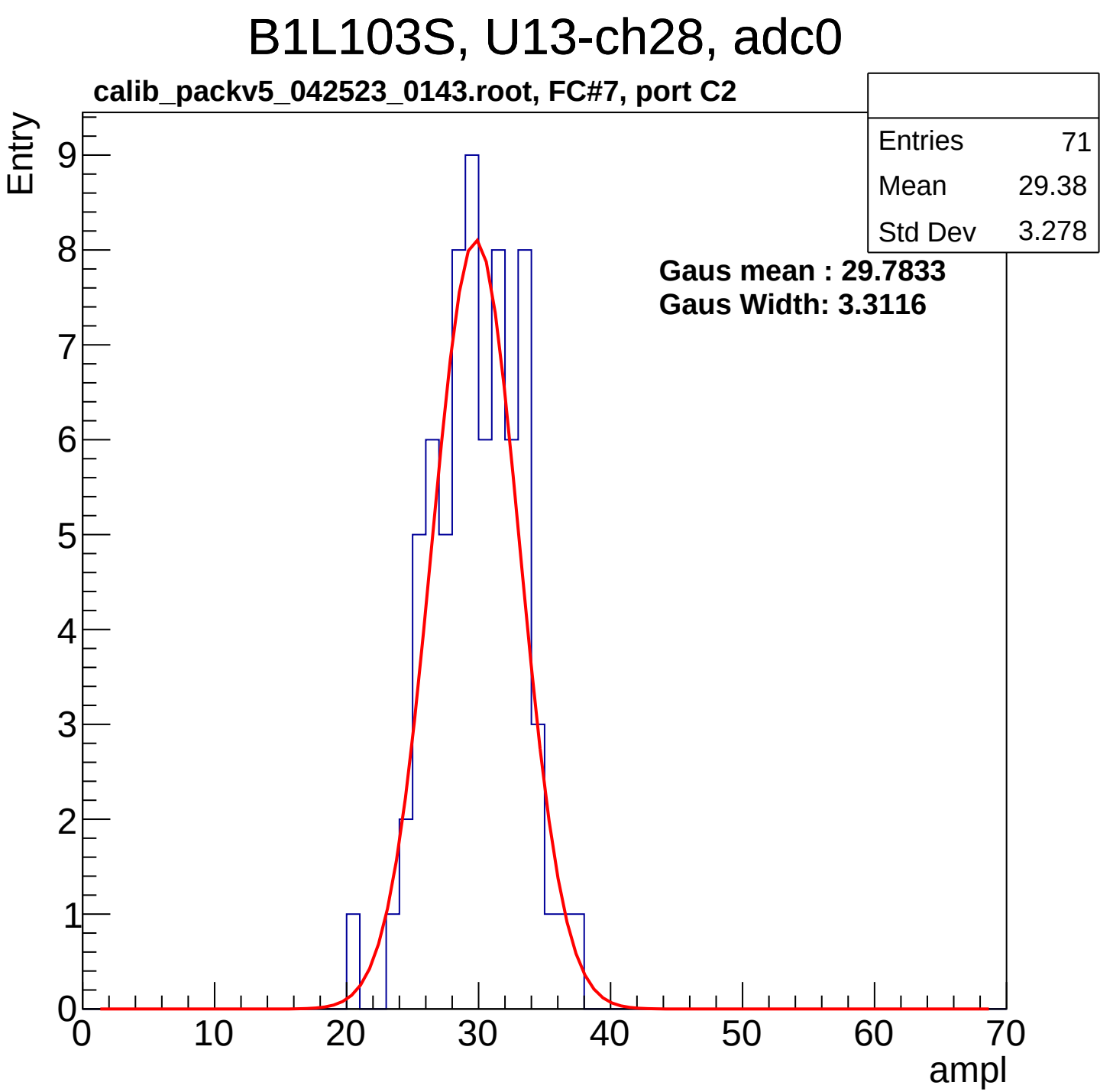

B1L103S, U13-ch27, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch27, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch28, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 29.38
Std Dev 3.278
Gaus mean : 29.7833
Gaus Width: 3.3116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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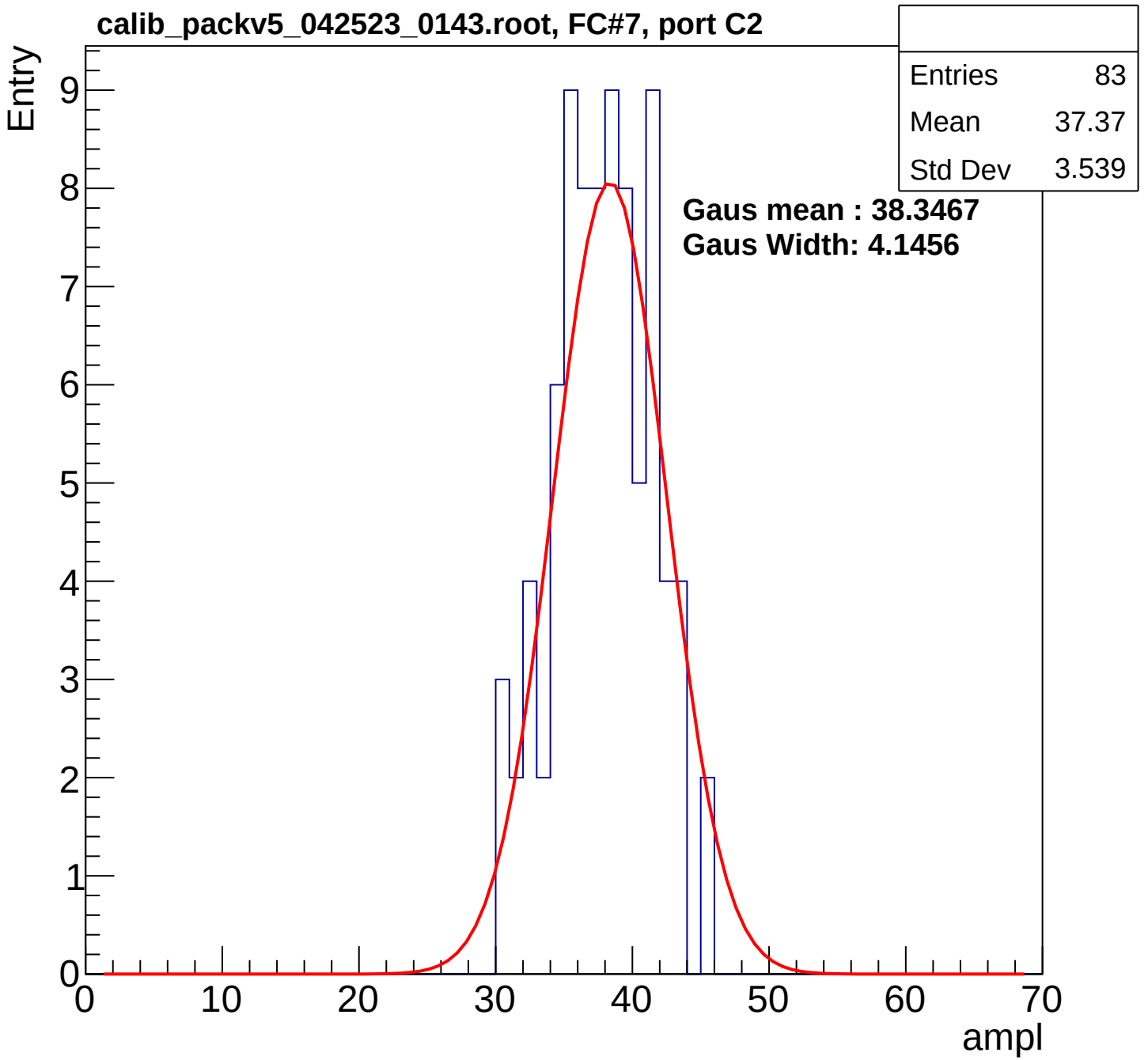
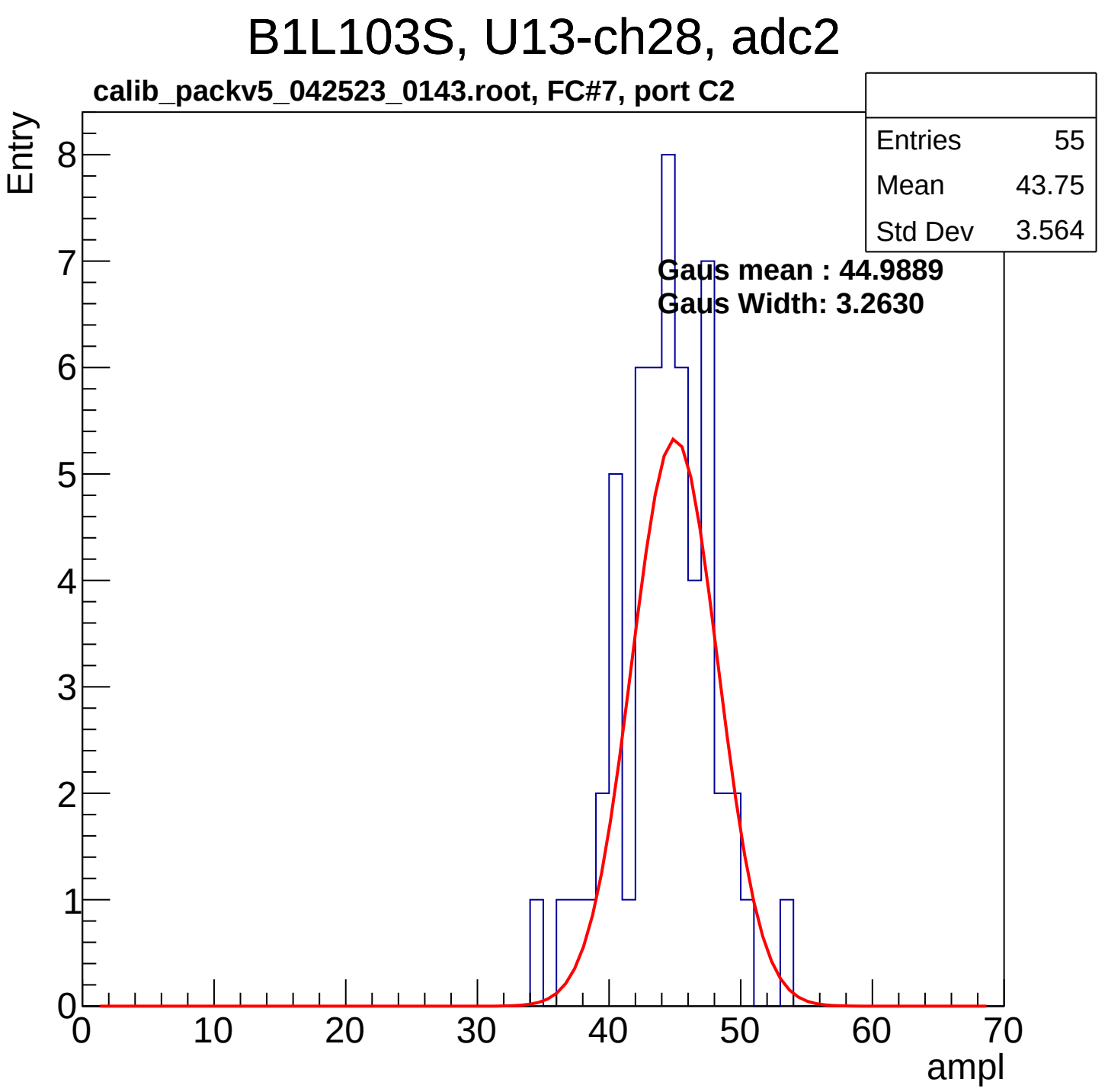
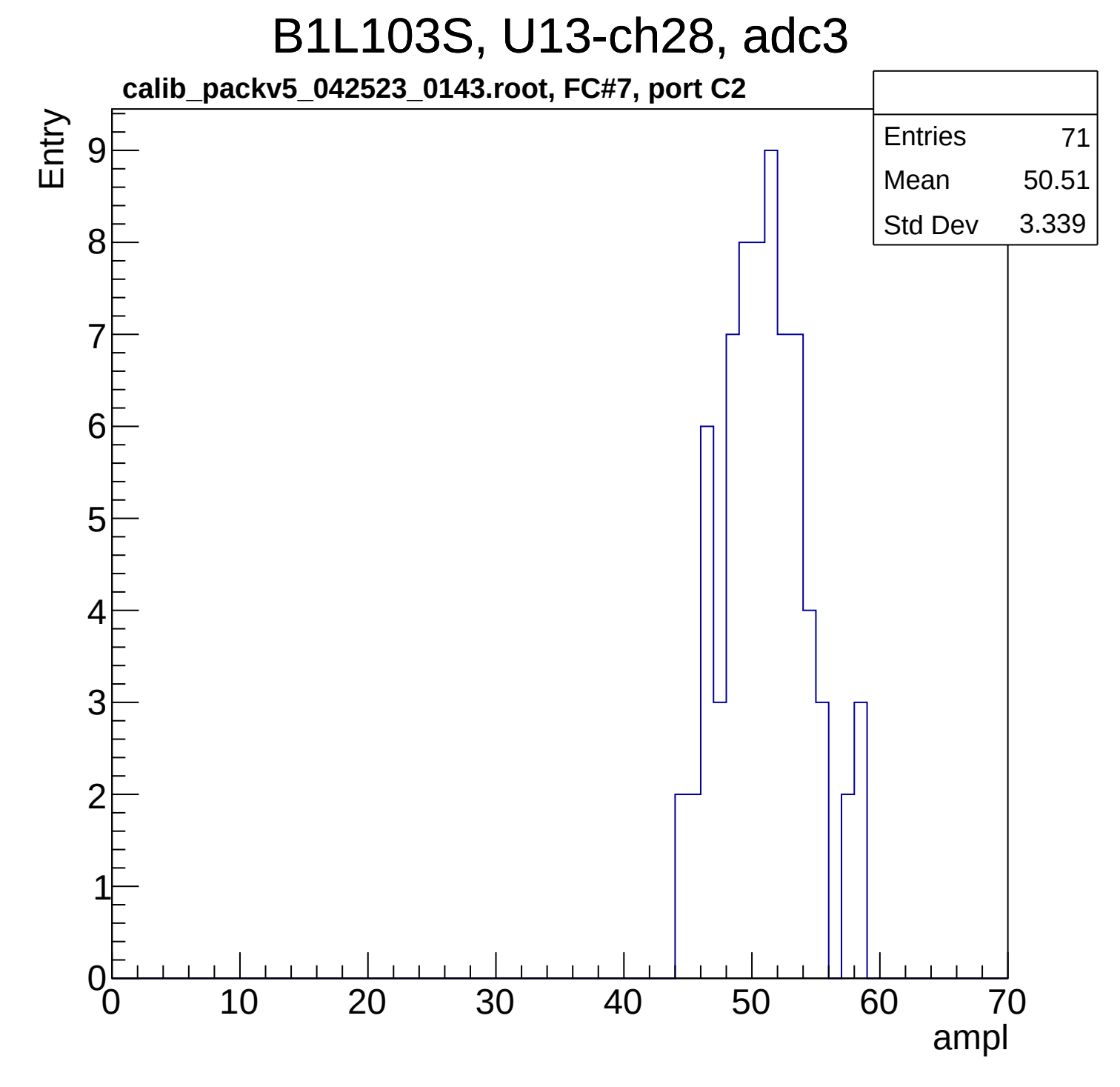
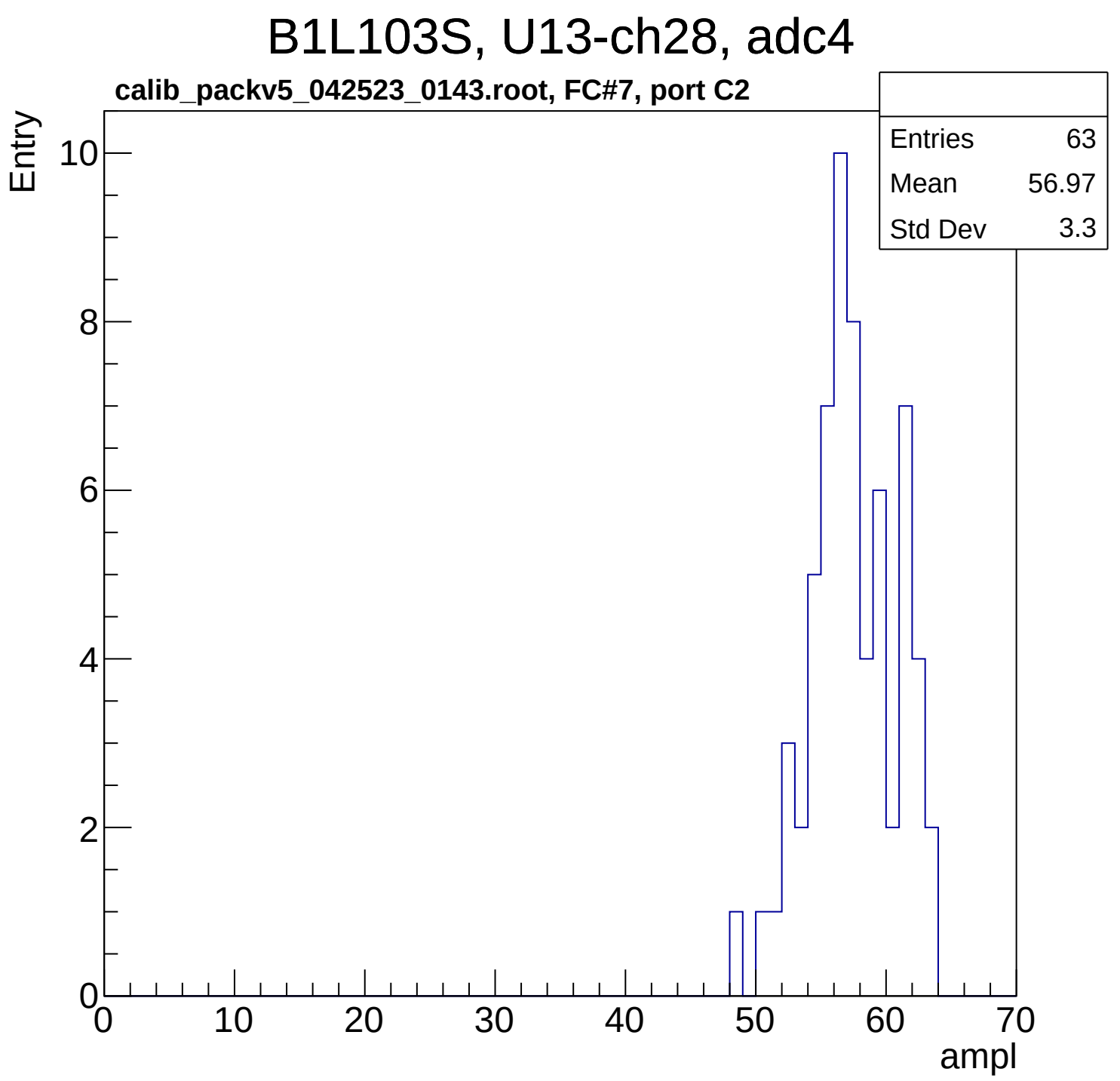
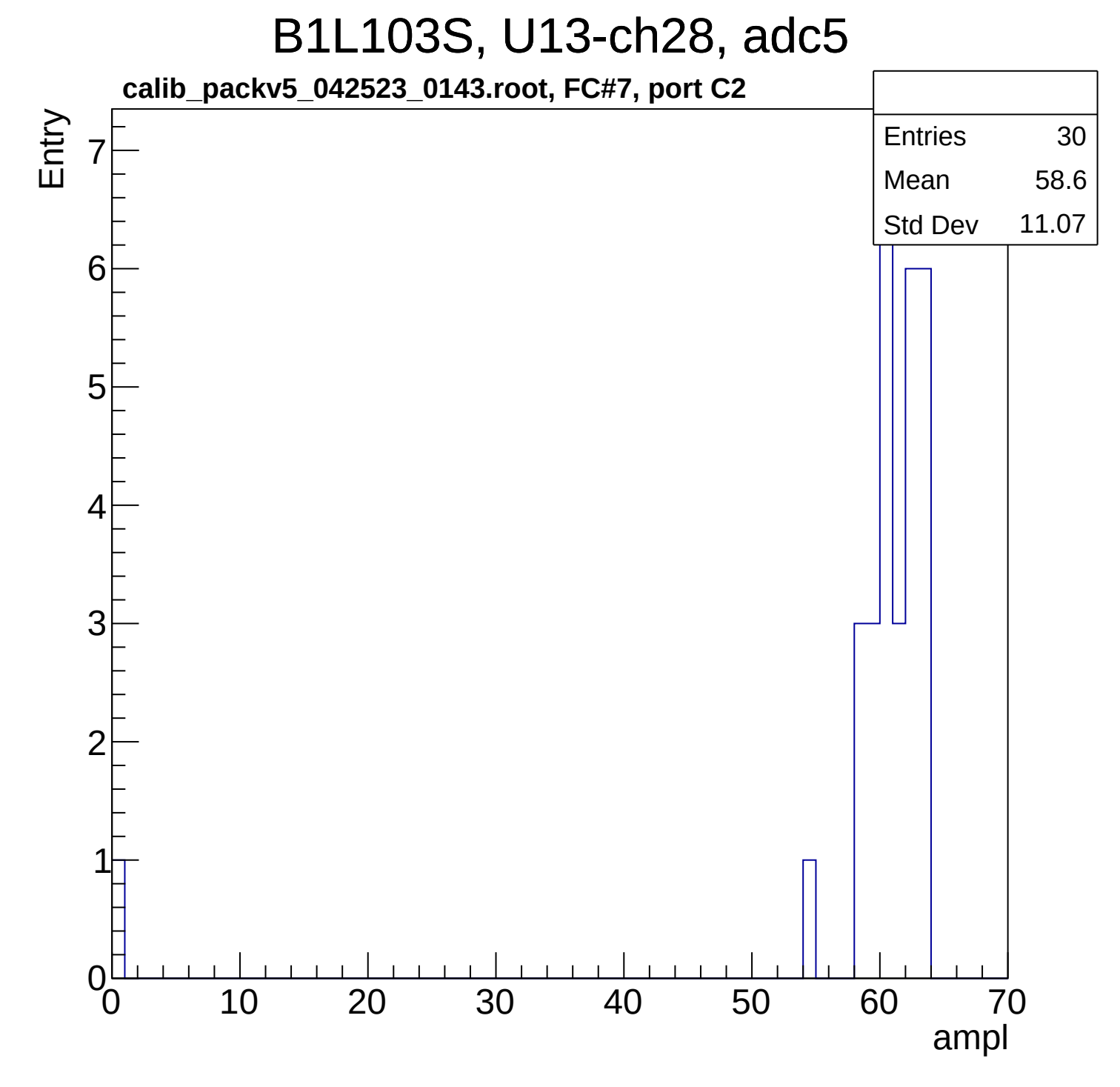
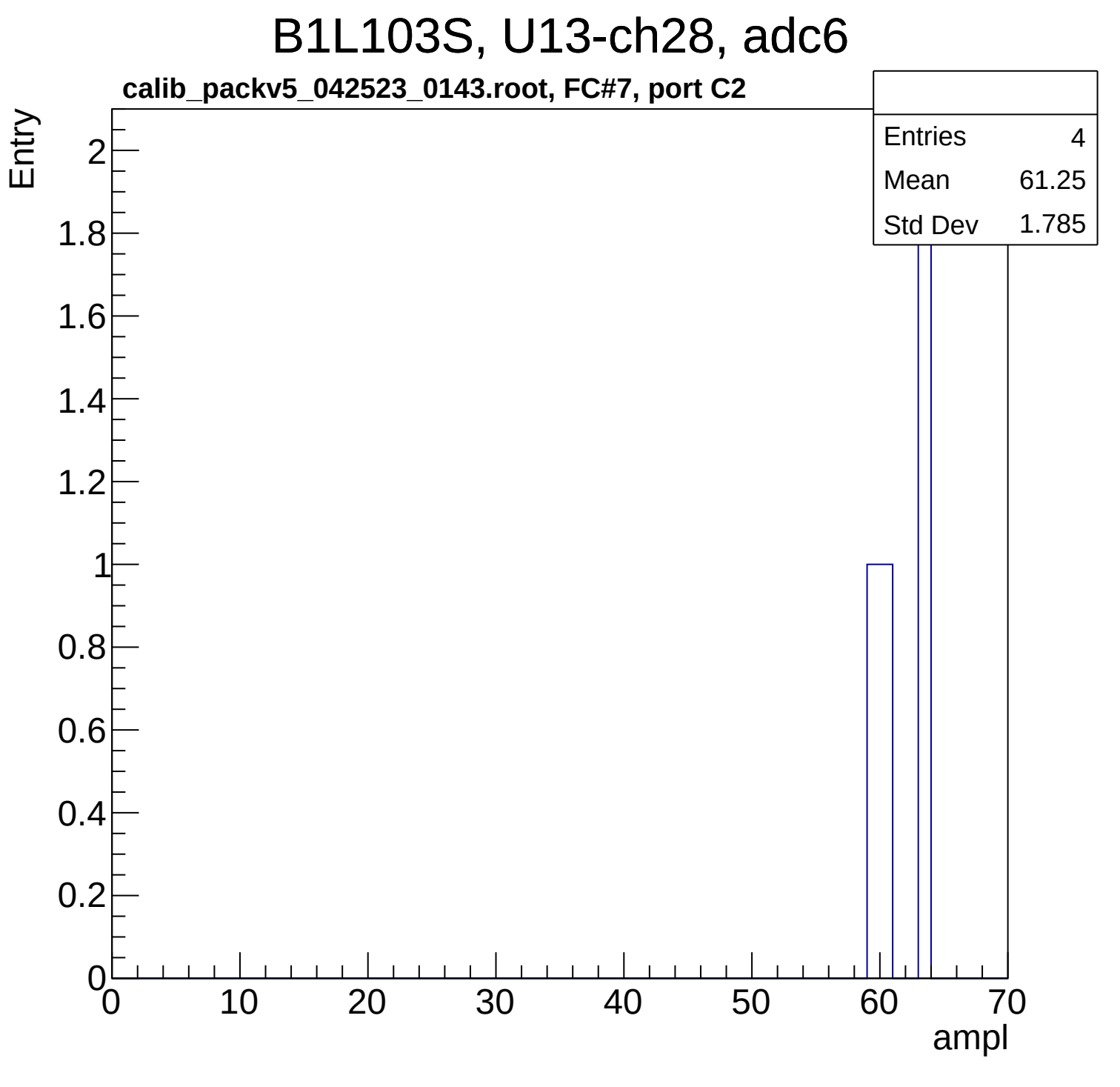

B1L103S, U13-ch28, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 37.37
Std Dev 3.539
Gaus mean : 38.3467
Gaus Width: 4.1456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

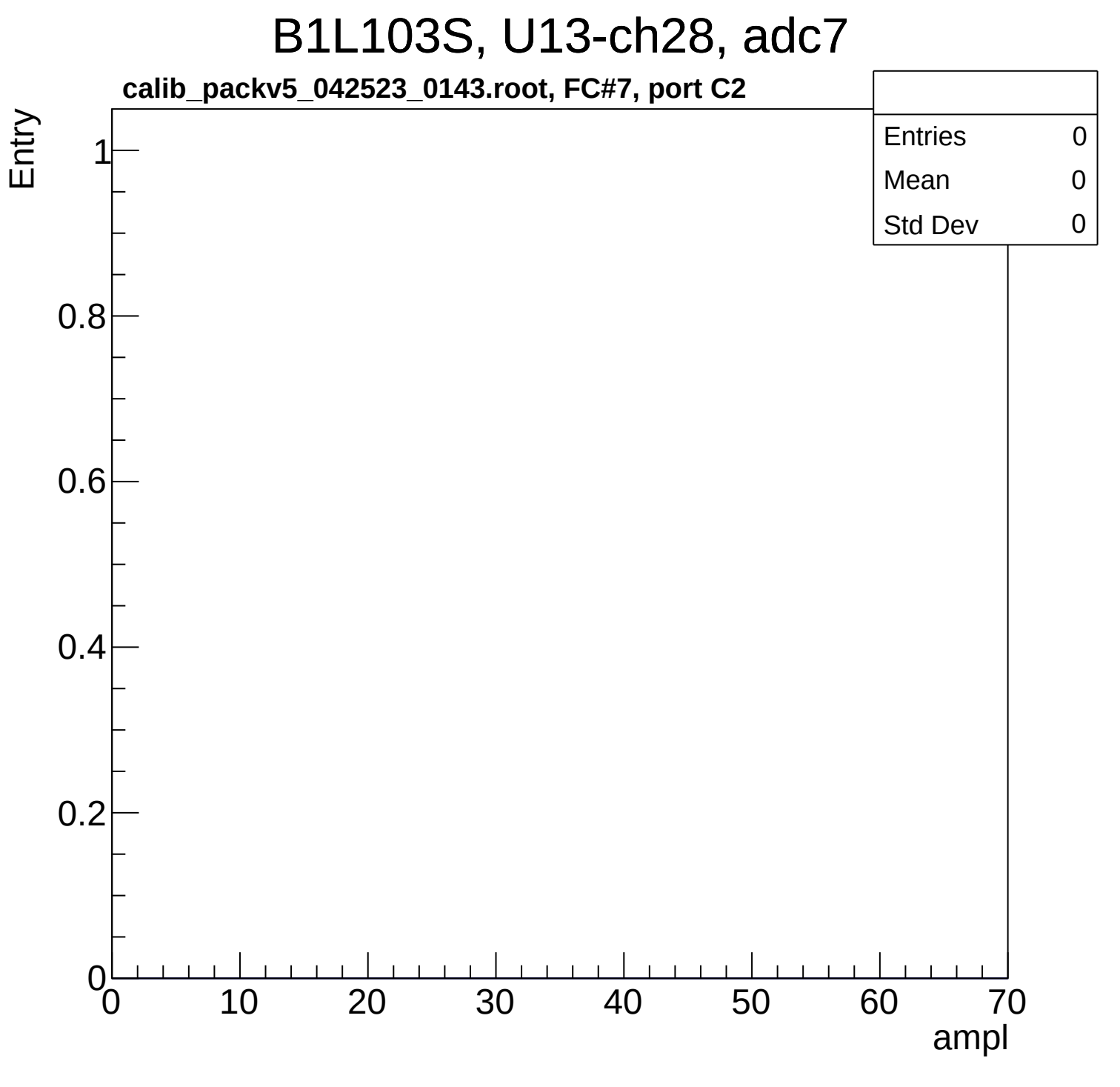

B1L103S, U13-ch28, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 43.75
Std Dev 3.564
Gaus mean : 44.9889
Gaus Width: 3.2630
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

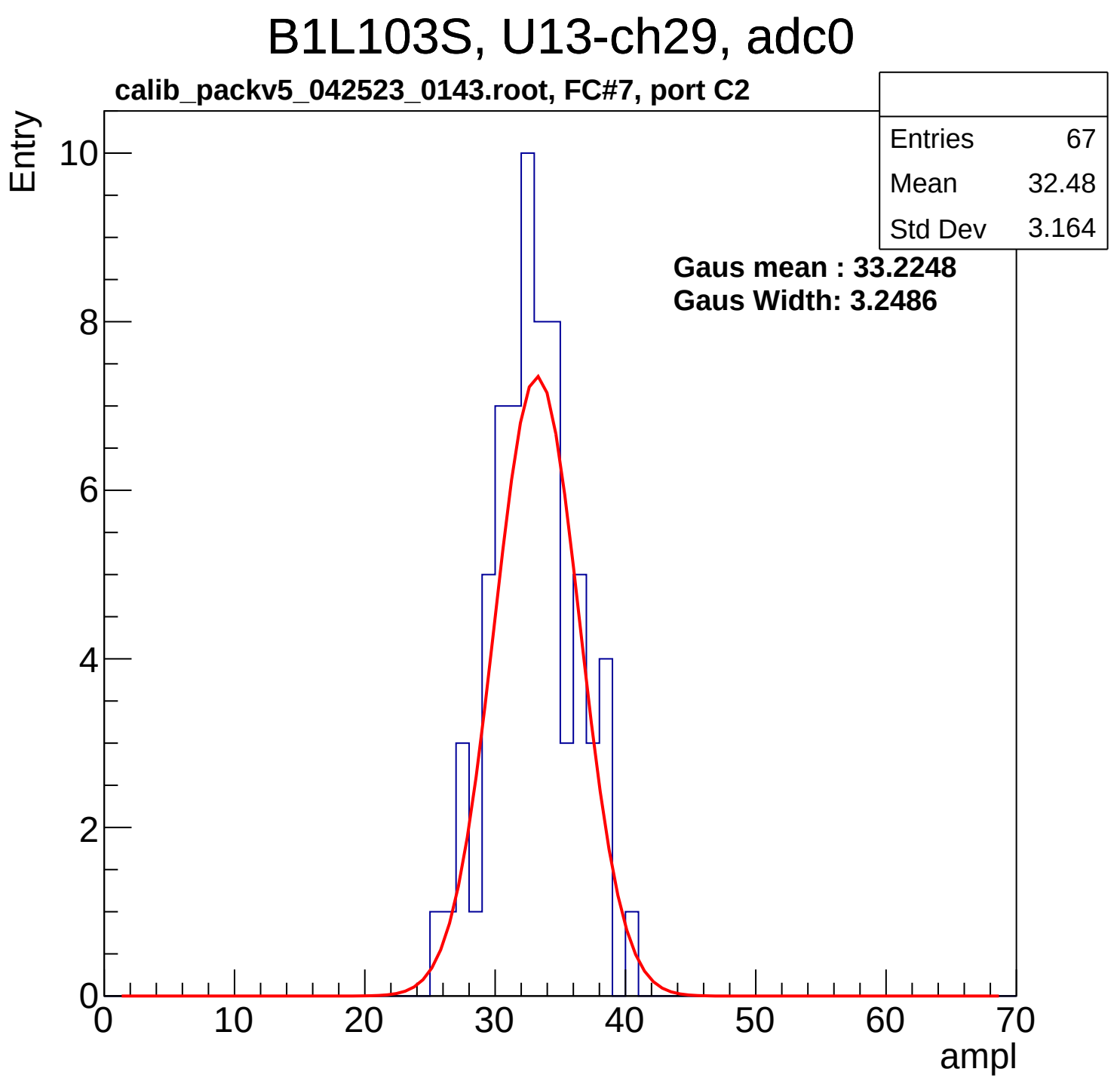

B1L103S, U13-ch28, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 50.51
Std Dev 3.339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

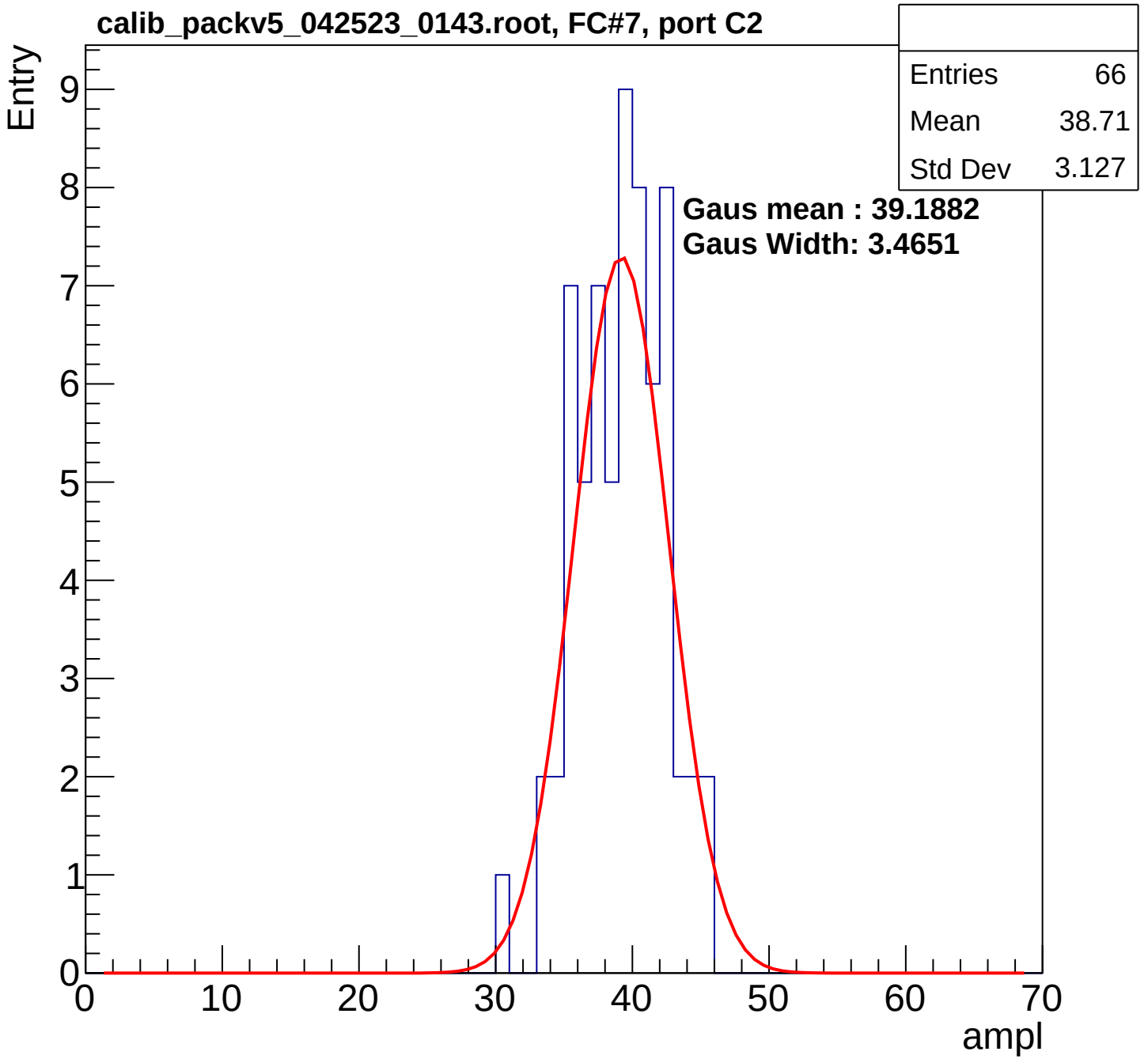

B1L103S, U13-ch28, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 56.97
Std Dev 3.3
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

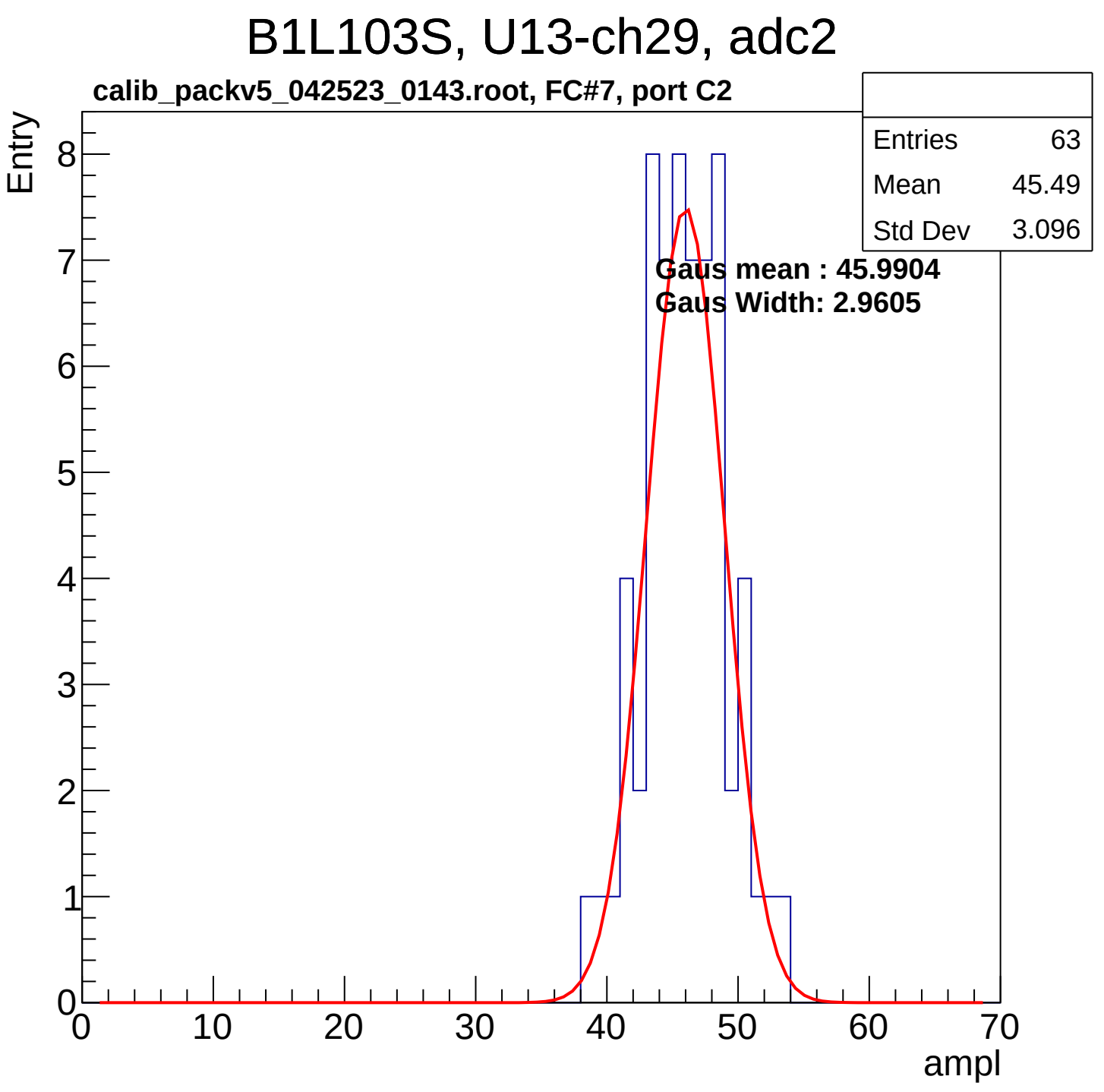

B1L103S, U13-ch28, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 58.6
Std Dev 11.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

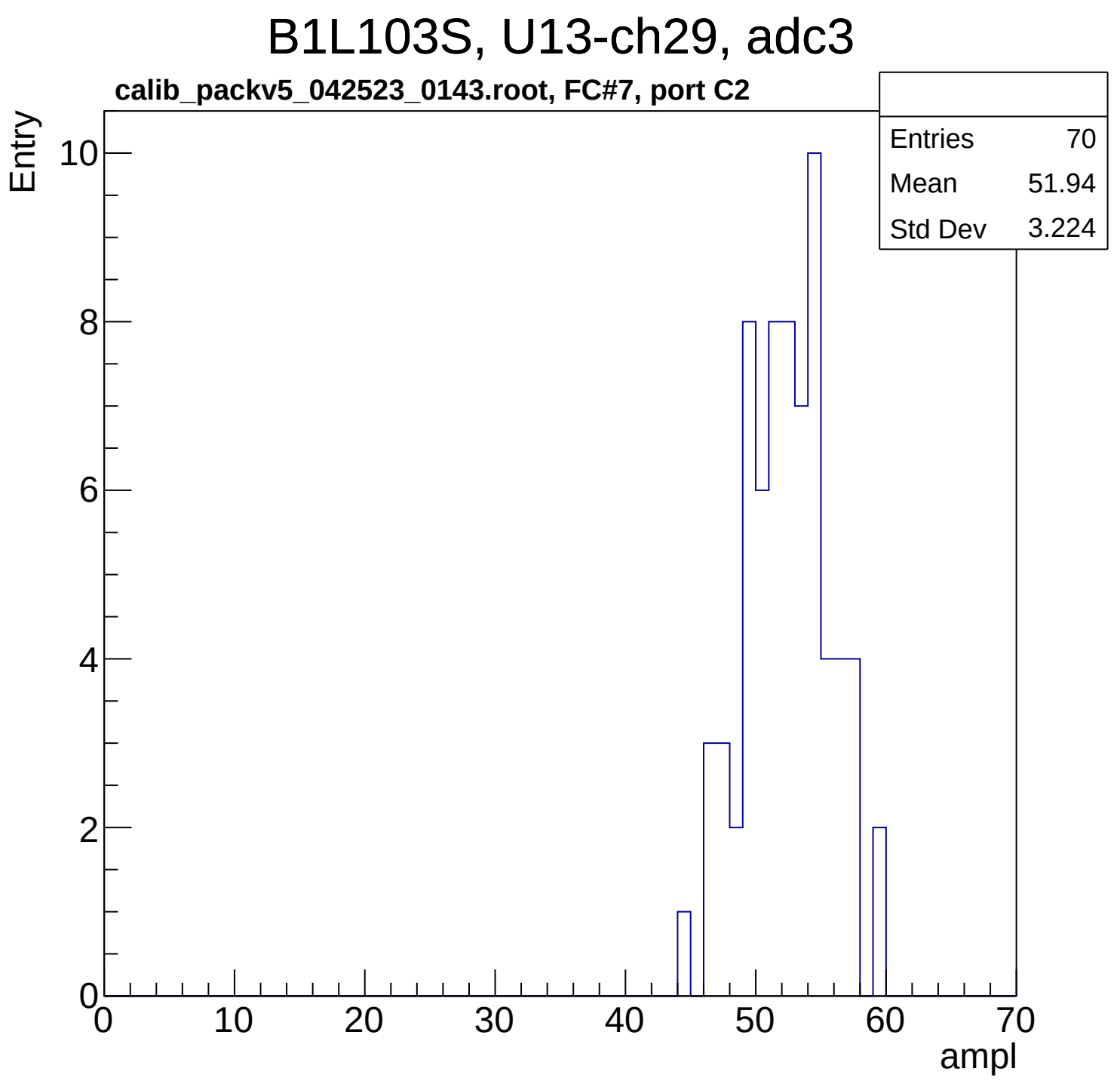

B1L103S, U13-ch28, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.25
Std Dev 1.785
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

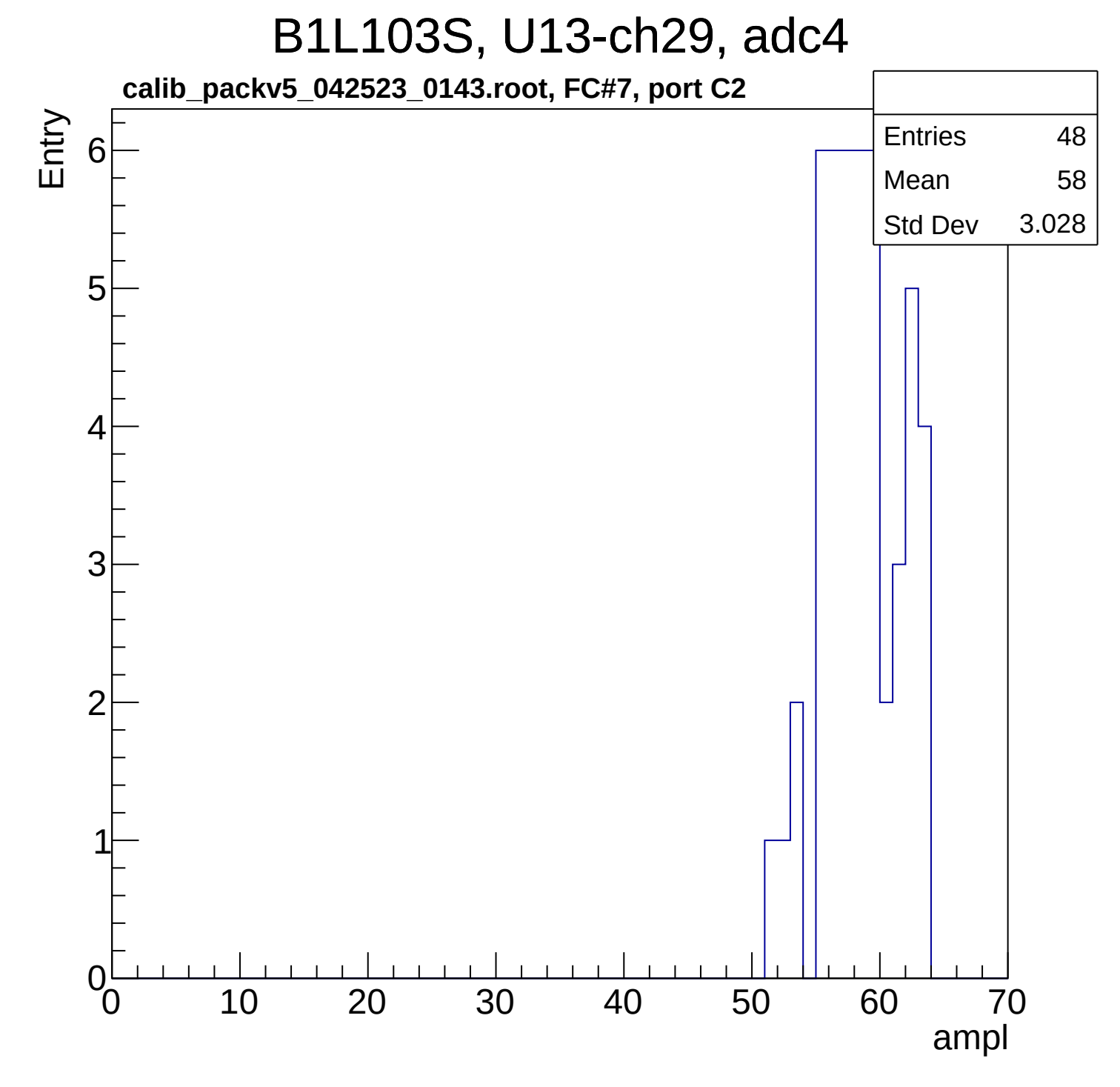

B1L103S, U13-ch28, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch29, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 32.48
Std Dev 3.164
Gaus mean : 33.2248
Gaus Width: 3.2486
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch29, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 38.71
Std Dev 3.127
Gaus mean : 39.1882
Gaus Width: 3.4651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch29, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 45.49
Std Dev 3.096
Gaus mean : 45.9904
Gaus Width: 2.9605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch29, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 51.94
Std Dev 3.224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch29, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58
Std Dev 3.028
Entry
0
1
2
3
4
5
6
0
10
20
30
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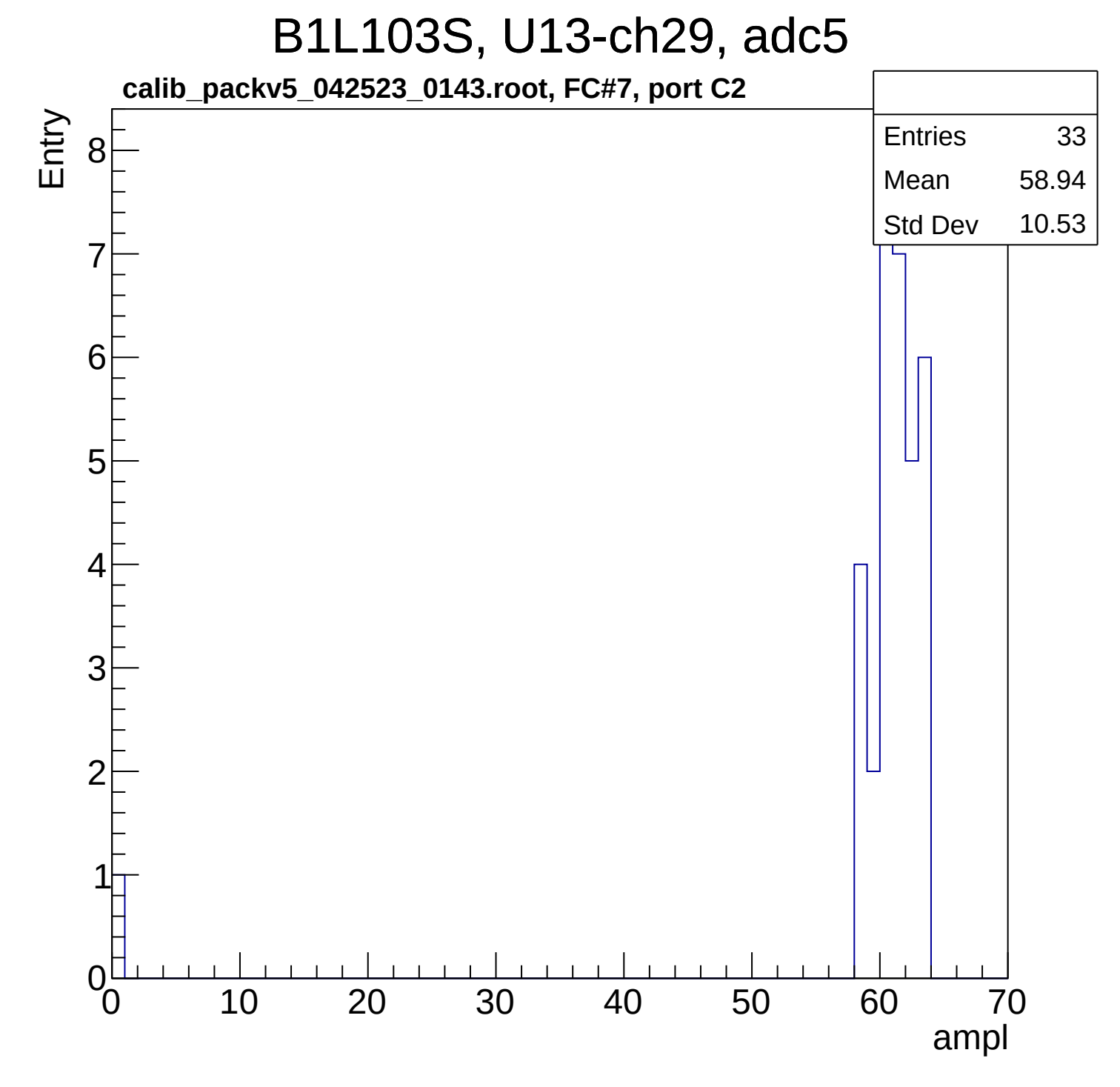
50
60
70
ampl


B1L103S, U13-ch29, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.94
Std Dev 10.53
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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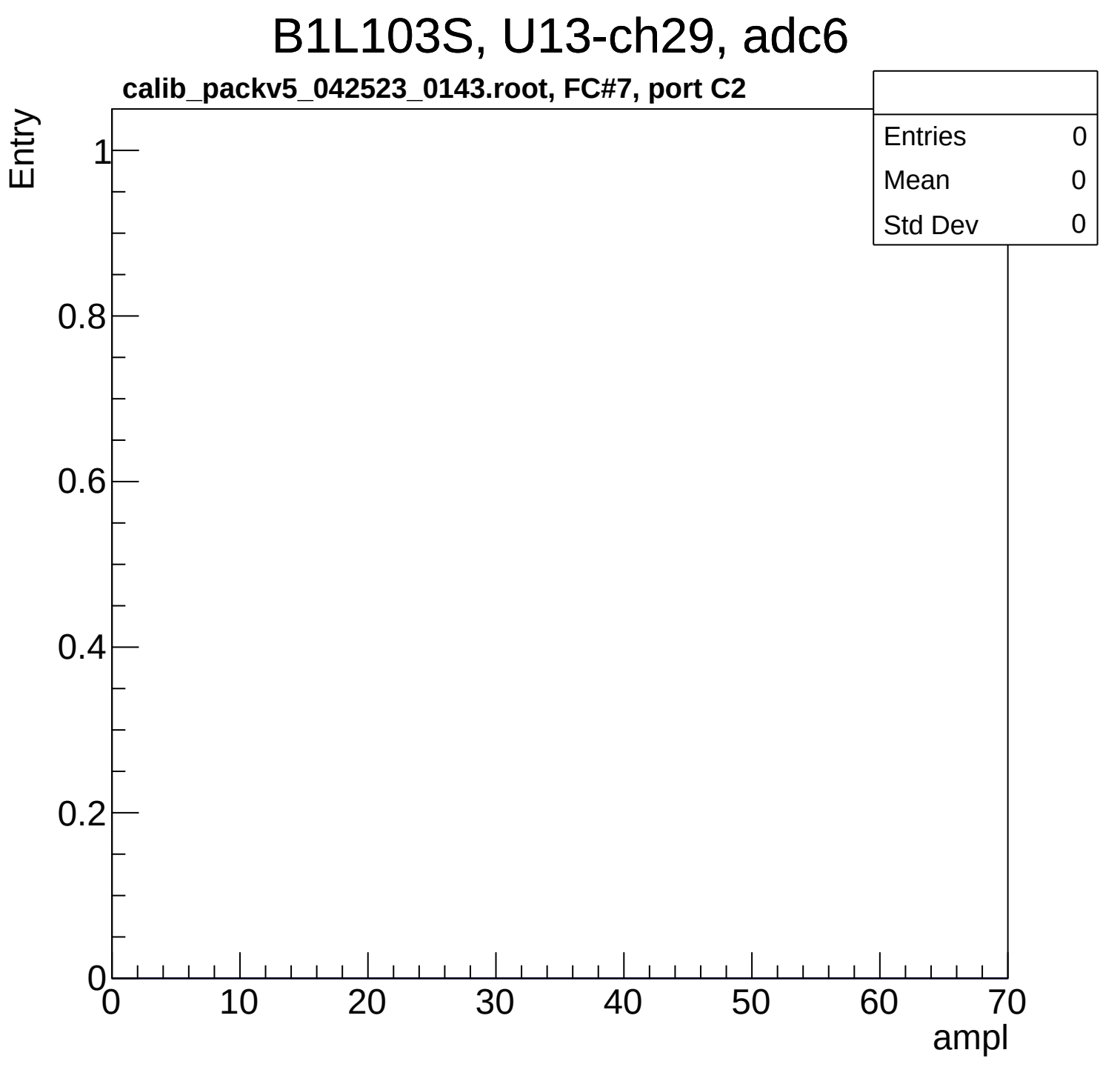
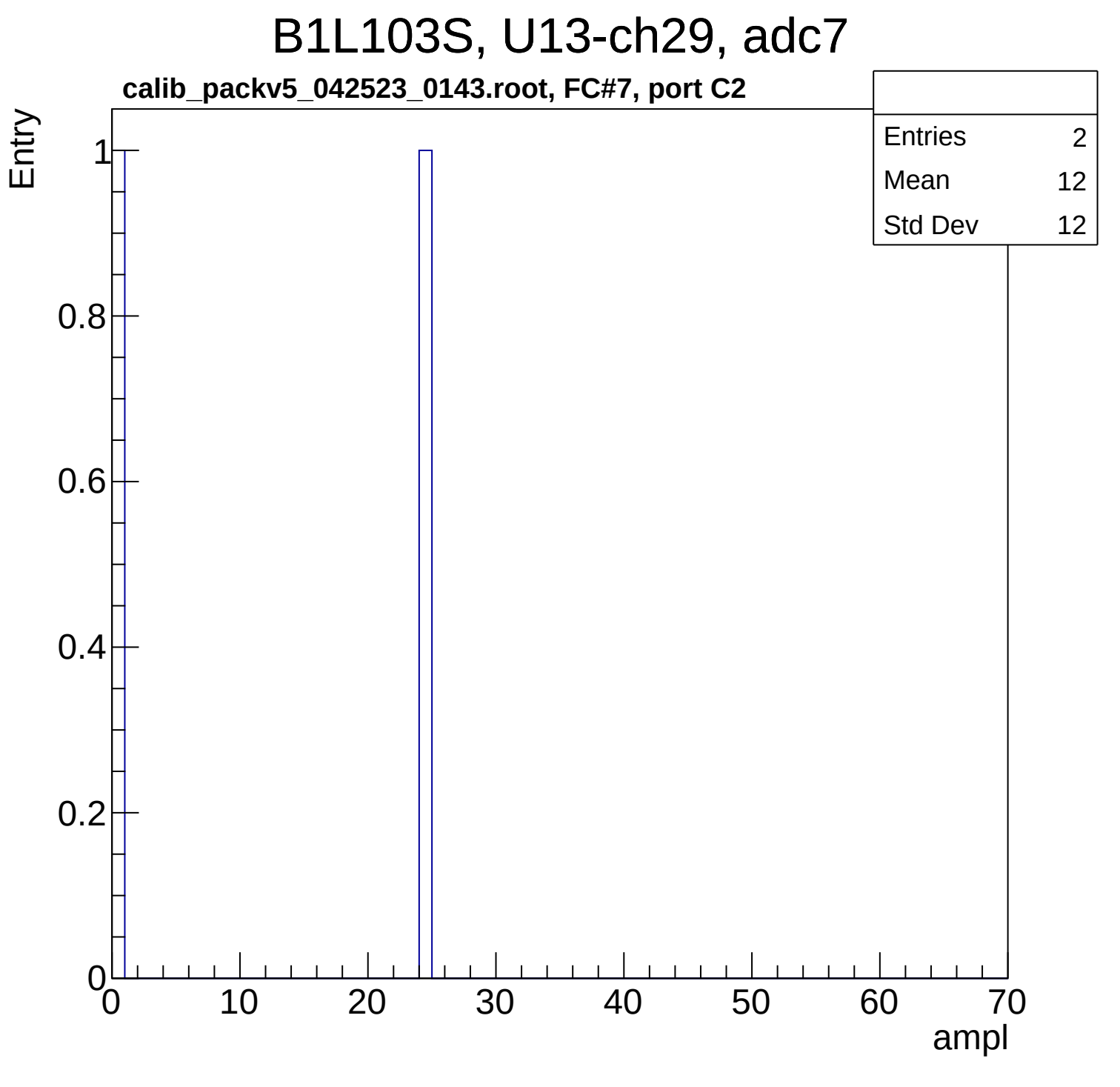

B1L103S, U13-ch29, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch29, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
0.2
0
0
10
20
30
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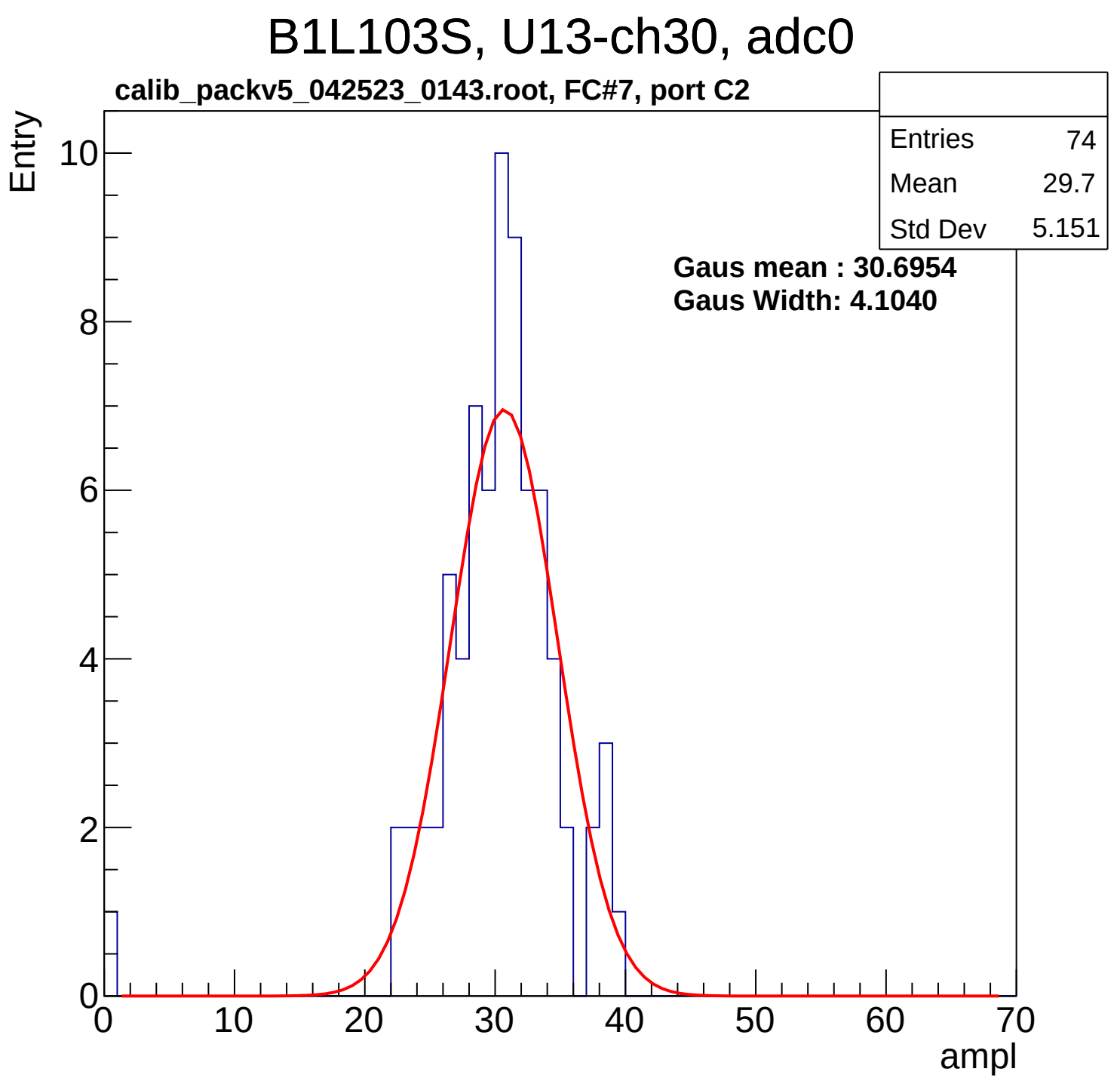
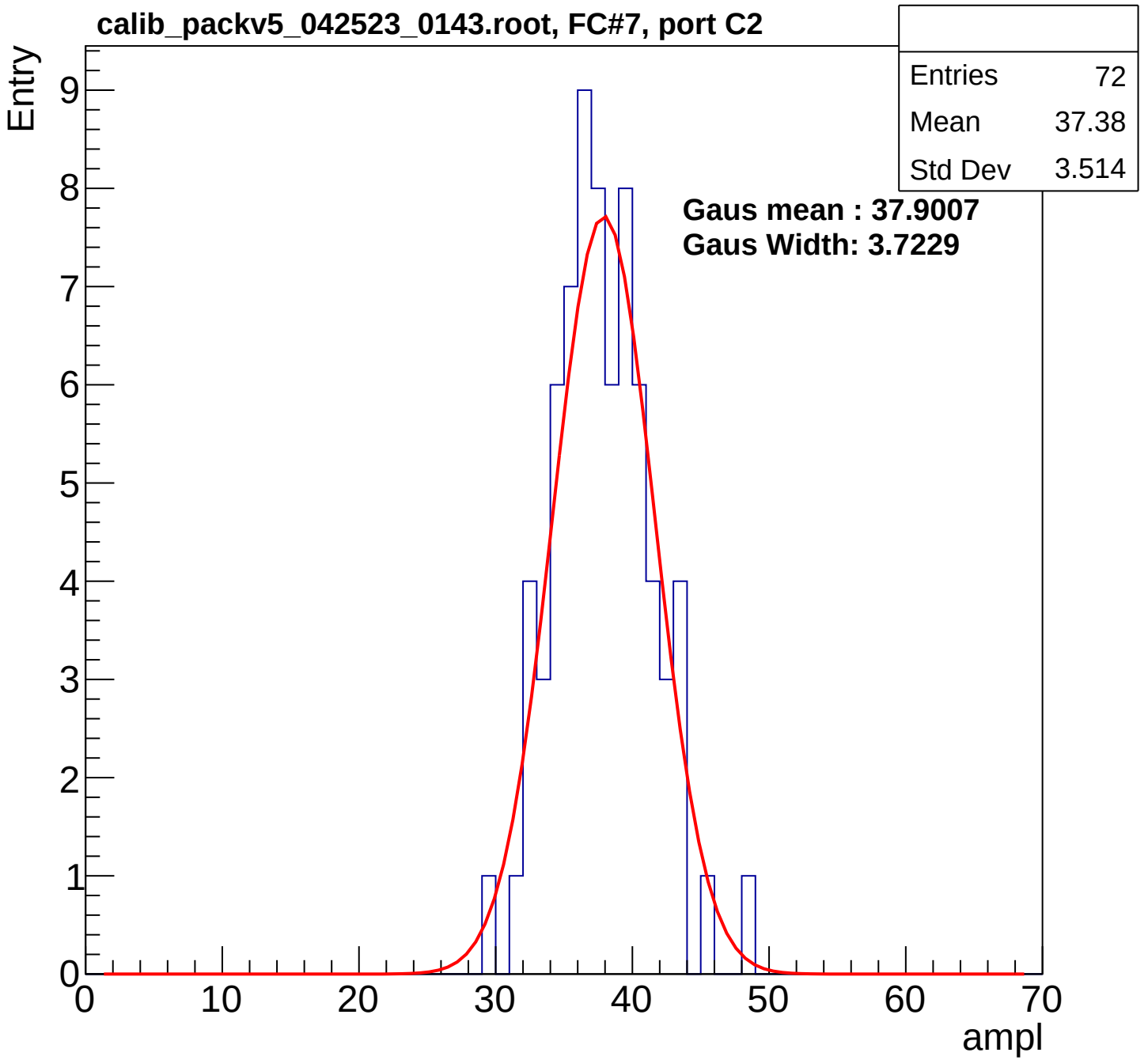
50
60
70
ampl


B1L103S, U13-ch30, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.7
Std Dev 5.151
Gaus mean : 30.6954
Gaus Width: 4.1040
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch30, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.38
Std Dev 3.514
Gaus mean : 37.9007
Gaus Width: 3.7229
Entry
ampl
0
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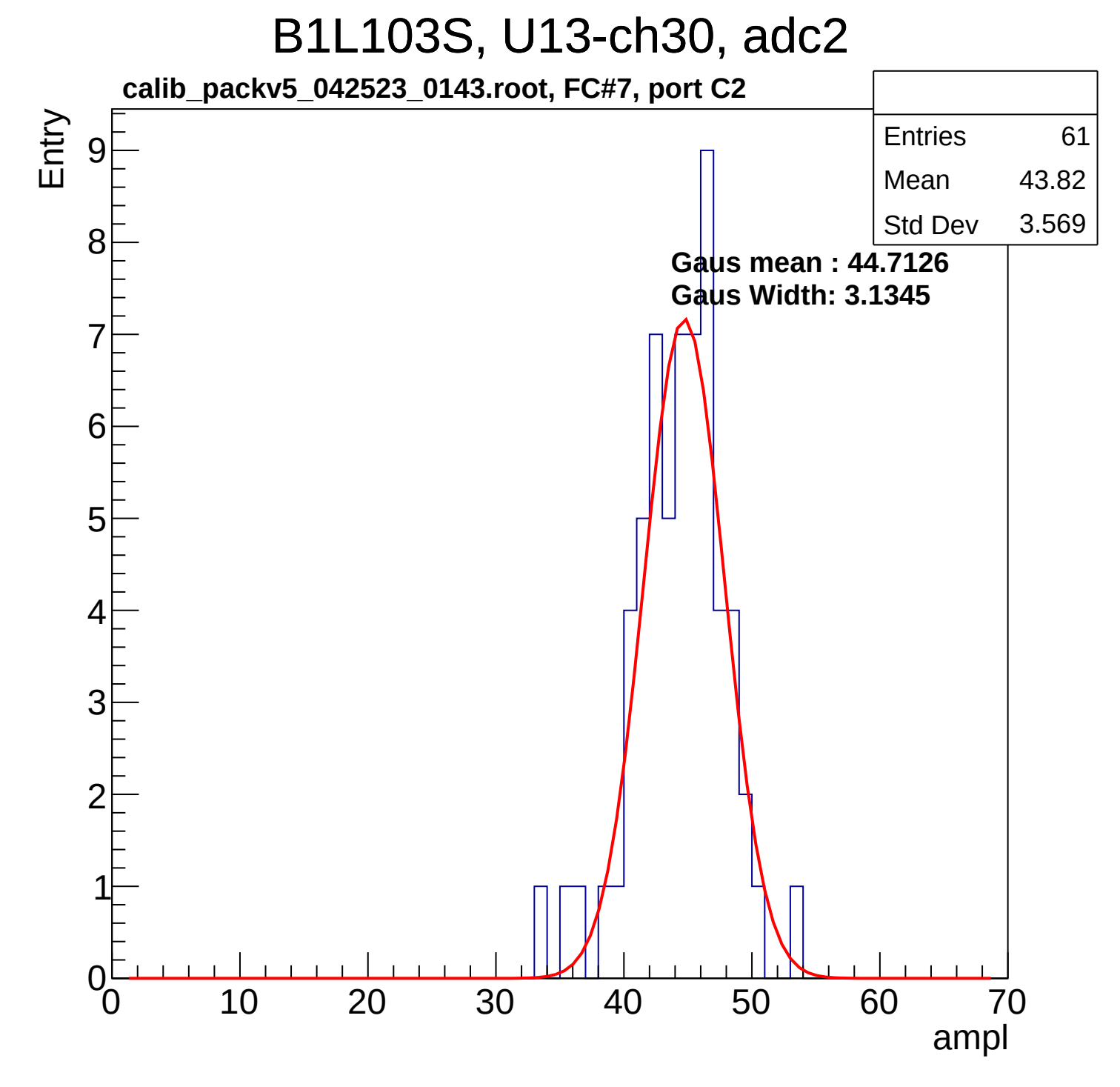
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch30, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.82
Std Dev 3.569
Gaus mean : 44.7126
Gaus Width: 3.1345
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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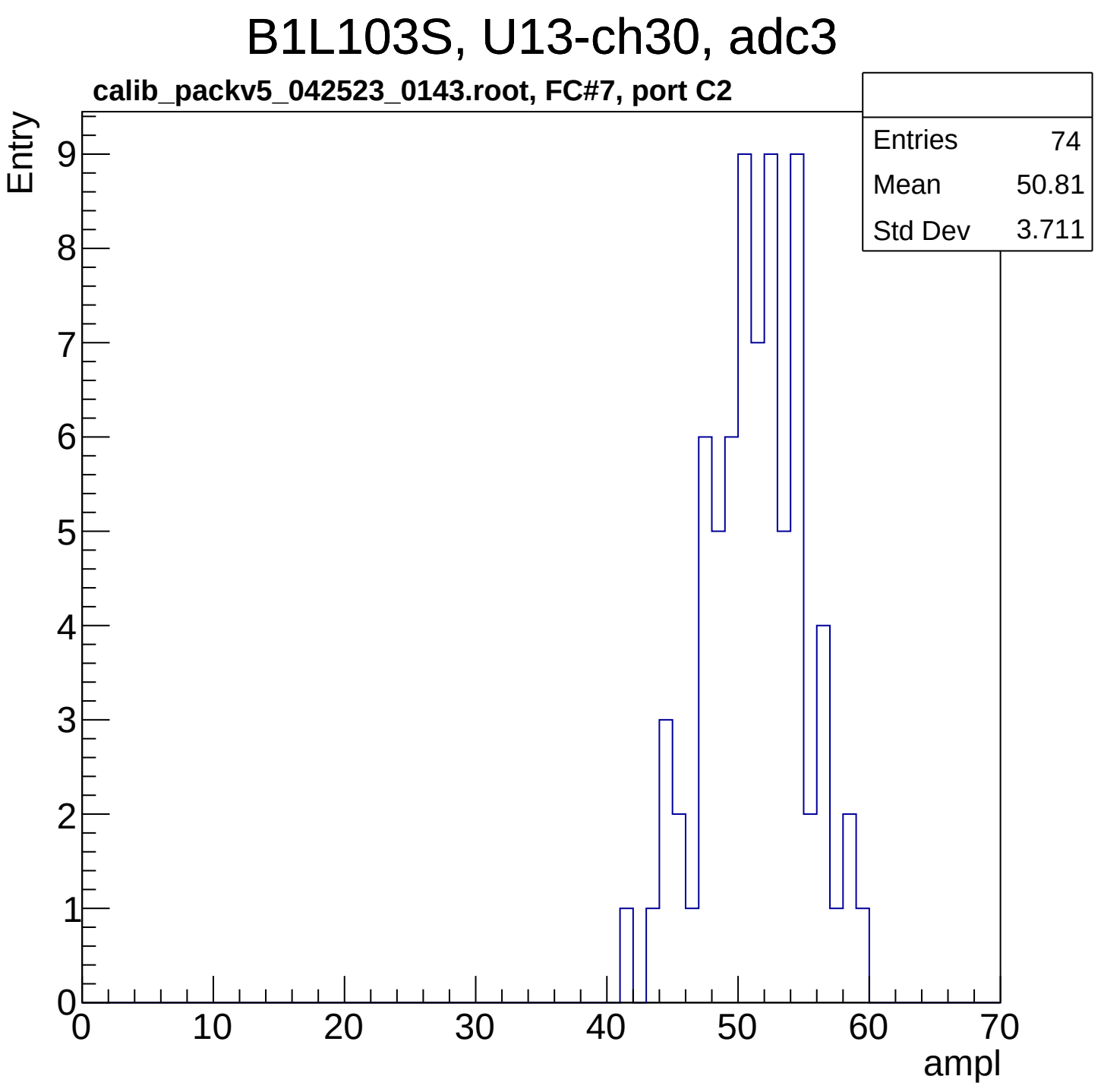
B1L103S, U13-ch30, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 50.81
Std Dev 3.711
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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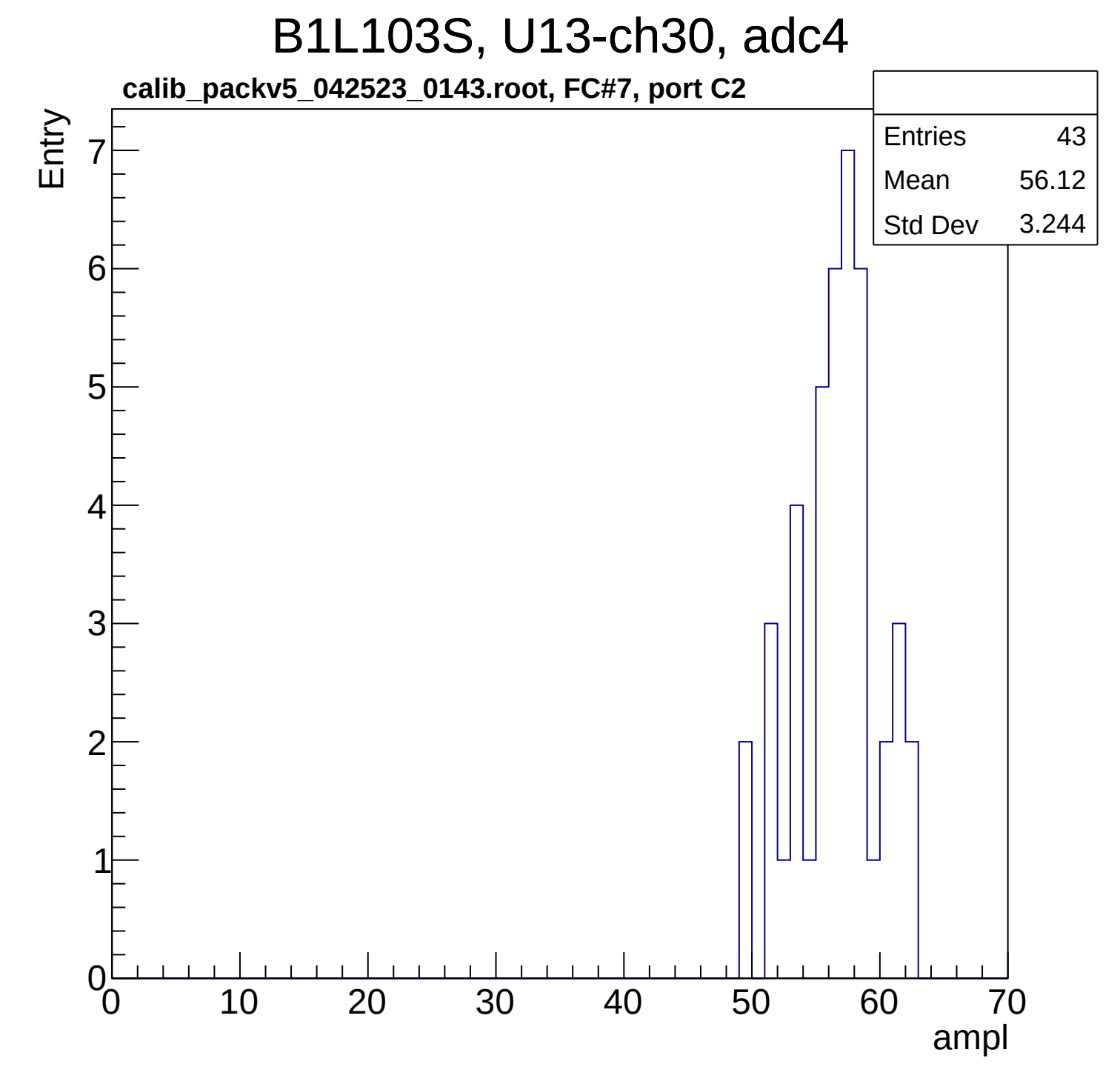
B1L103S, U13-ch30, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 56.12
Std Dev 3.244
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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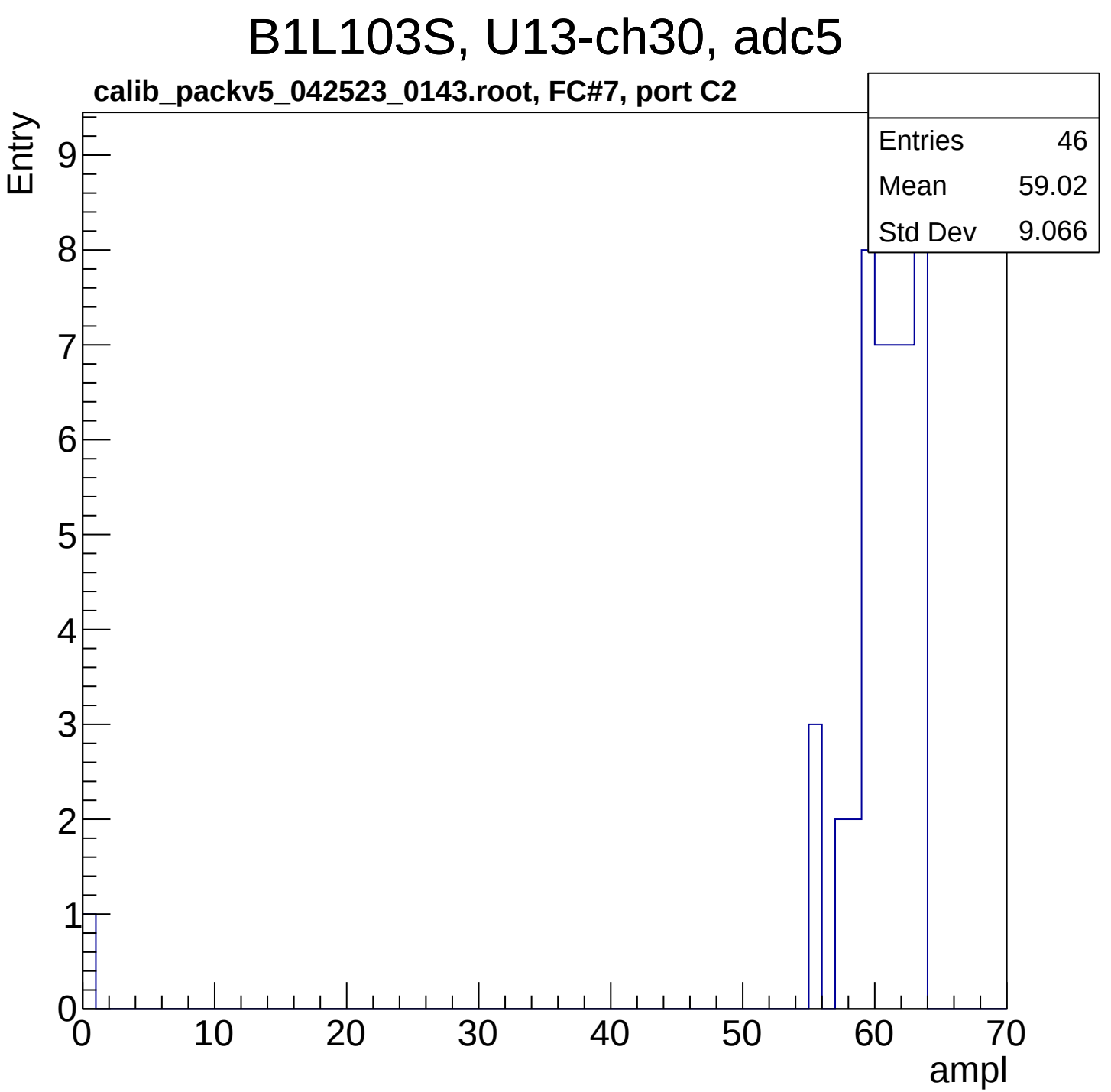
B1L103S, U13-ch30, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.02
Std Dev 9.066
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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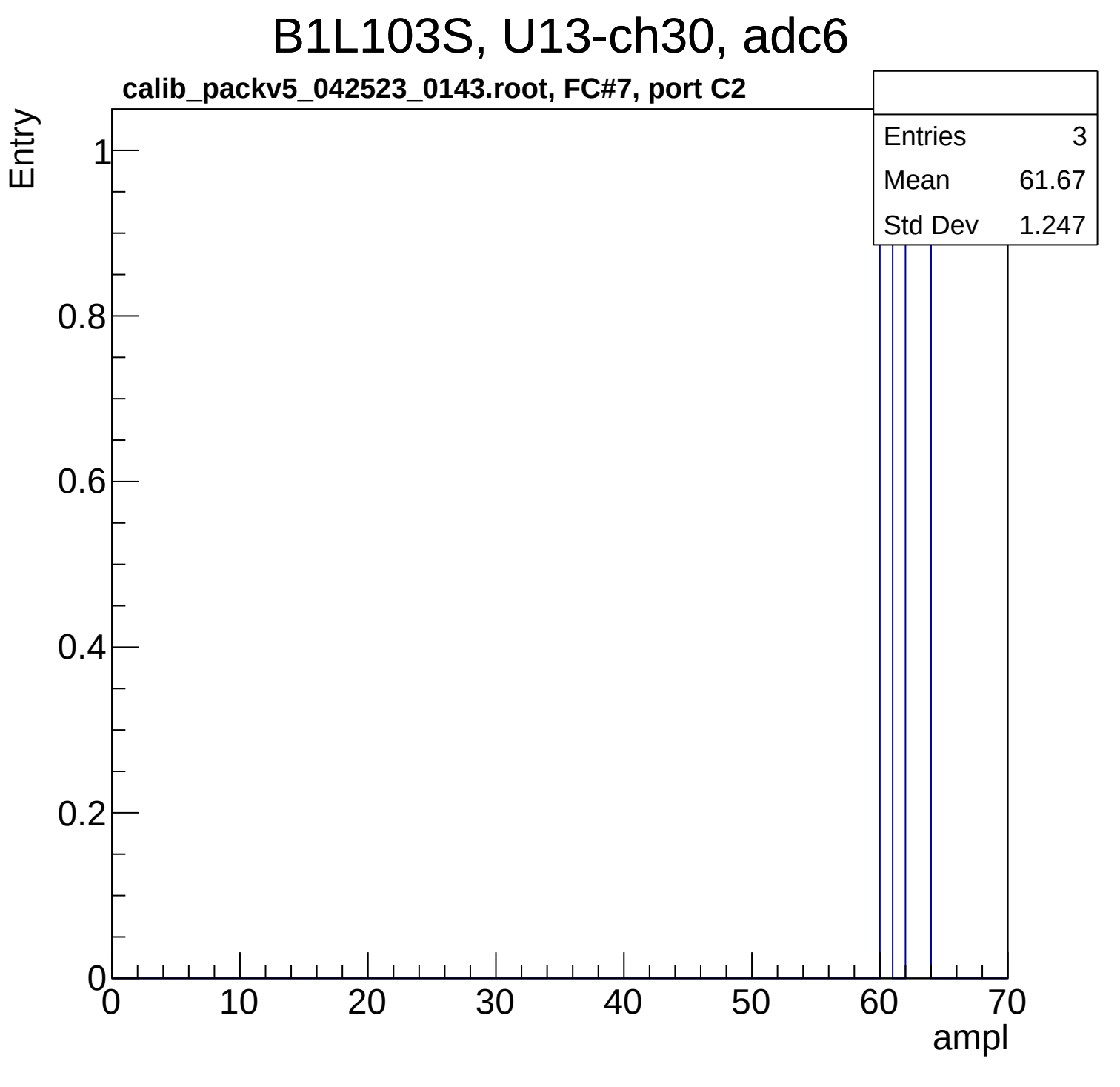
B1L103S, U13-ch30, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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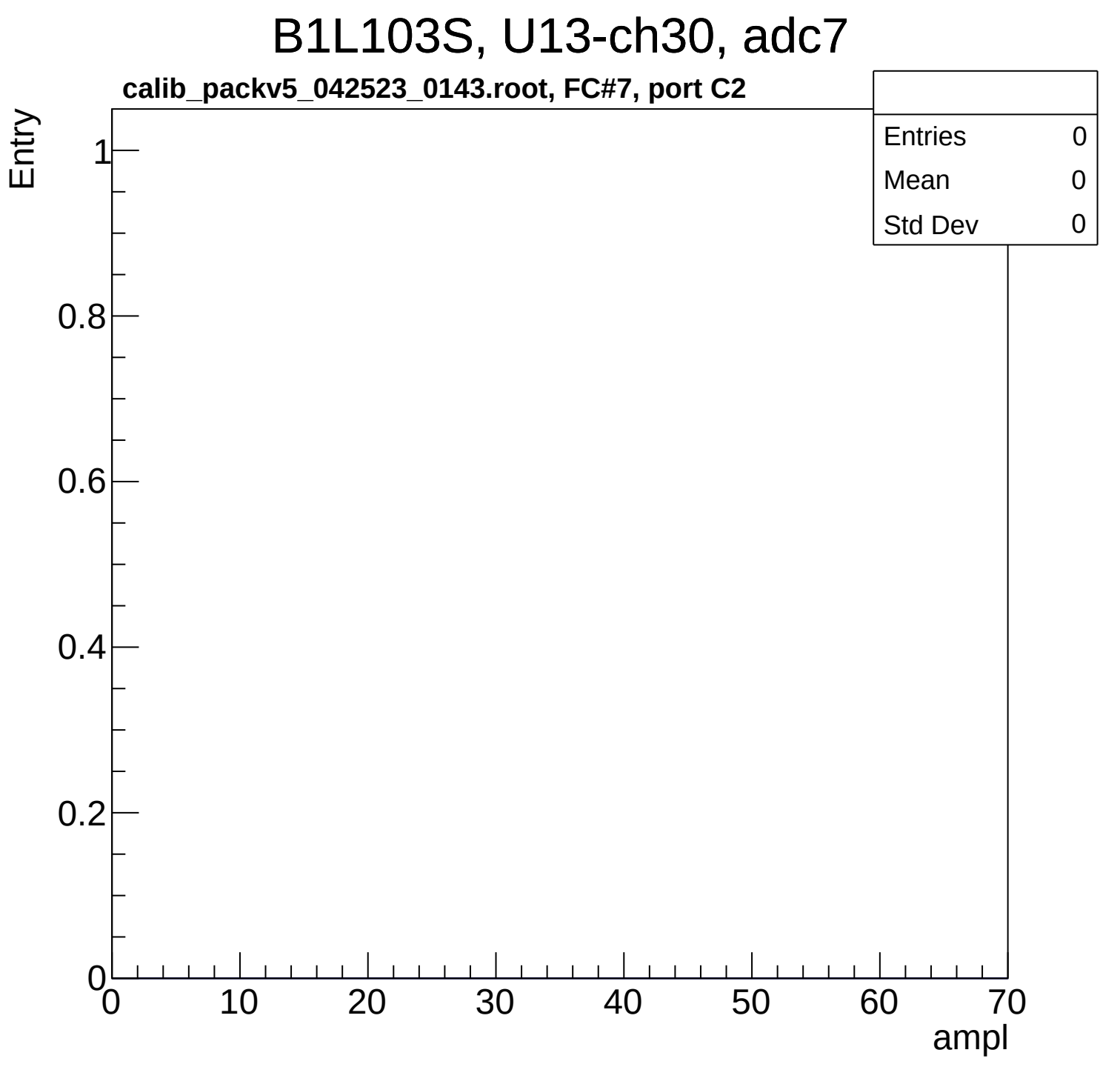
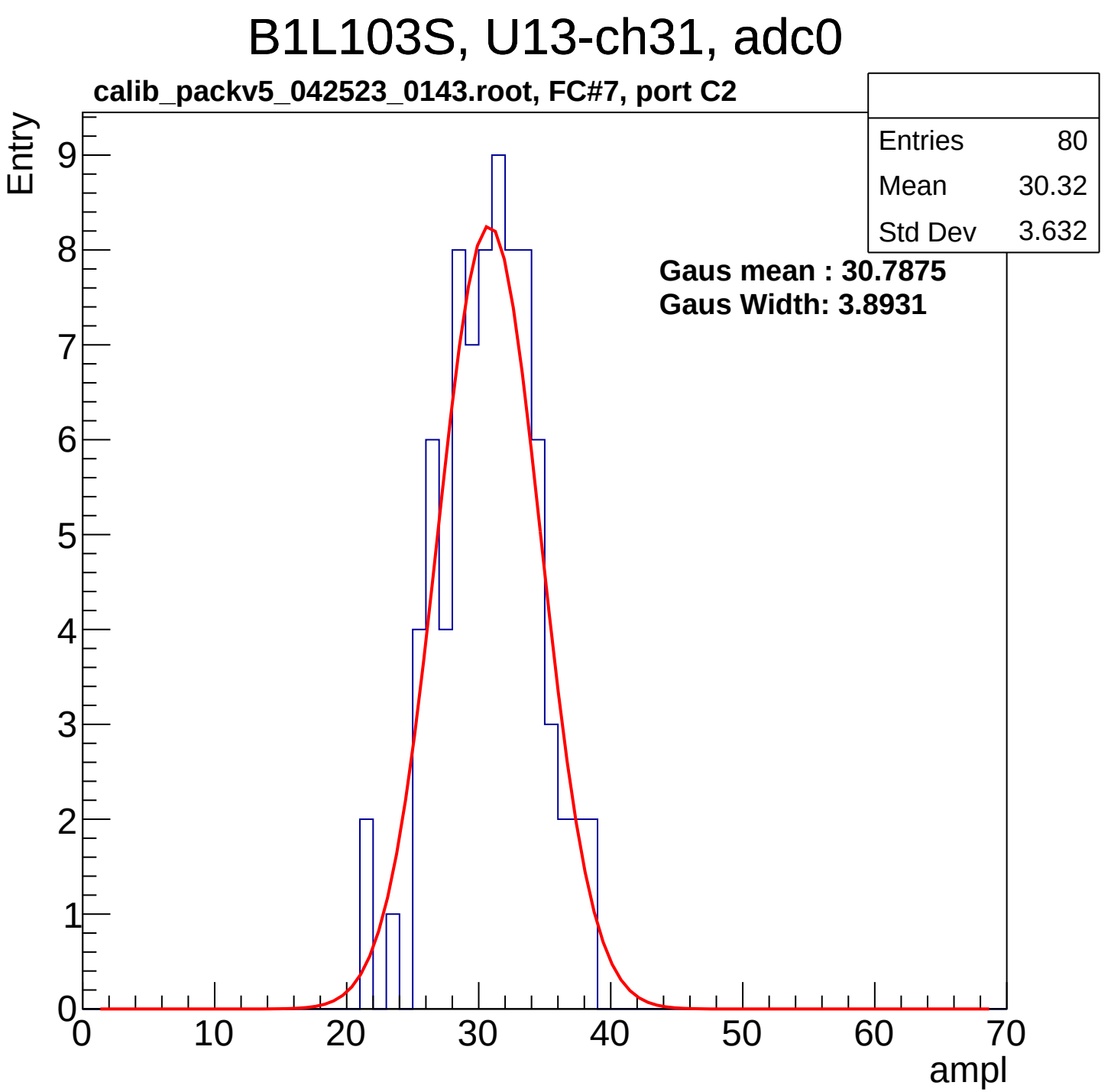
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch30, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch31, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 30.32
Std Dev 3.632
Gaus mean : 30.7875
Gaus Width: 3.8931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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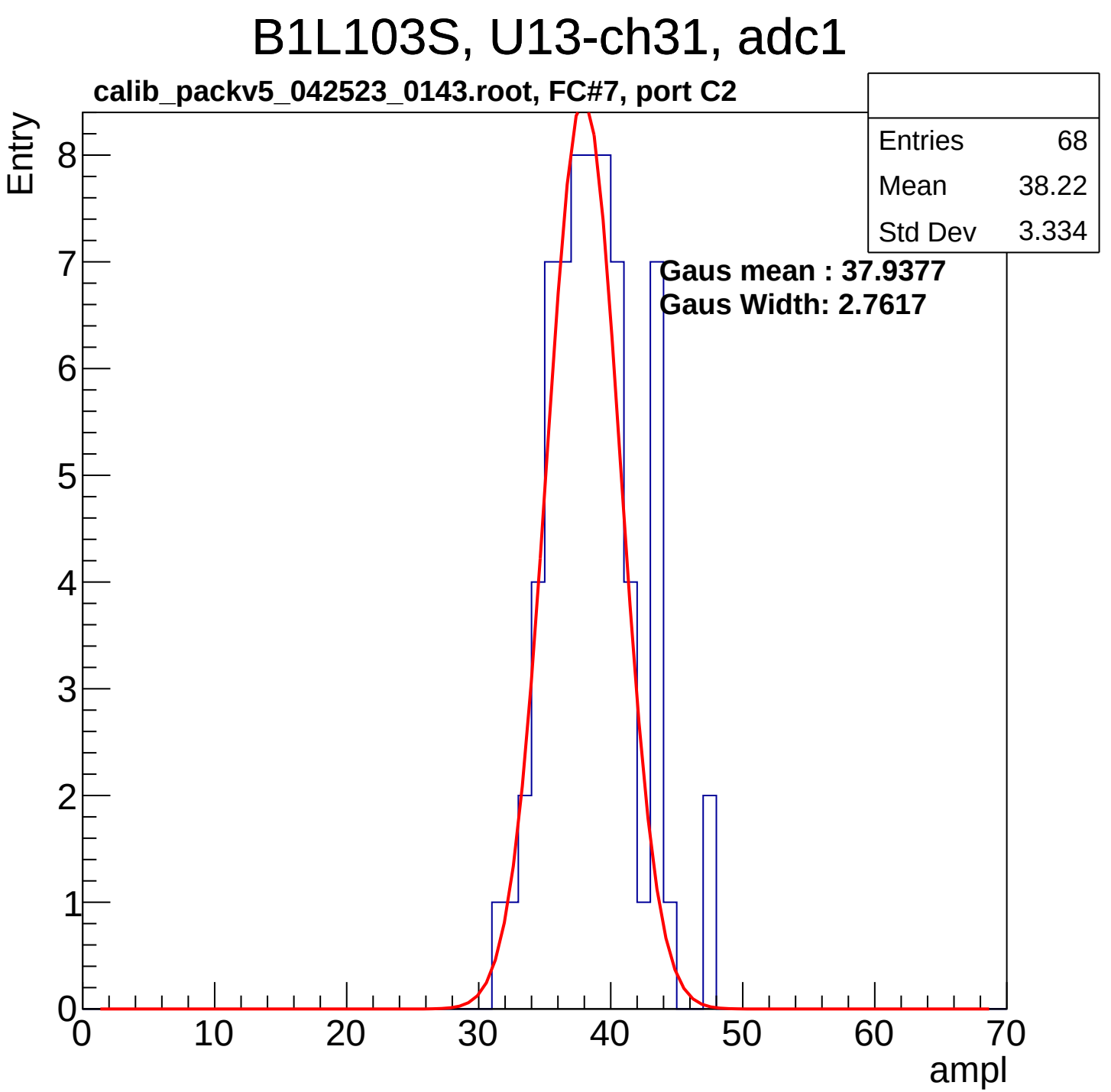
B1L103S, U13-ch31, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 38.22
Std Dev 3.334
Gaus mean : 37.9377
Gaus Width: 2.7617
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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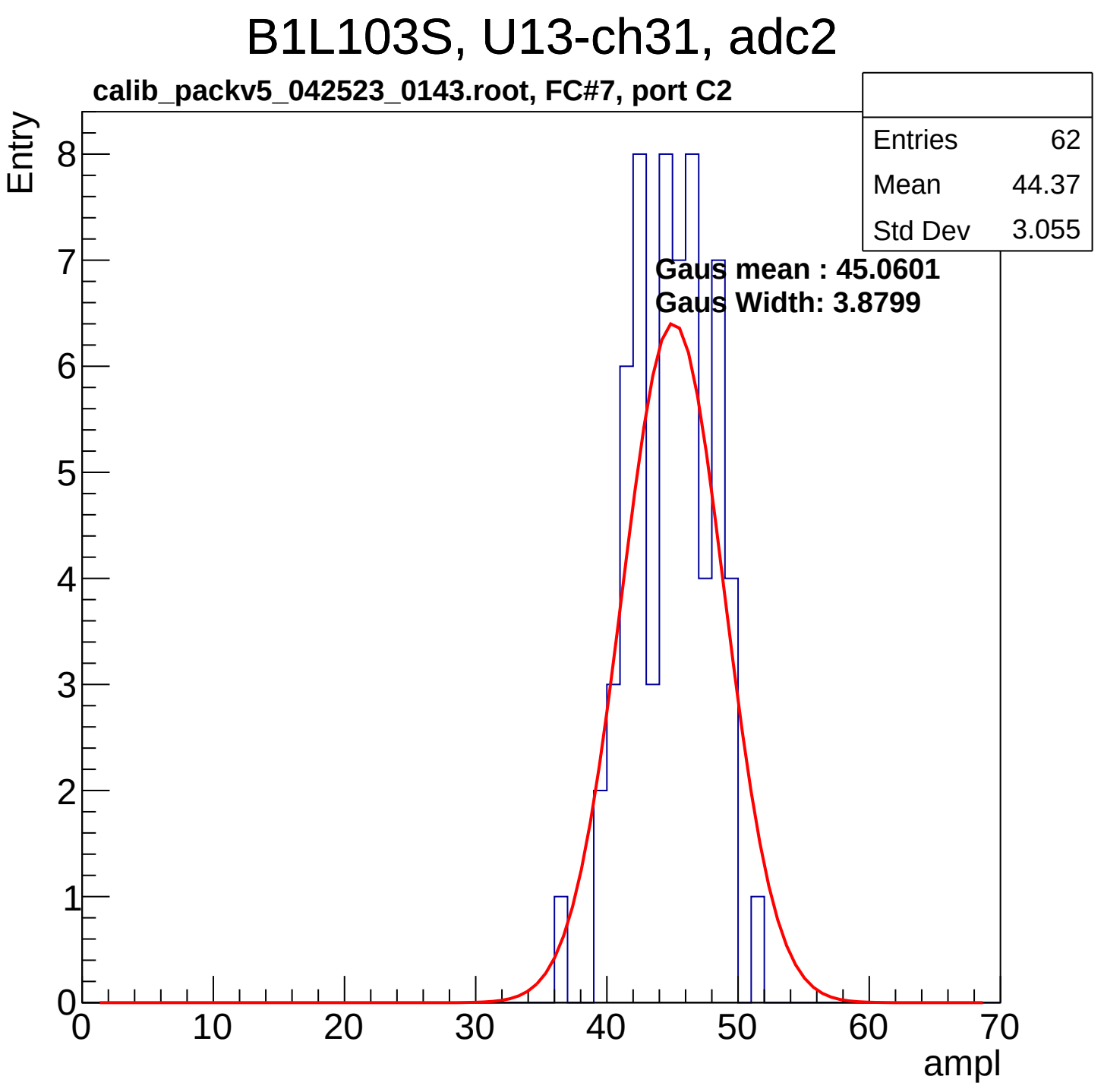
B1L103S, U13-ch31, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 44.37
Std Dev 3.055
Gaus mean : 45.0601
Gaus Width: 3.8799
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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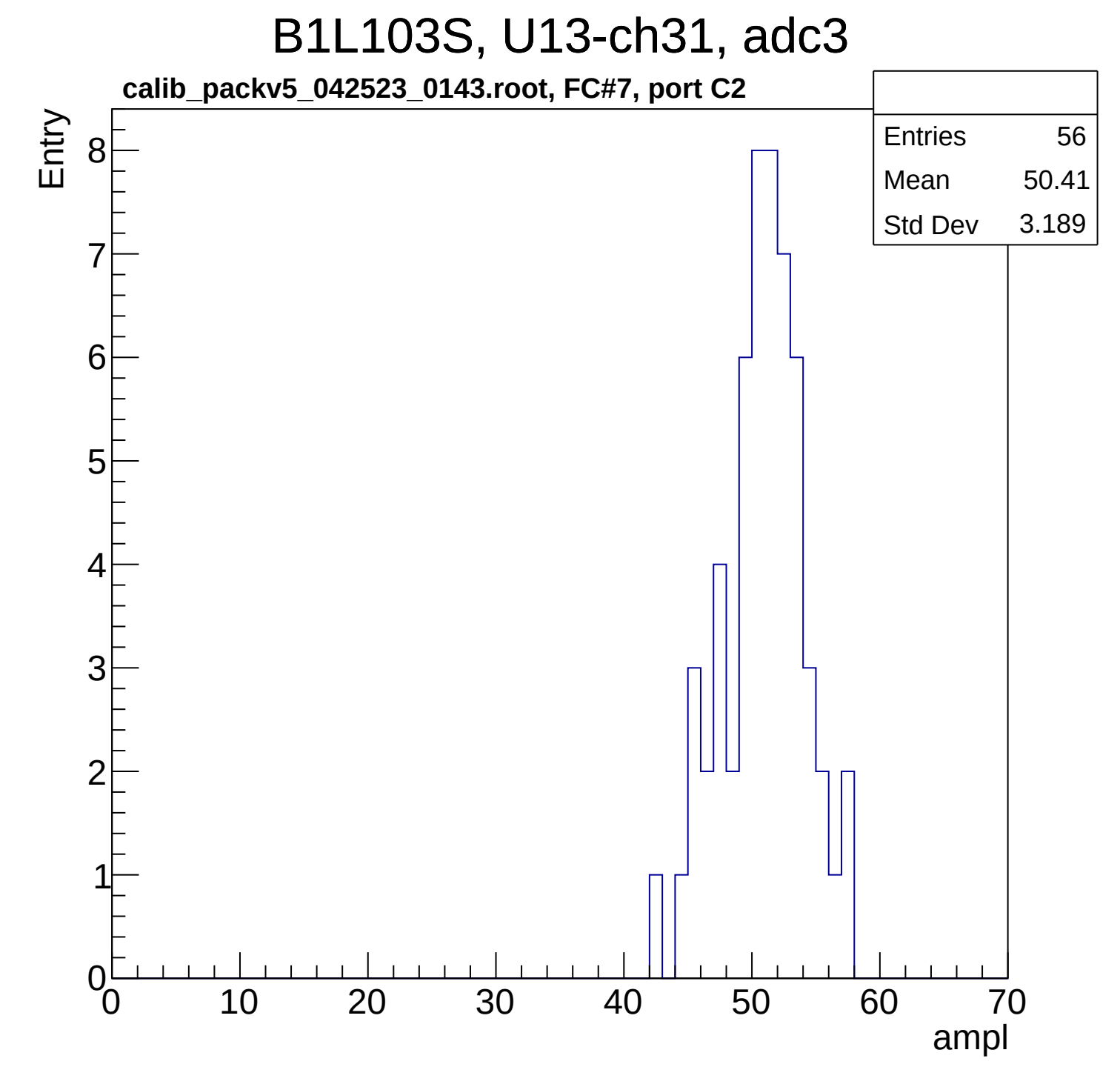
B1L103S, U13-ch31, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 50.41
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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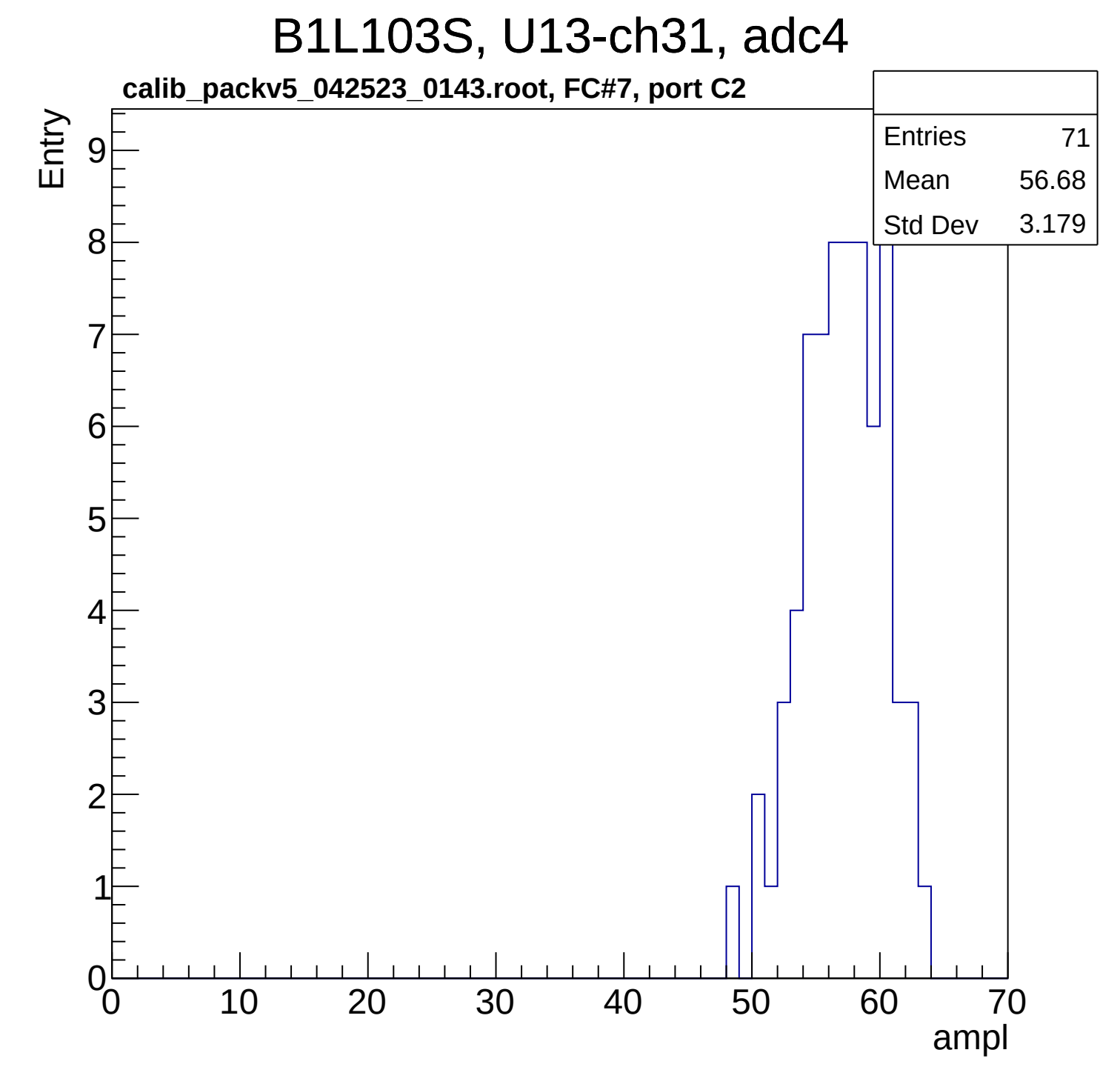
B1L103S, U13-ch31, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 56.68
Std Dev 3.179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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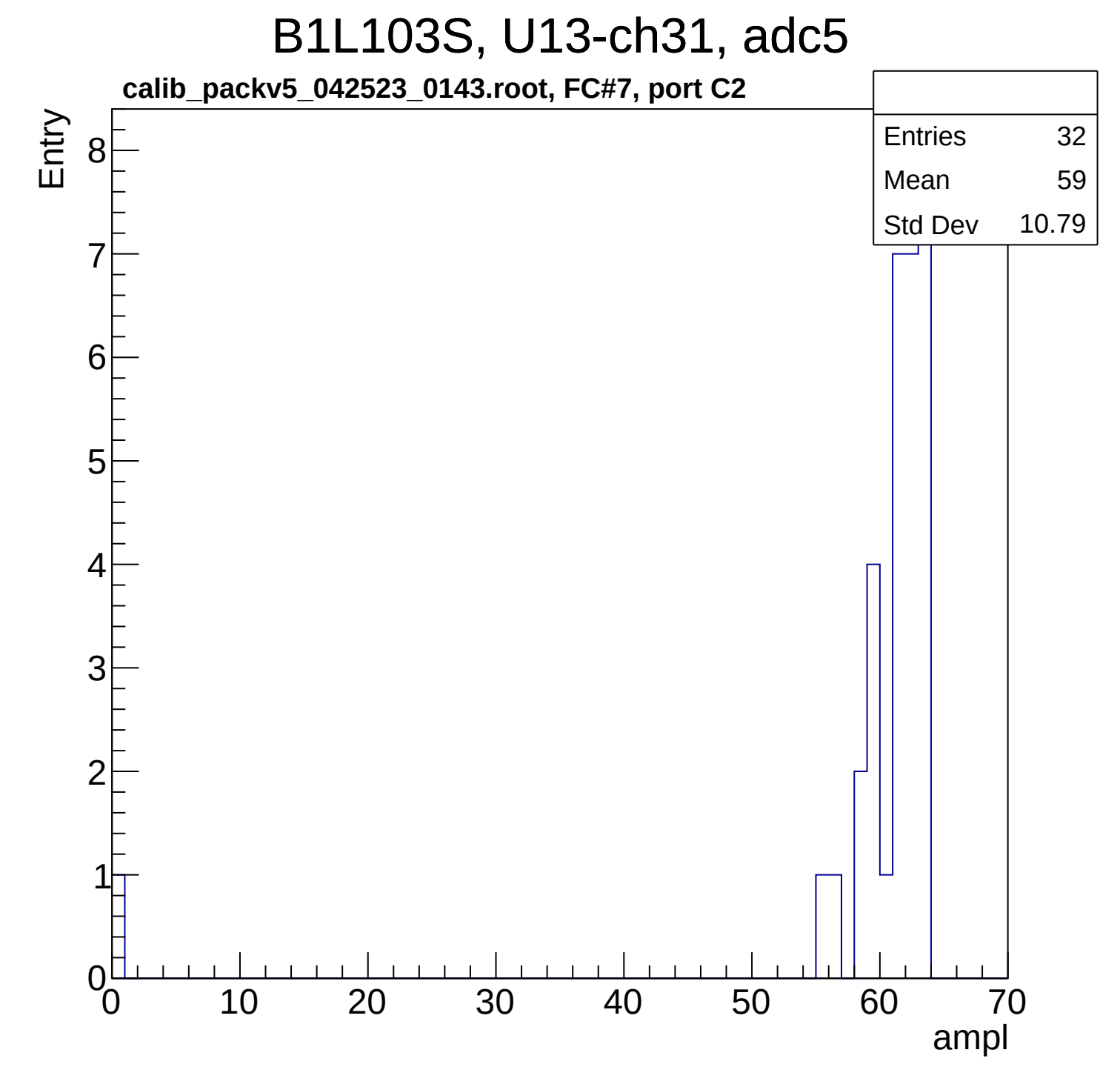
B1L103S, U13-ch31, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 59
Std Dev 10.79
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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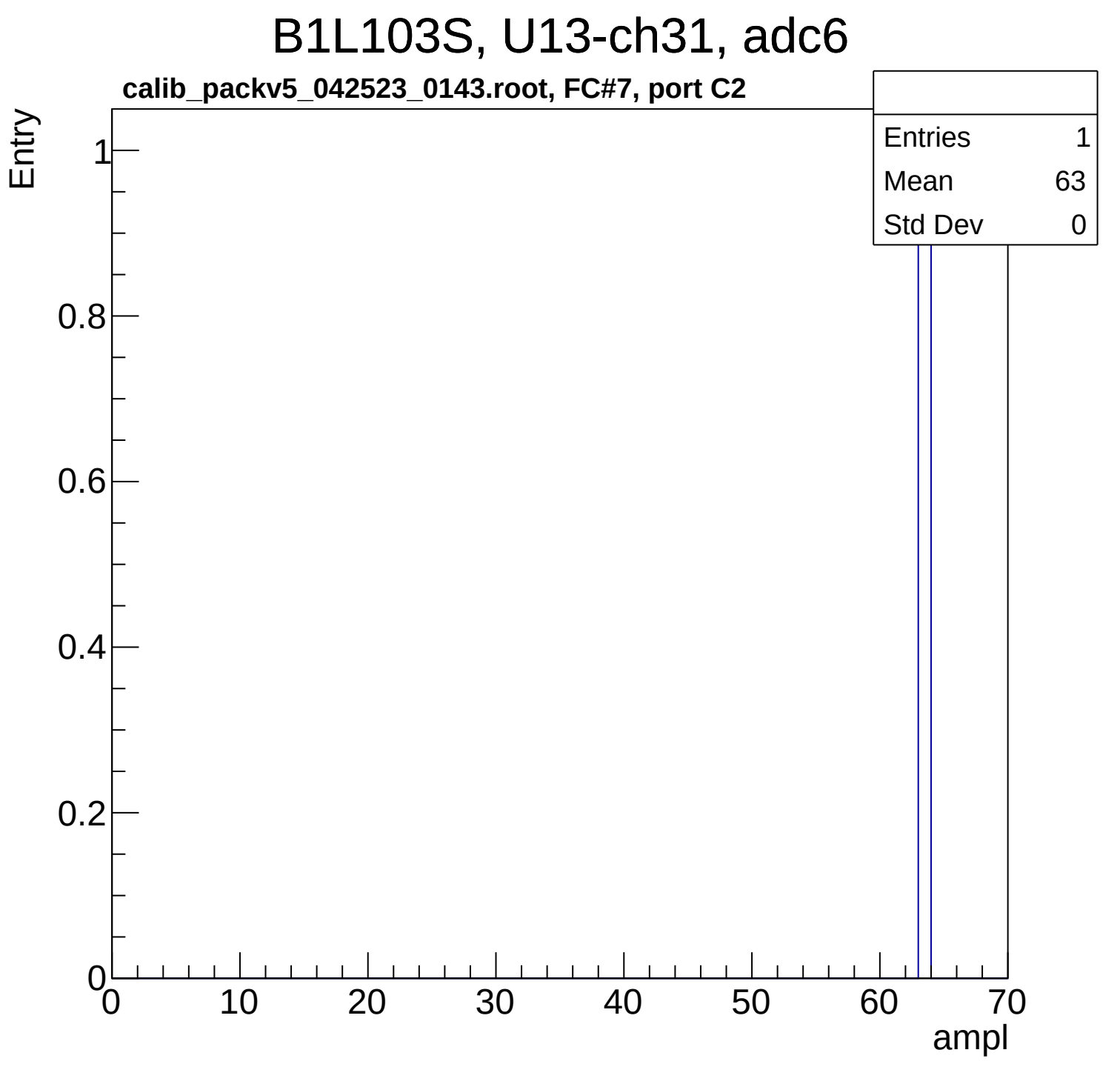
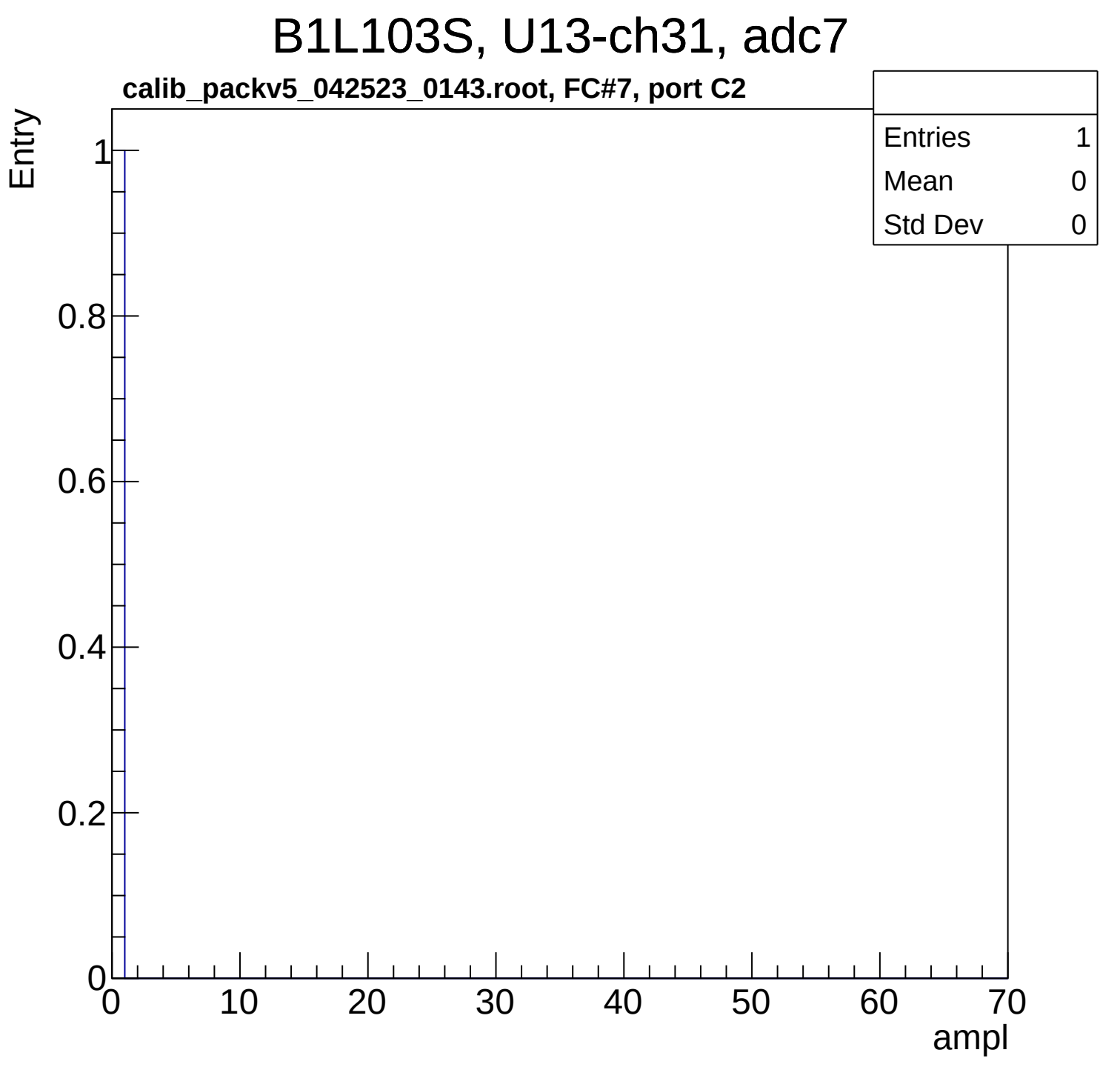

B1L103S, U13-ch31, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch31, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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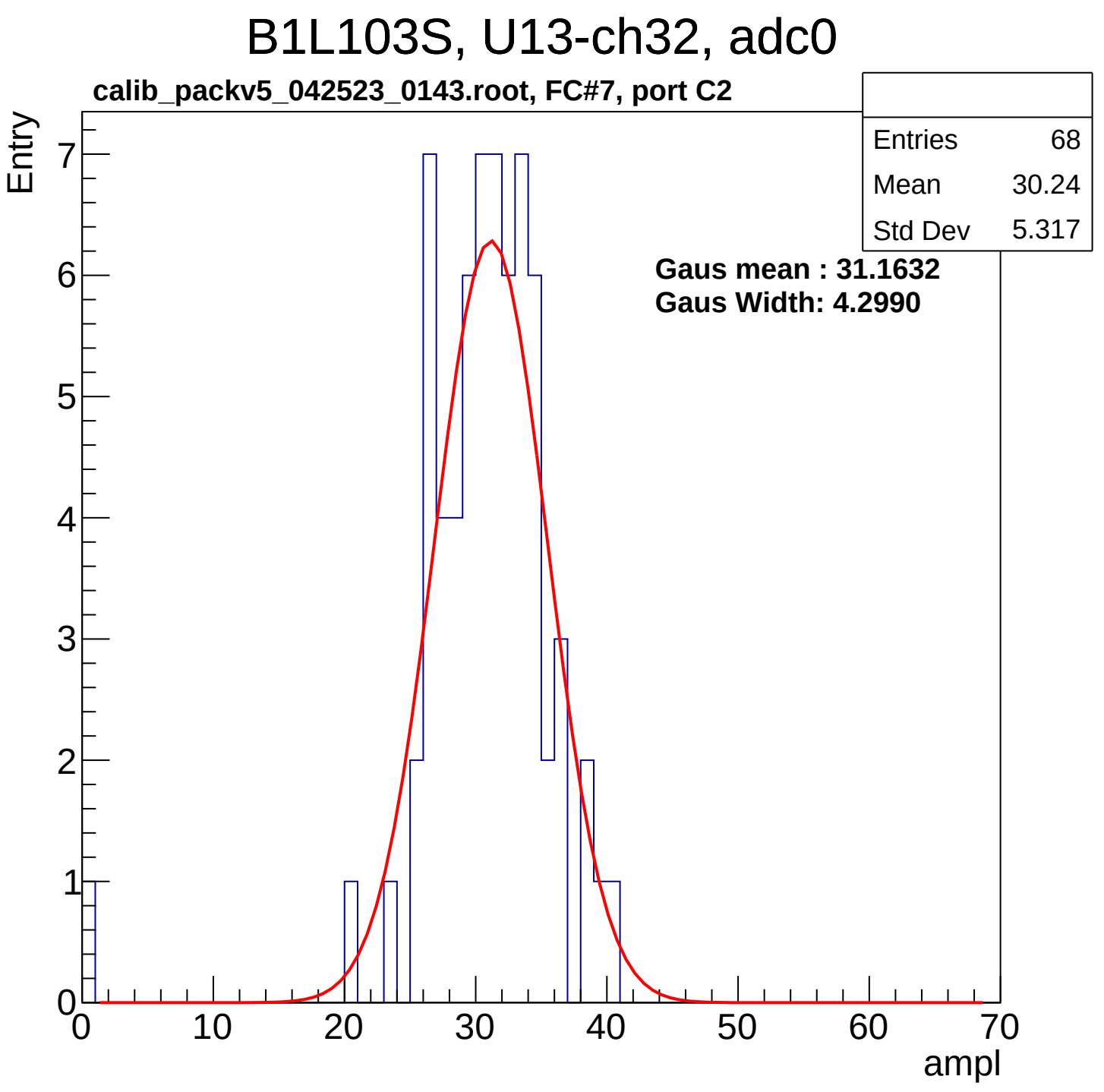
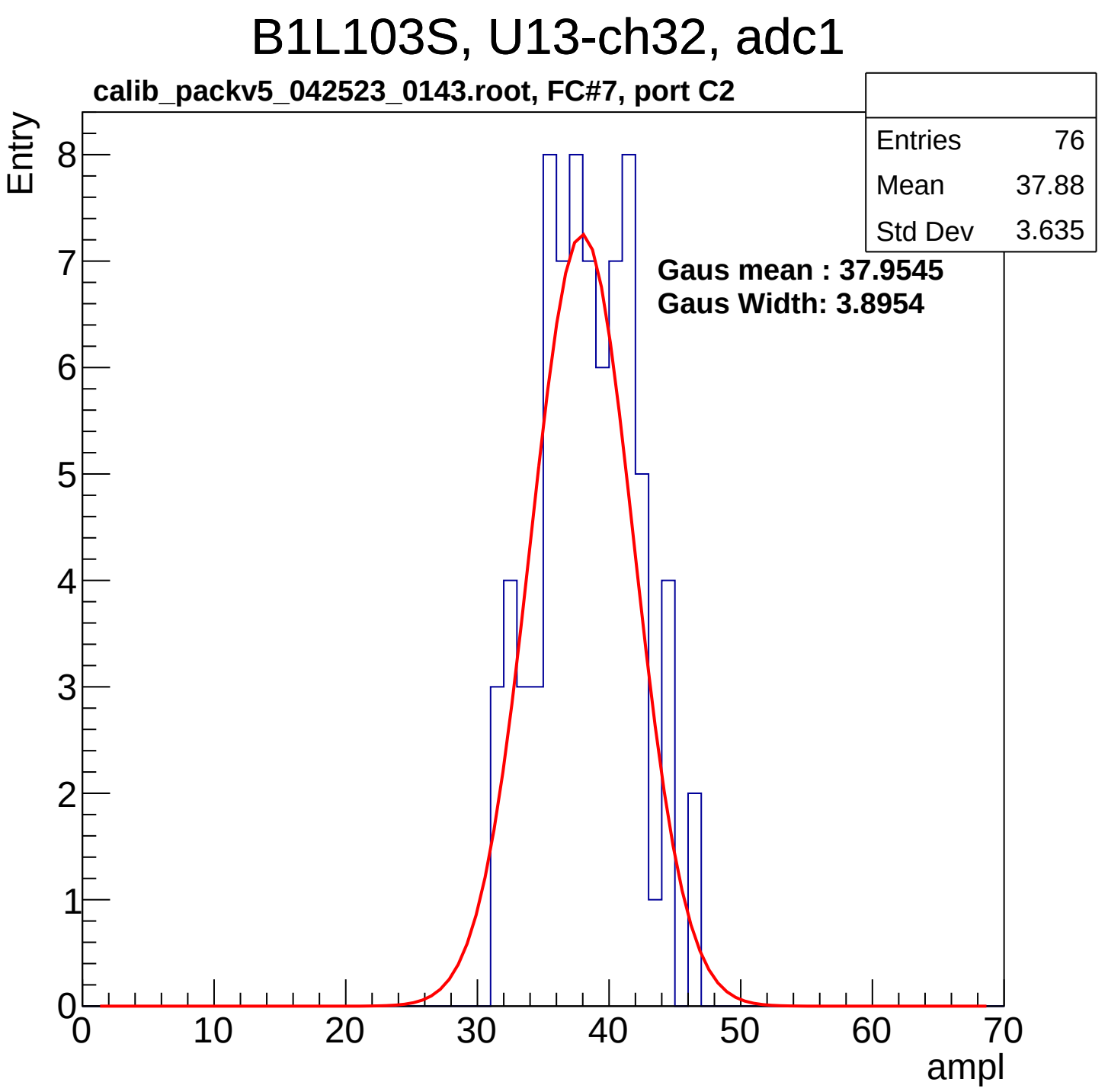
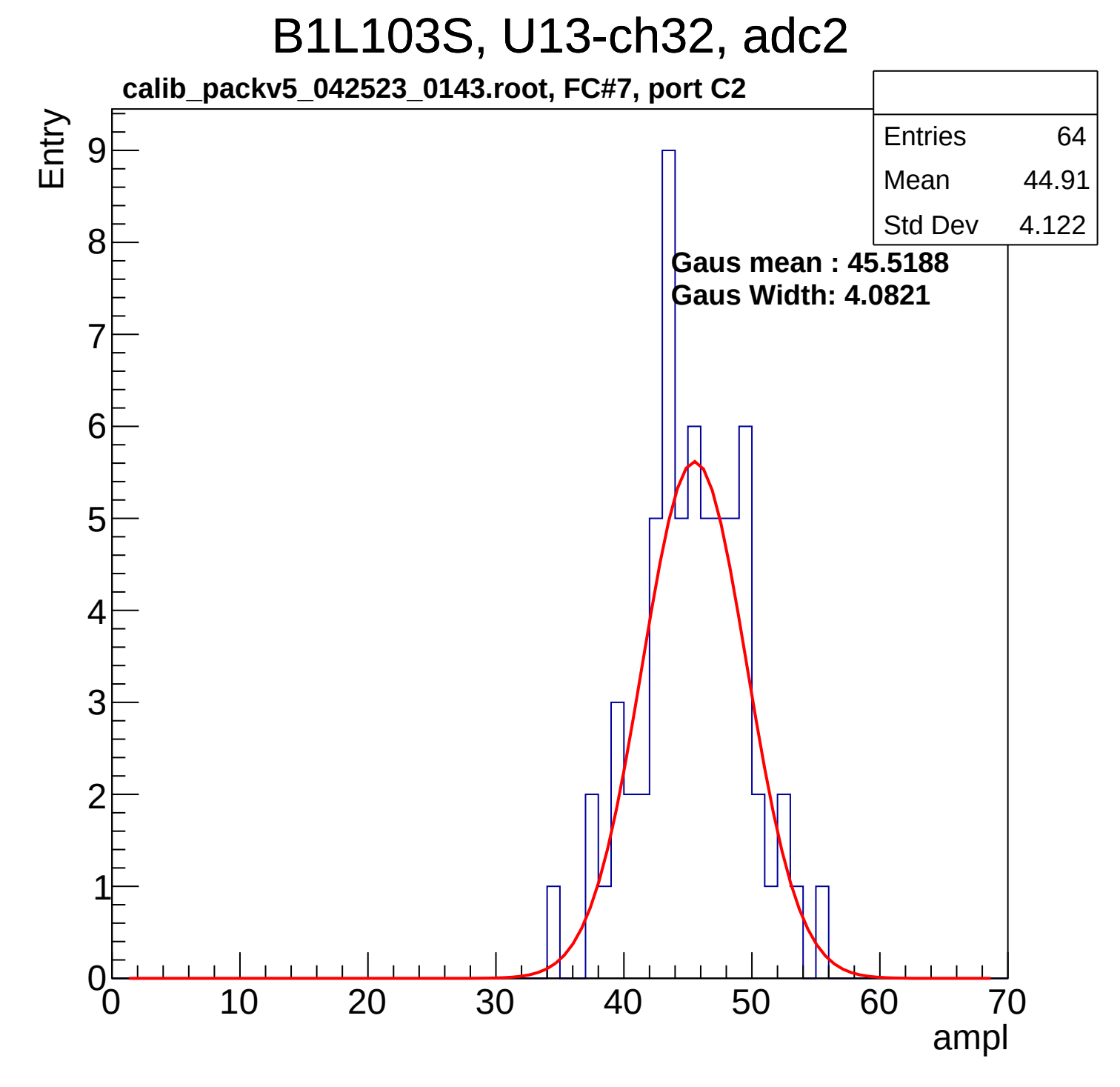
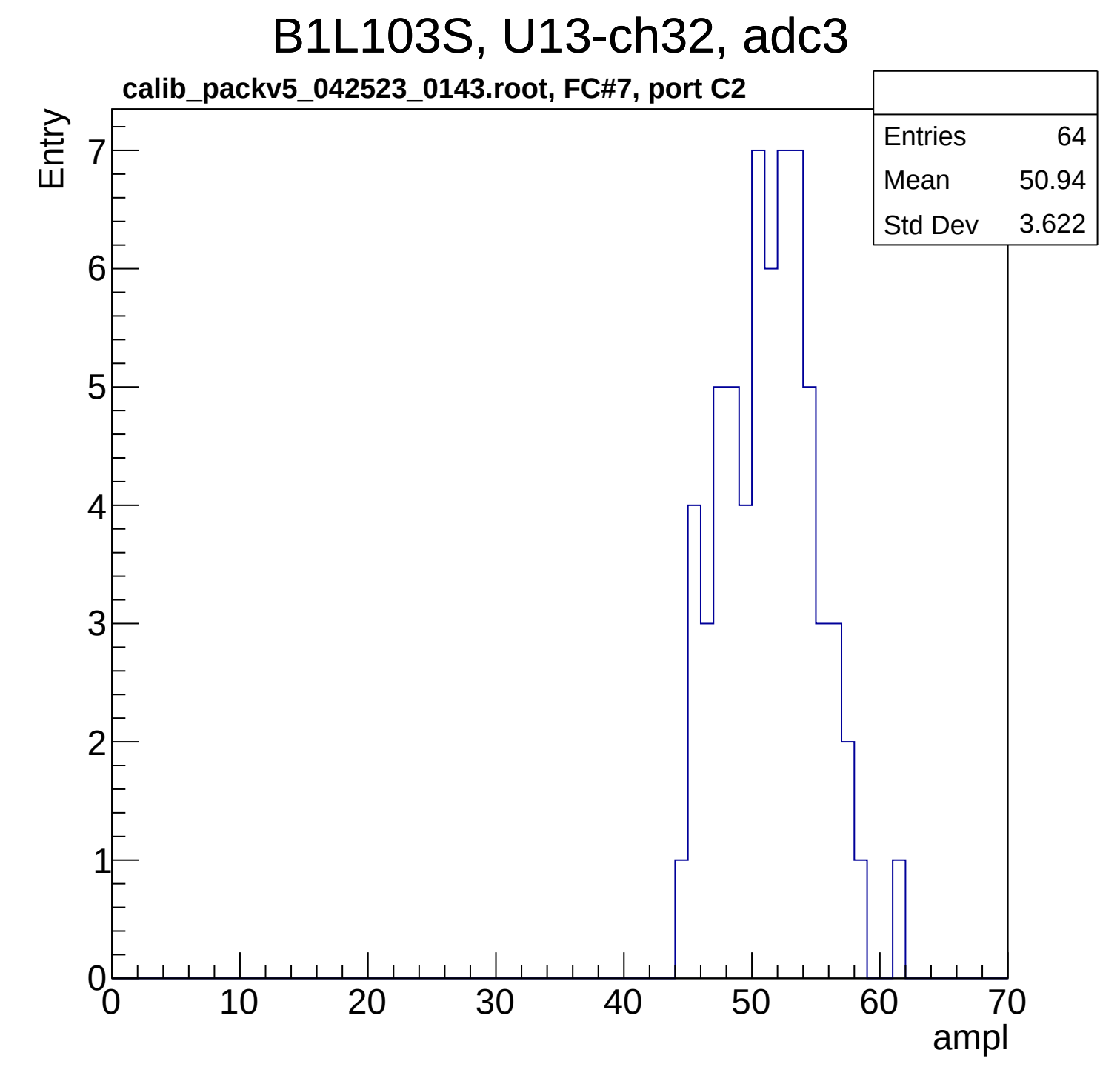
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch32, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.24
Std Dev 5.317
Gaus mean : 31.1632
Gaus Width: 4.2990
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch32, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 37.88
Std Dev 3.635
Gaus mean : 37.9545
Gaus Width: 3.8954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch32, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.91
Std Dev 4.122
Gaus mean : 45.5188
Gaus Width: 4.0821
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch32, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 50.94
Std Dev 3.622
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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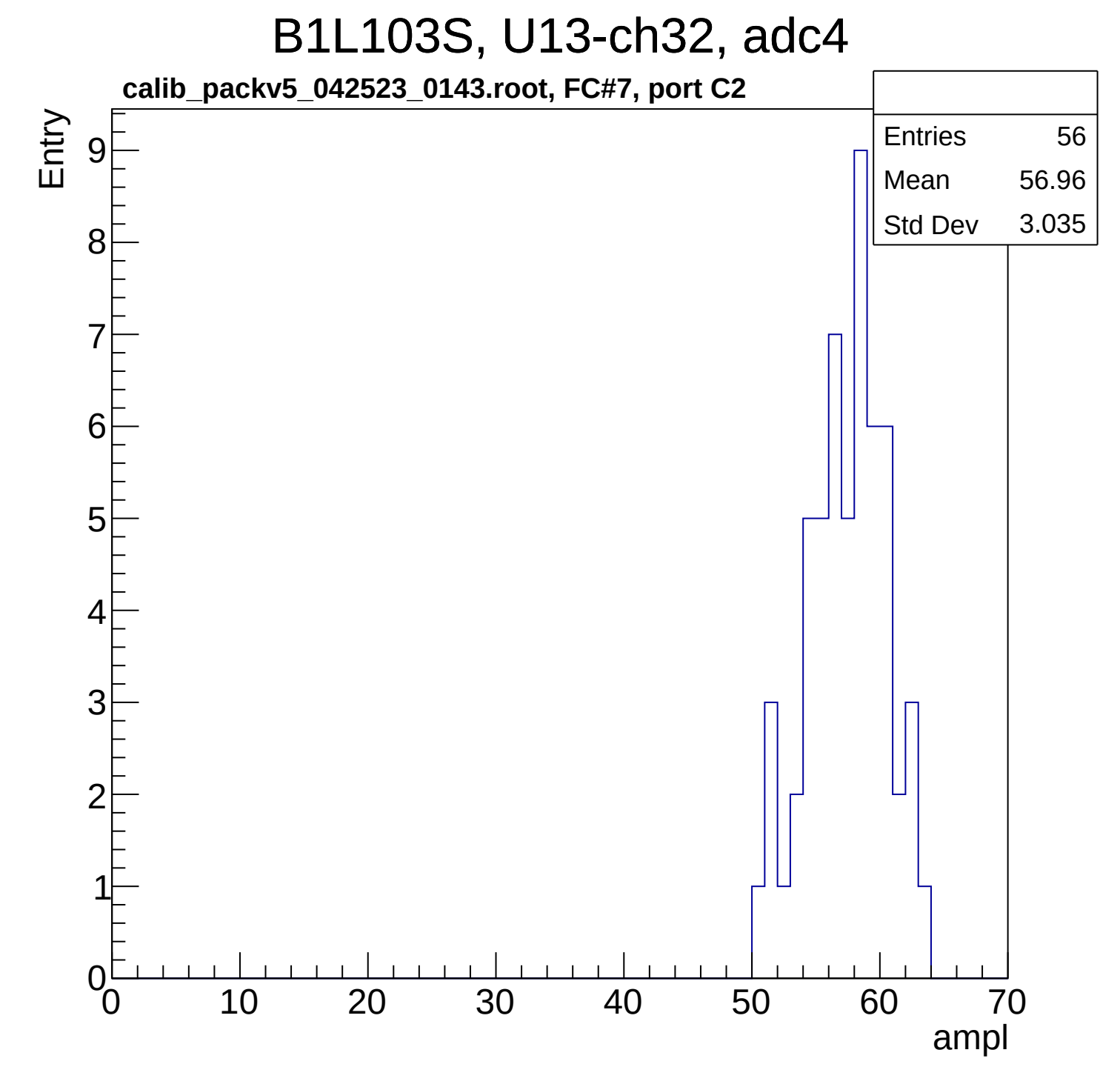
B1L103S, U13-ch32, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 56.96
Std Dev 3.035
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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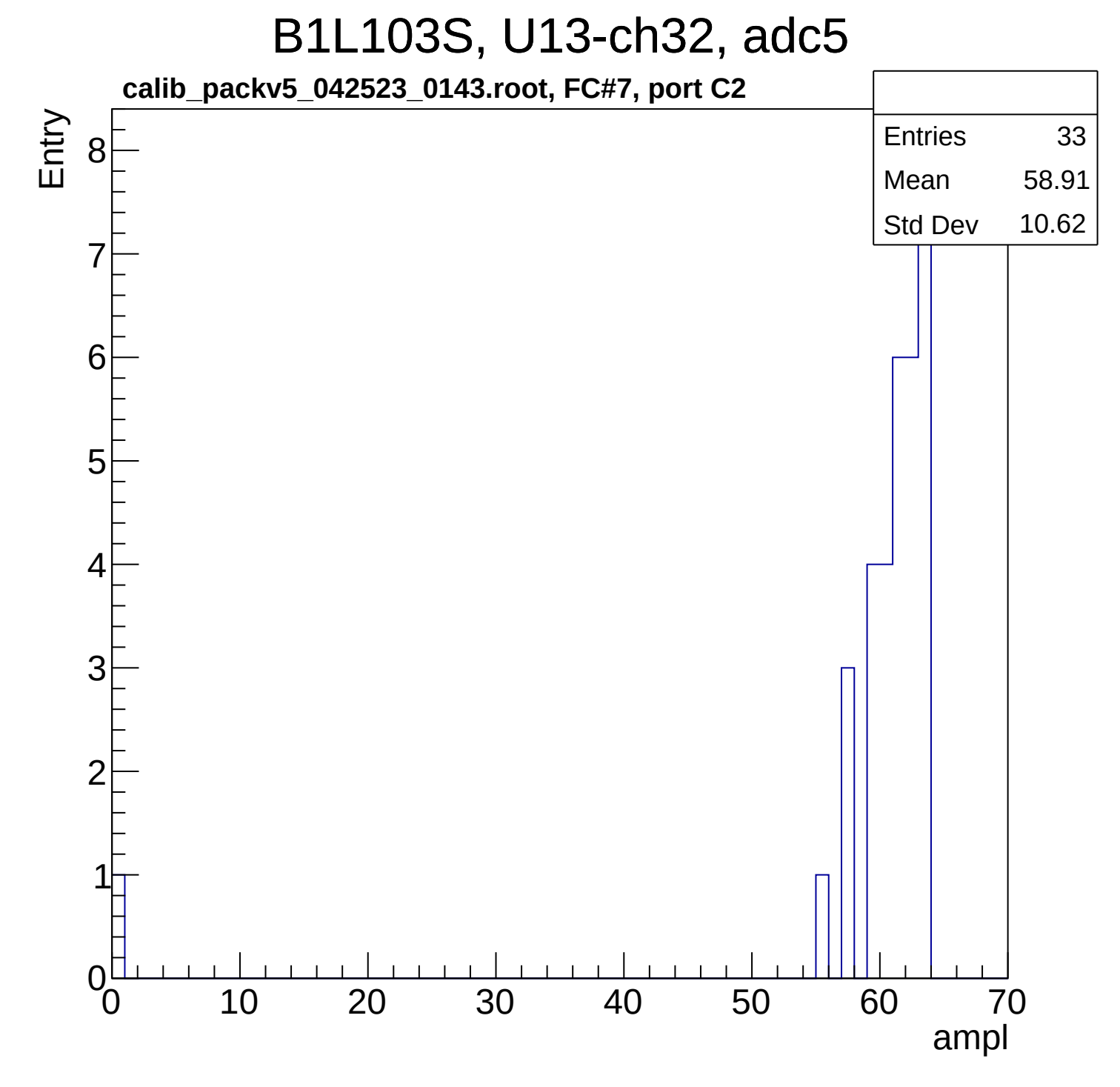
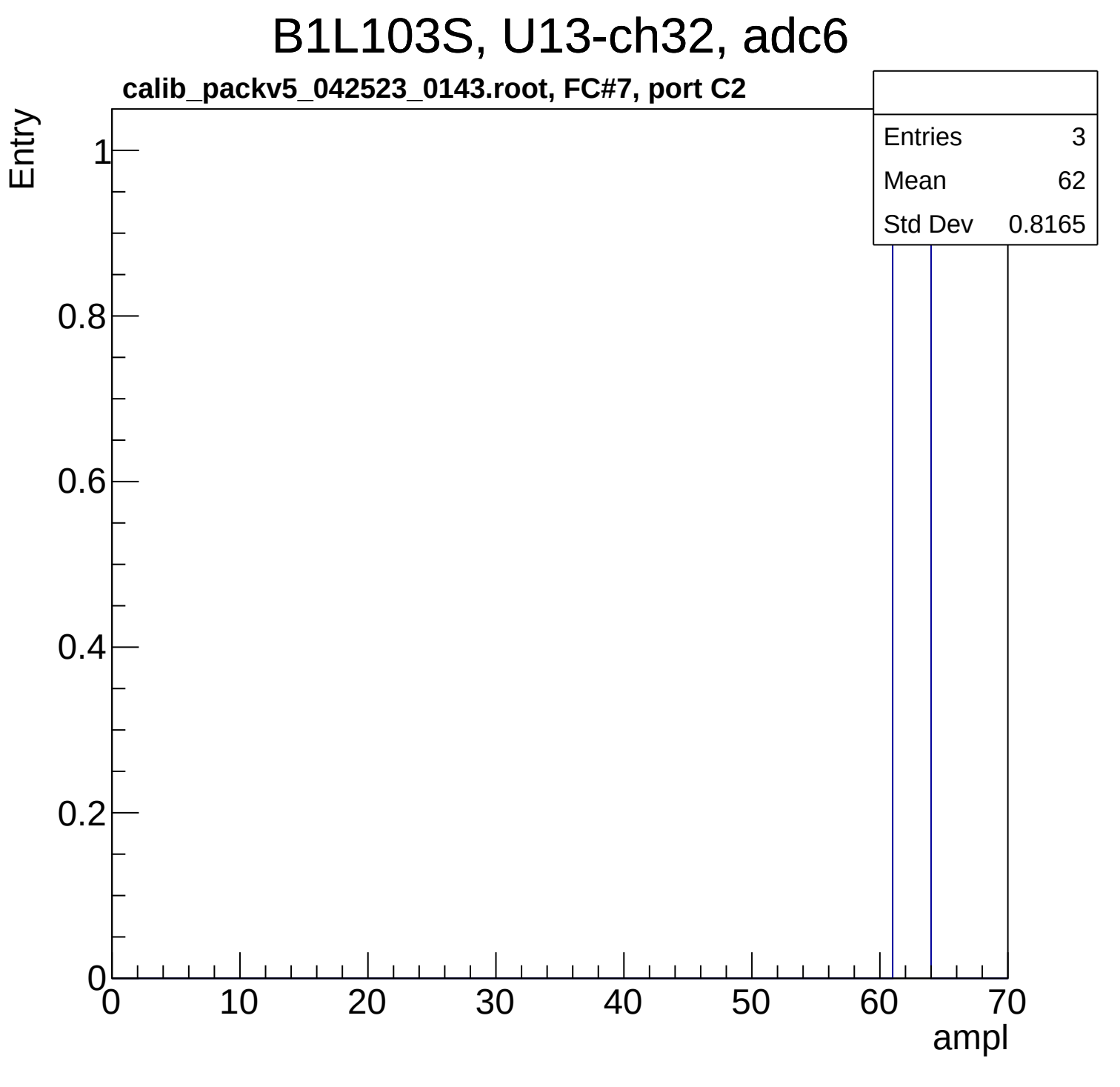
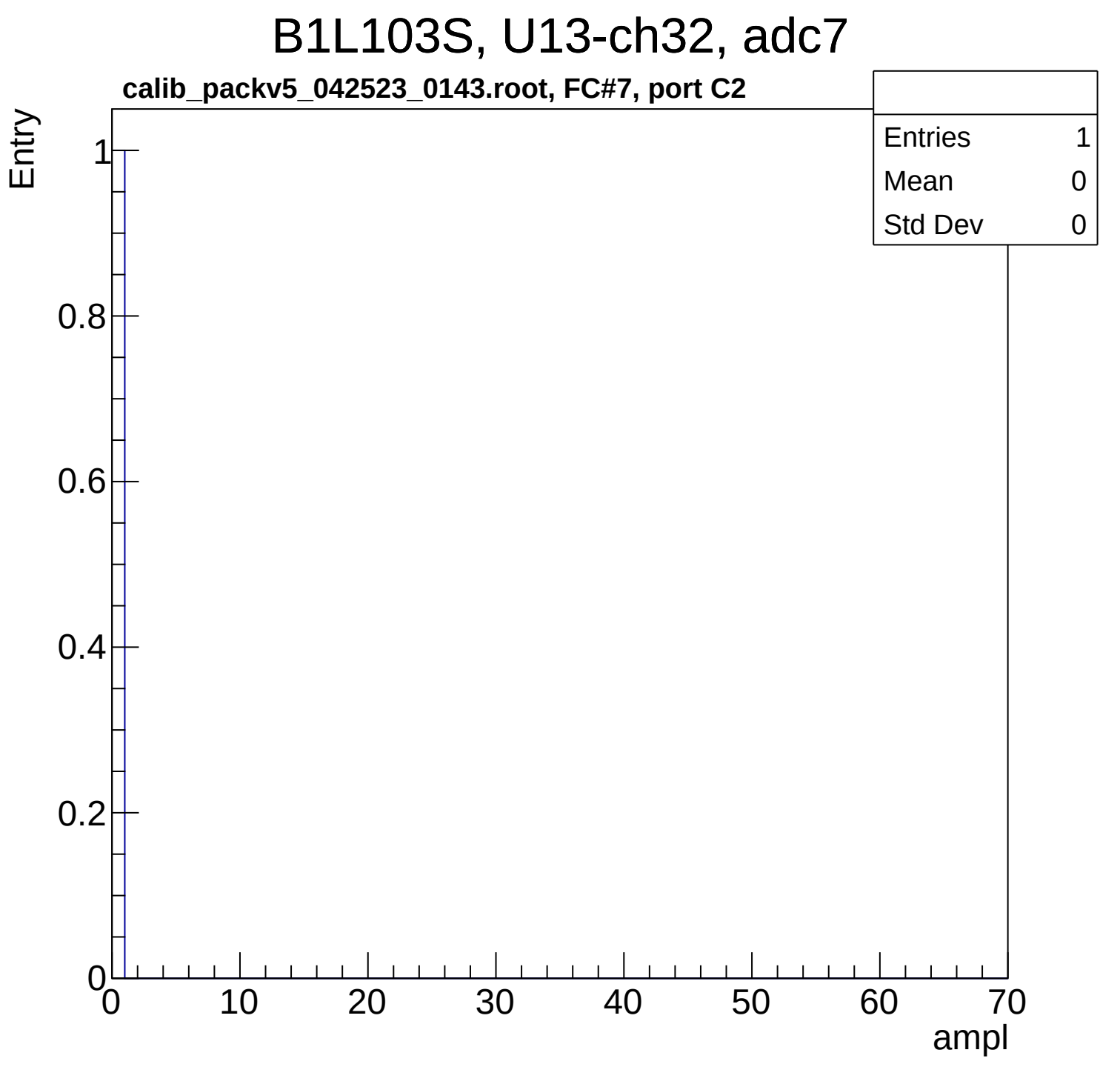
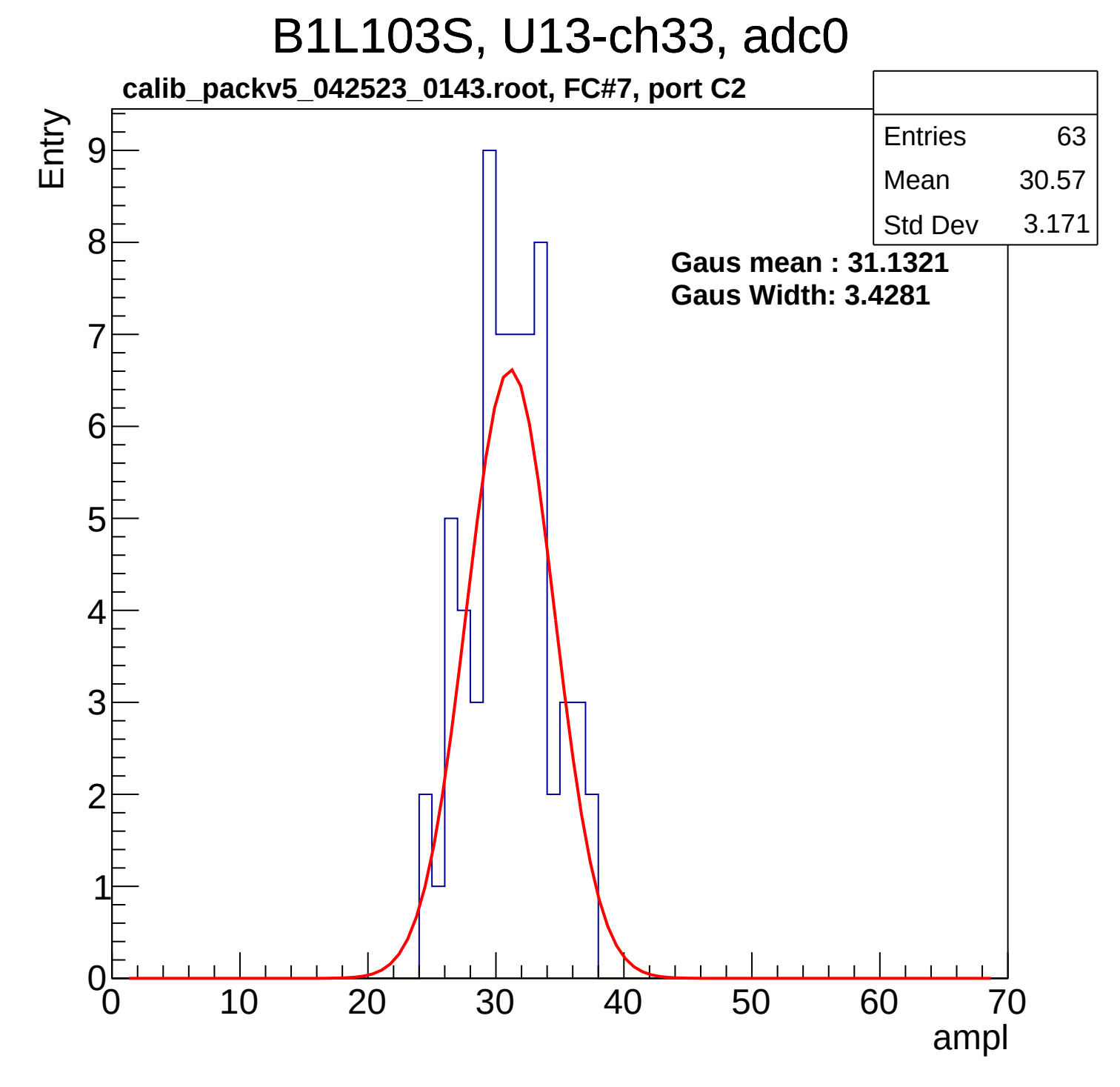
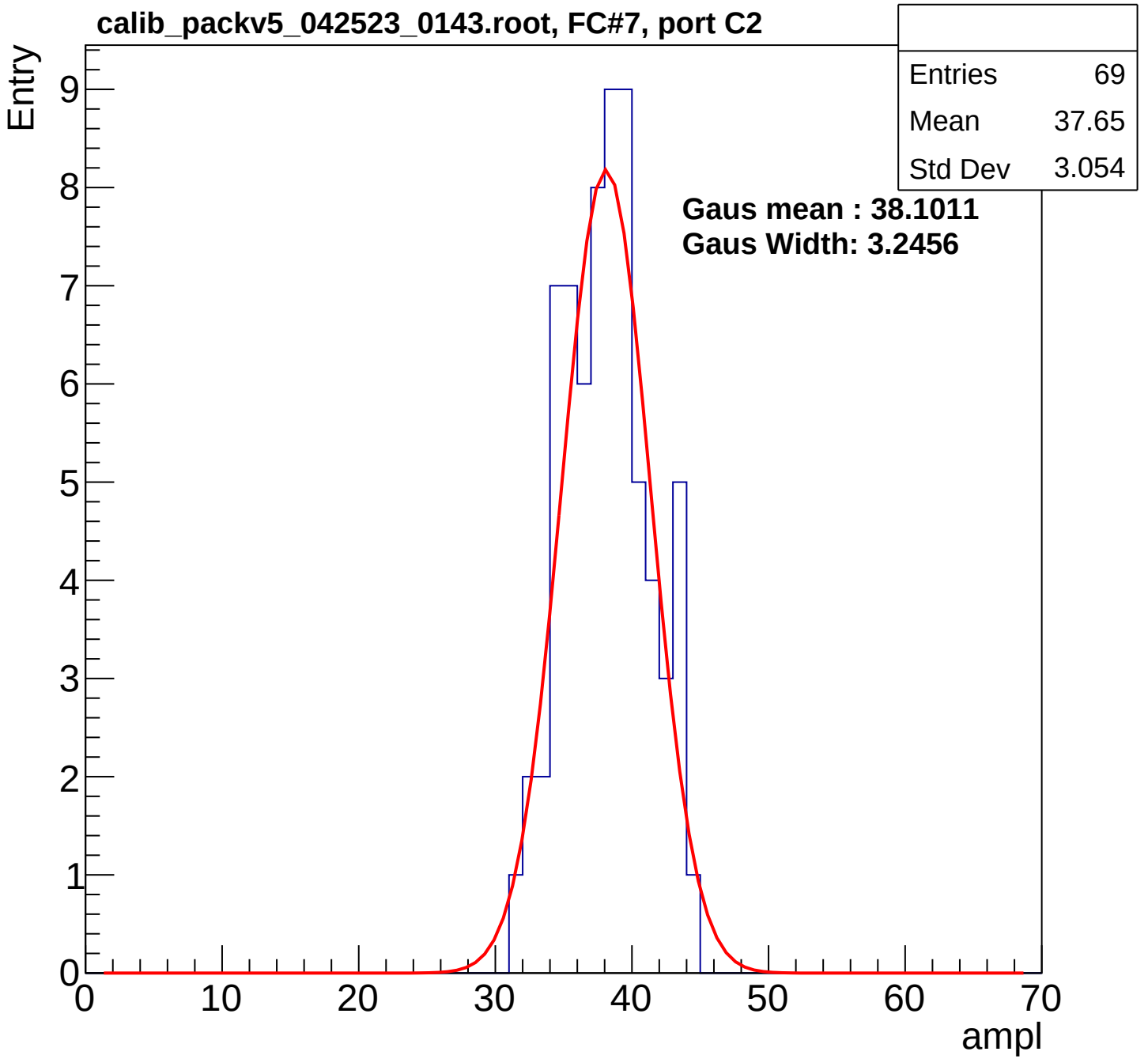

B1L103S, U13-ch32, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.91
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch32, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch32, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch33, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 30.57
Std Dev 3.171
Gaus mean : 31.1321
Gaus Width: 3.4281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch33, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 37.65
Std Dev 3.054
Gaus mean : 38.1011
Gaus Width: 3.2456
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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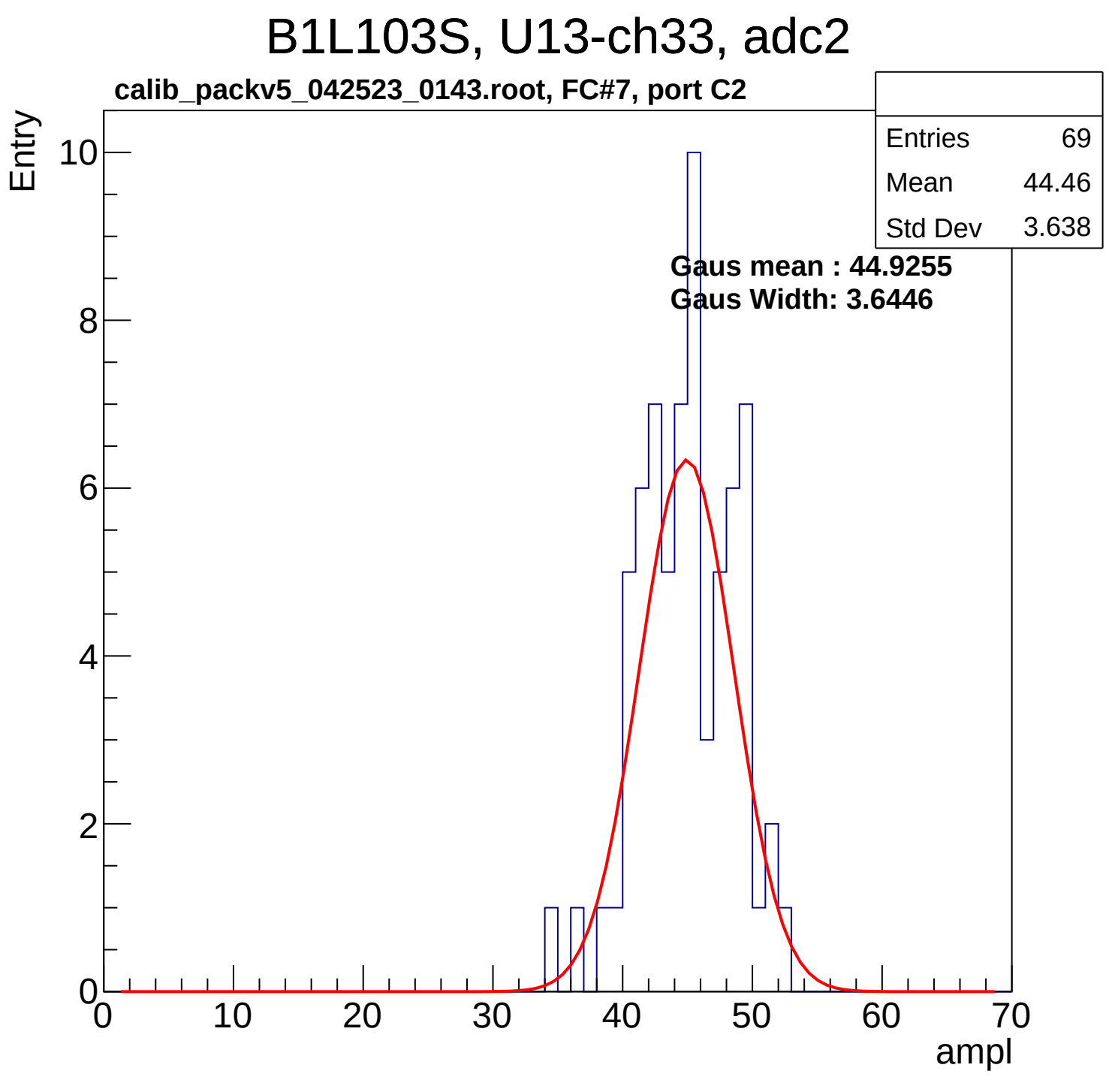
B1L103S, U13-ch33, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 44.46
Std Dev 3.638
Gaus mean : 44.9255
Gaus Width: 3.6446
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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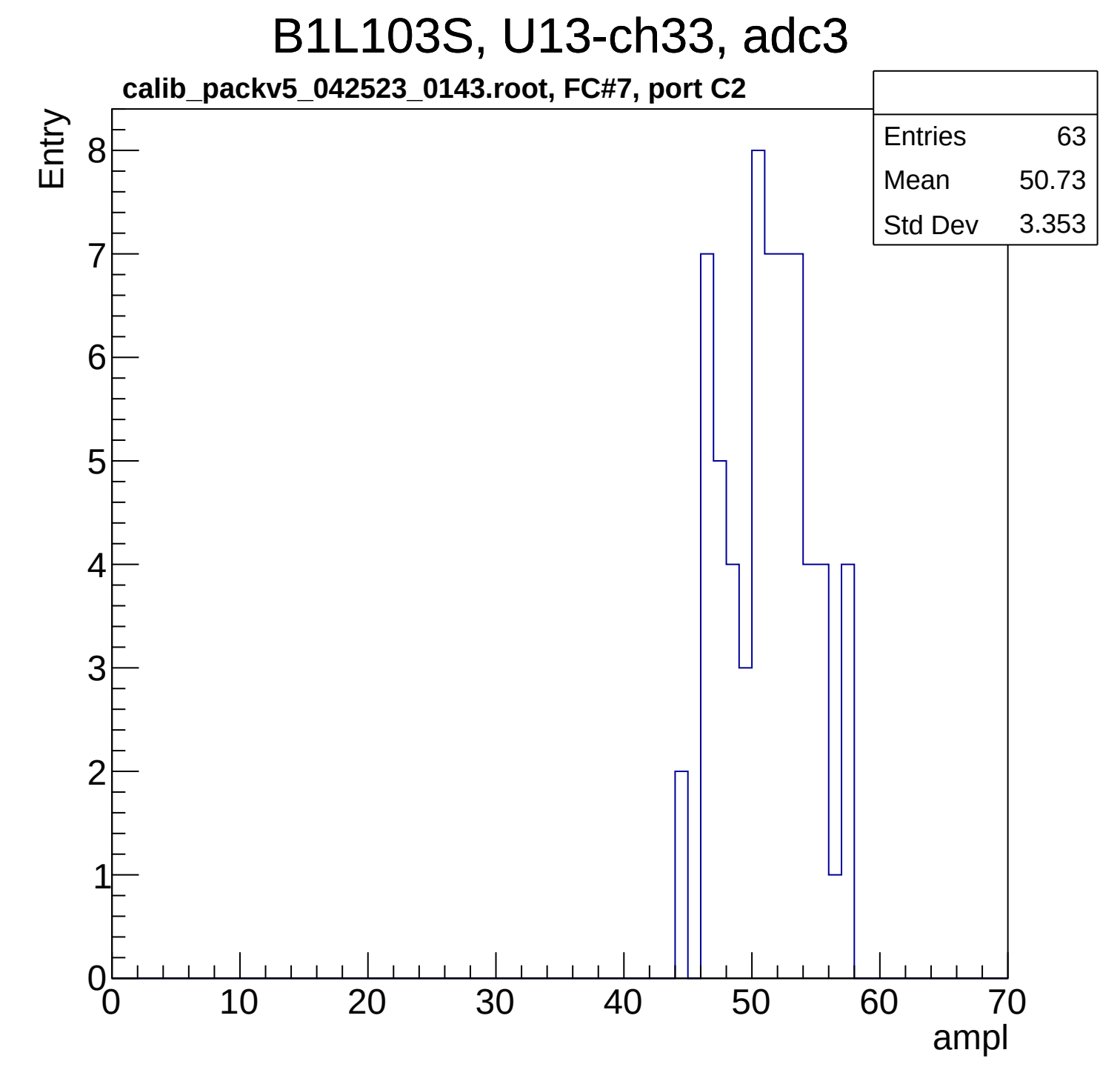
B1L103S, U13-ch33, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 50.73
Std Dev 3.353
Entry
ampl
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5
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0
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60
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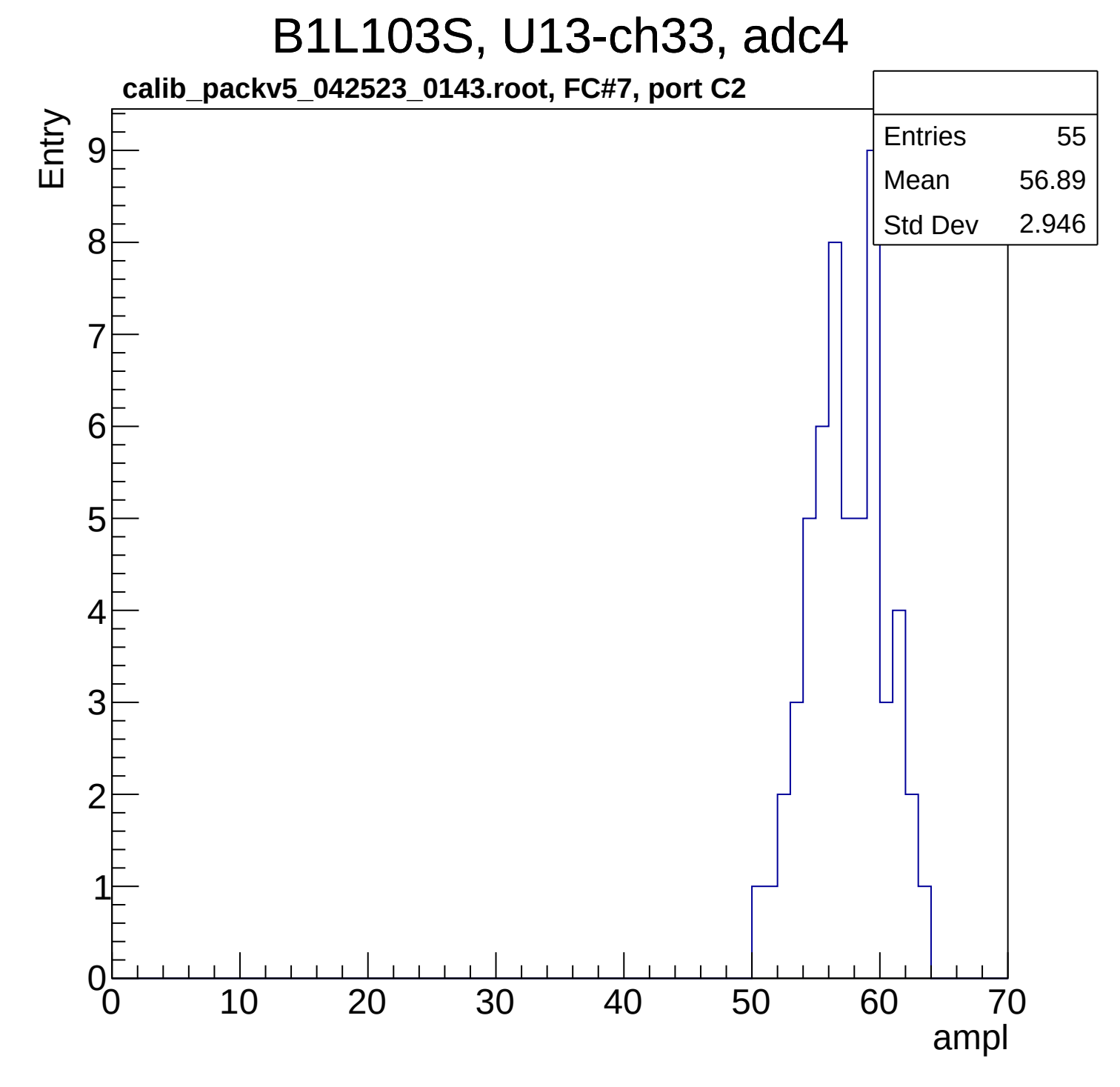
B1L103S, U13-ch33, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 56.89
Std Dev 2.946
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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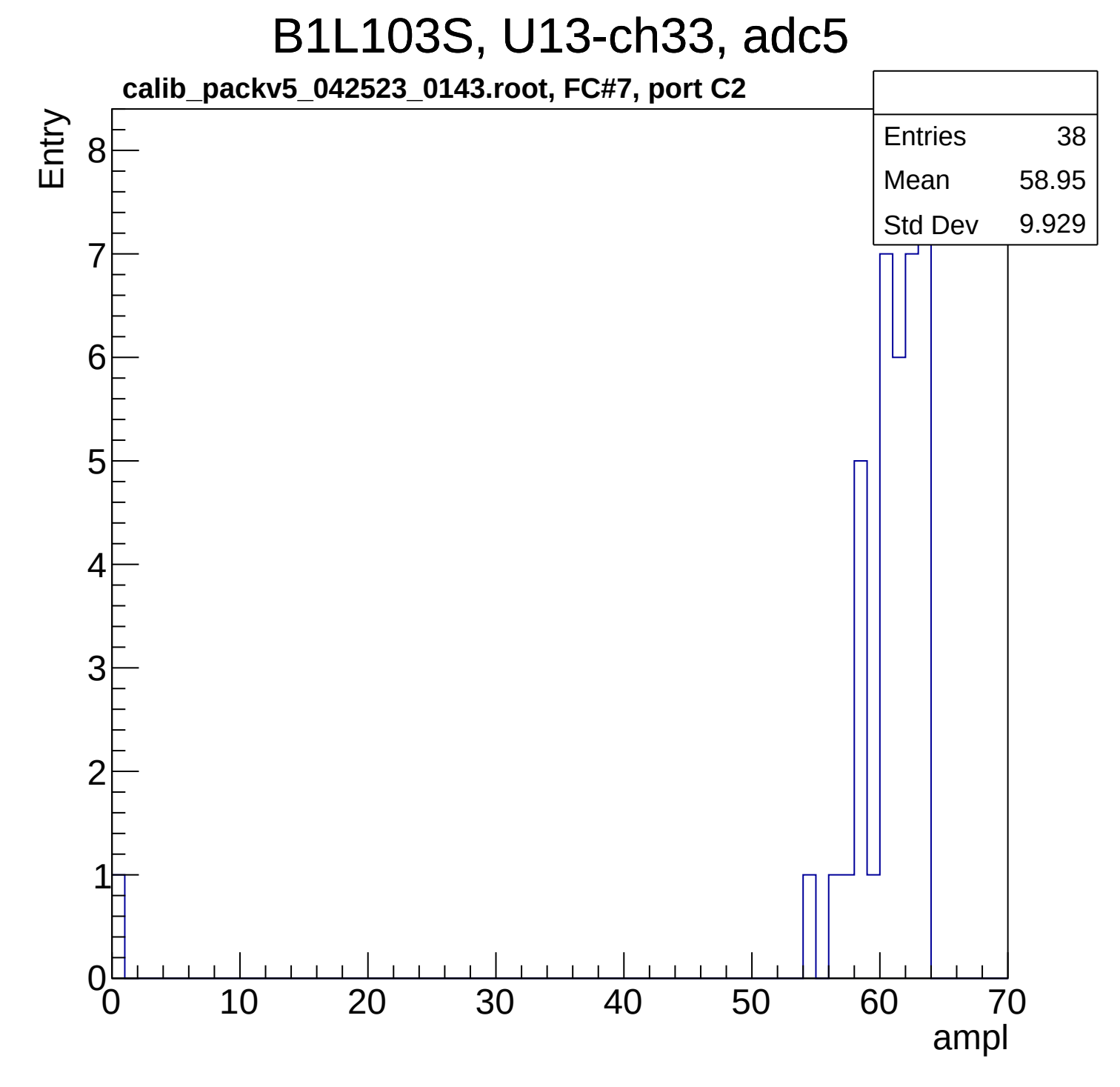
60
70


B1L103S, U13-ch33, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.95
Std Dev 9.929
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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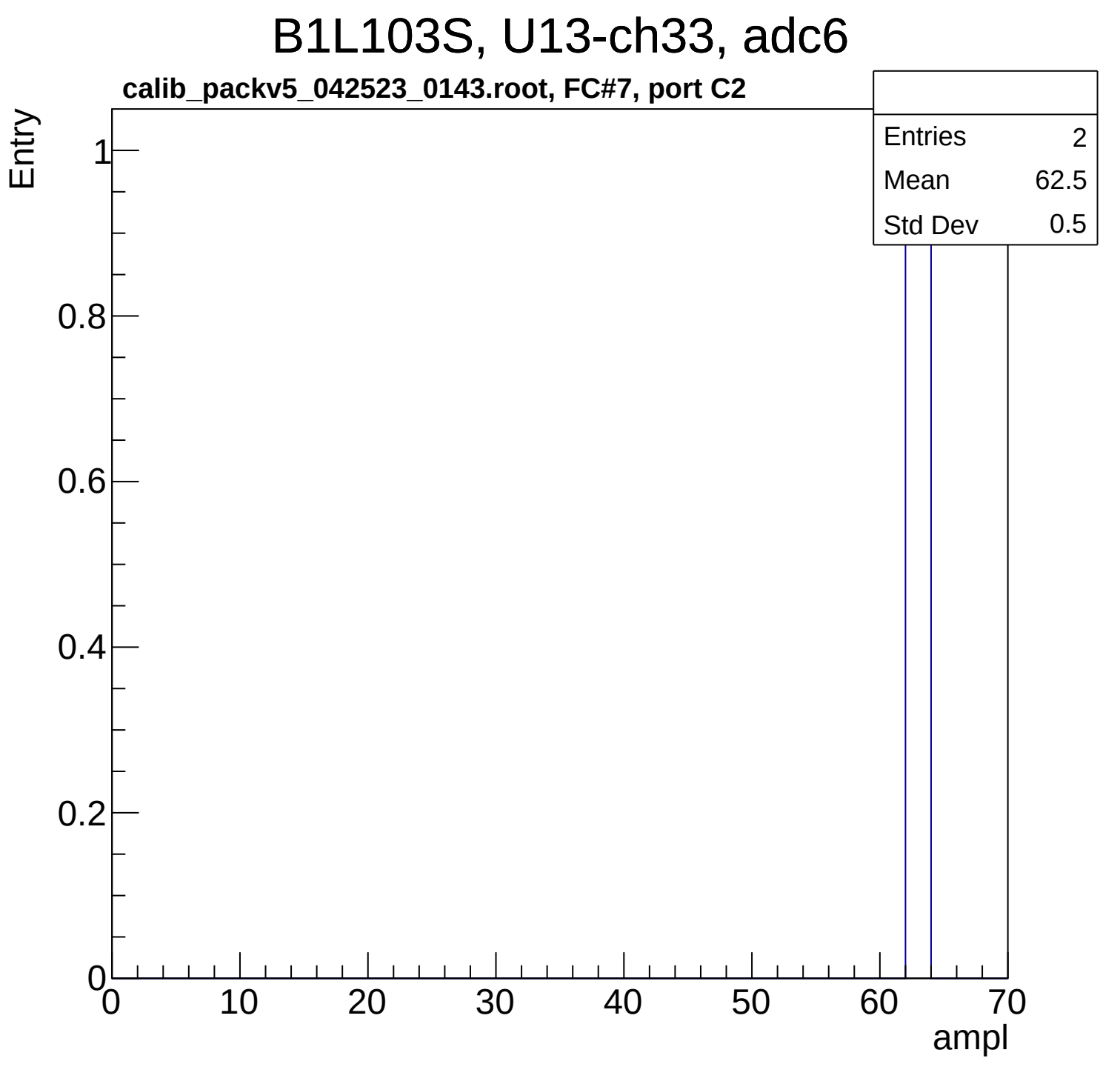
B1L103S, U13-ch33, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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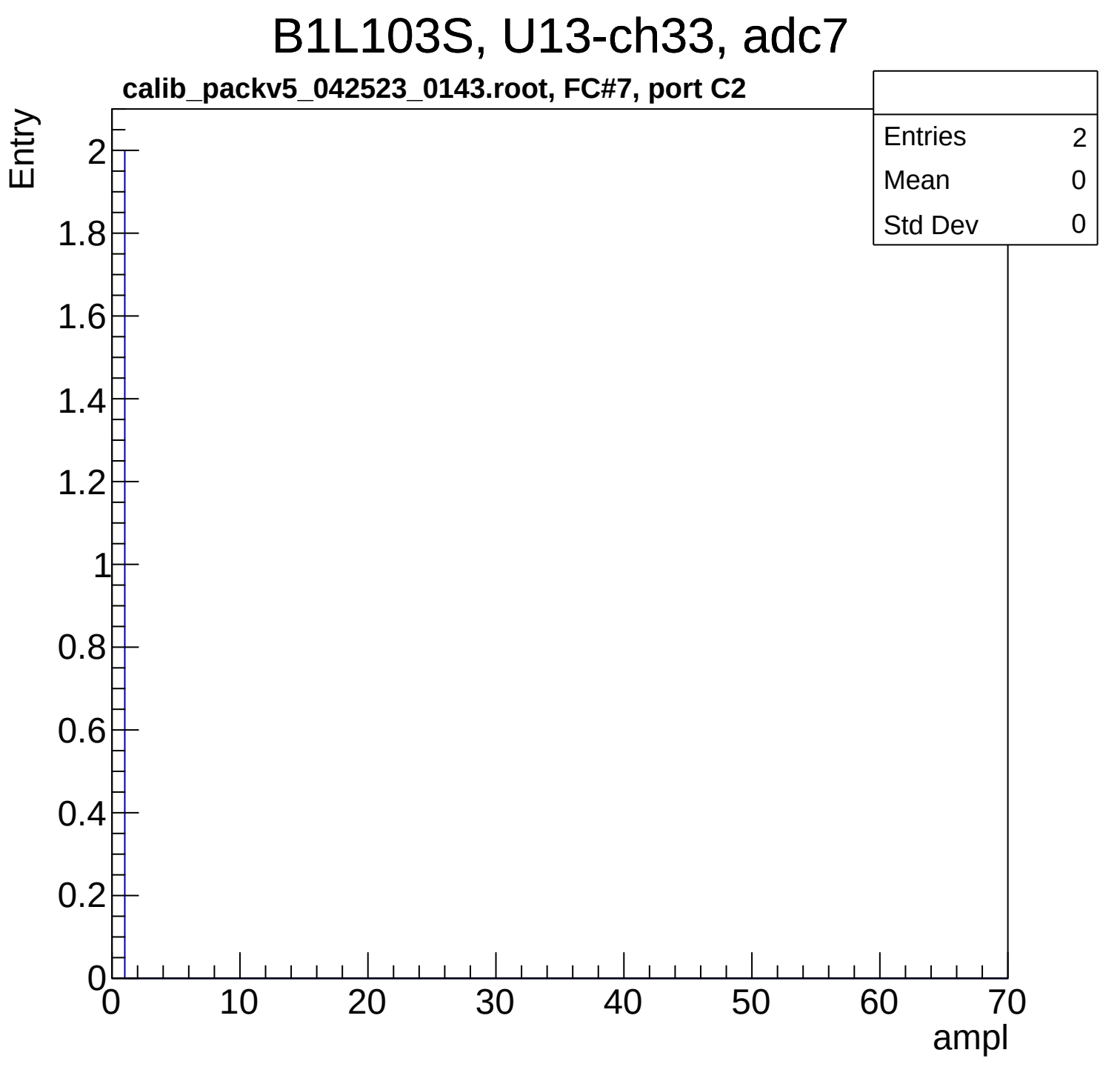
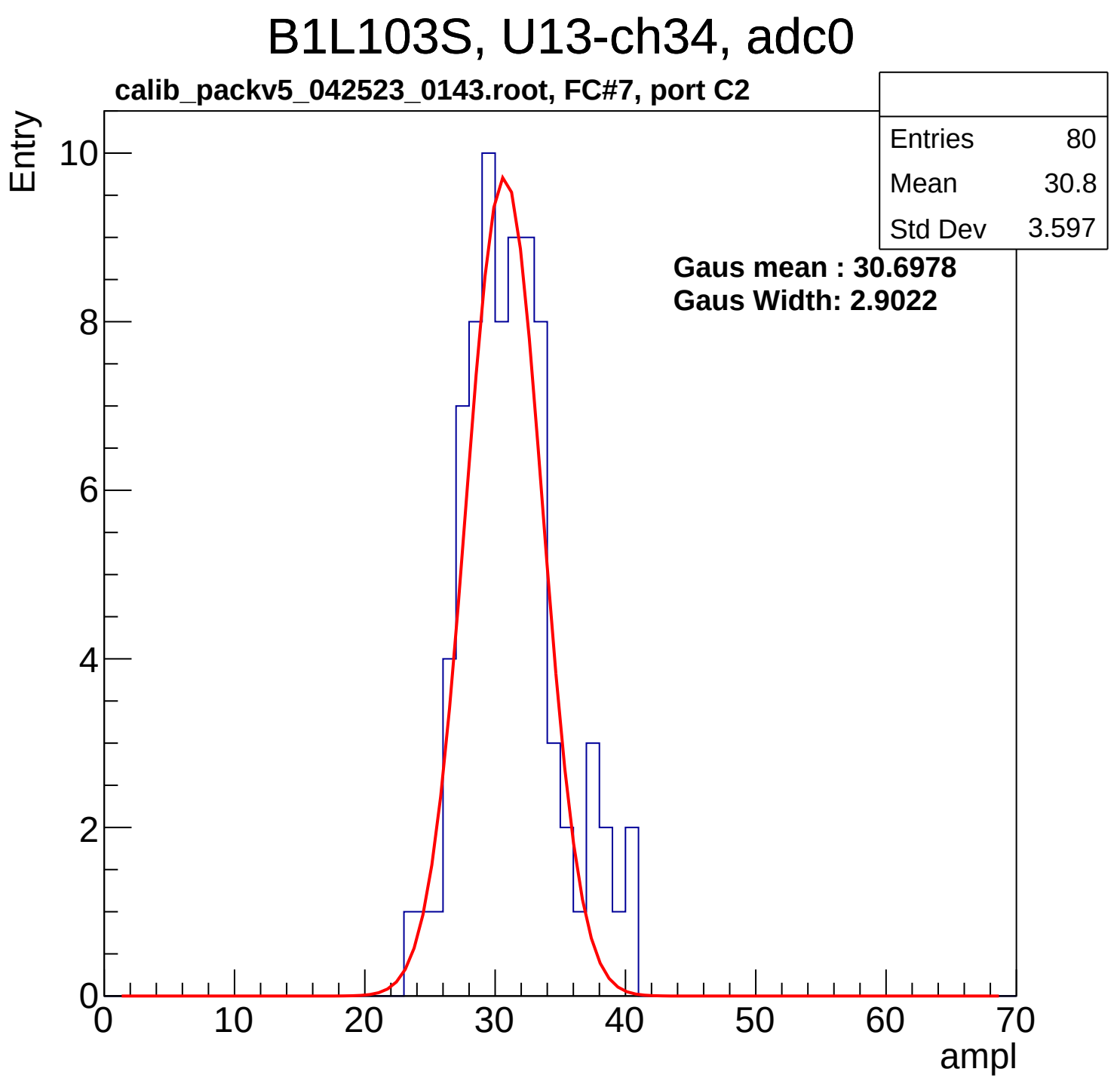
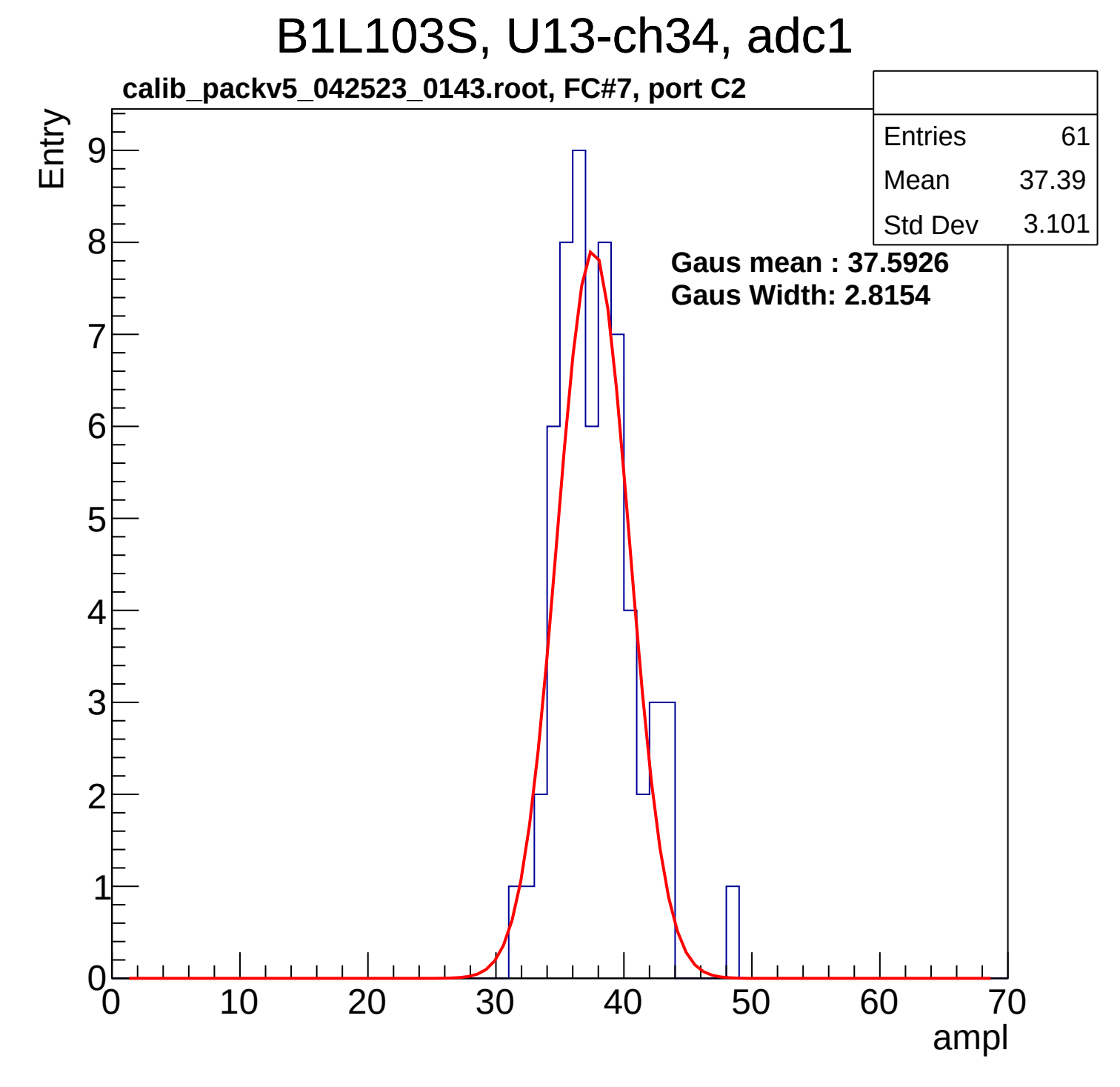
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch33, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch34, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 30.8
Std Dev 3.597
Gaus mean : 30.6978
Gaus Width: 2.9022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch34, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37.39
Std Dev 3.101
Gaus mean : 37.5926
Gaus Width: 2.8154
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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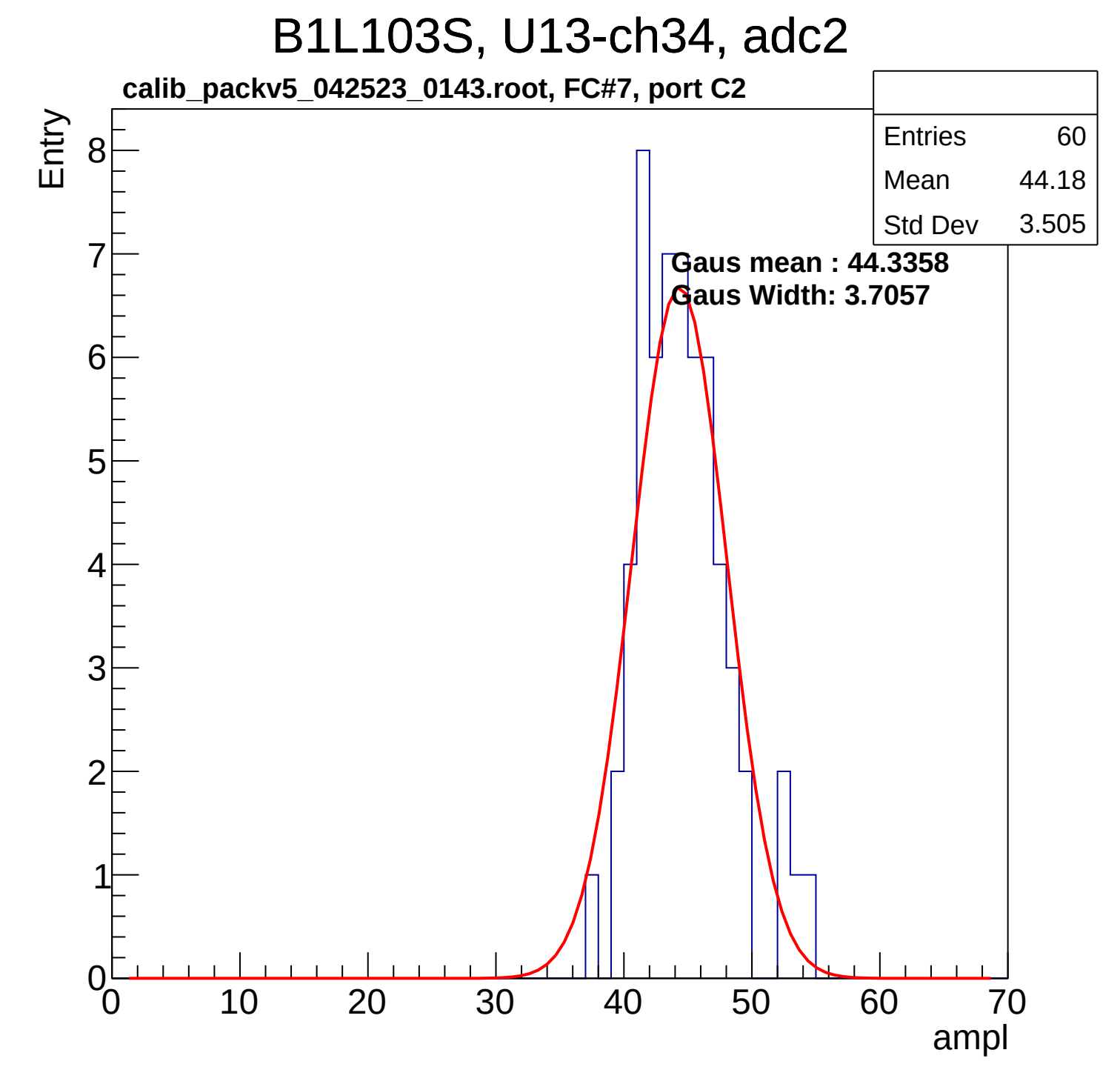
B1L103S, U13-ch34, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 44.18
Std Dev 3.505
Gaus mean : 44.3358
Gaus Width: 3.7057
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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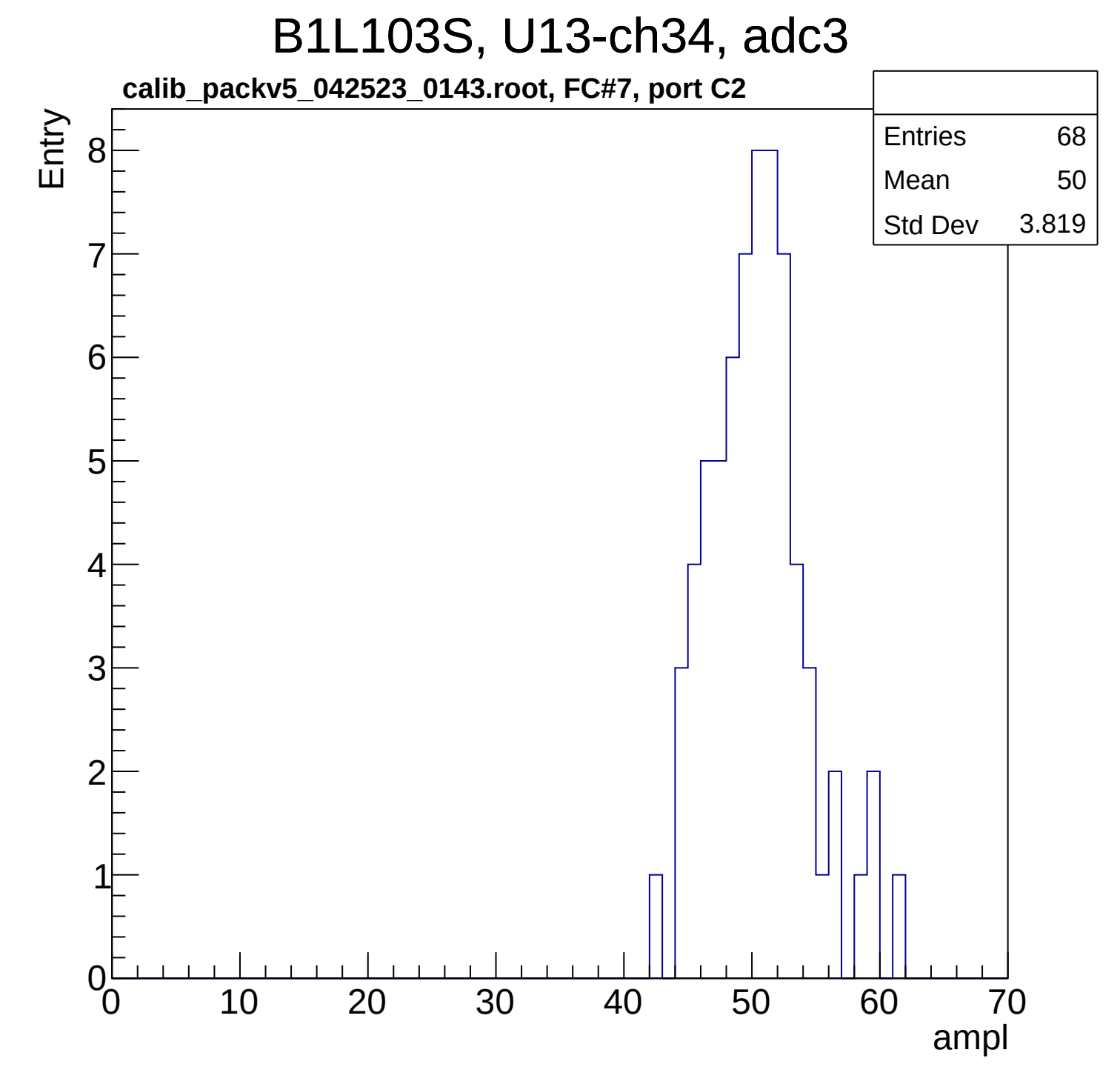
B1L103S, U13-ch34, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 50
Std Dev 3.819
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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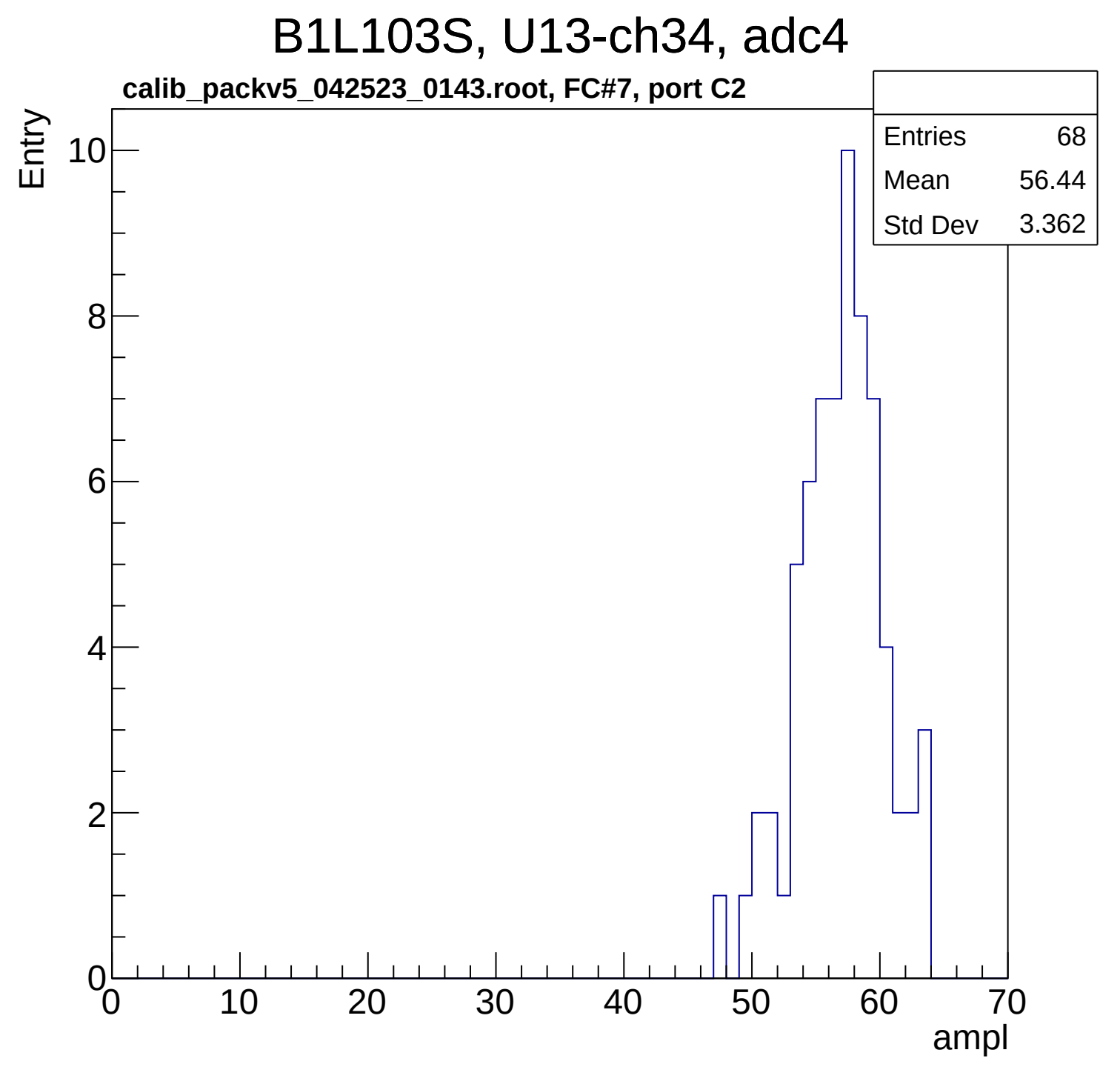
B1L103S, U13-ch34, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 56.44
Std Dev 3.362
Entry
ampl
0
2
4
6
8
10
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40
50
60
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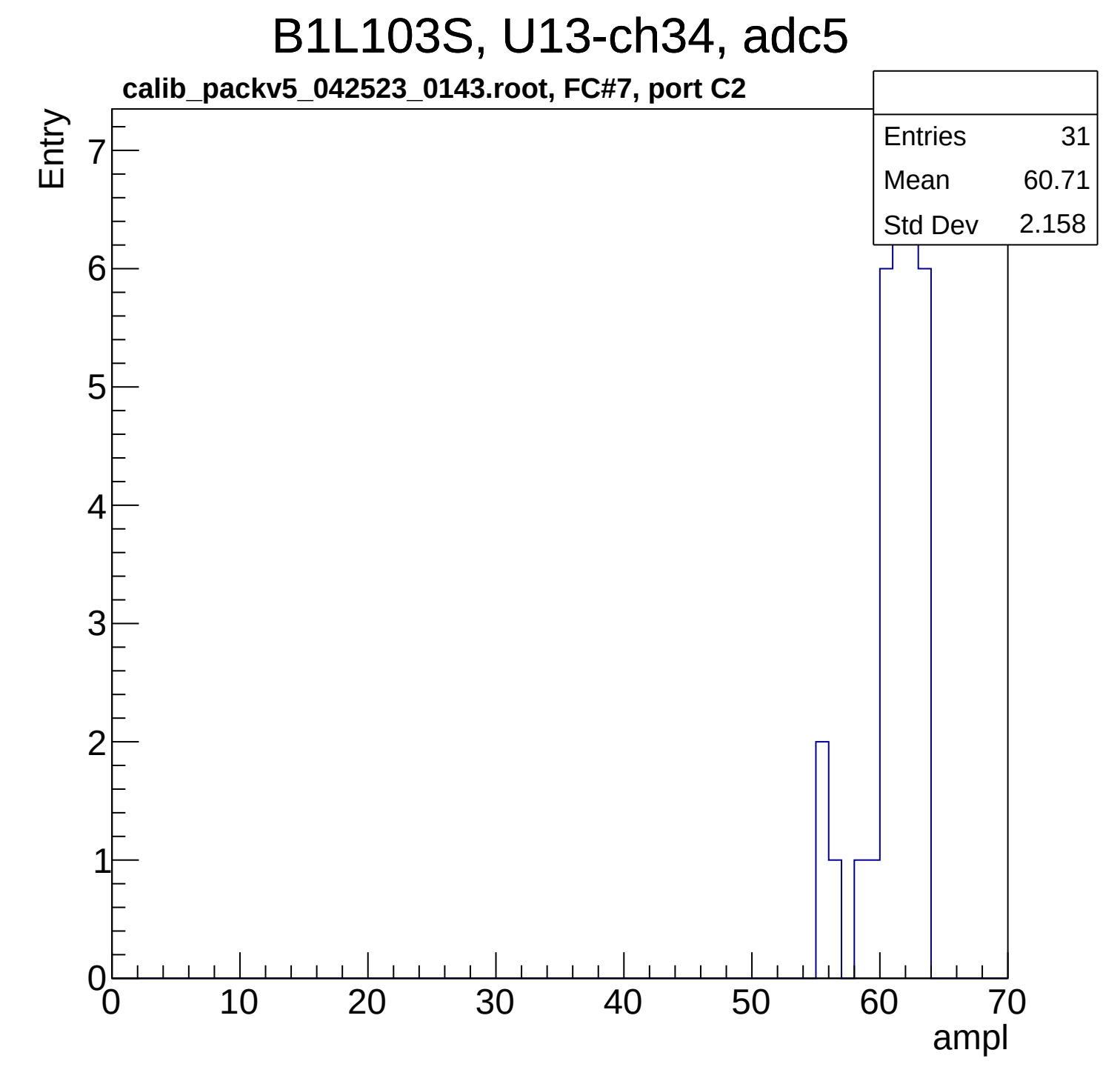
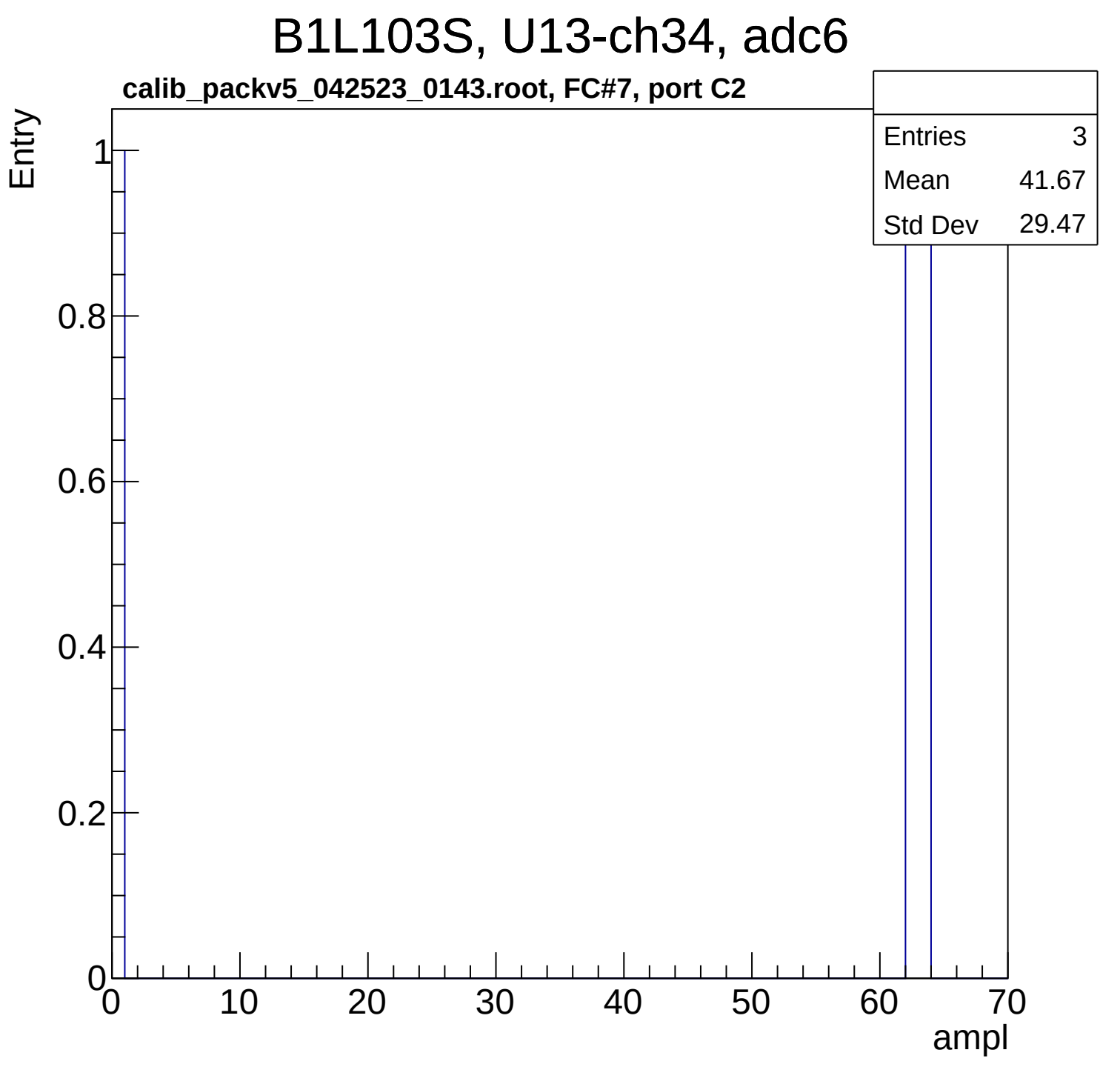
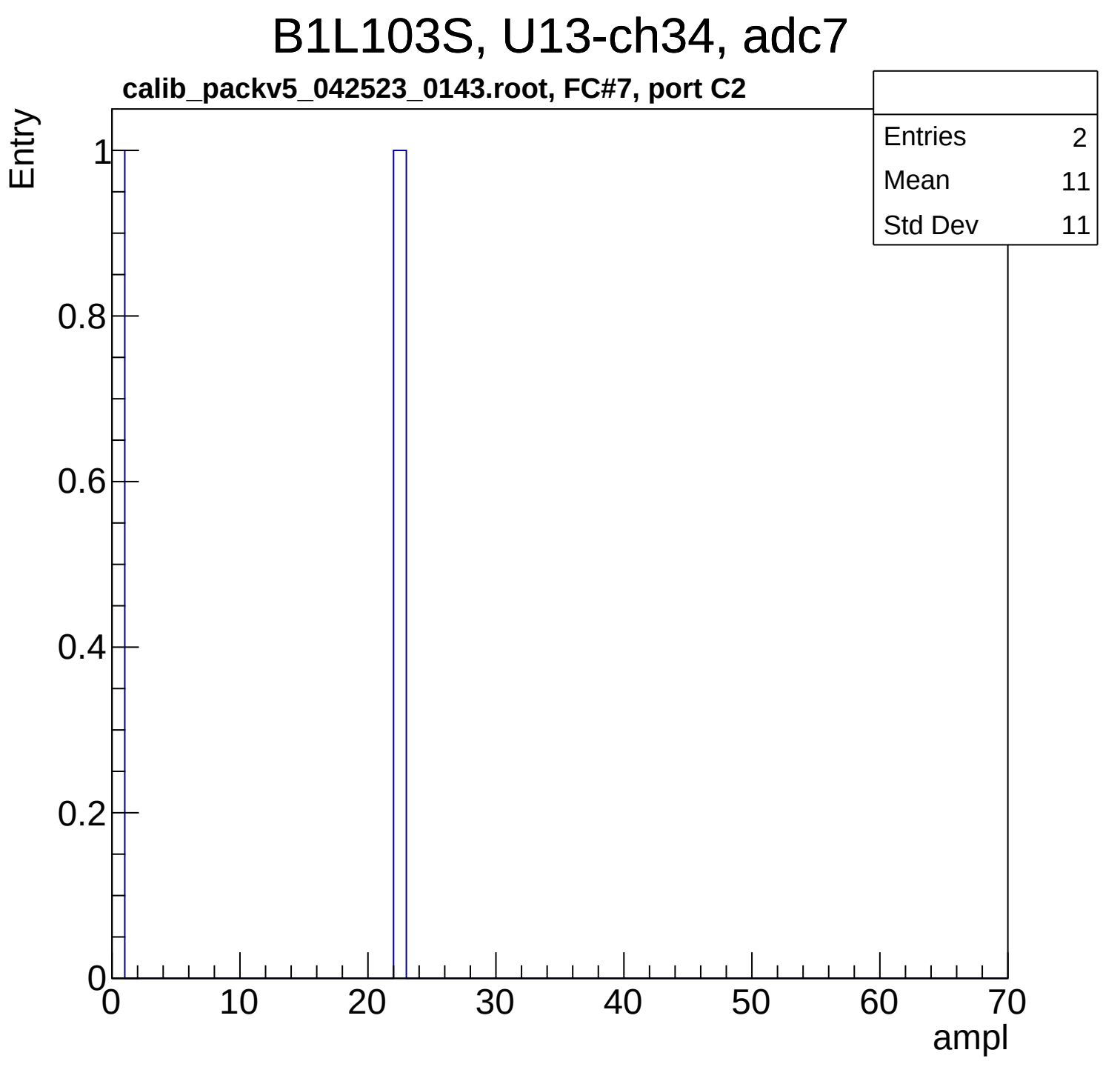

B1L103S, U13-ch34, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 60.71
Std Dev 2.158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch34, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch34, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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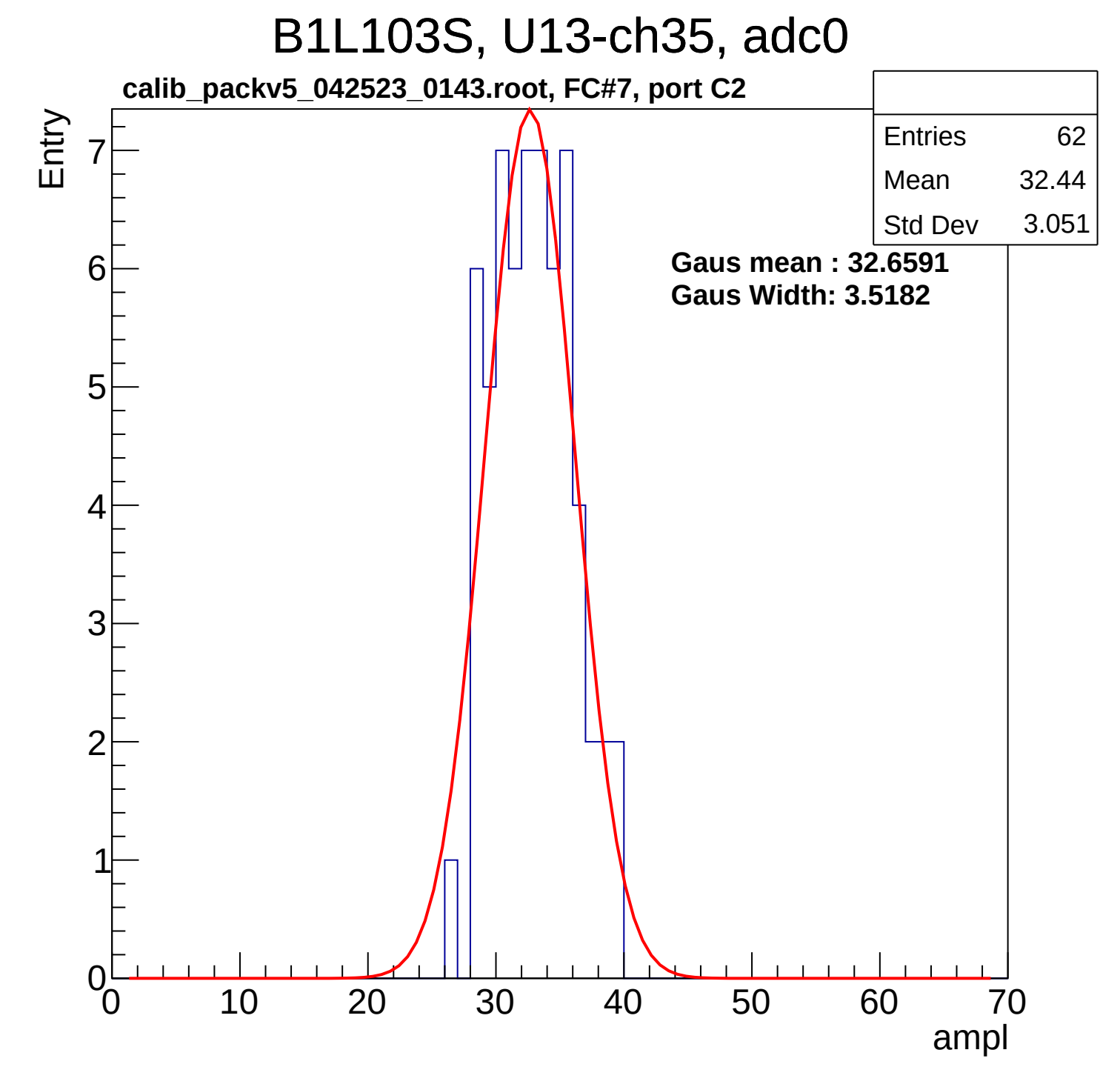
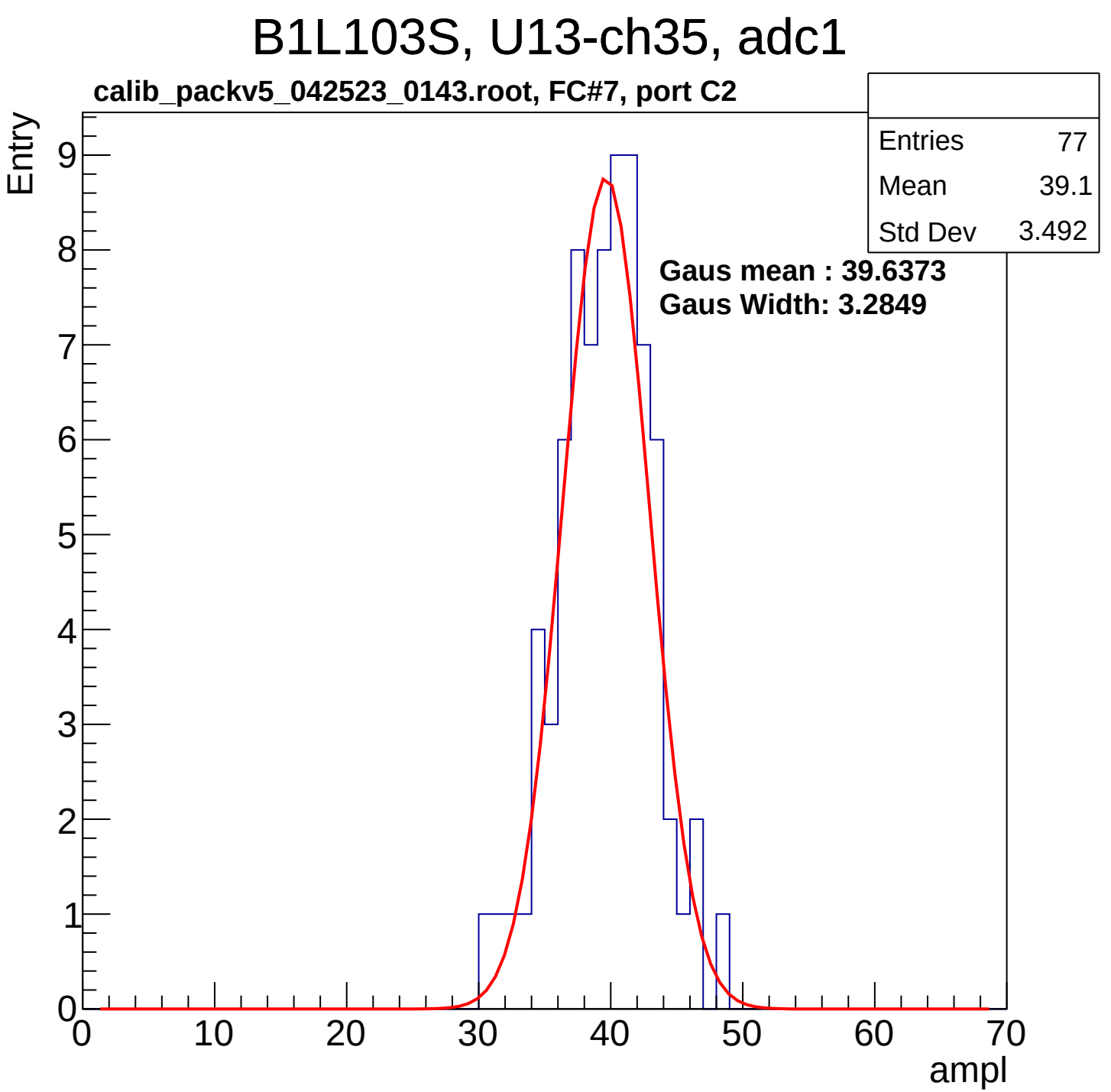
40
50
60
70
ampl


B1L103S, U13-ch35, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 32.44
Std Dev 3.051
Gaus mean : 32.6591
Gaus Width: 3.5182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch35, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 39.1
Std Dev 3.492
Gaus mean : 39.6373
Gaus Width: 3.2849
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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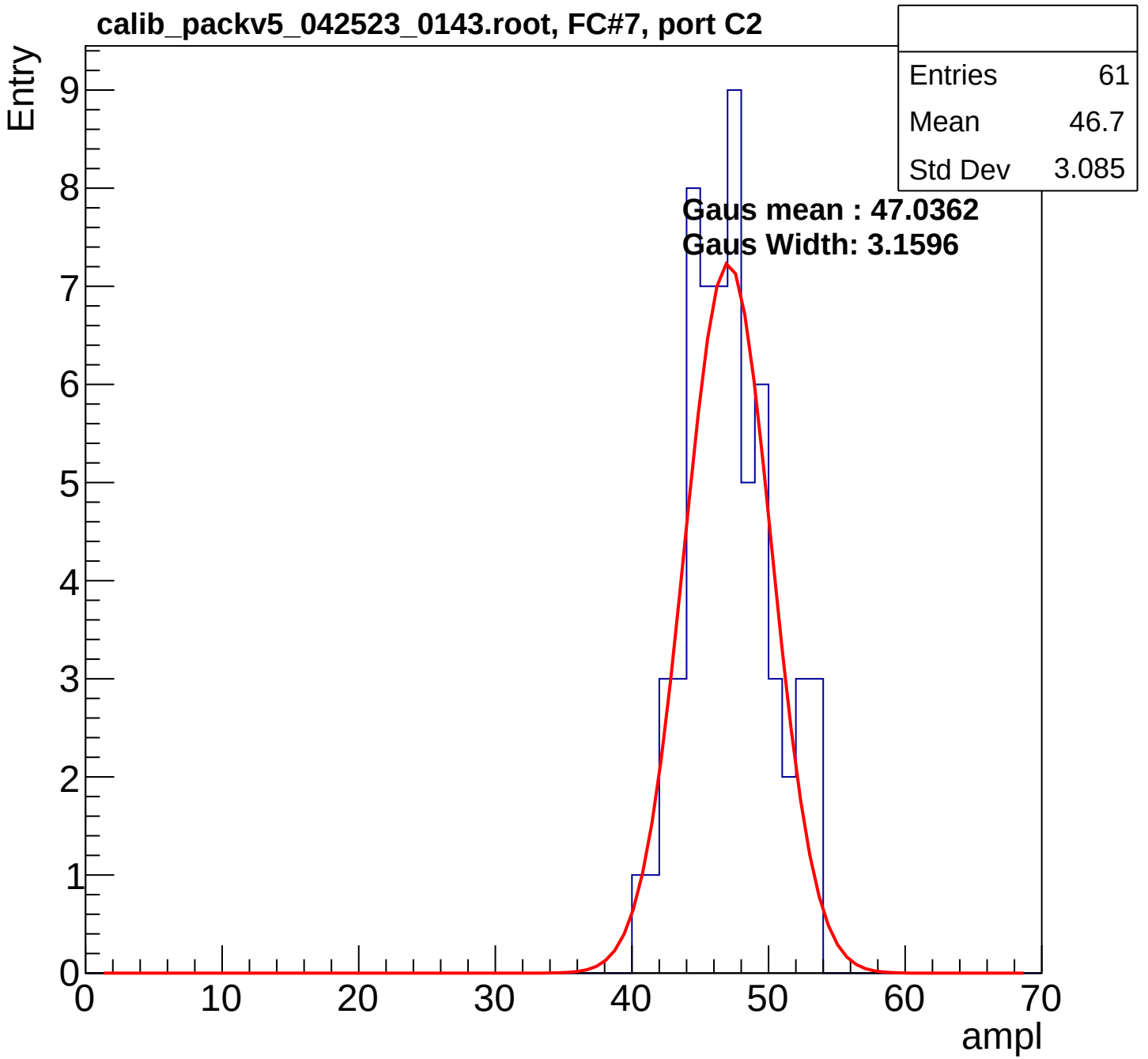
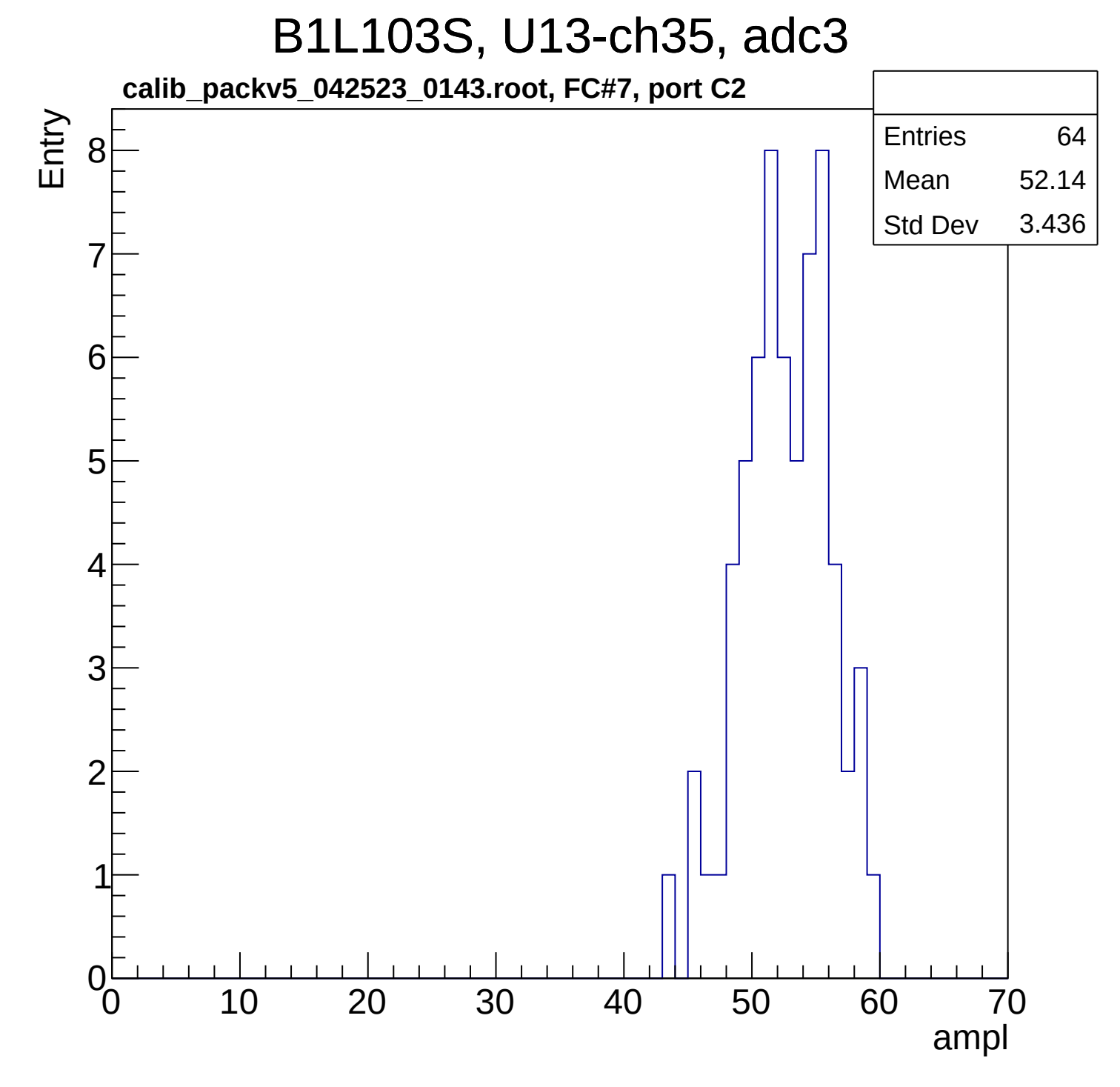
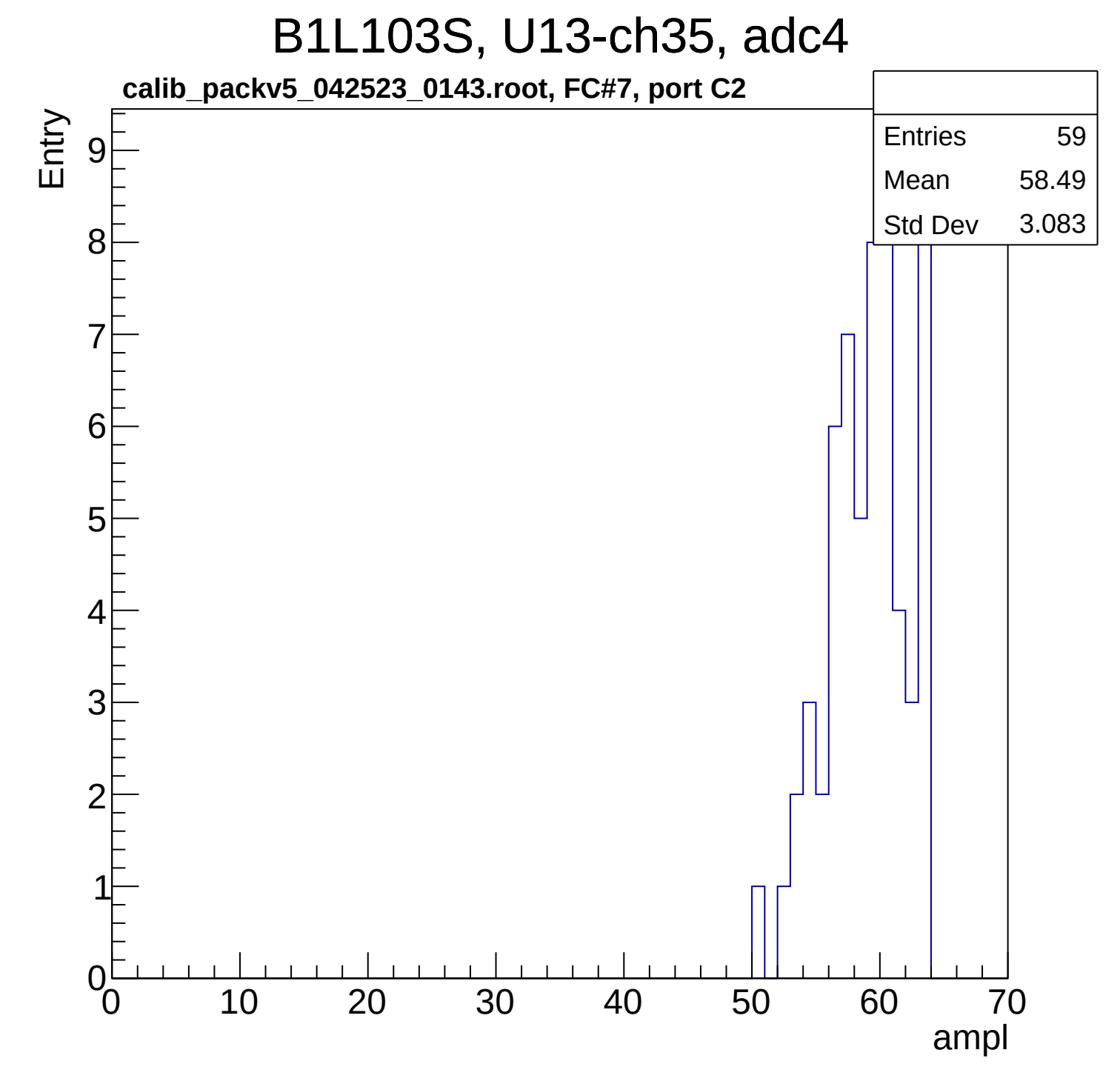
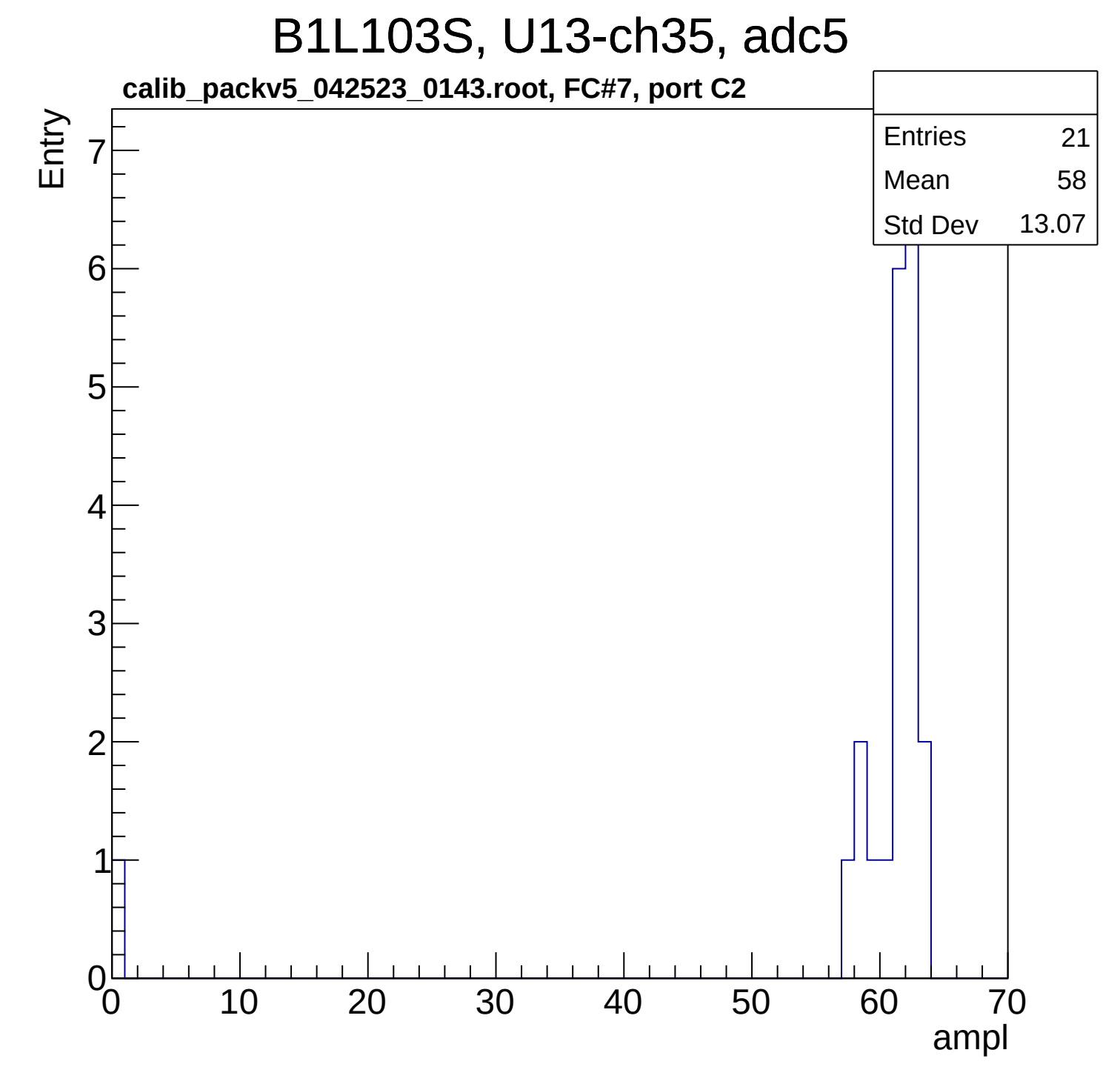
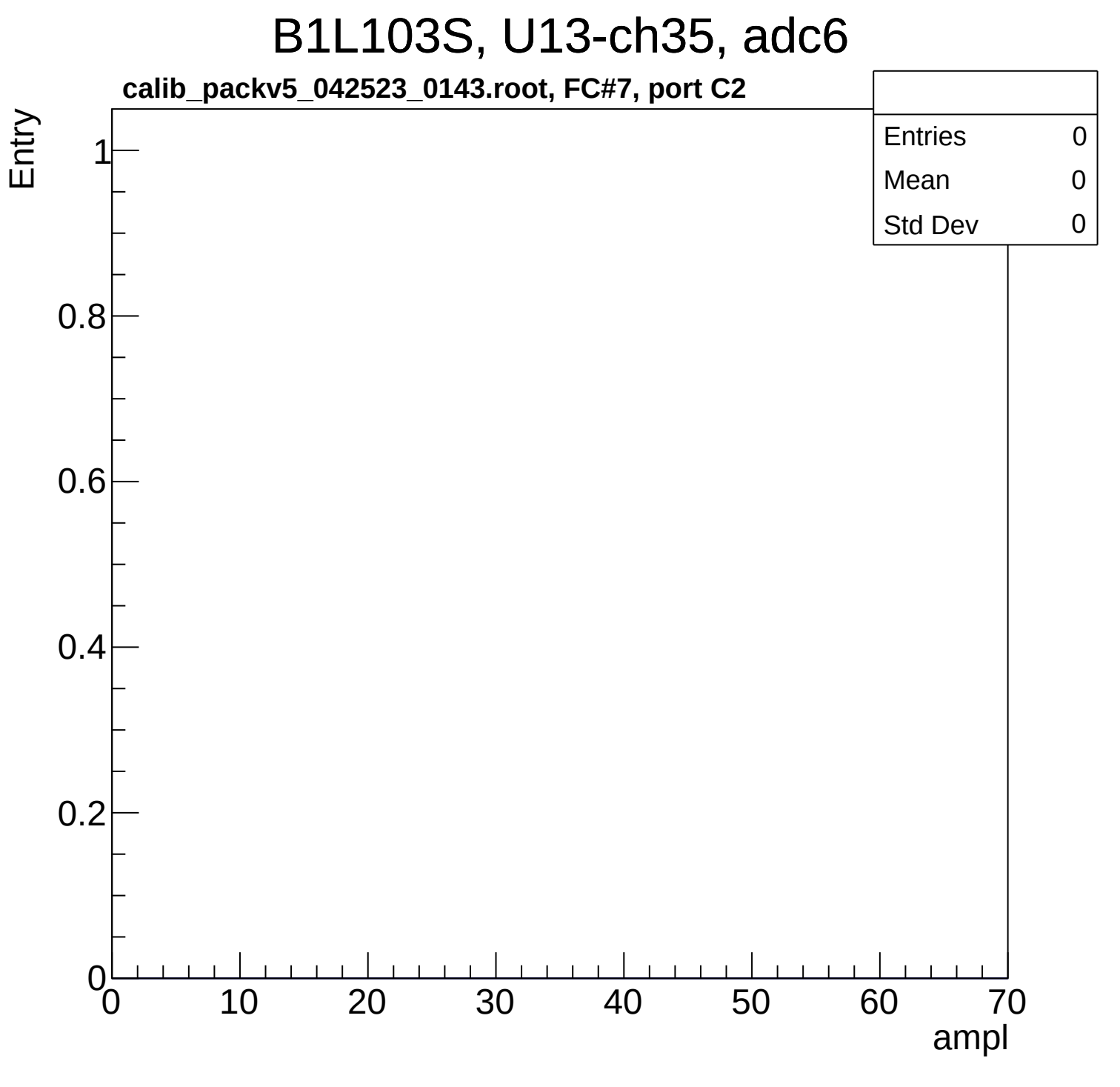

B1L103S, U13-ch35, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 46.7
Std Dev 3.085
Gaus mean : 47.0362
Gaus Width: 3.1596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch35, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 52.14
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch35, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.49
Std Dev 3.083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch35, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 58
Std Dev 13.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch35, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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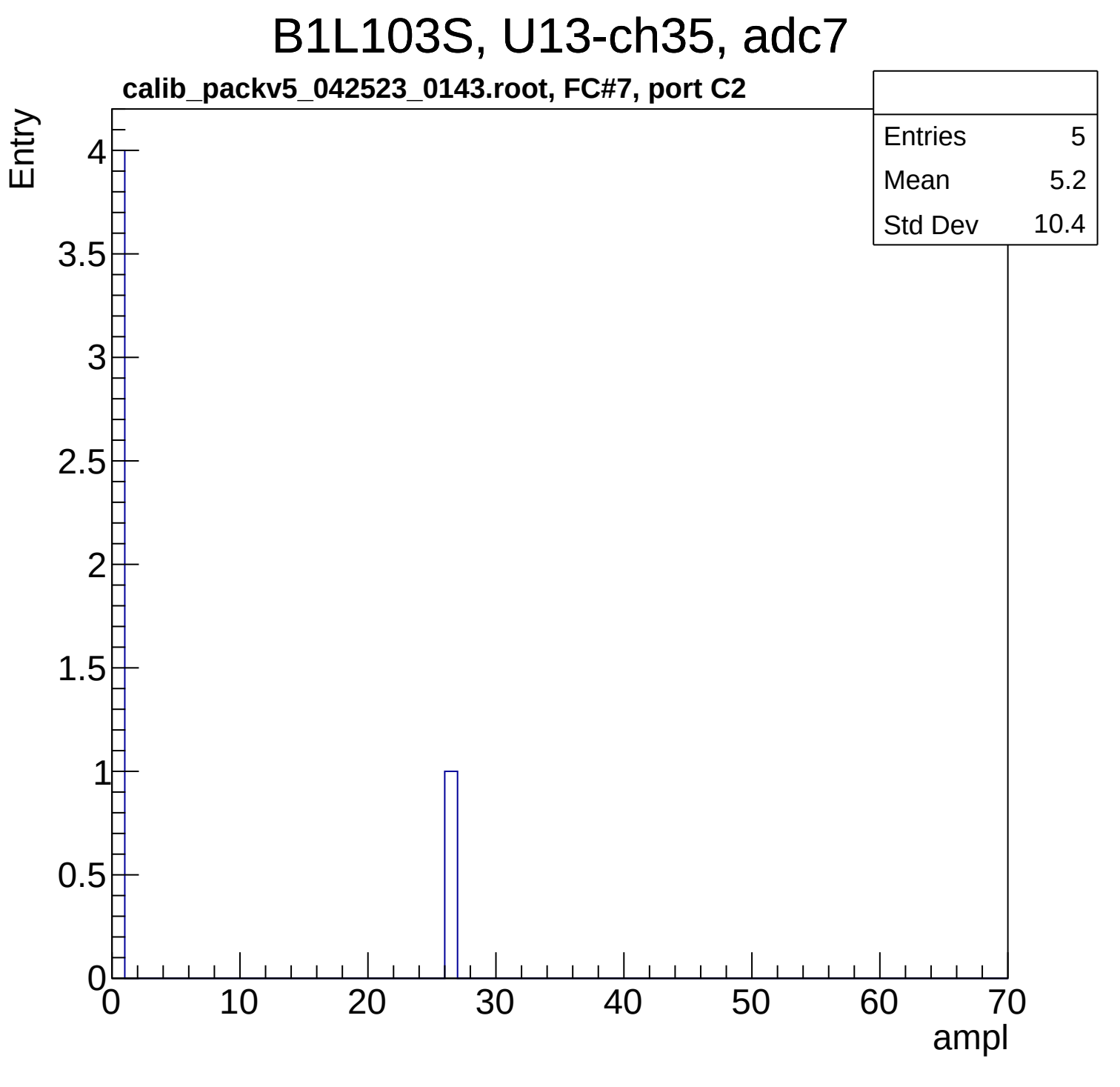
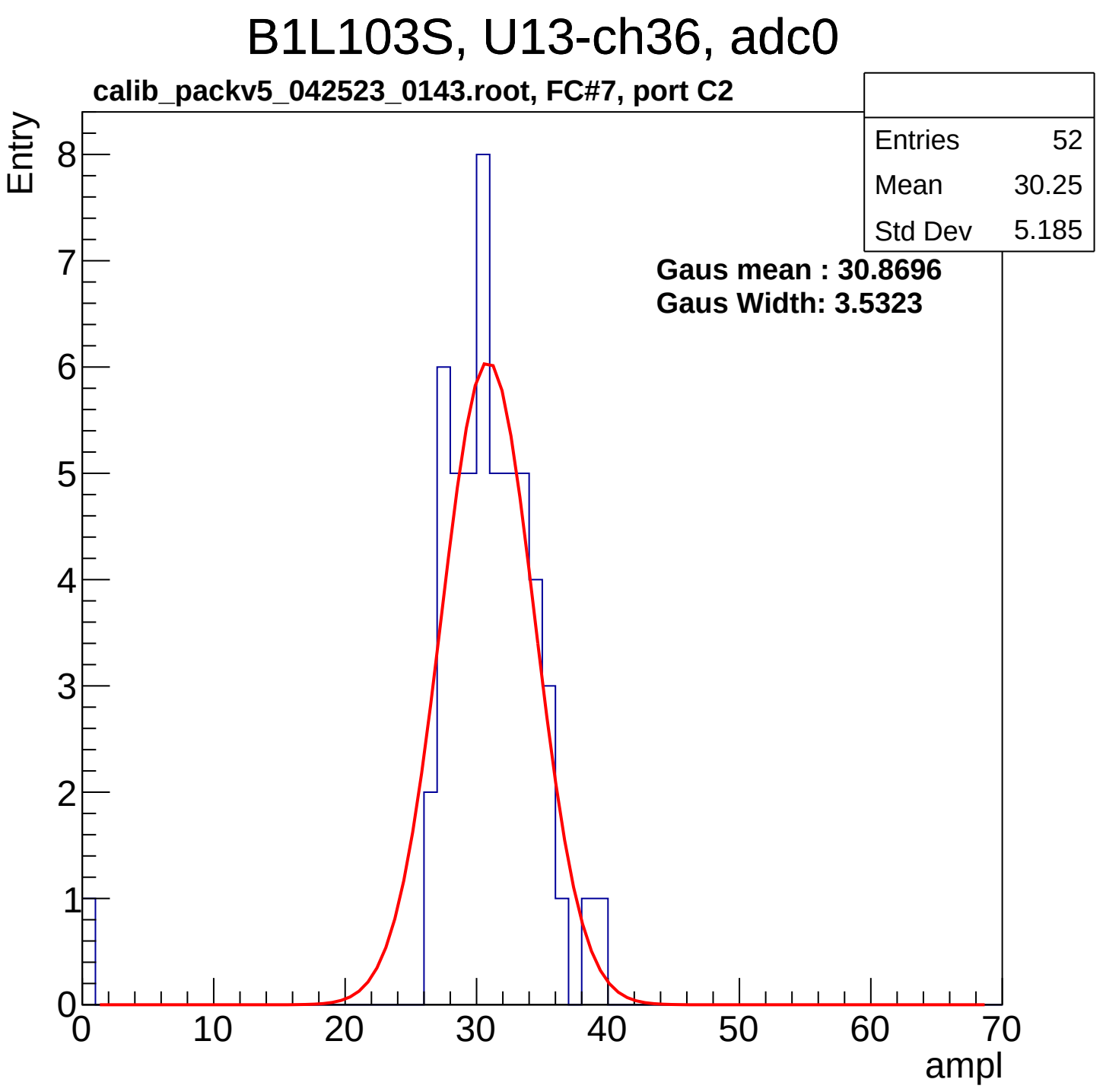
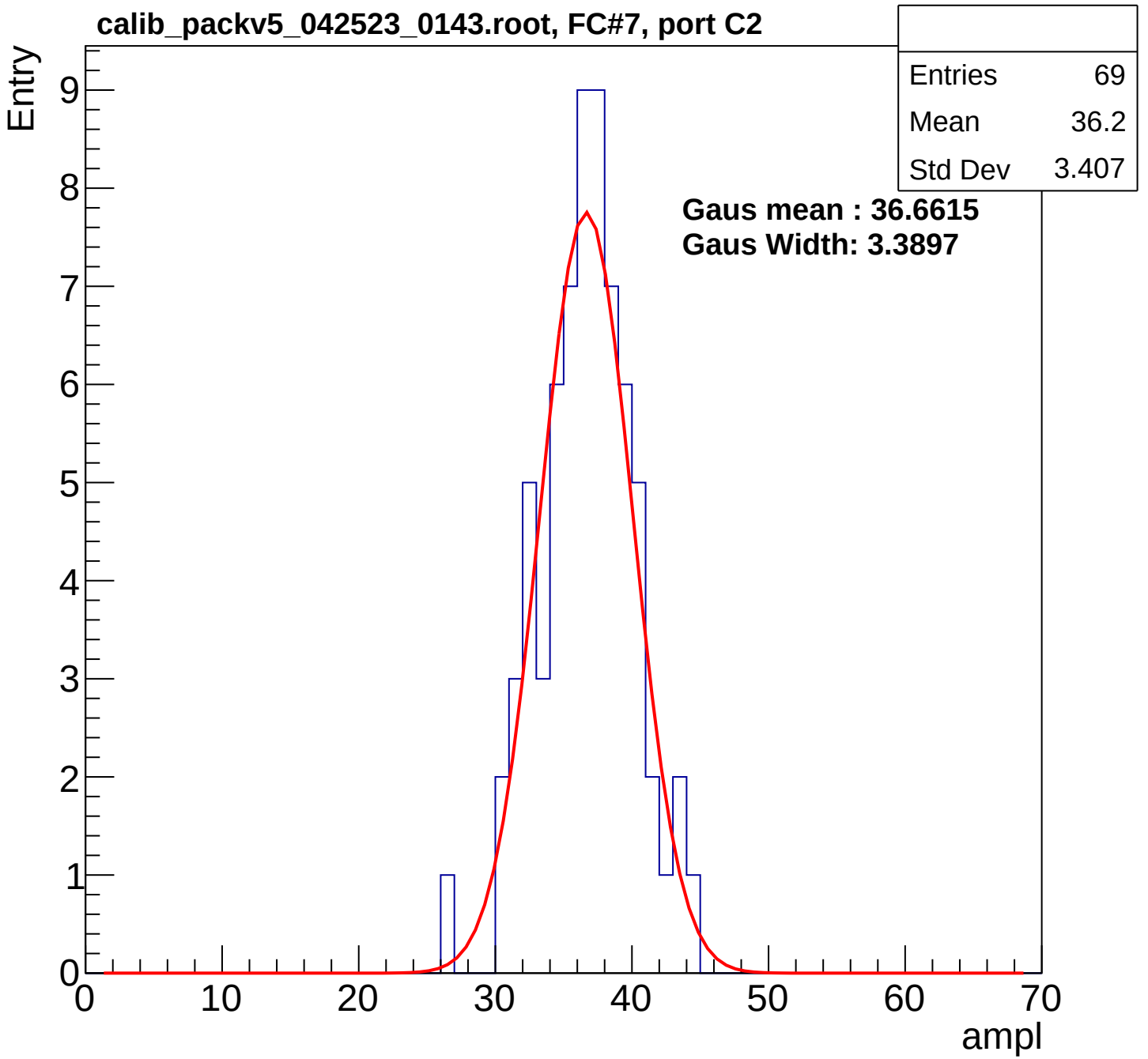
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch35, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 5.2
Std Dev 10.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 30.25
Std Dev 5.185
Gaus mean : 30.8696
Gaus Width: 3.5323
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.2
Std Dev 3.407
Gaus mean : 36.6615
Gaus Width: 3.3897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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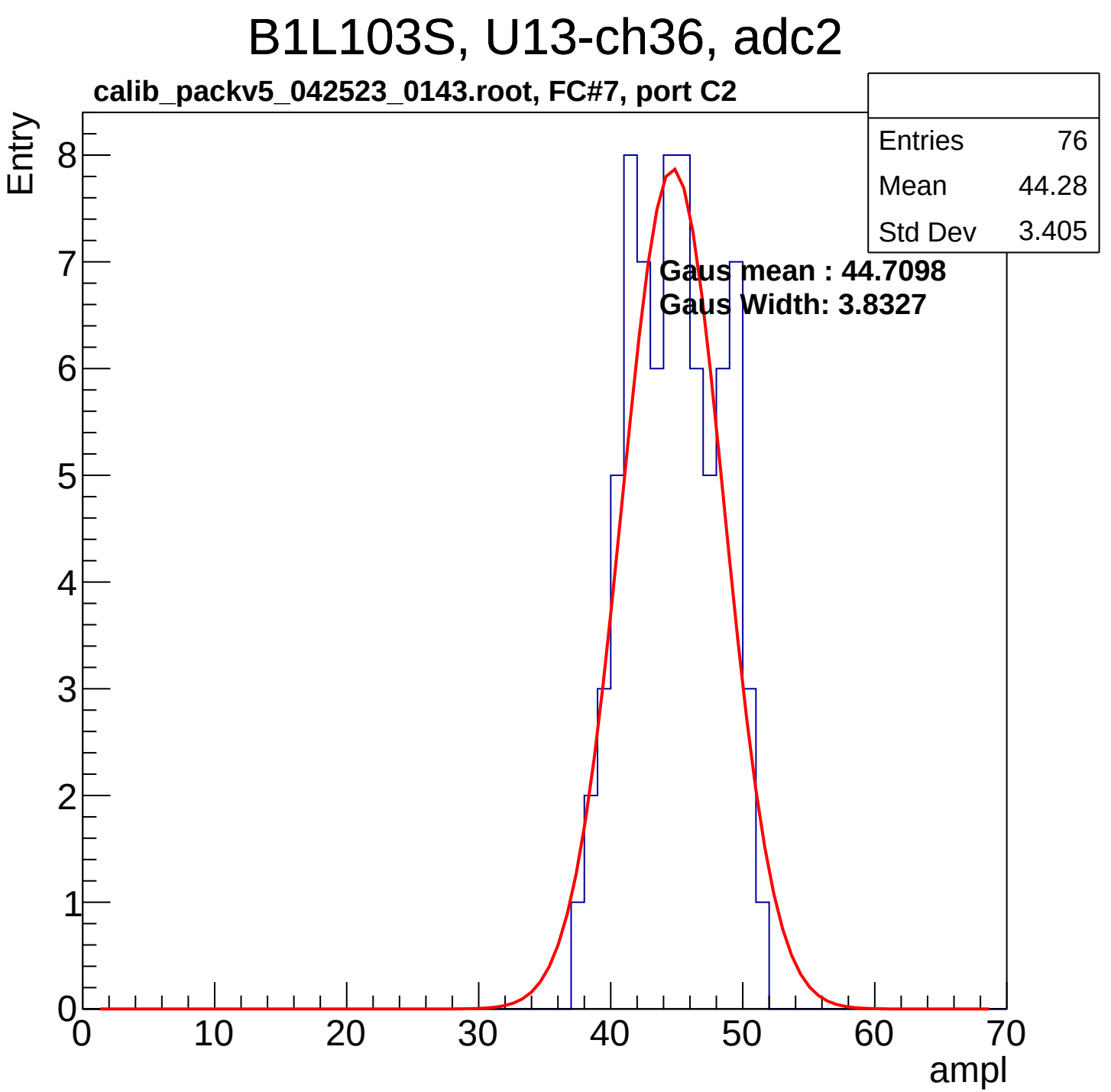
B1L103S, U13-ch36, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 44.28
Std Dev 3.405
Gaus mean : 44.7098
Gaus Width: 3.8327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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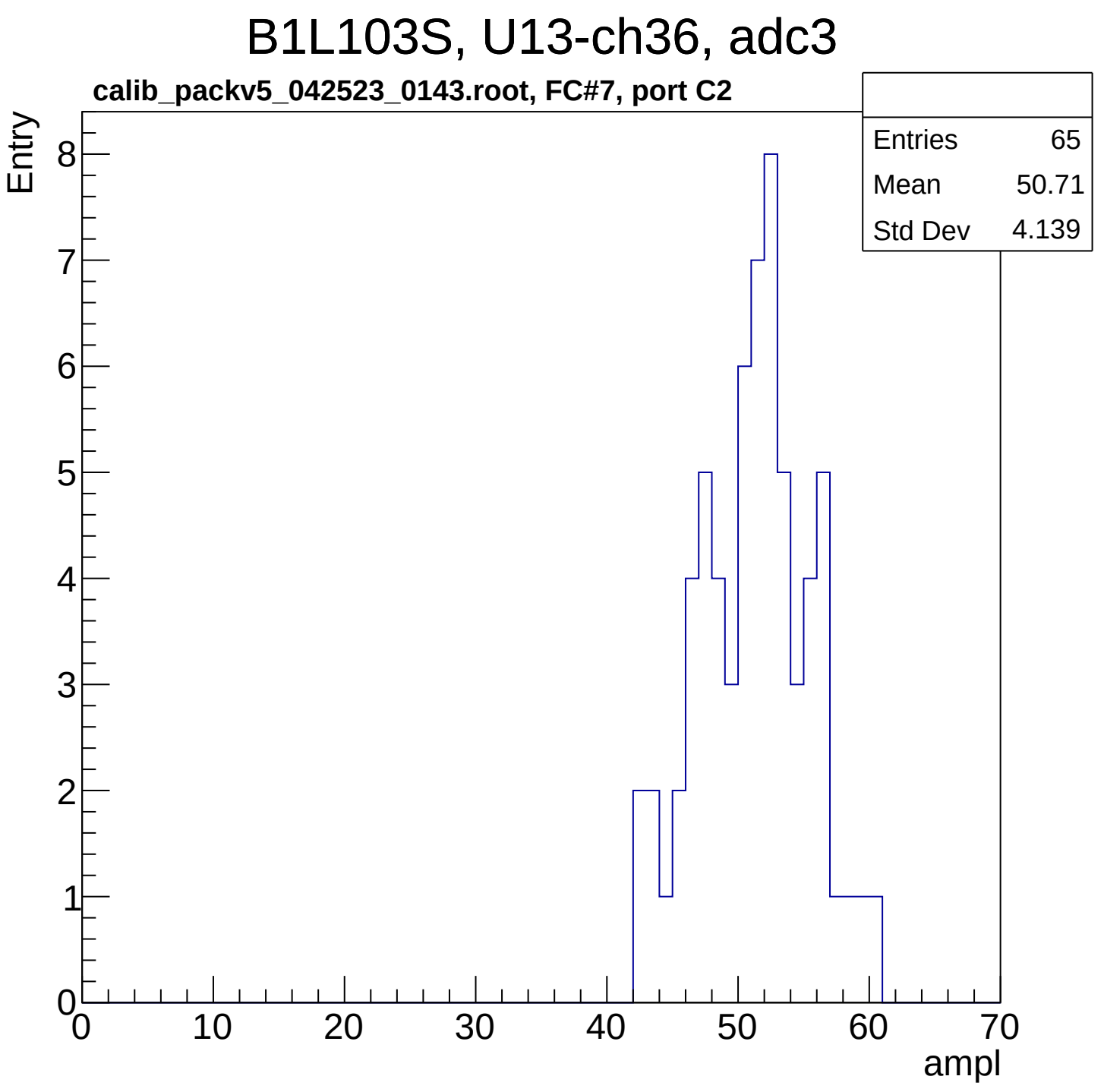
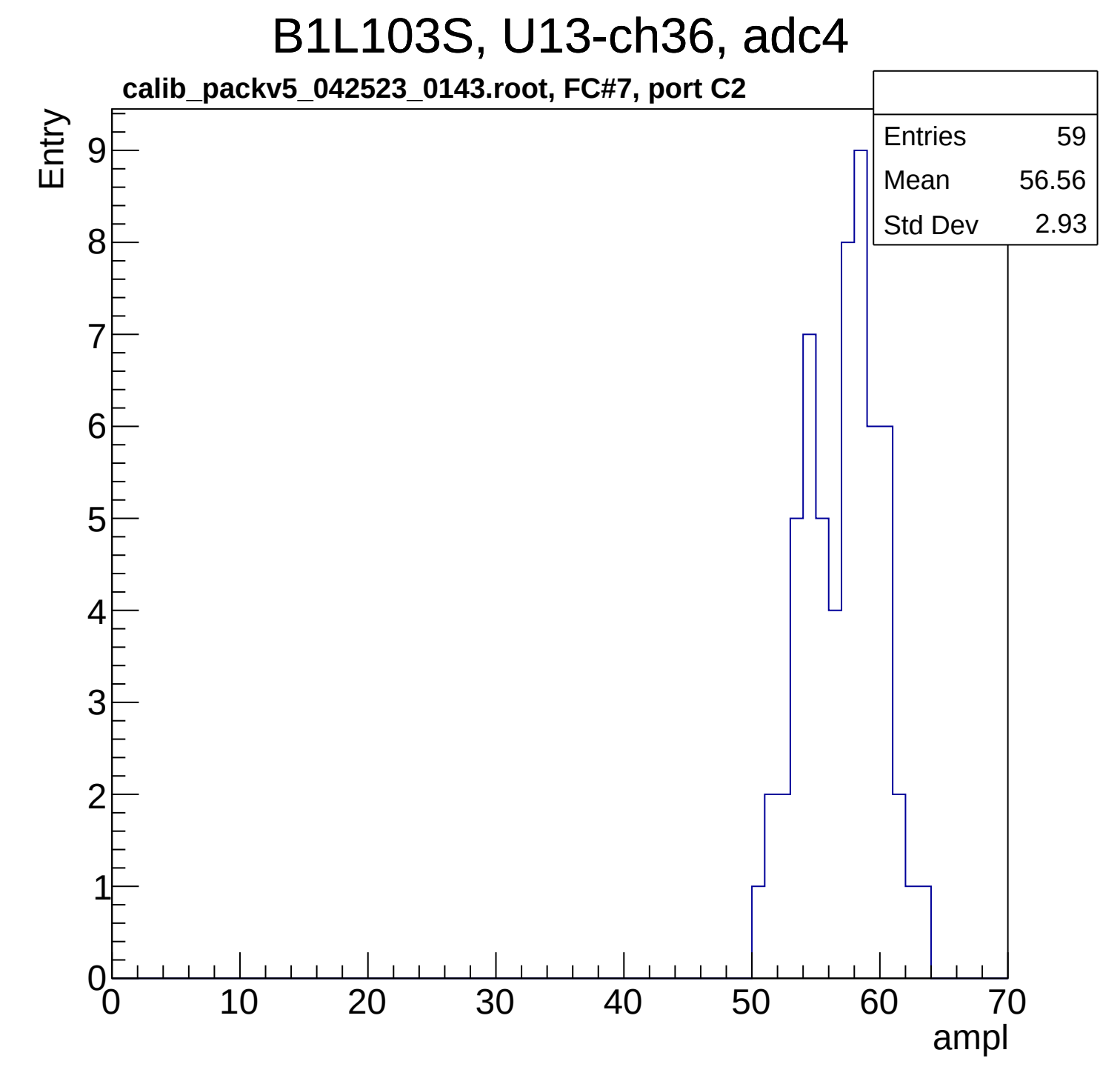
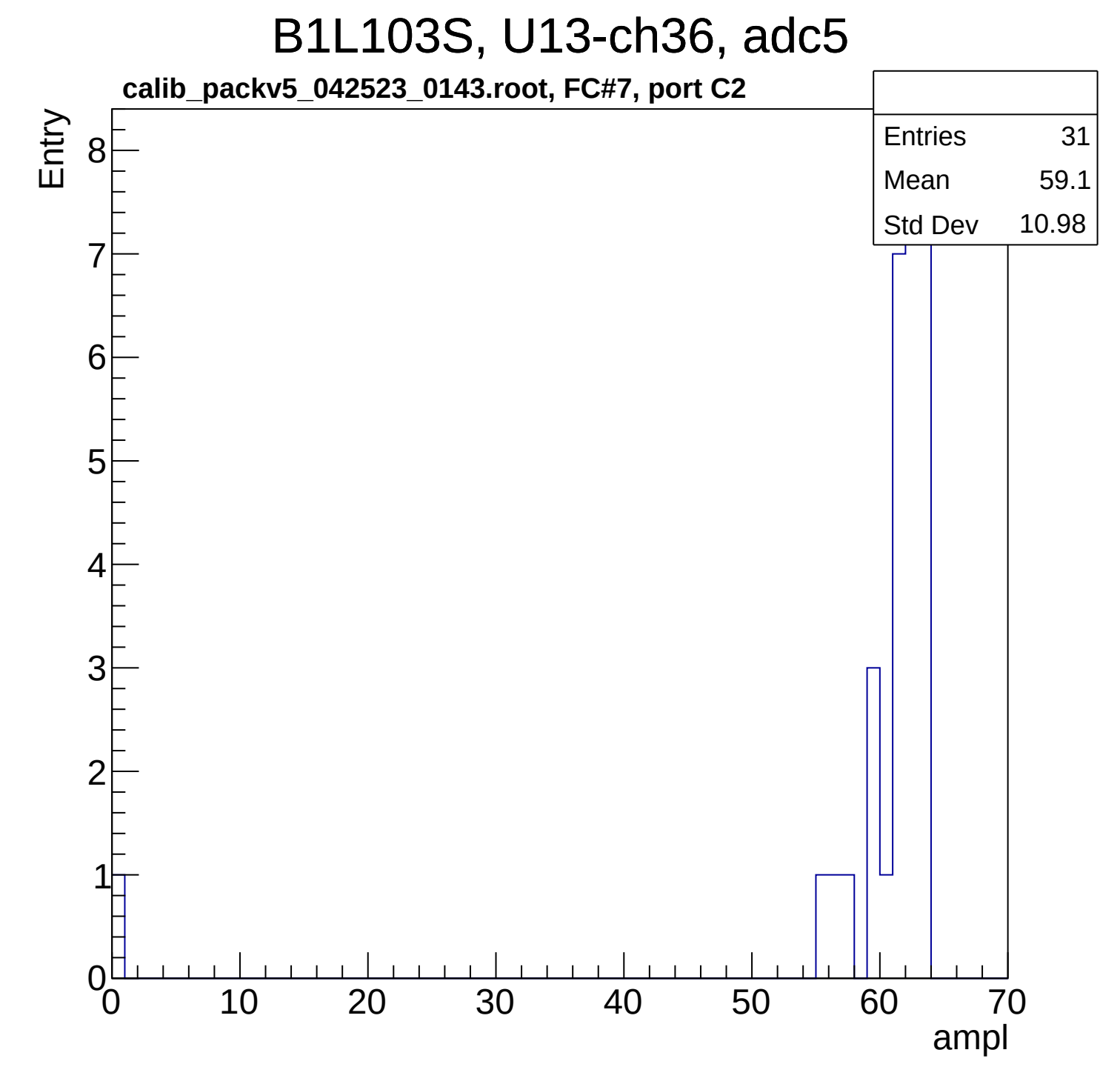
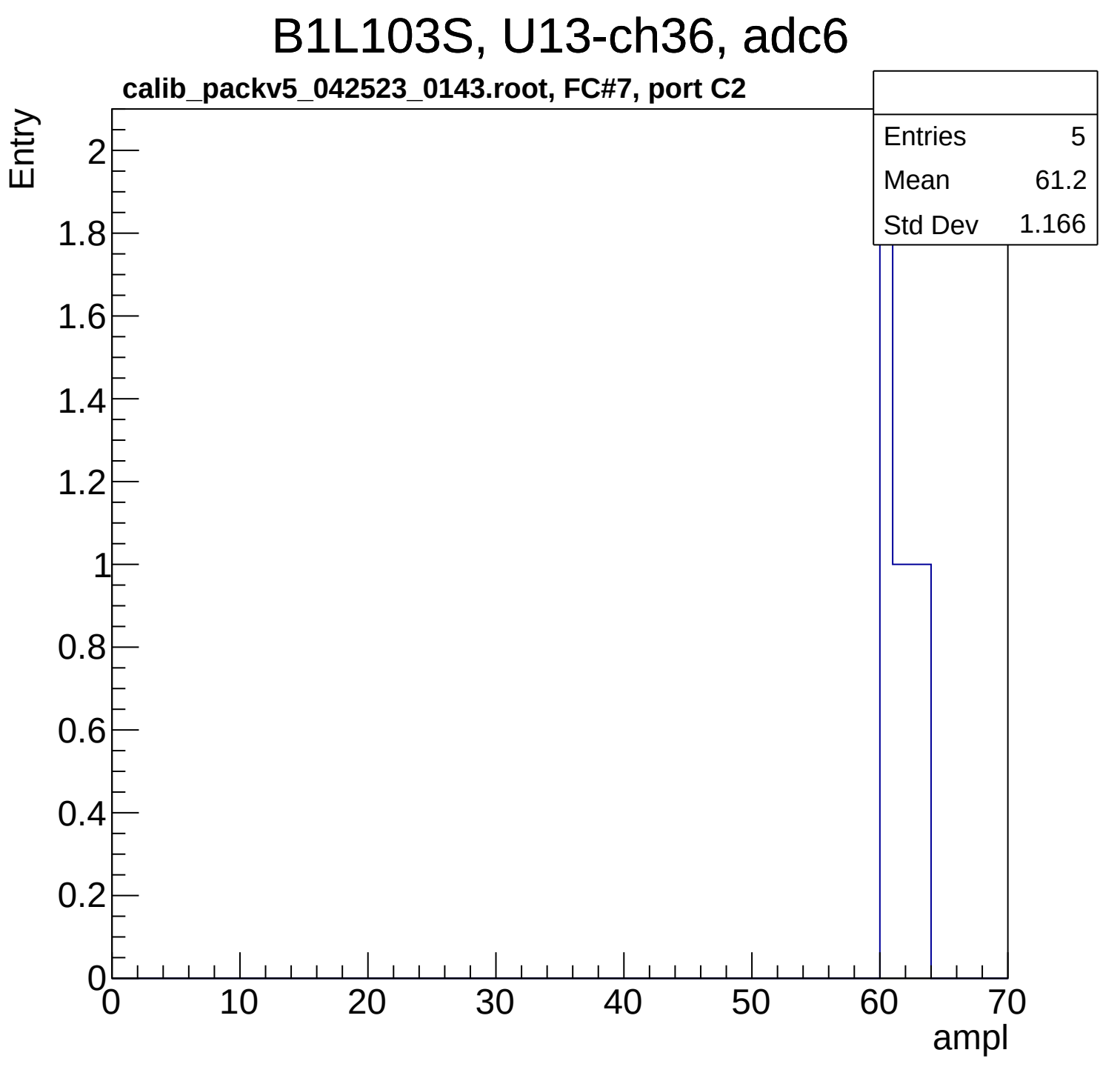
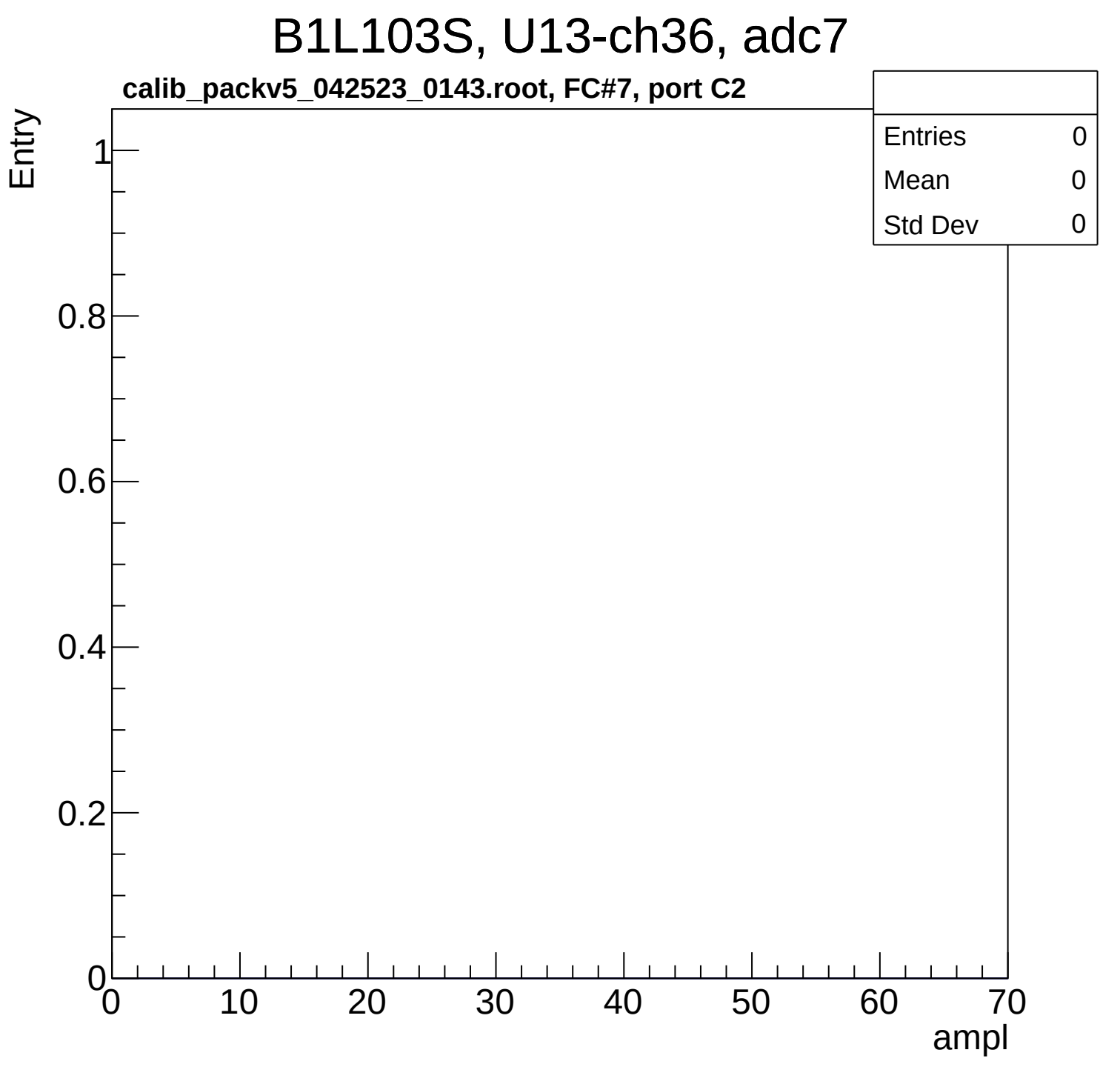
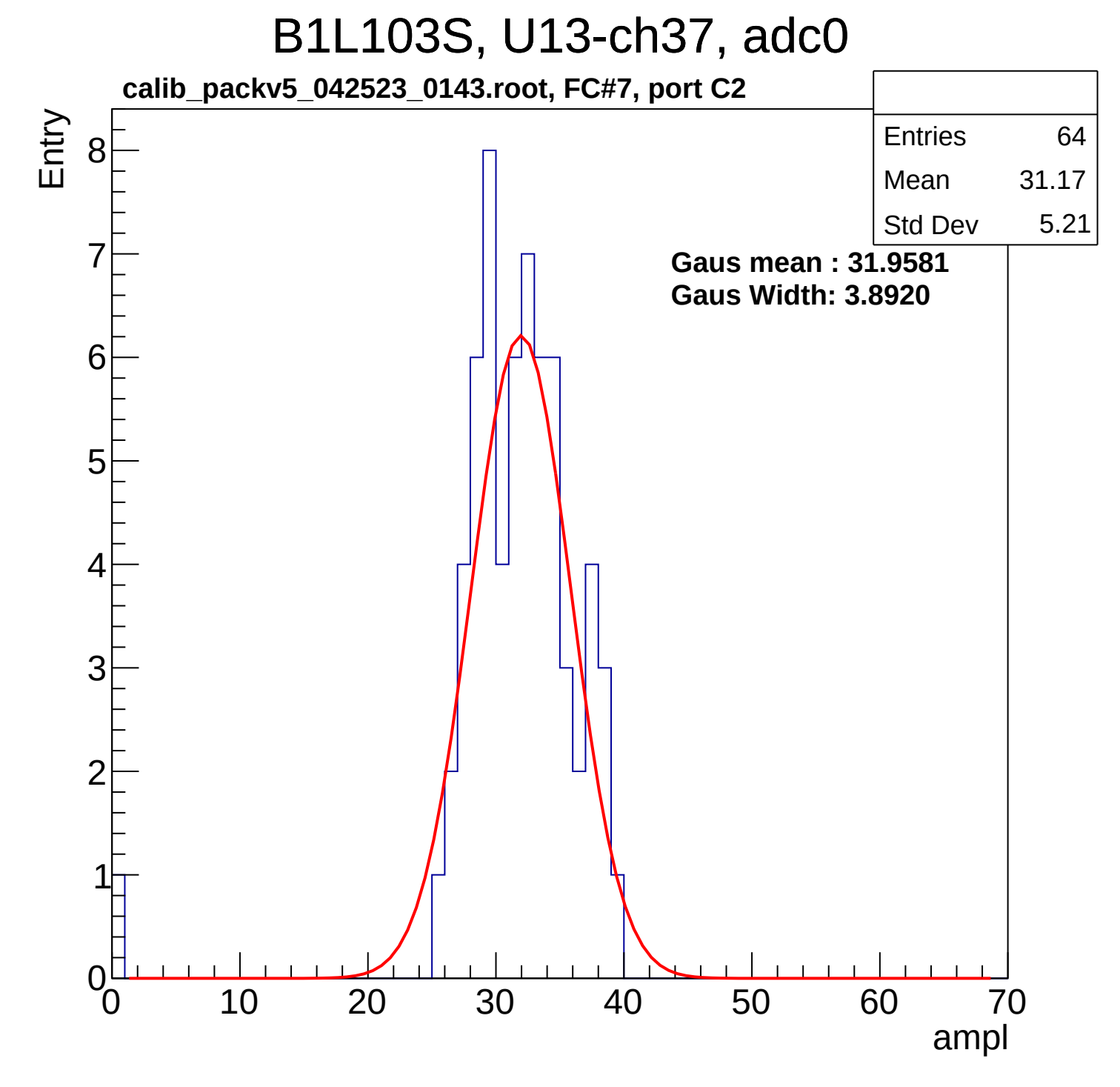

B1L103S, U13-ch36, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 50.71
Std Dev 4.139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 56.56
Std Dev 2.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 59.1
Std Dev 10.98
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.2
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch36, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch37, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 31.17
Std Dev 5.21
Gaus mean : 31.9581
Gaus Width: 3.8920
Entry
ampl
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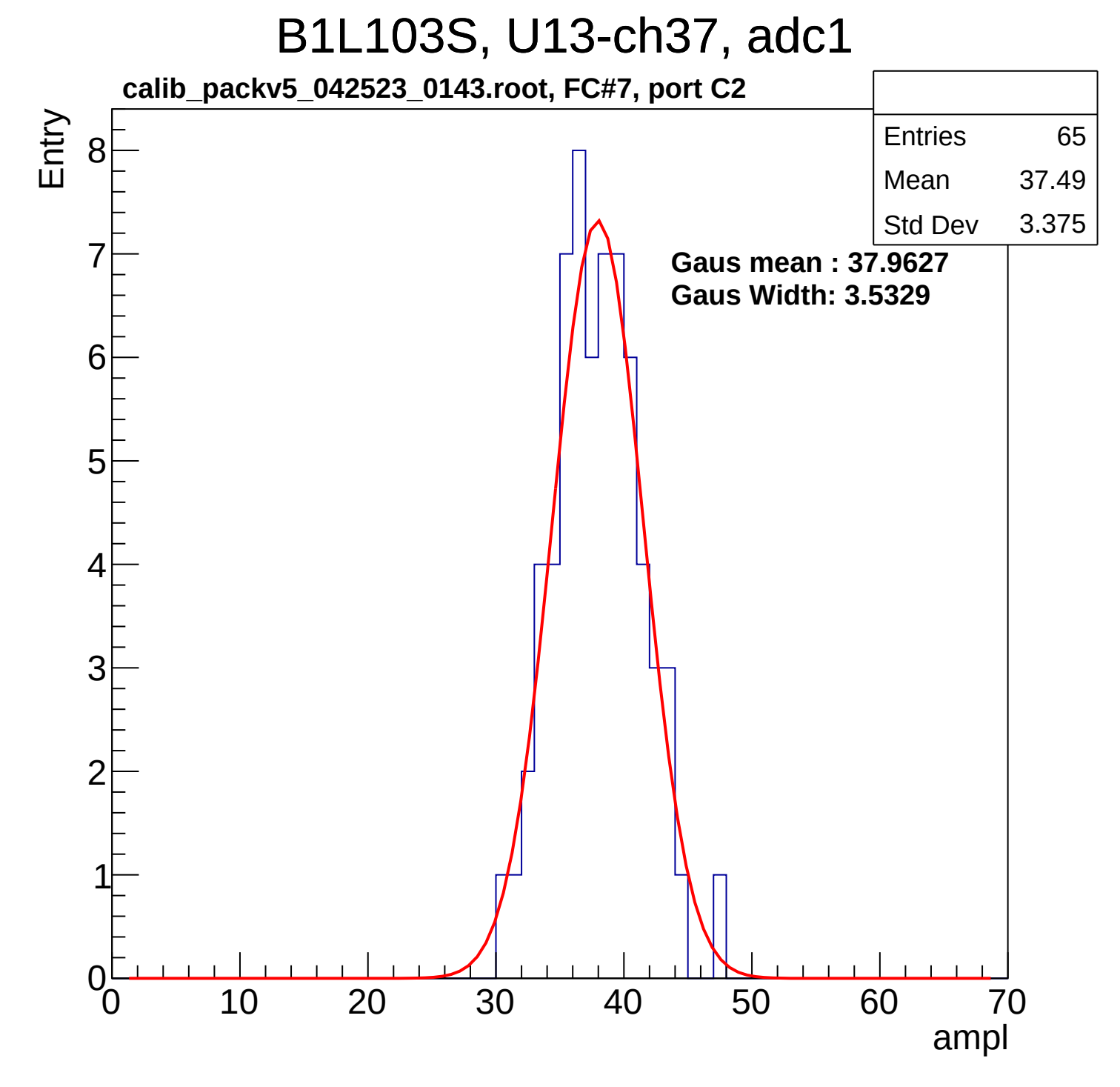
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch37, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 37.49
Std Dev 3.375
Gaus mean : 37.9627
Gaus Width: 3.5329
Entry
ampl
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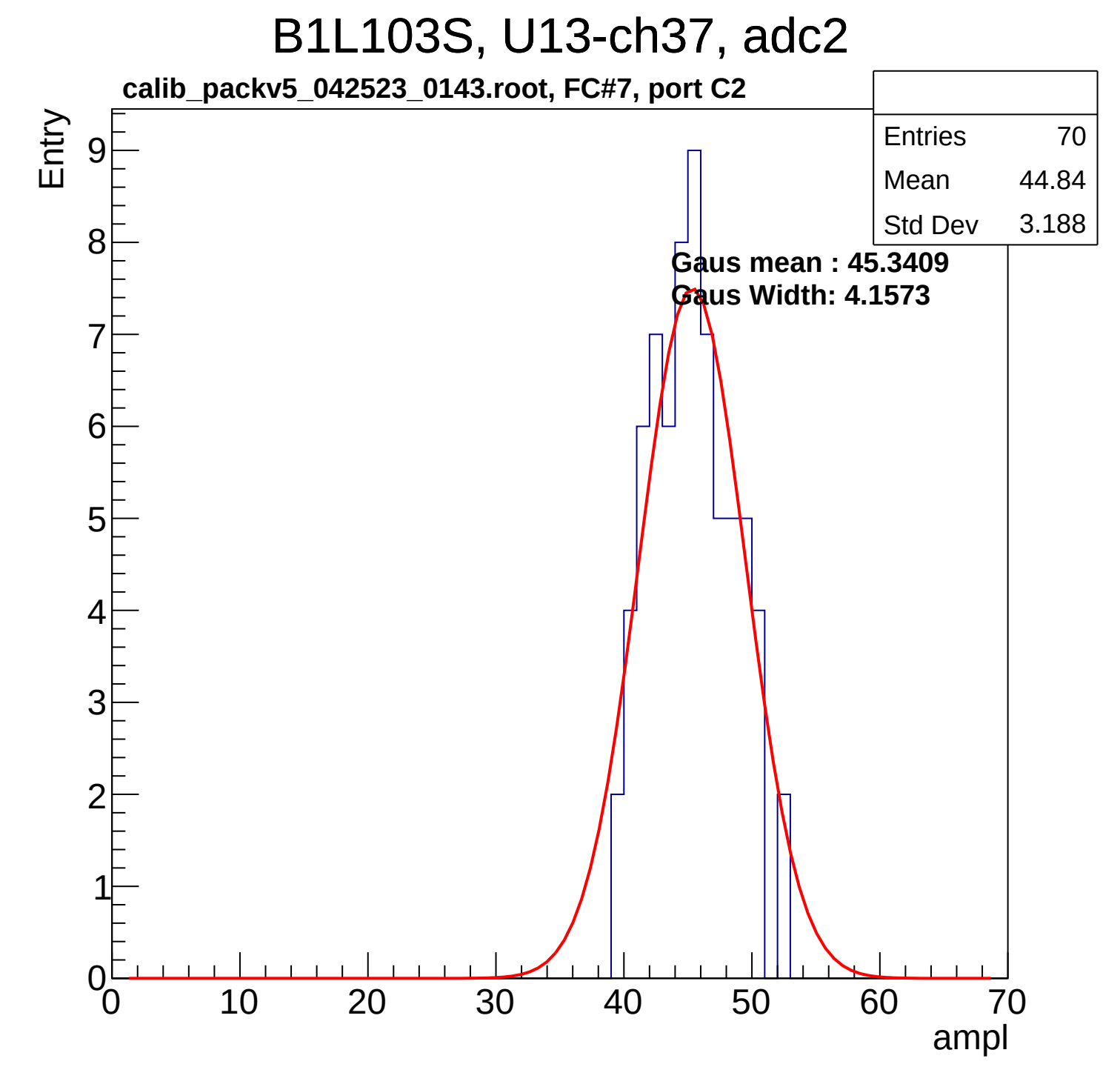
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch37, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 44.84
Std Dev 3.188
Gaus mean : 45.3409
Gaus Width: 4.1573
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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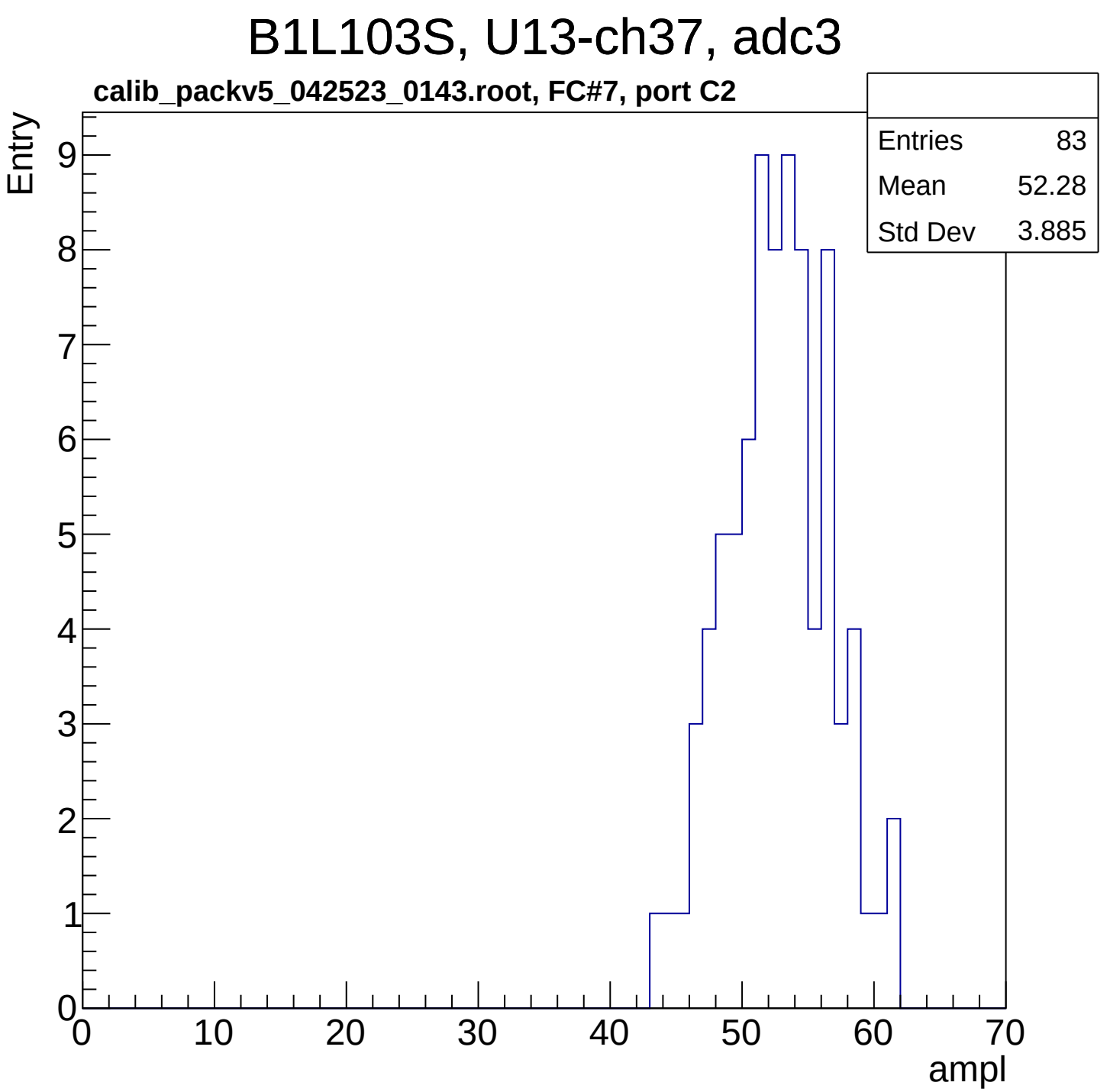
B1L103S, U13-ch37, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 52.28
Std Dev 3.885
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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60
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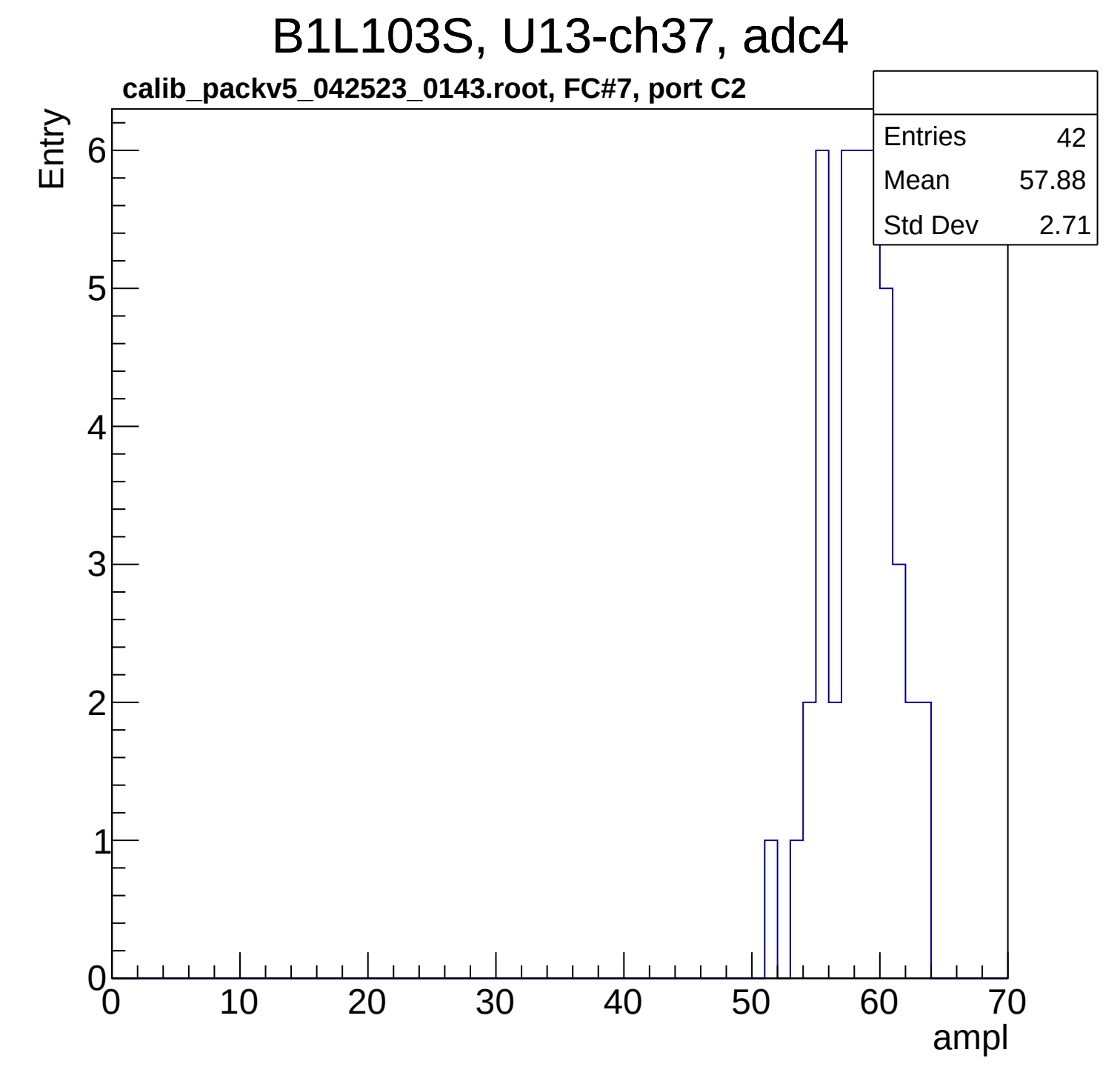
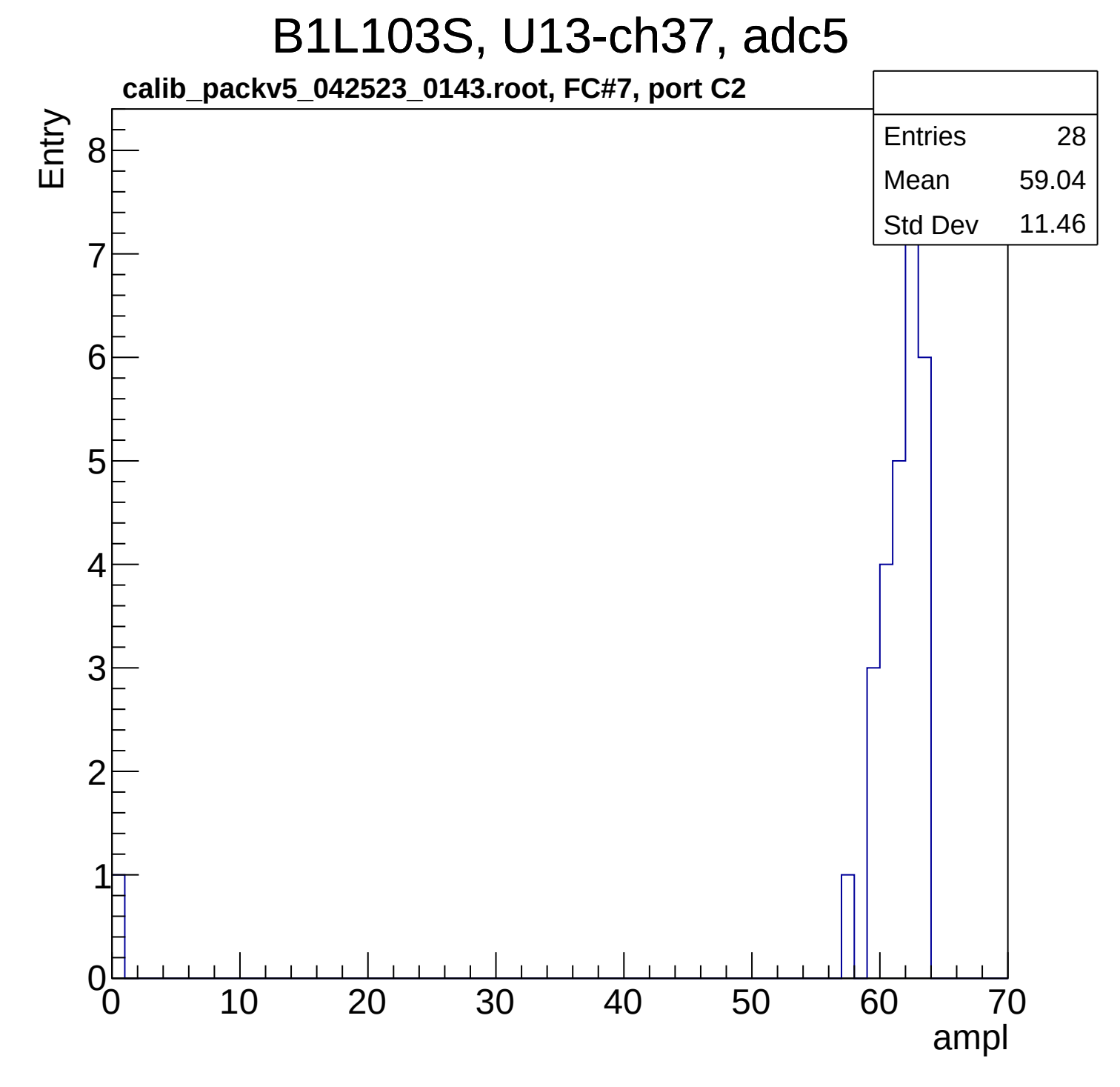

B1L103S, U13-ch37, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 57.88
Std Dev 2.71
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch37, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 28
Mean 59.04
Std Dev 11.46
Entry
ampl
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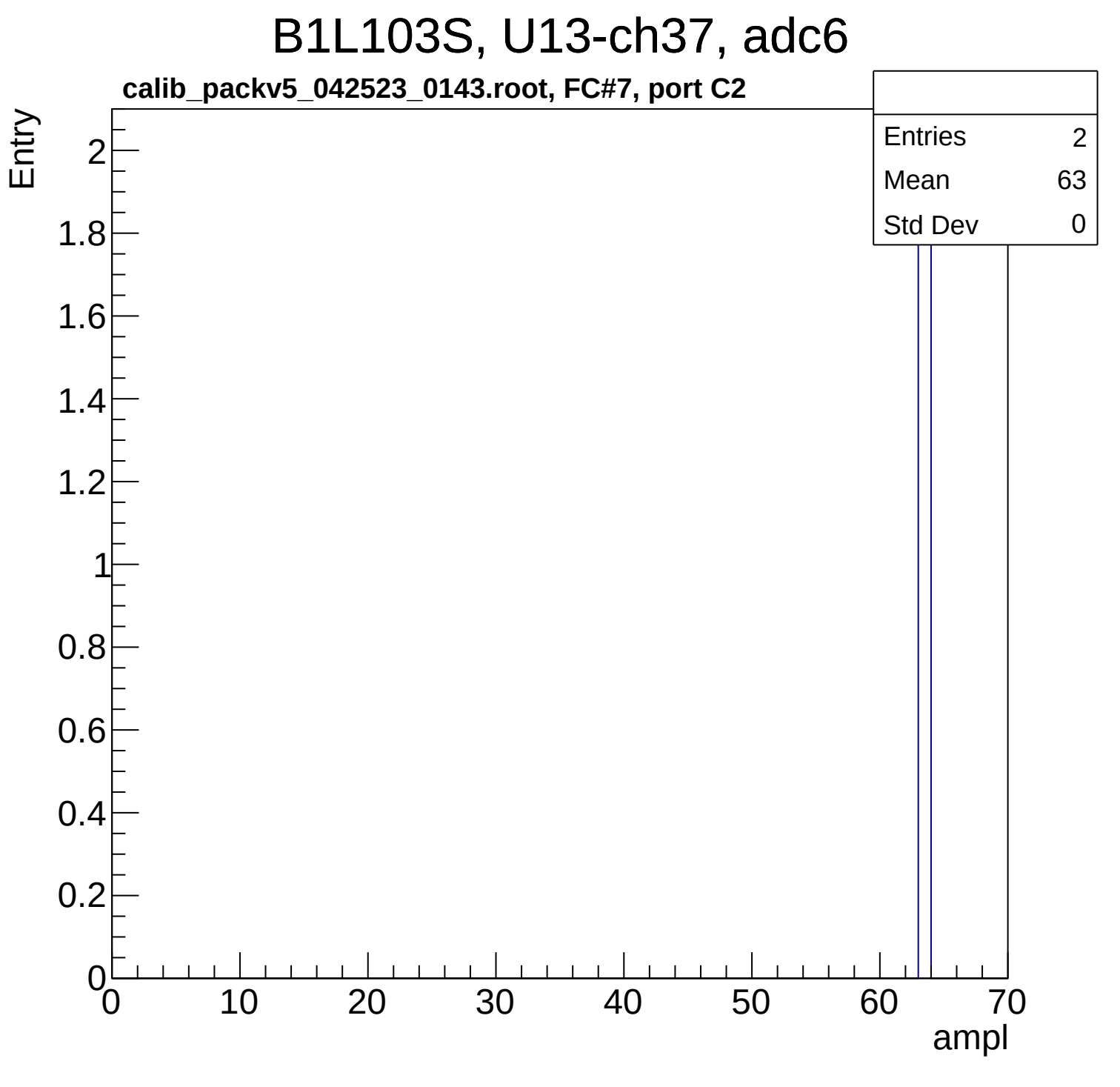
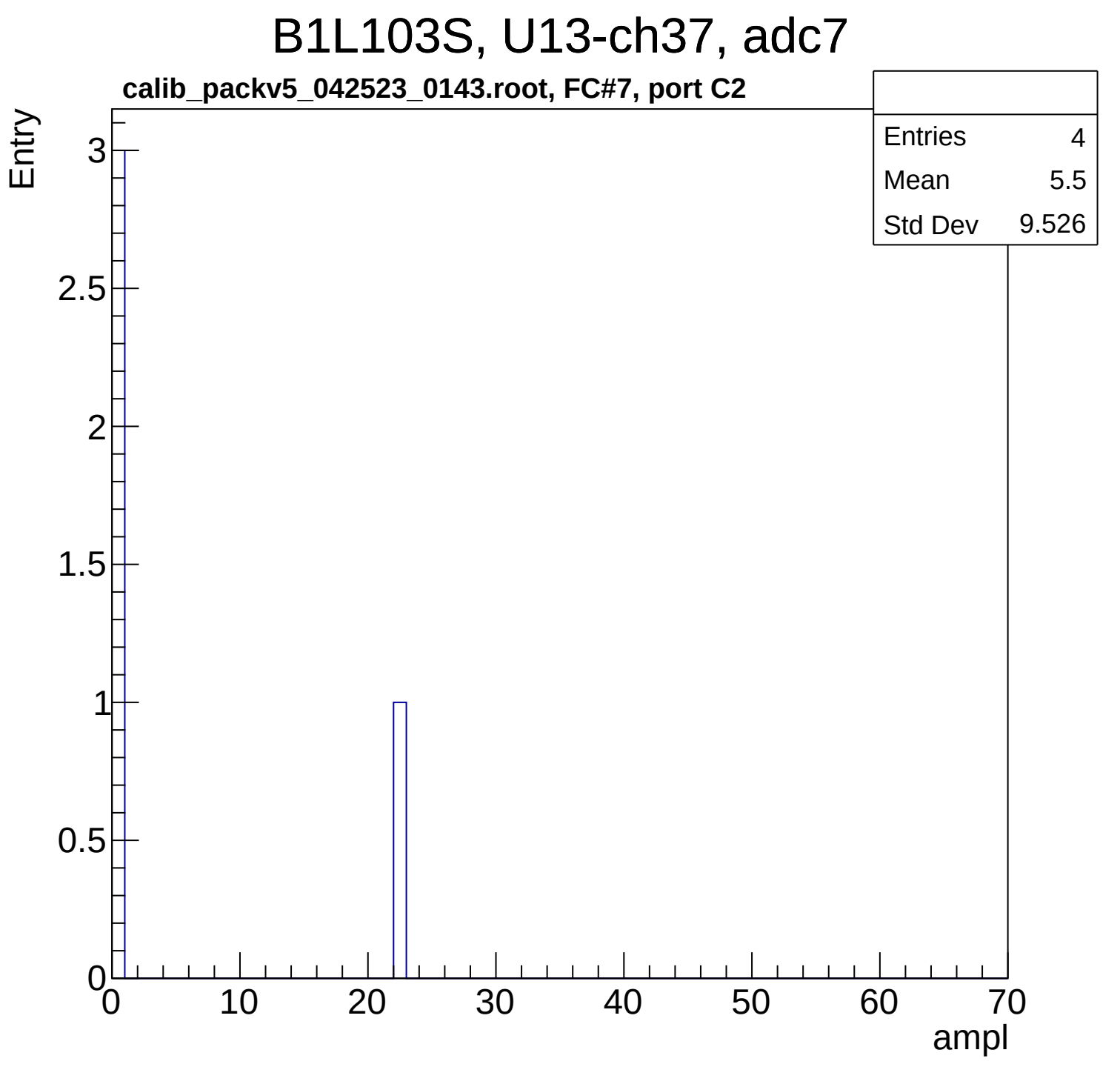
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch37, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch37, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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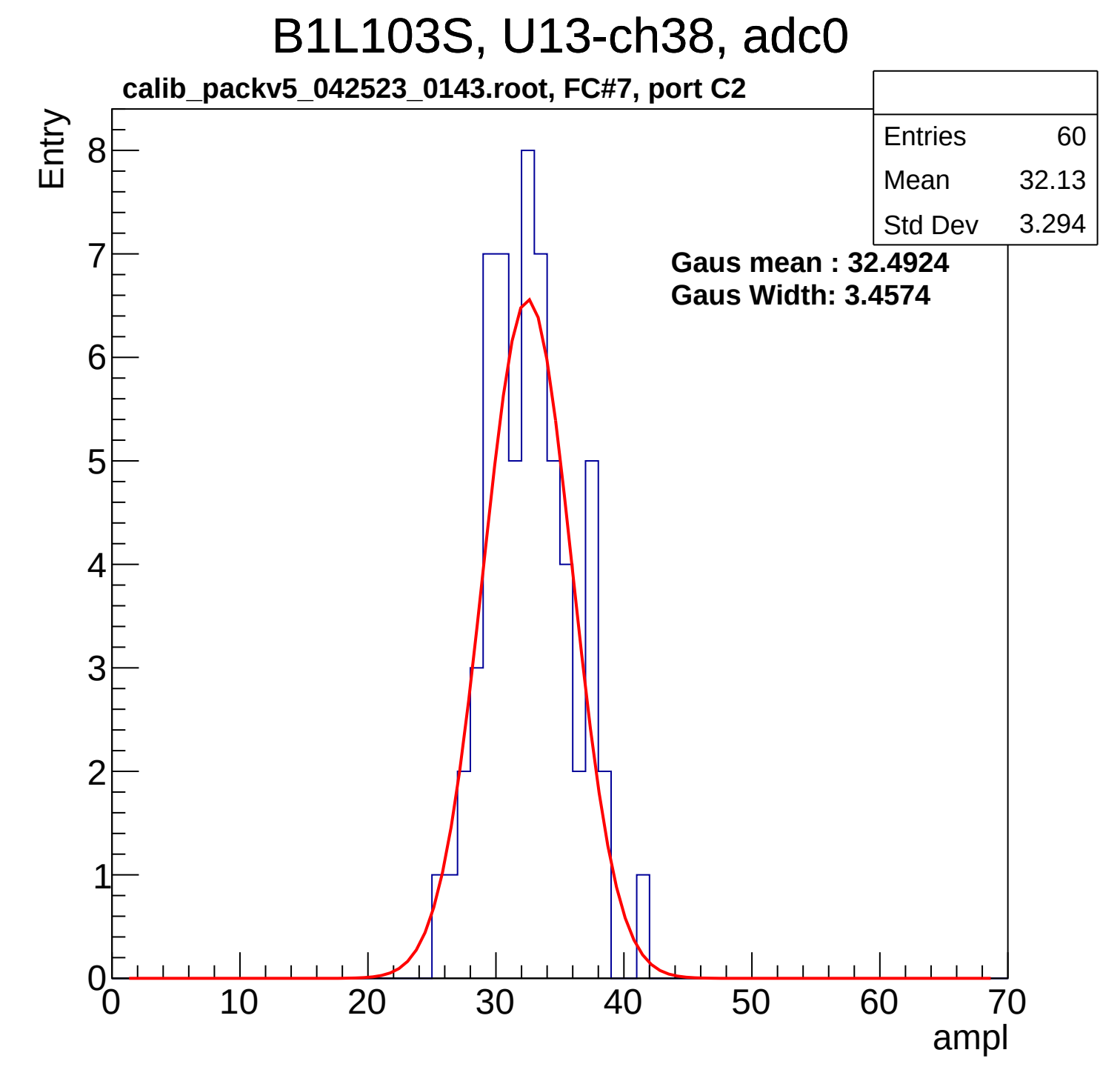
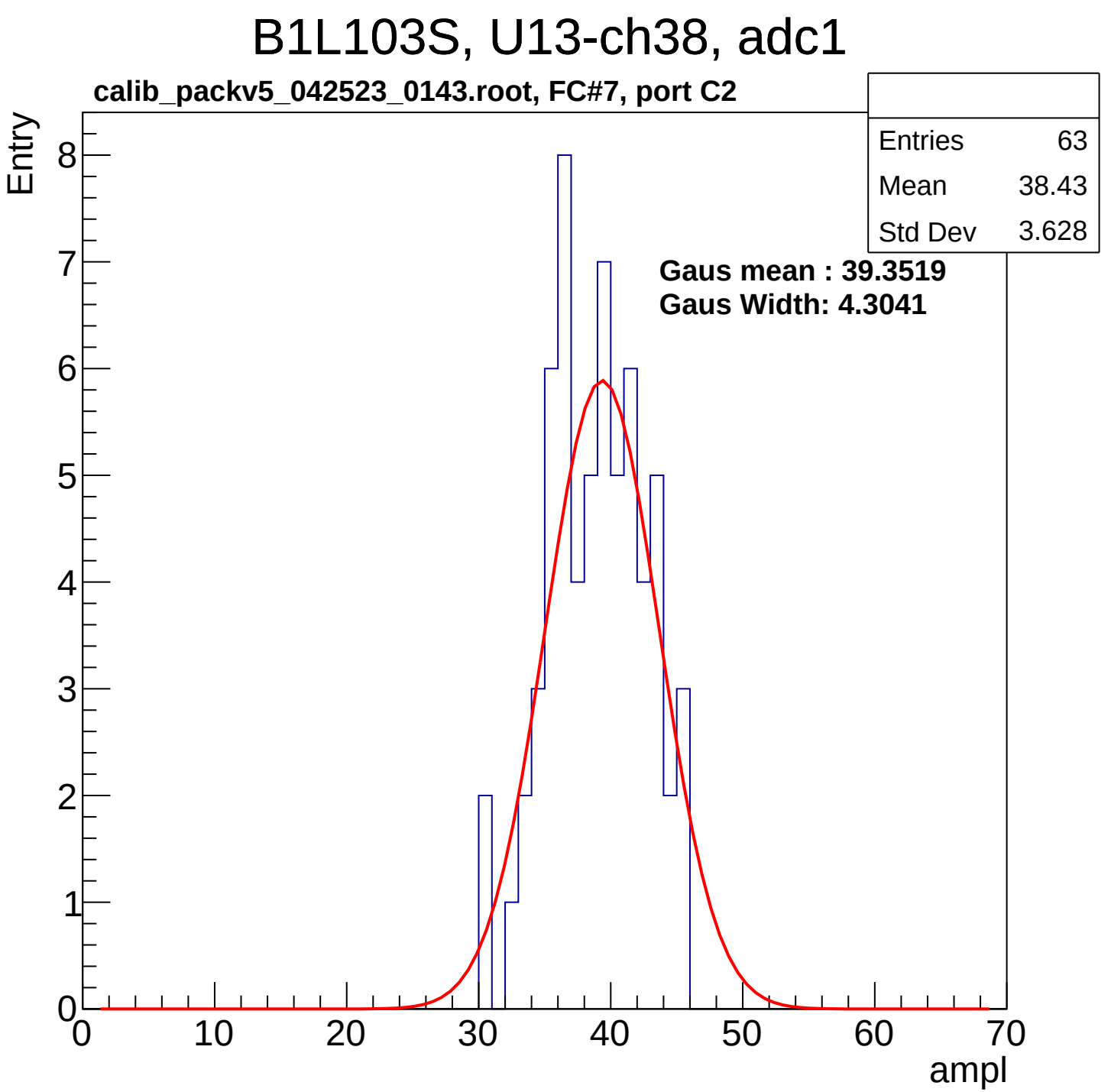
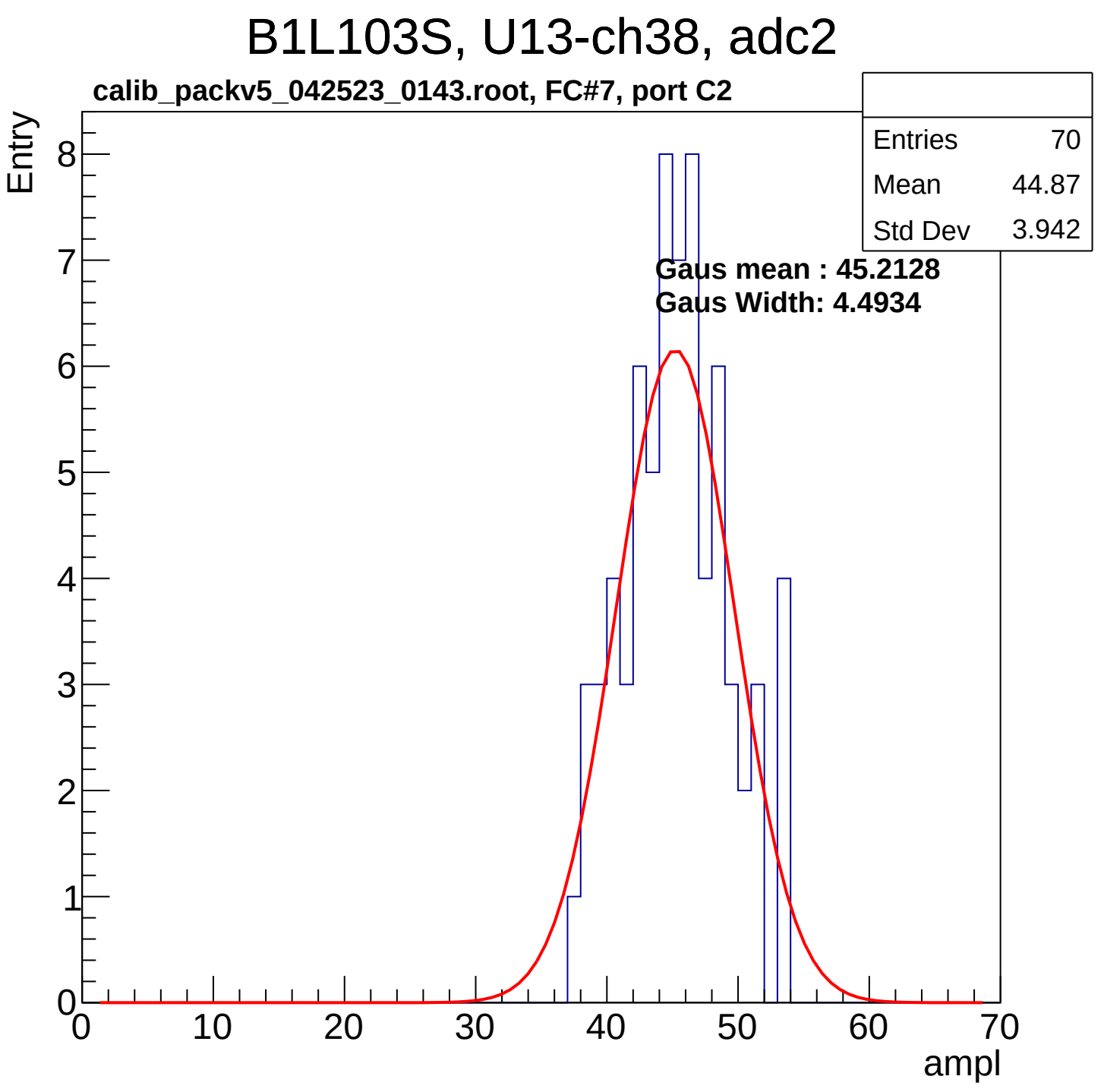
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch38, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 32.13
Std Dev 3.294
Gaus mean : 32.4924
Gaus Width: 3.4574
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch38, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 38.43
Std Dev 3.628
Gaus mean : 39.3519
Gaus Width: 4.3041
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch38, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 44.87
Std Dev 3.942
Gaus mean : 45.2128
Gaus Width: 4.4934
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
50
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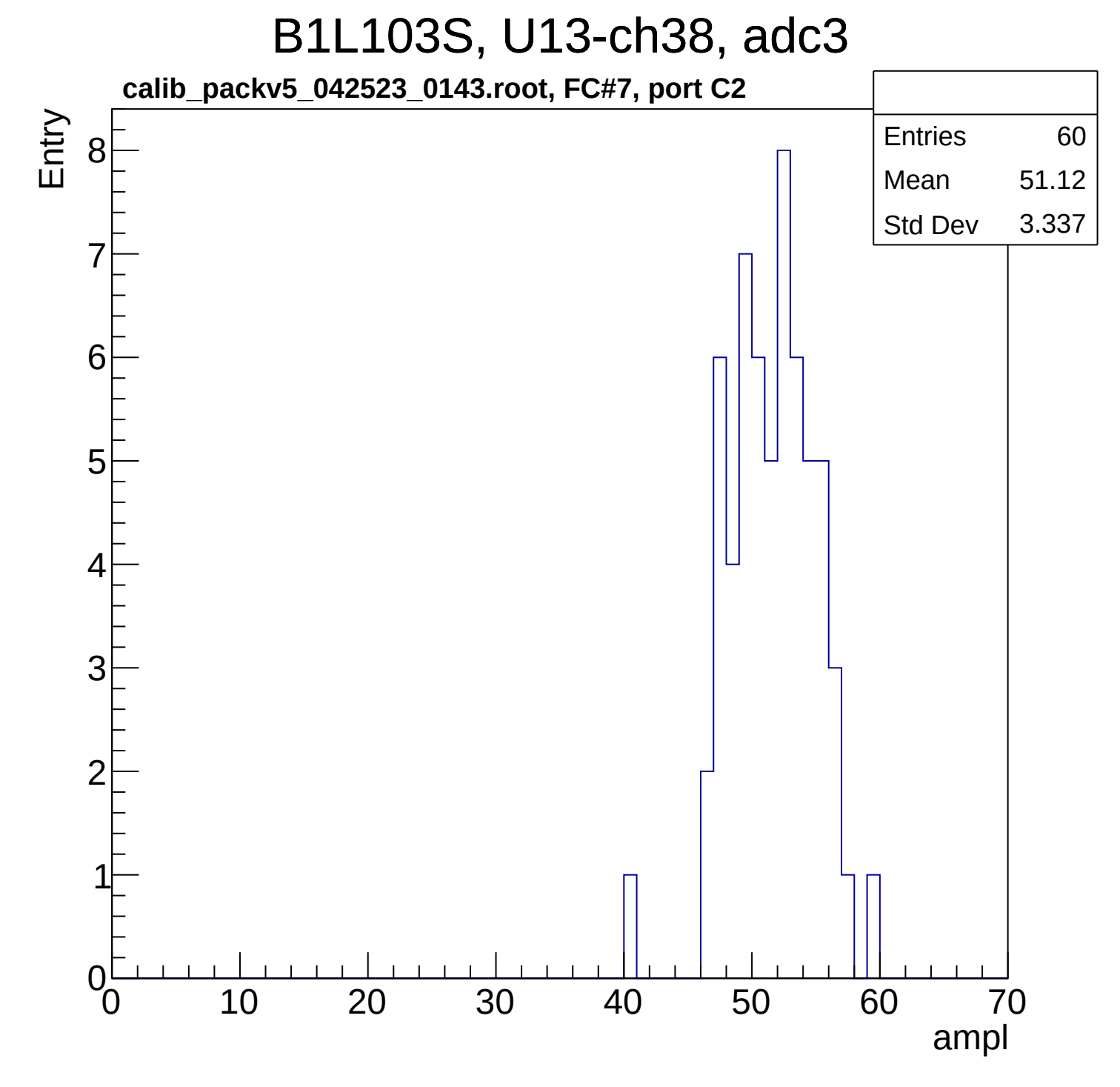
B1L103S, U13-ch38, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 51.12
Std Dev 3.337
Entry
ampl
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4
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40
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60
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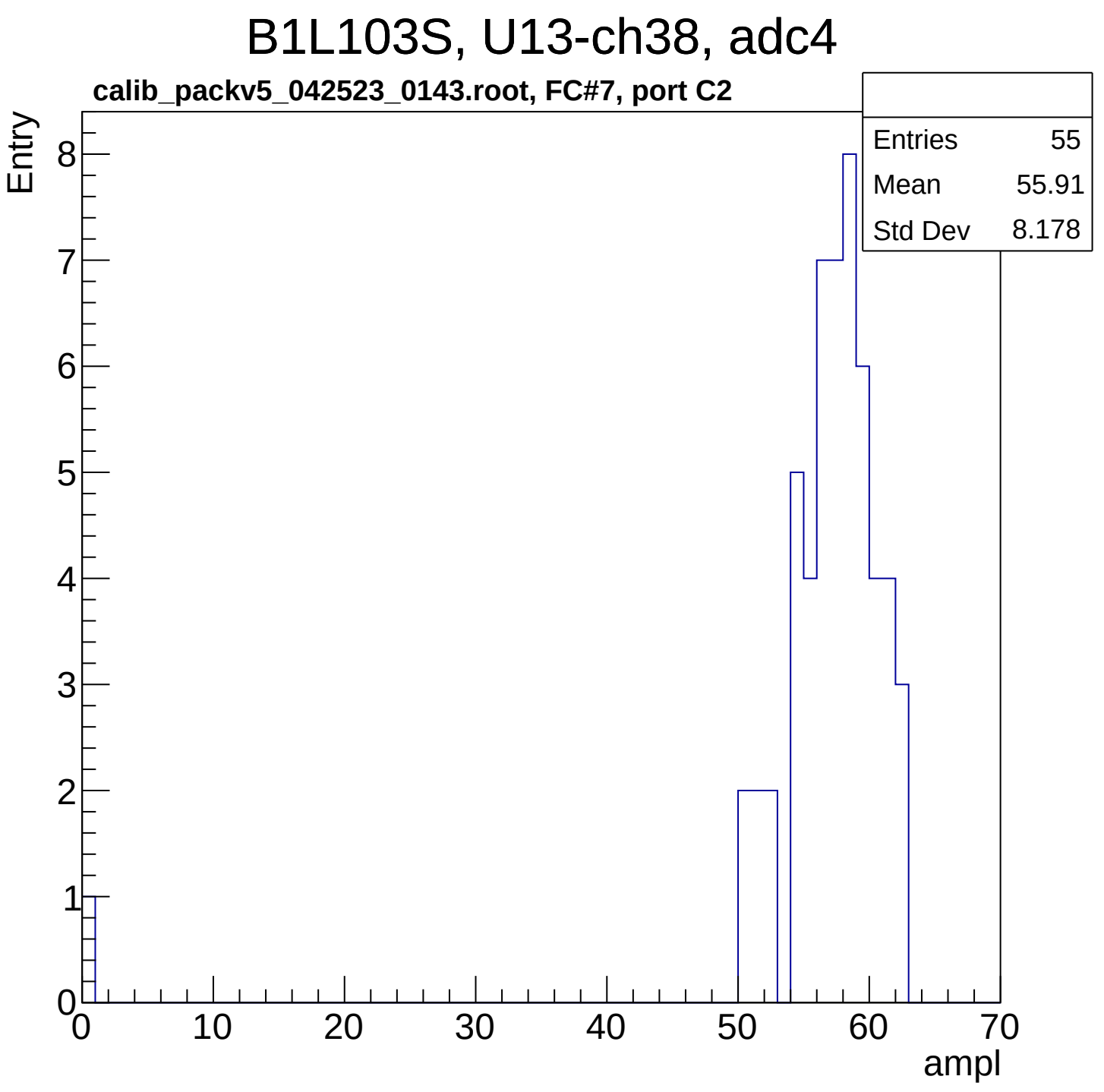
B1L103S, U13-ch38, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.91
Std Dev 8.178
Entry
ampl
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4
5
6
7
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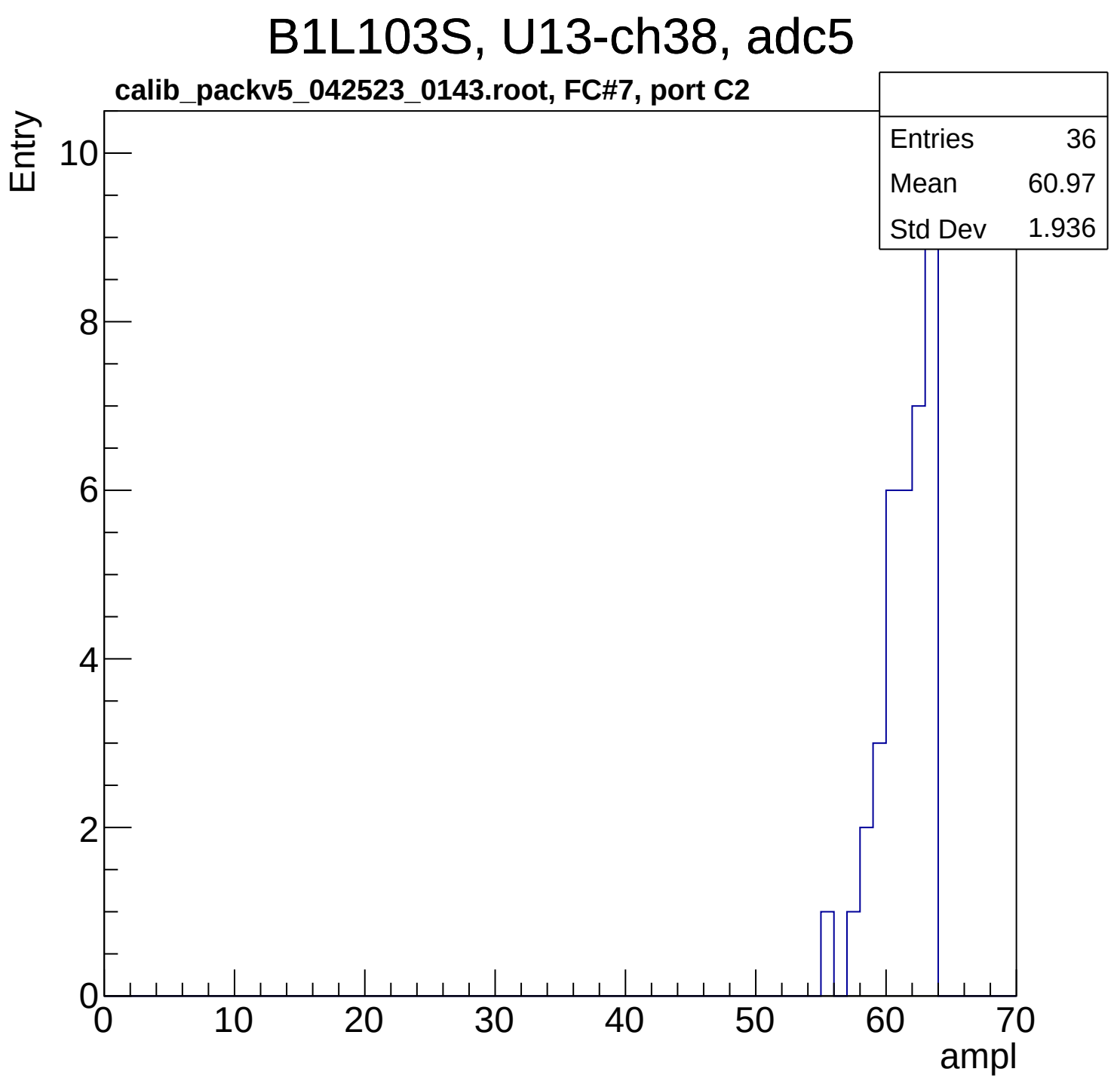
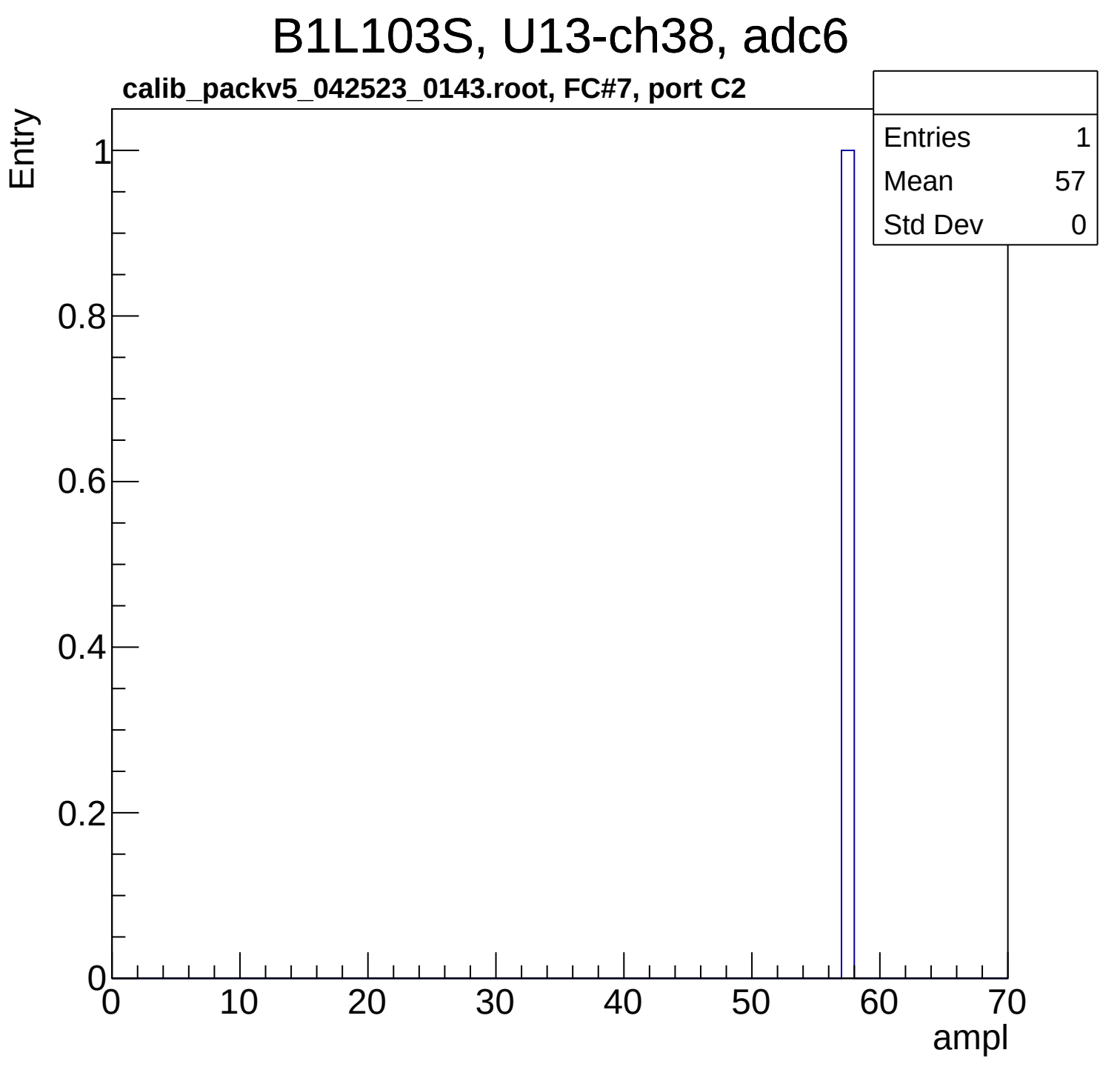
0
10
20
30
40
50
60
70


B1L103S, U13-ch38, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 60.97
Std Dev 1.936
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch38, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 57
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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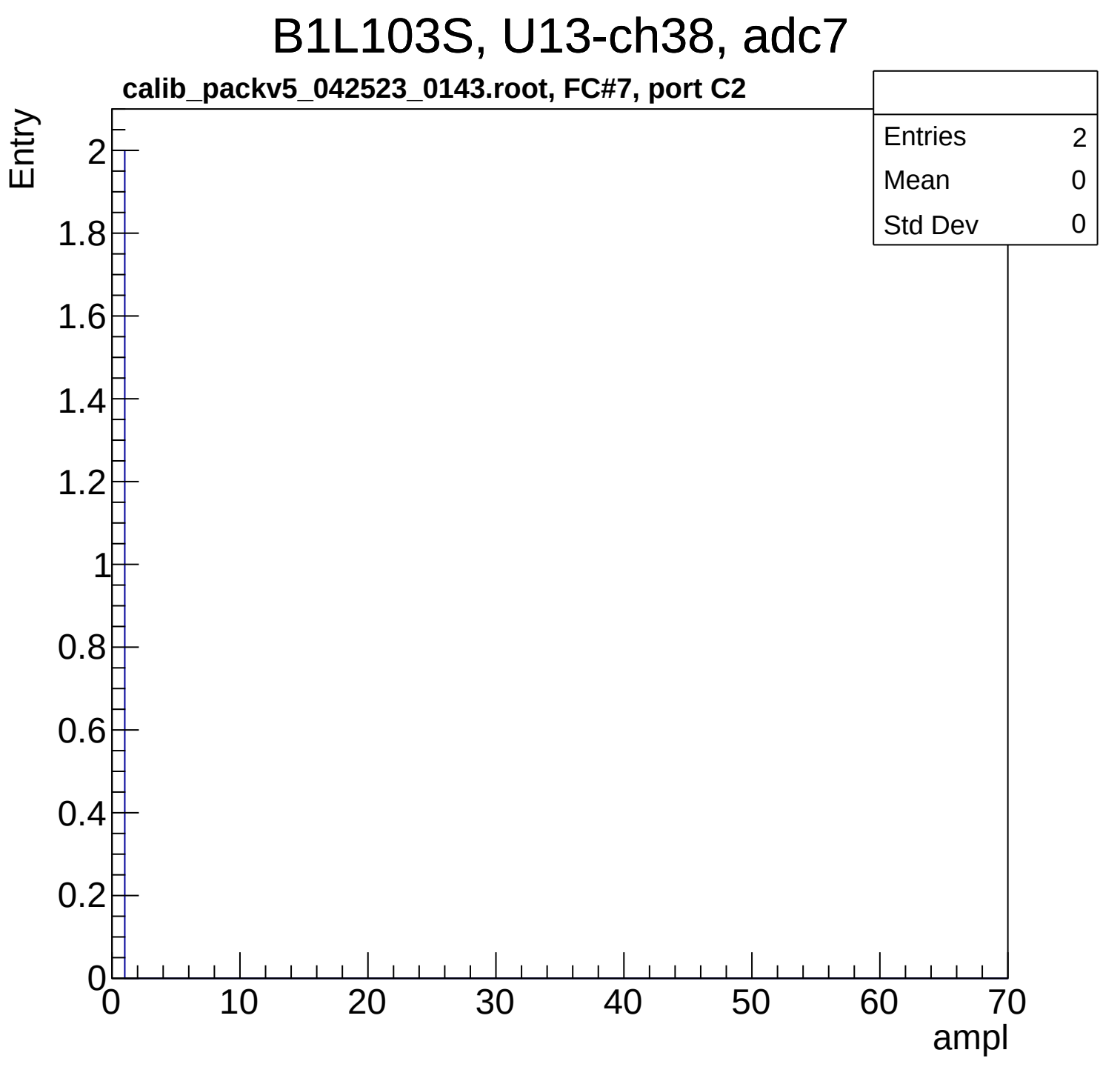
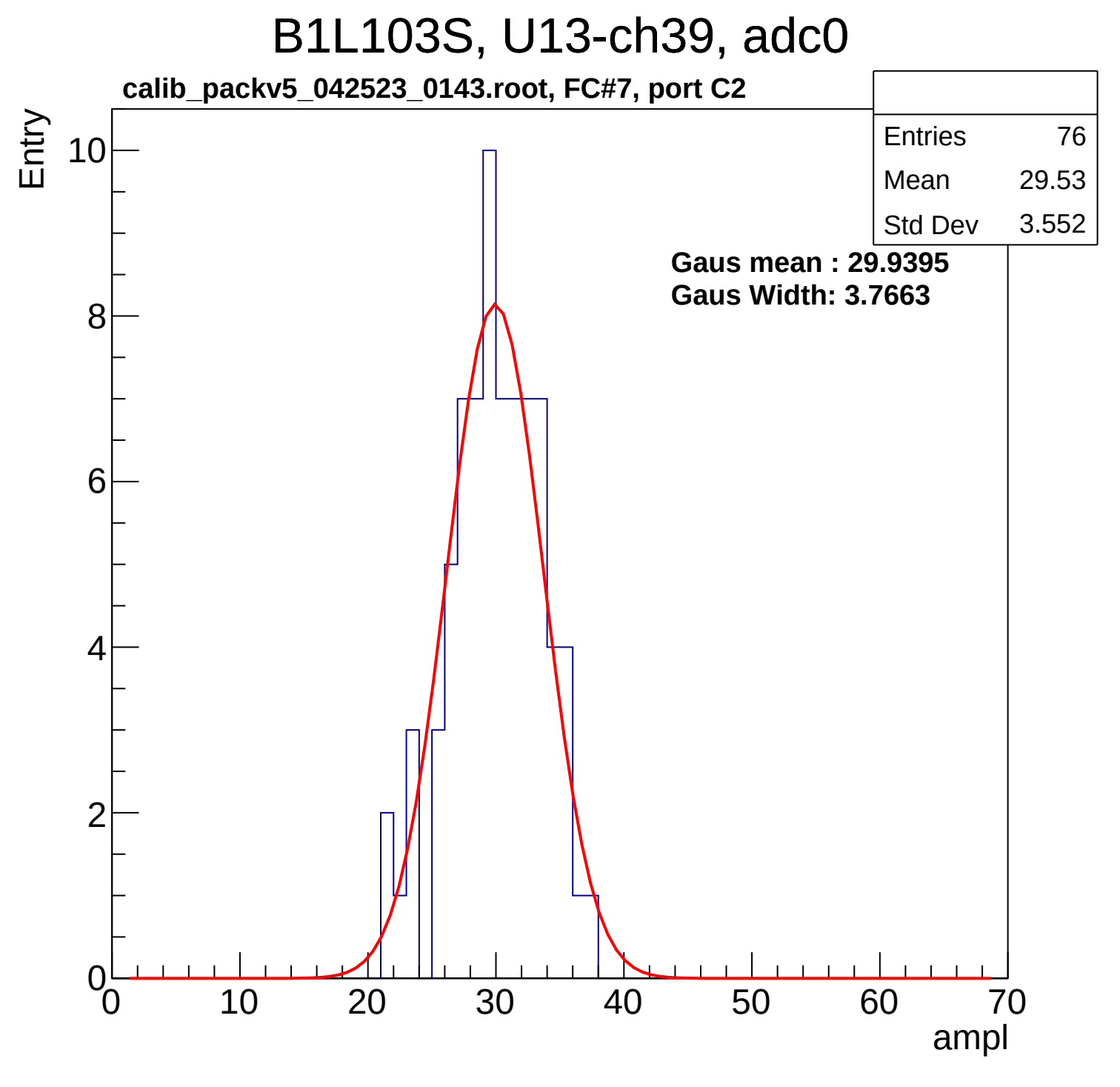
50
60
70
ampl


B1L103S, U13-ch38, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch39, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.53
Std Dev 3.552
Gaus mean : 29.9395
Gaus Width: 3.7663
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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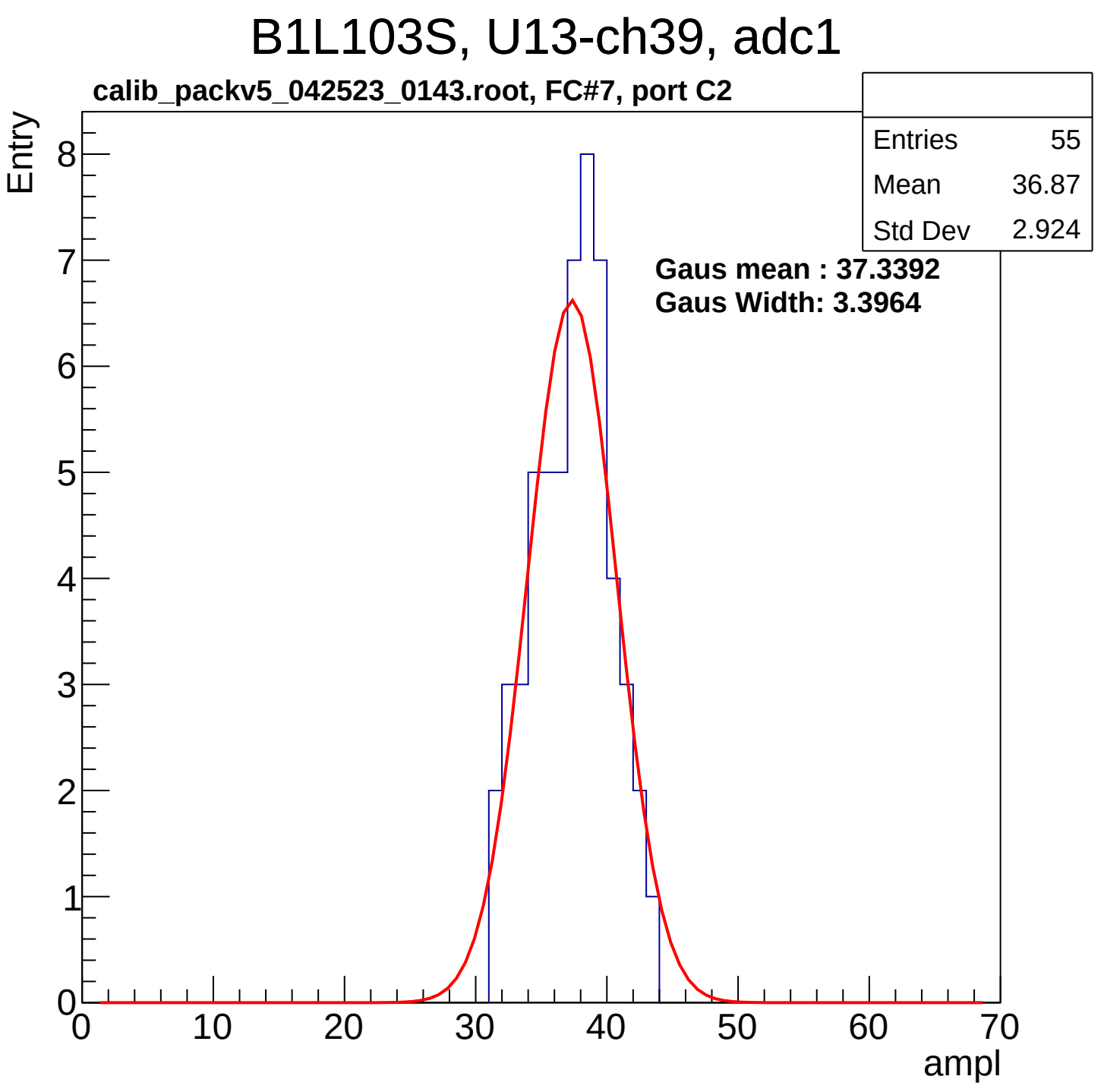
B1L103S, U13-ch39, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 36.87
Std Dev 2.924
Gaus mean : 37.3392
Gaus Width: 3.3964
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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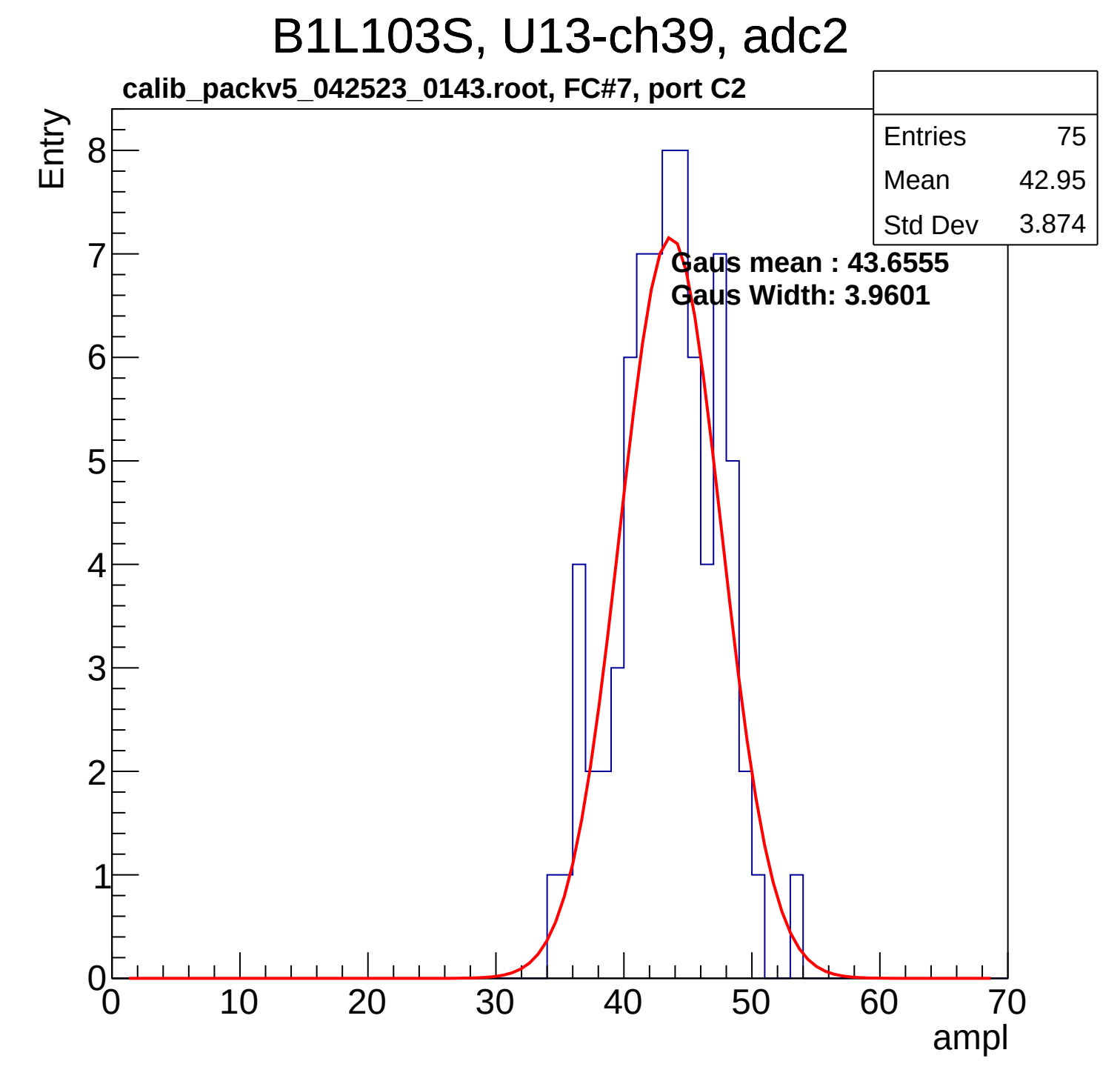
B1L103S, U13-ch39, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 42.95
Std Dev 3.874
Gaus mean : 43.6555
Gaus Width: 3.9601
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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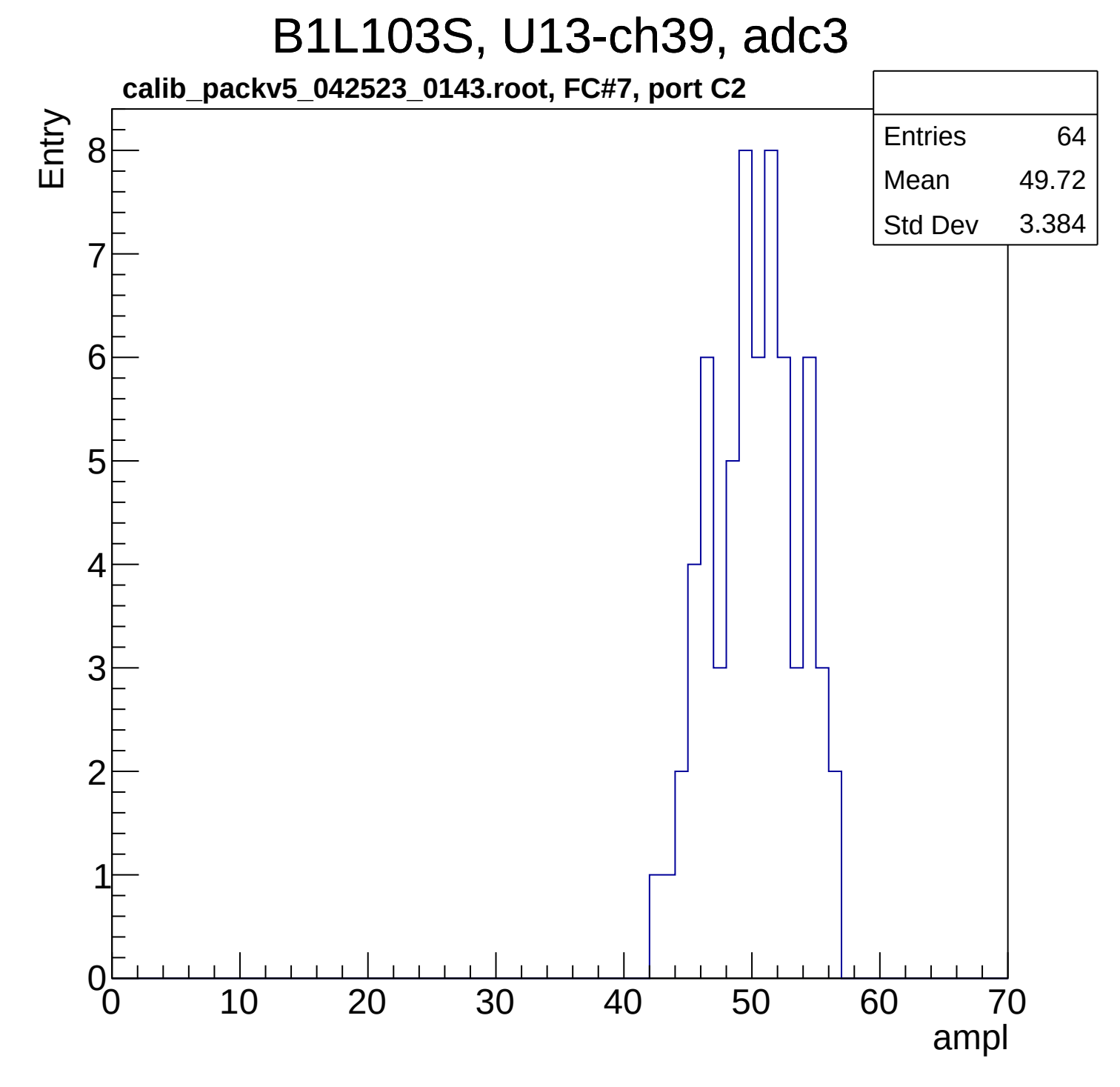
B1L103S, U13-ch39, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.72
Std Dev 3.384
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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50
60
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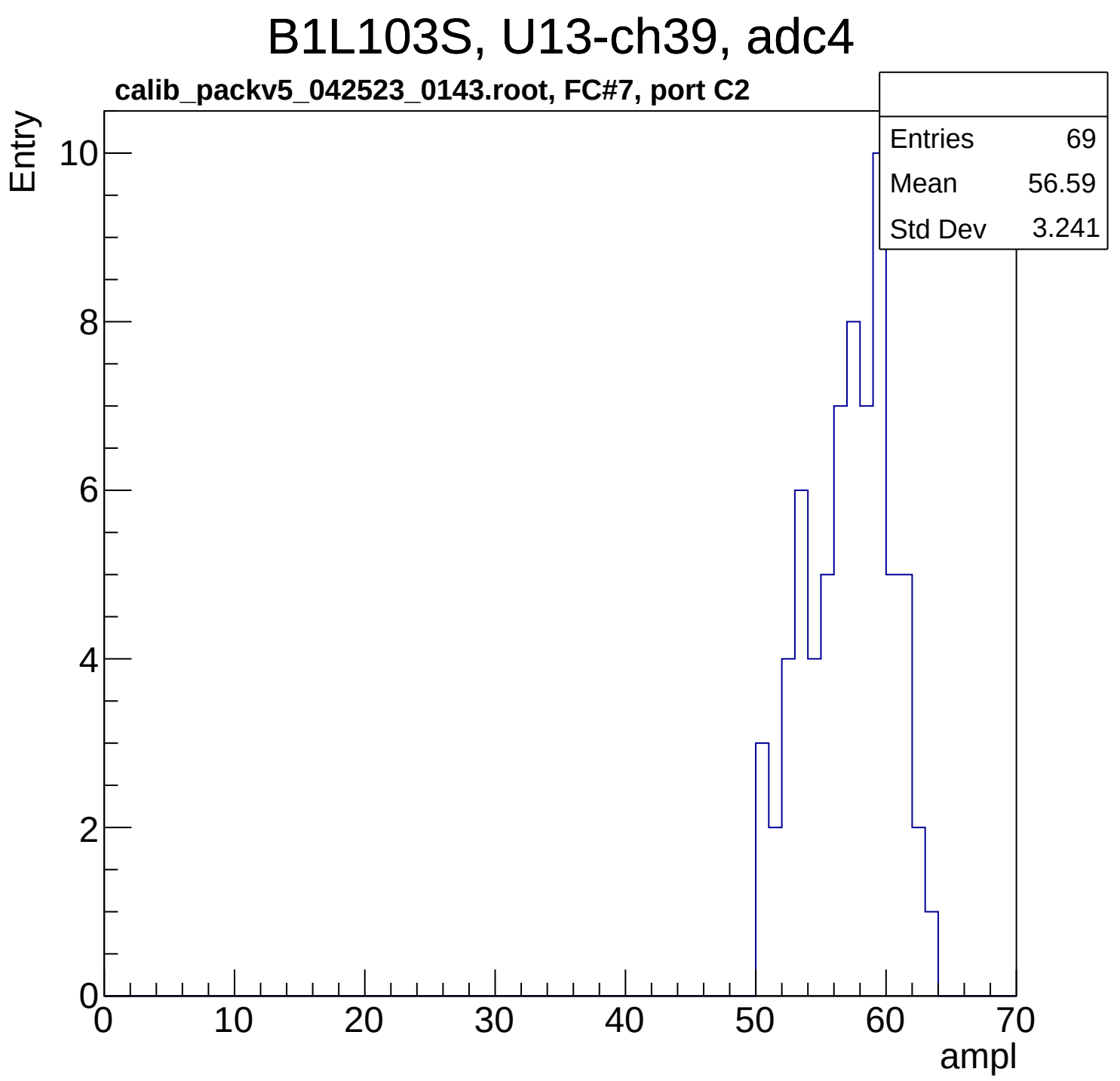
B1L103S, U13-ch39, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 56.59
Std Dev 3.241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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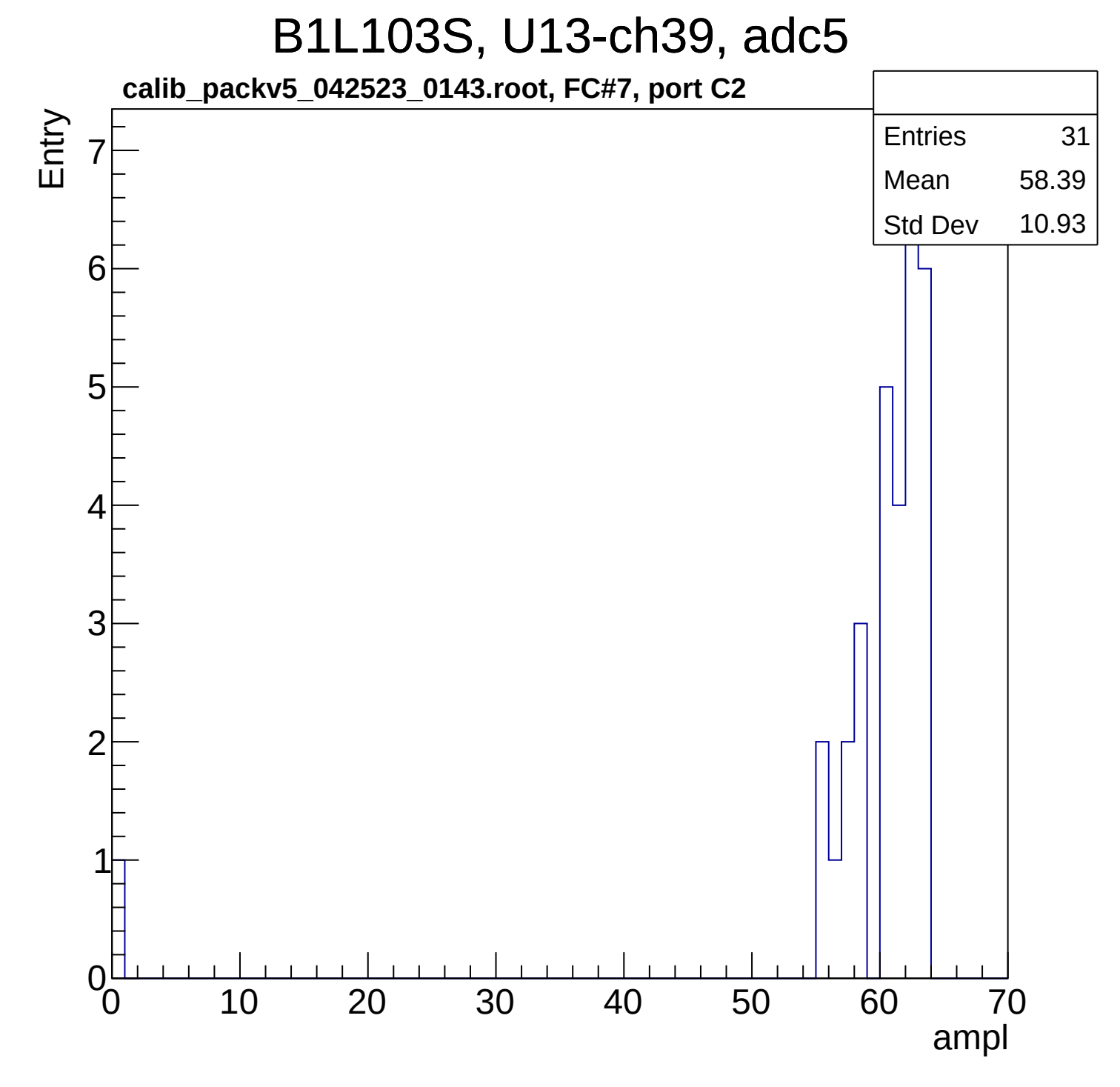
B1L103S, U13-ch39, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 58.39
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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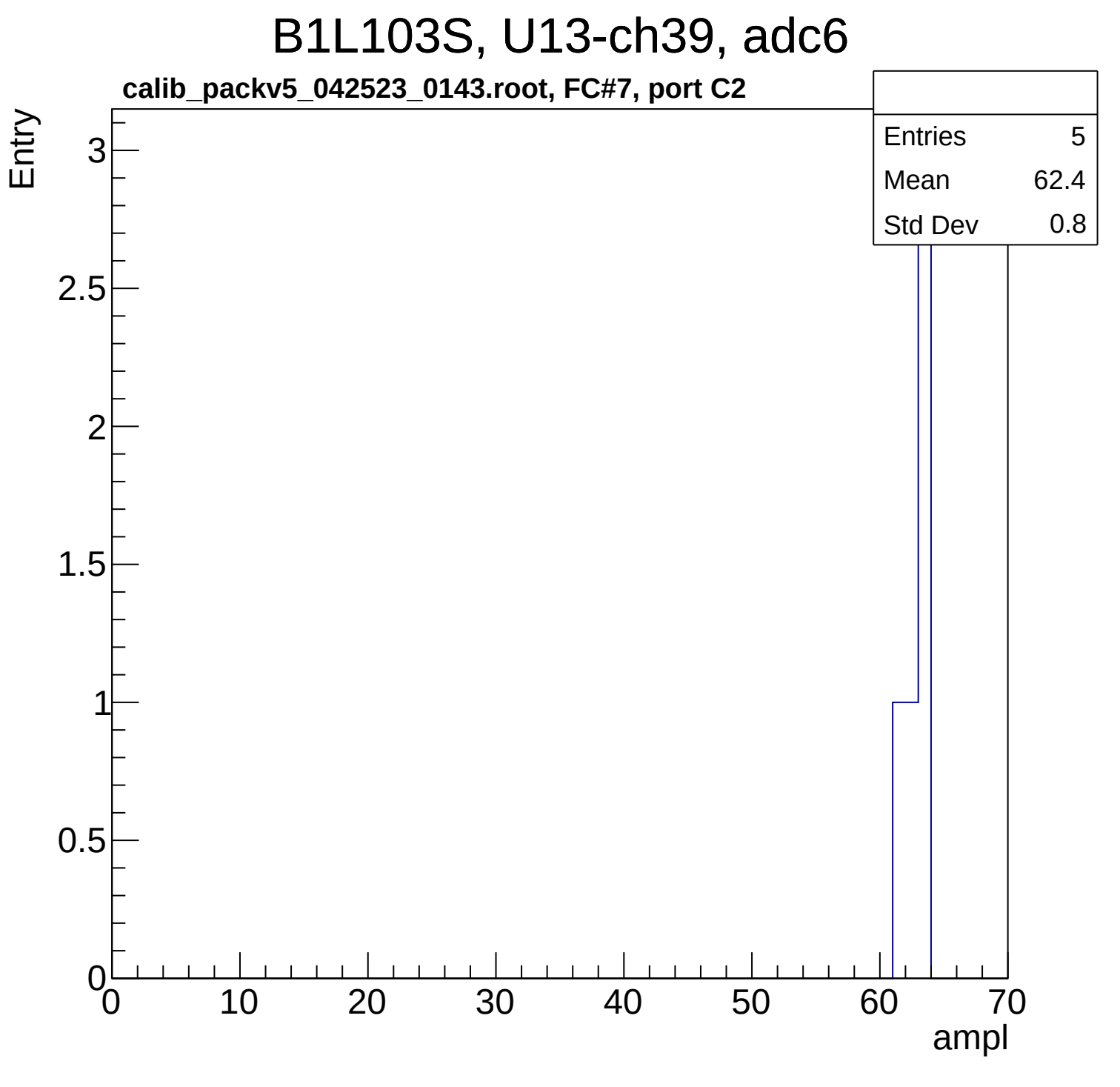
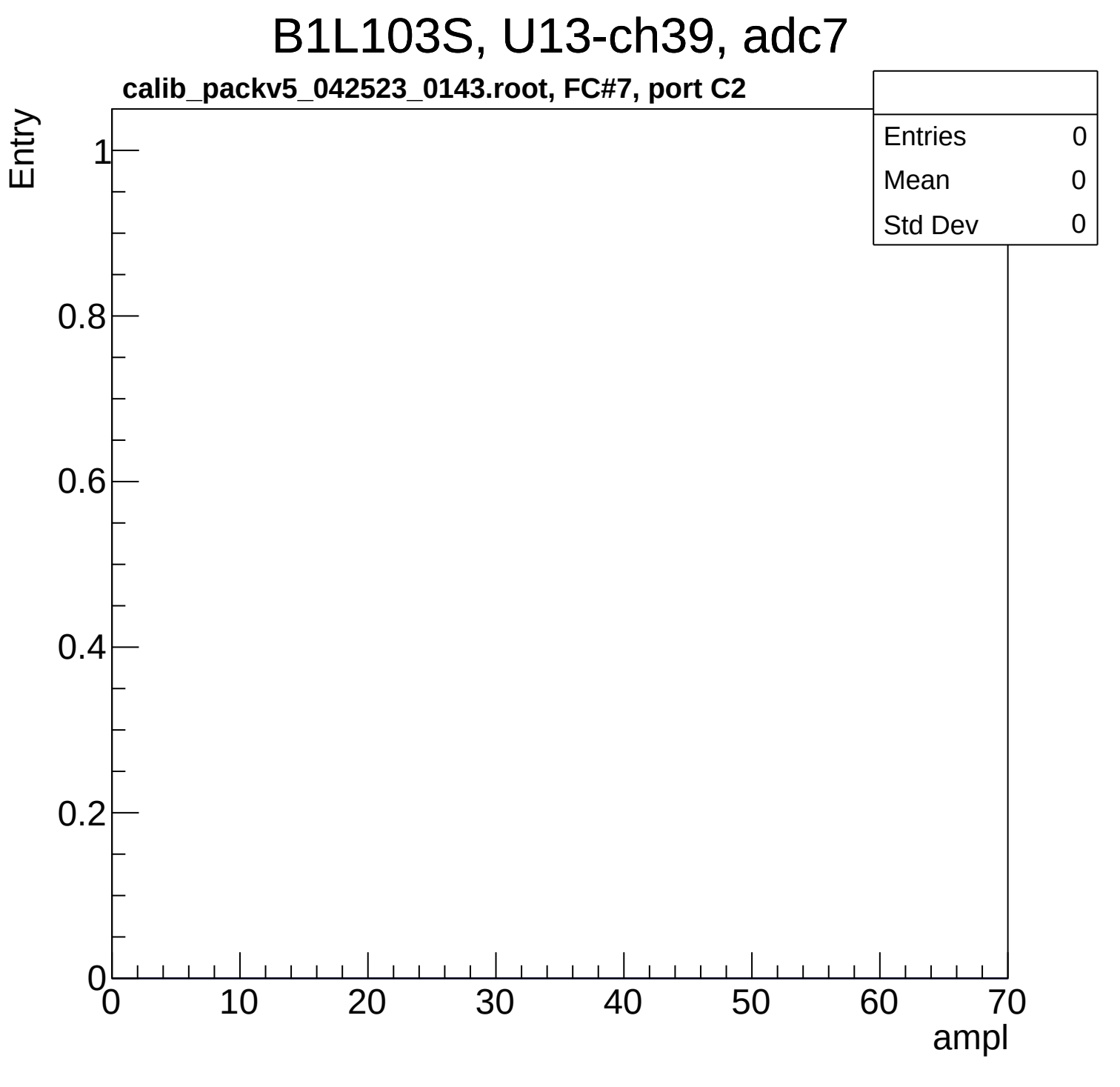
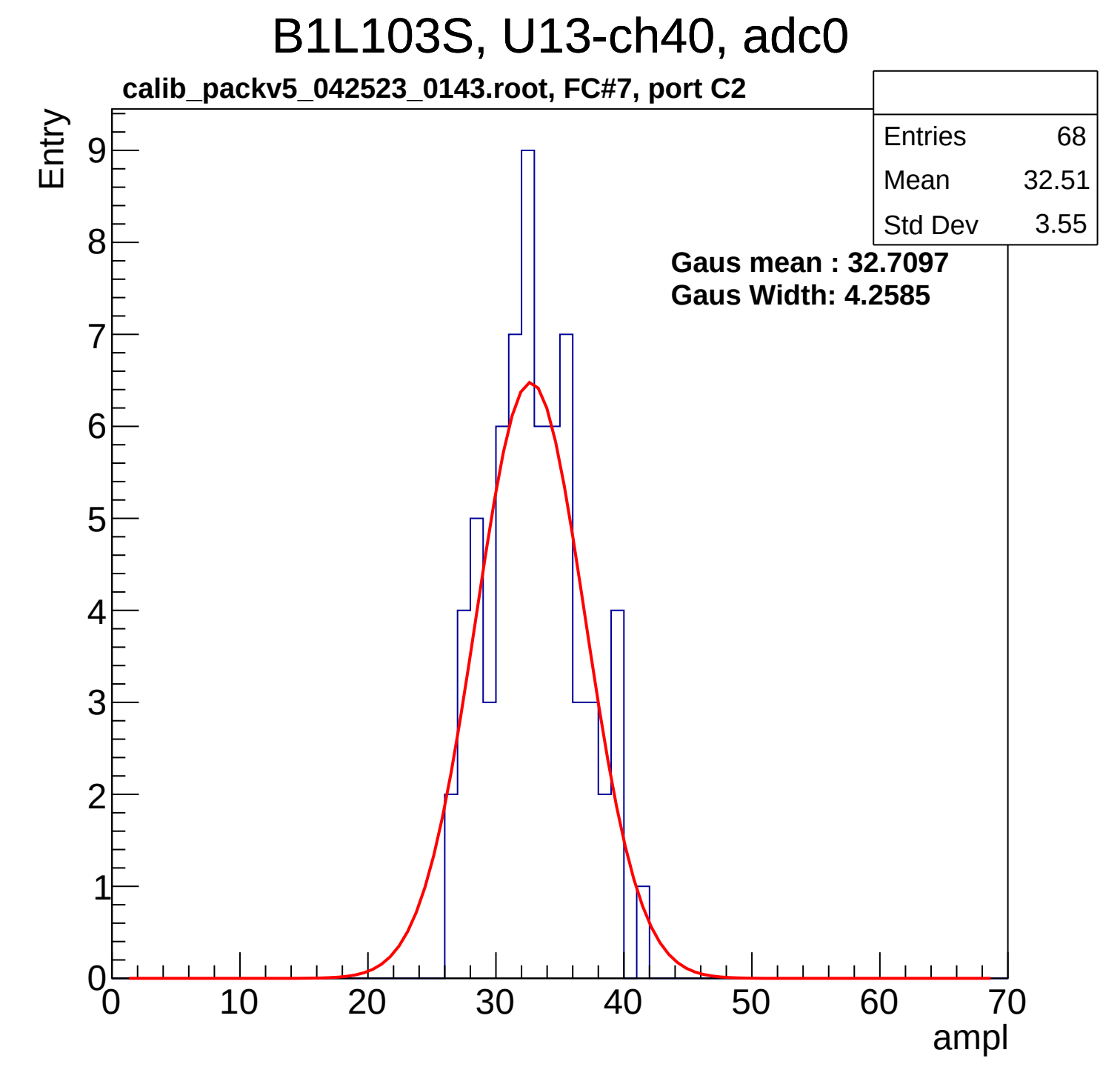
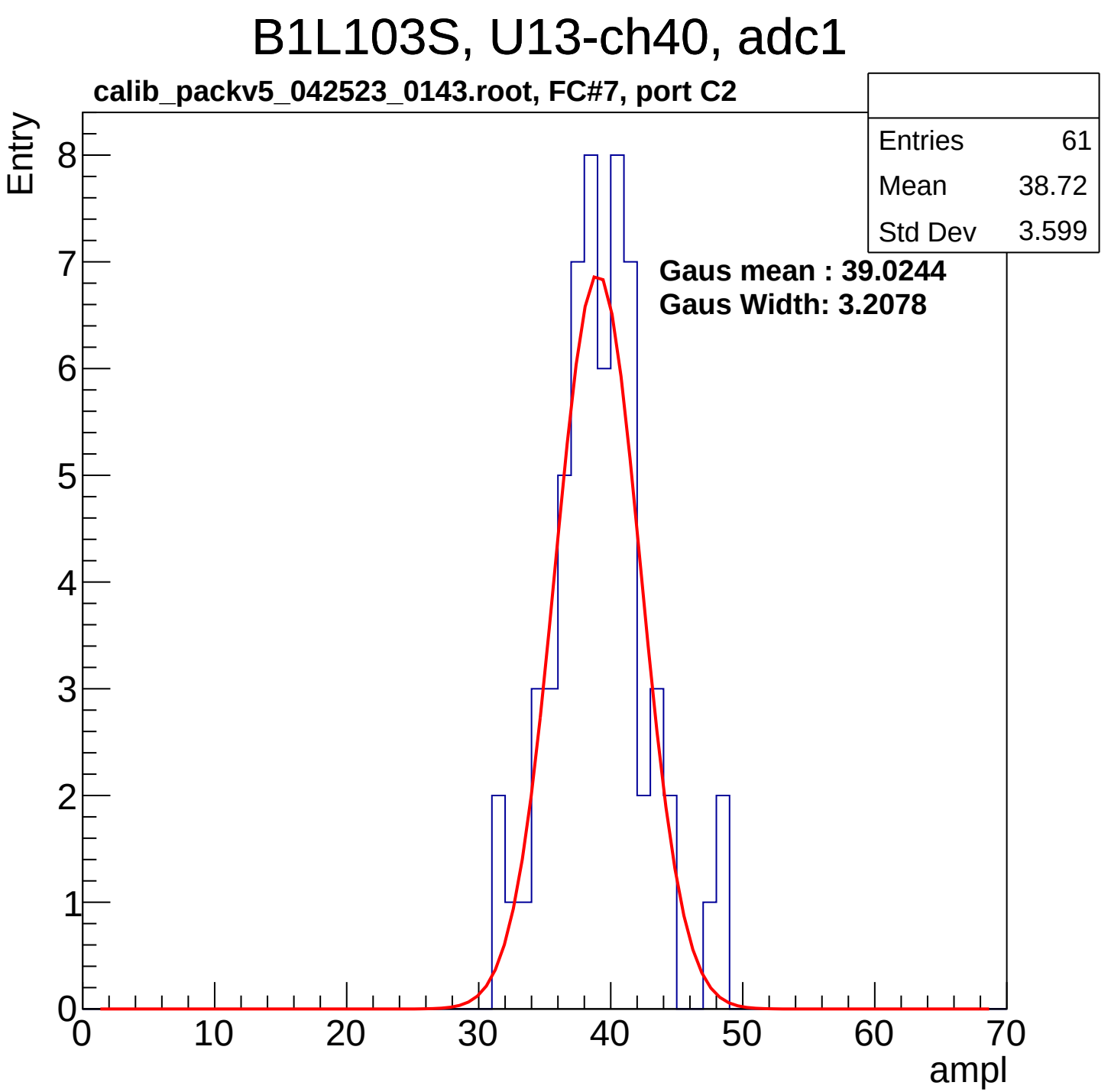

B1L103S, U13-ch39, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch39, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch40, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 32.51
Std Dev 3.55
Gaus mean : 32.7097
Gaus Width: 4.2585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch40, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 38.72
Std Dev 3.599
Gaus mean : 39.0244
Gaus Width: 3.2078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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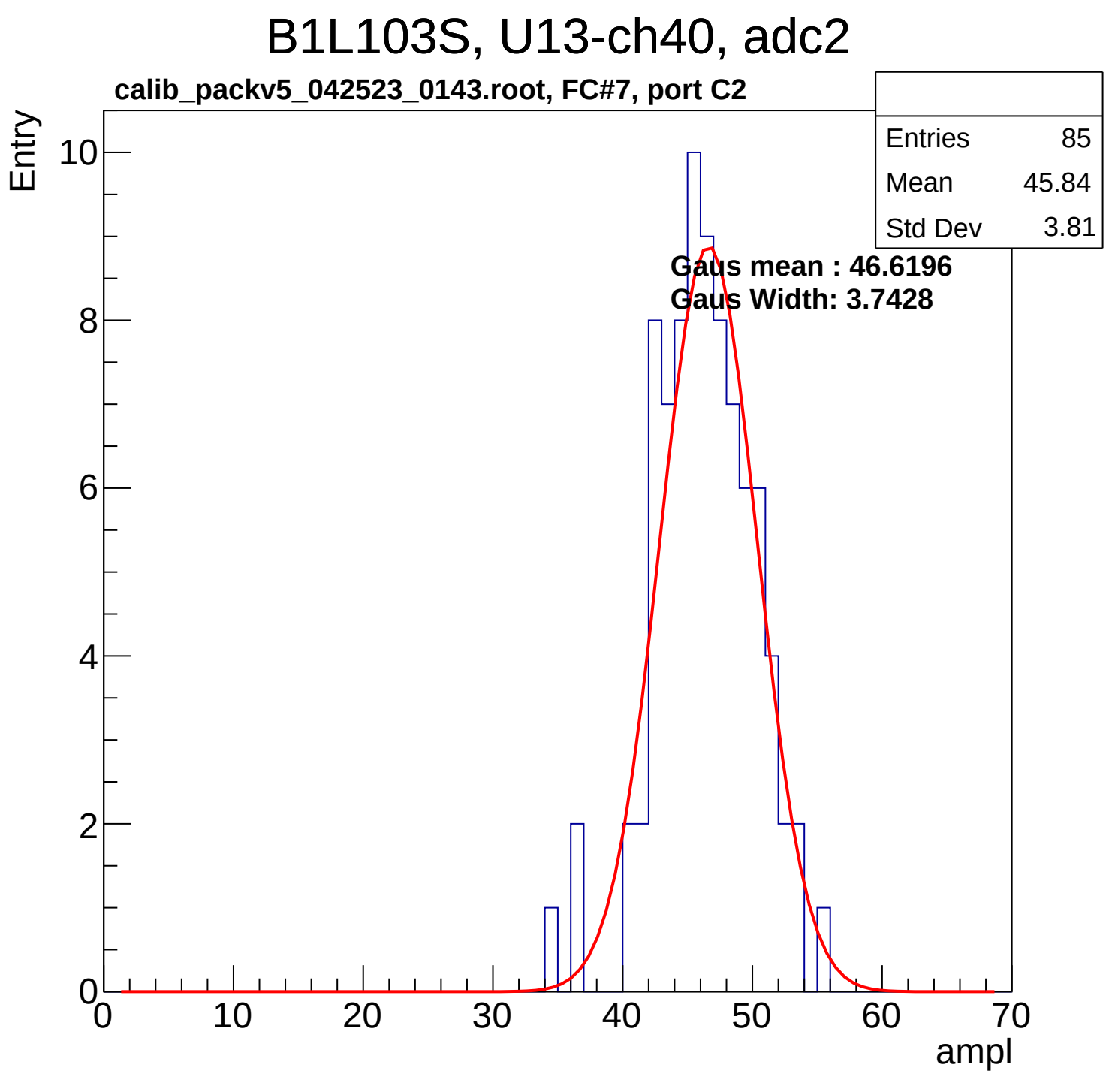
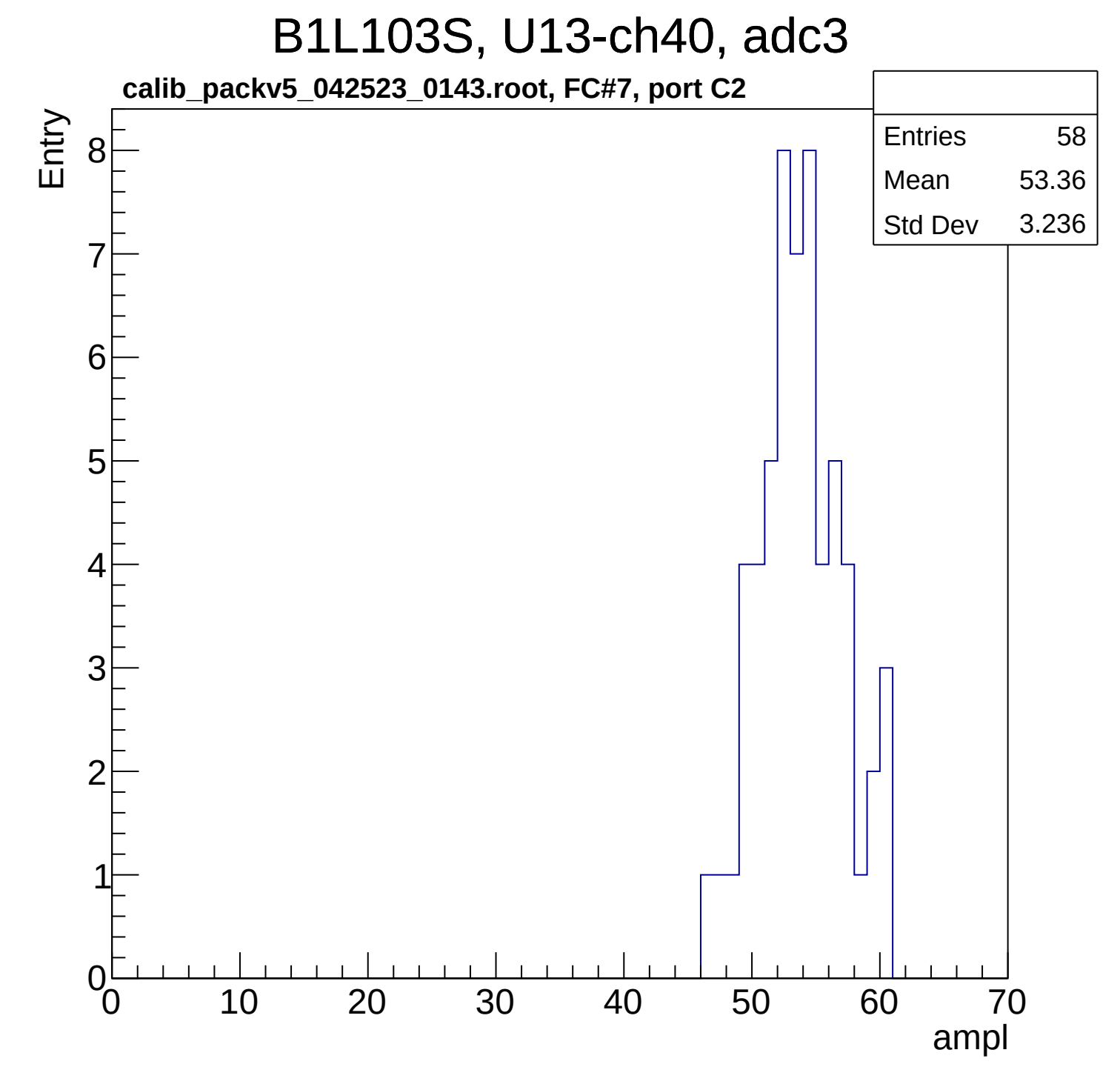
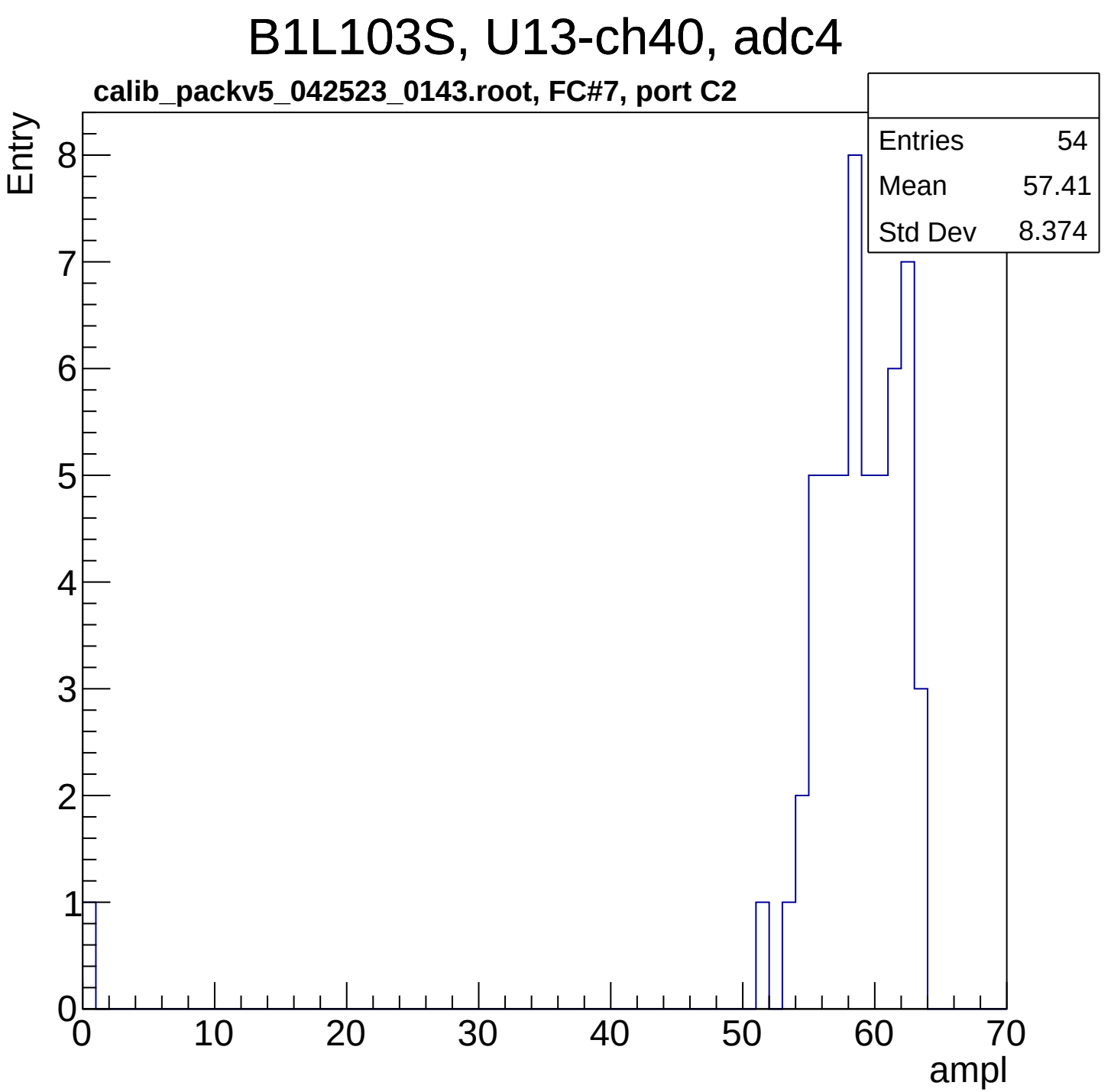
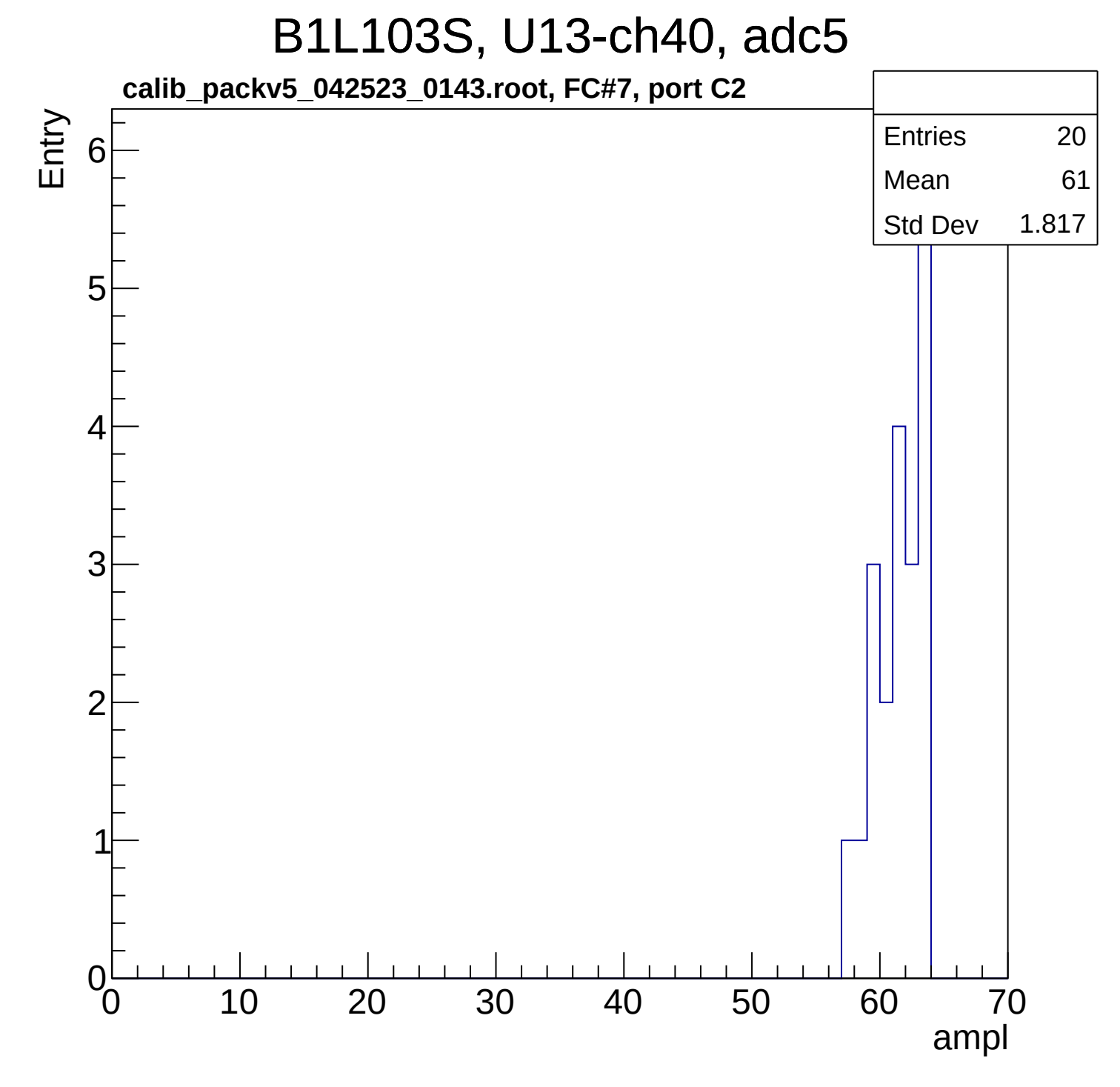
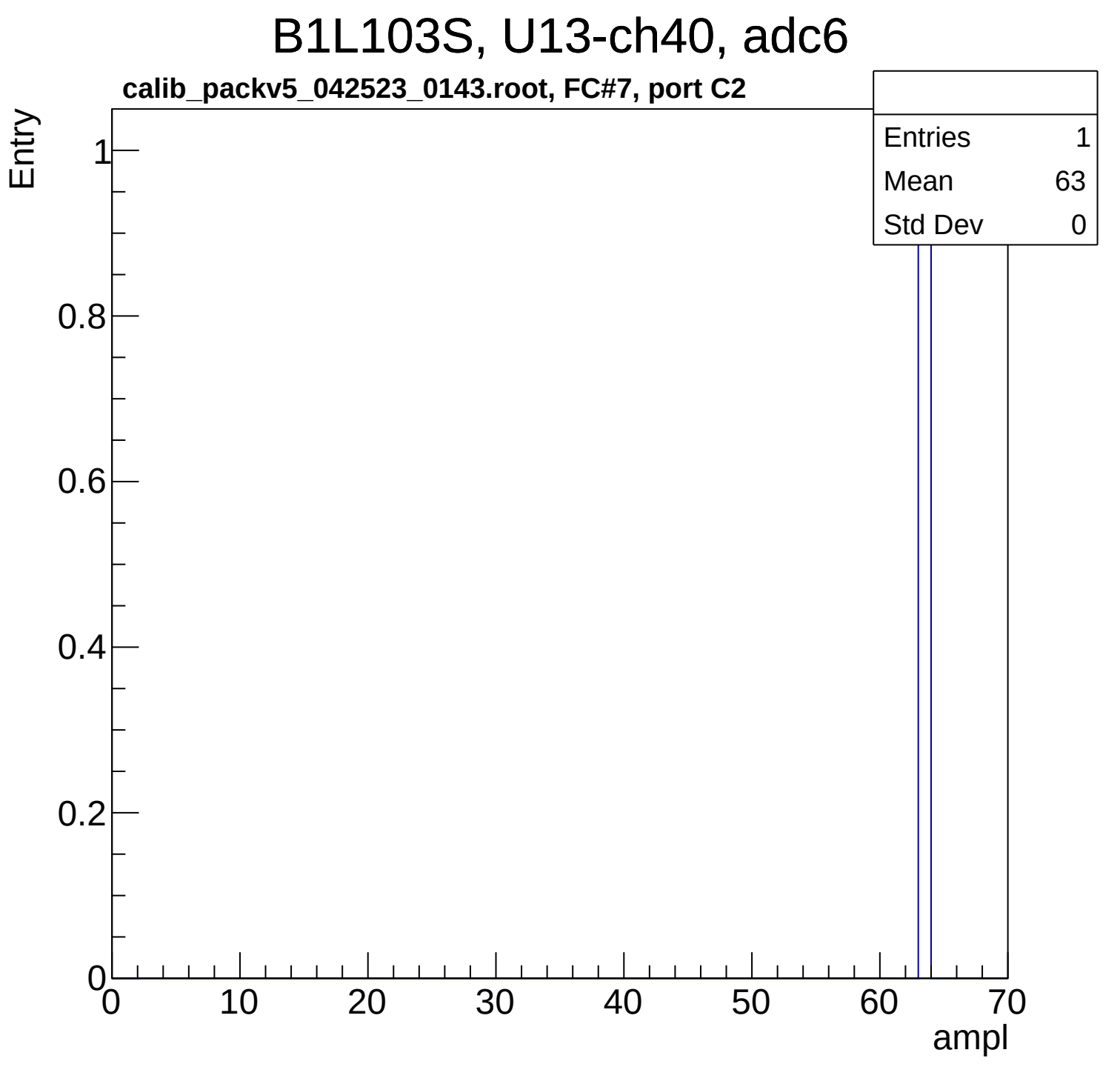
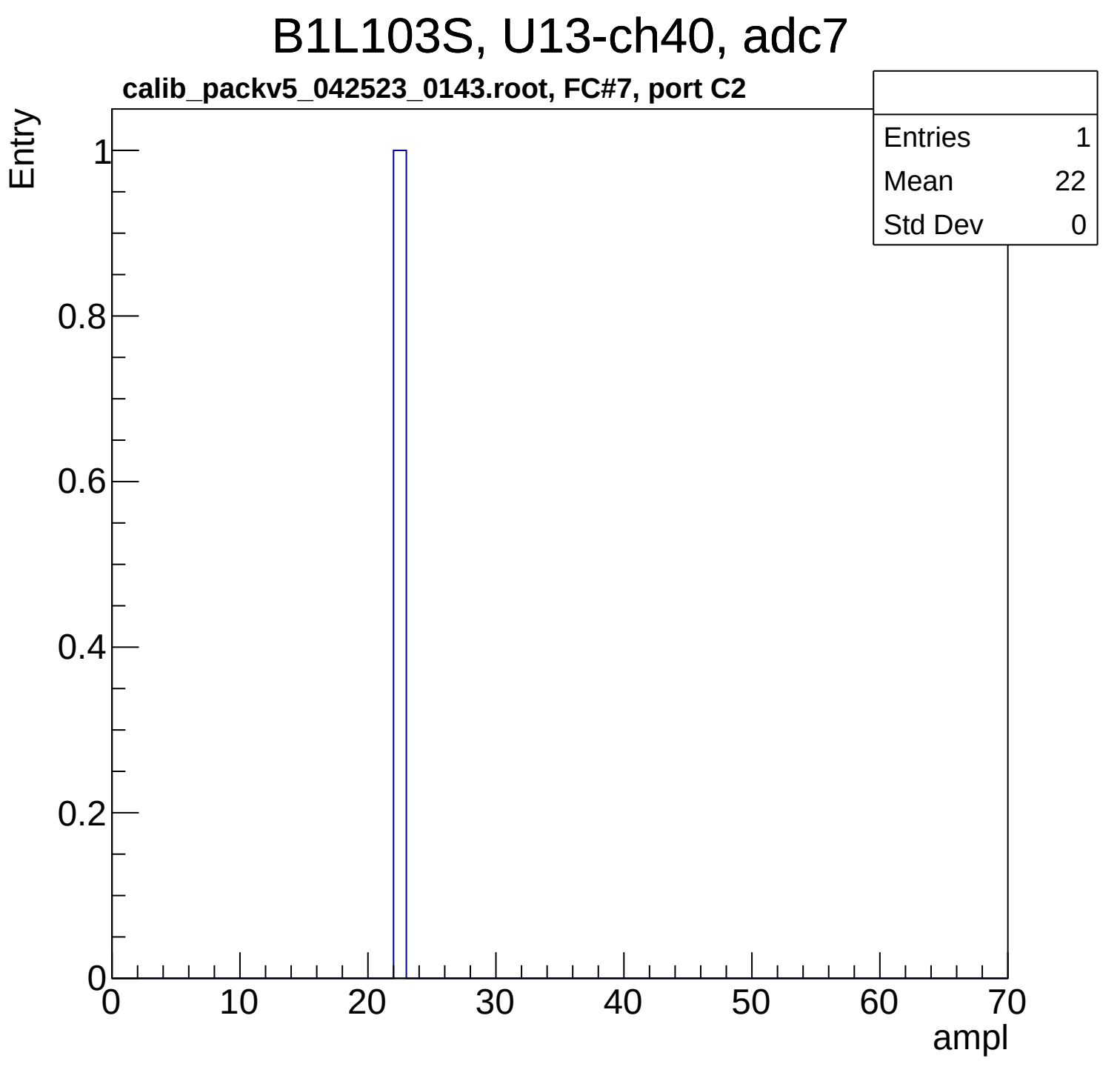

B1L103S, U13-ch40, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 45.84
Std Dev 3.81
Gaus mean : 46.6196
Gaus Width: 3.7428
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch40, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53.36
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch40, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.41
Std Dev 8.374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch40, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 61
Std Dev 1.817
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch40, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch40, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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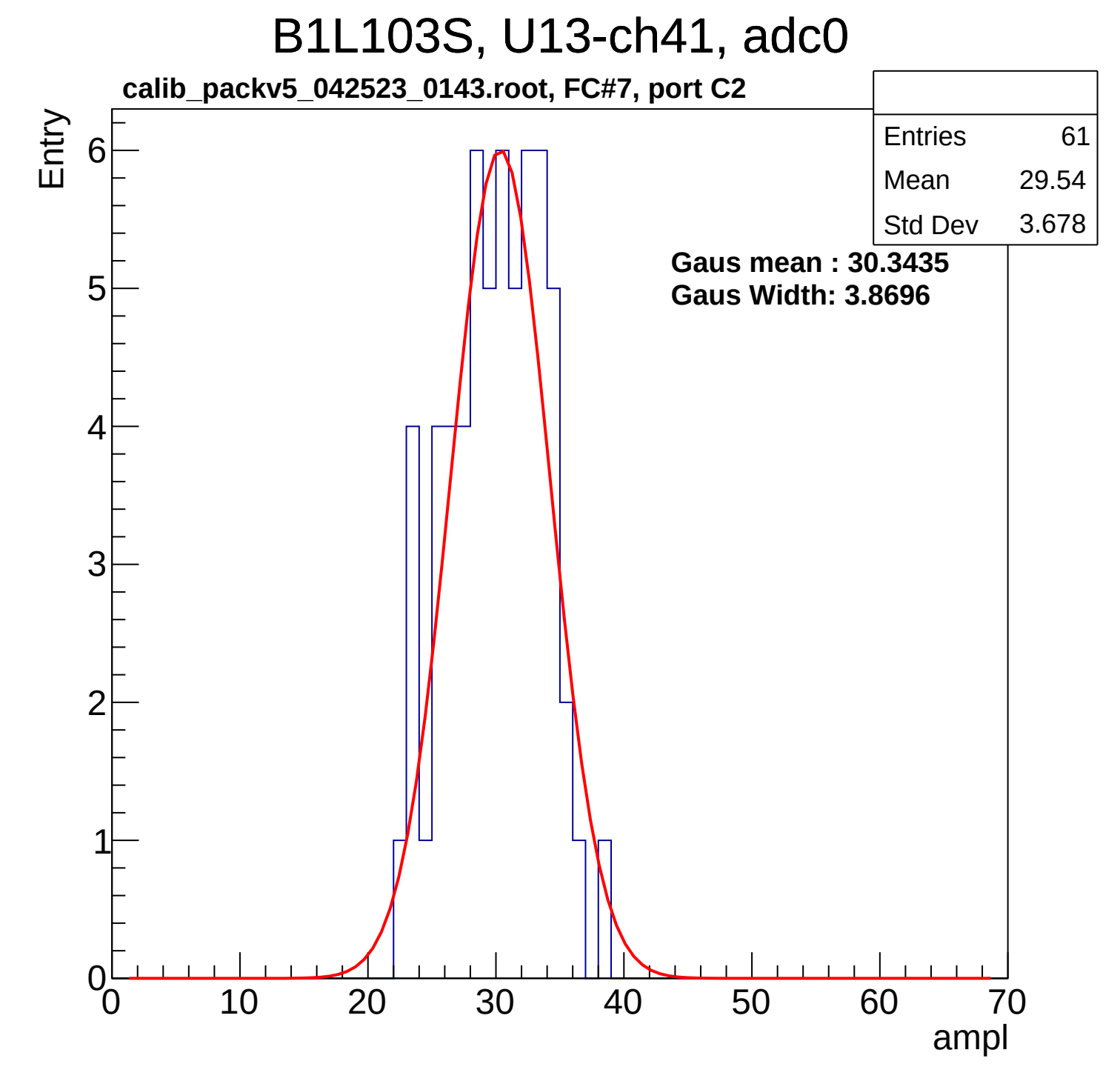
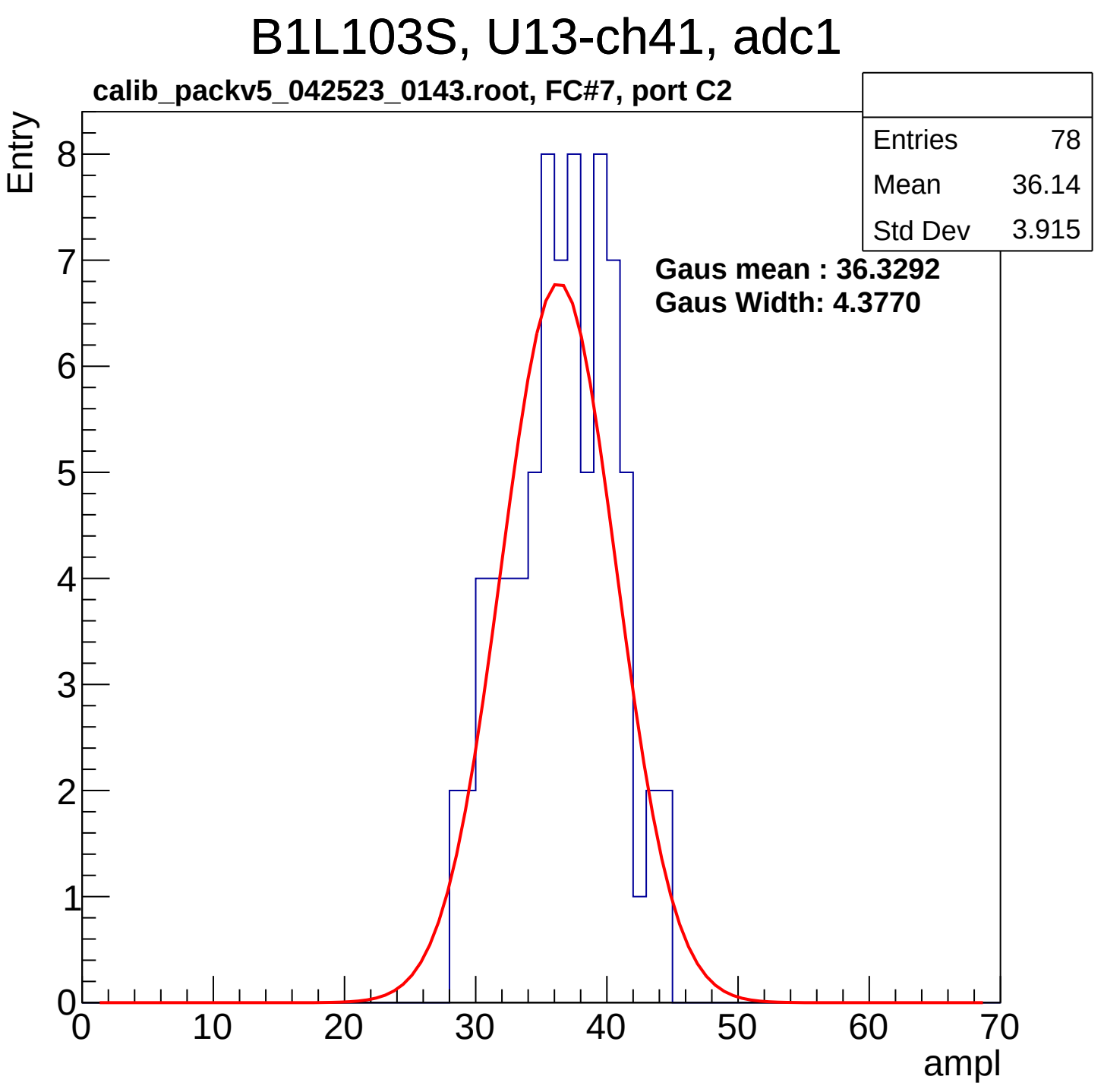
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch41, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 29.54
Std Dev 3.678
Gaus mean : 30.3435
Gaus Width: 3.8696
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch41, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.14
Std Dev 3.915
Gaus mean : 36.3292
Gaus Width: 4.3770
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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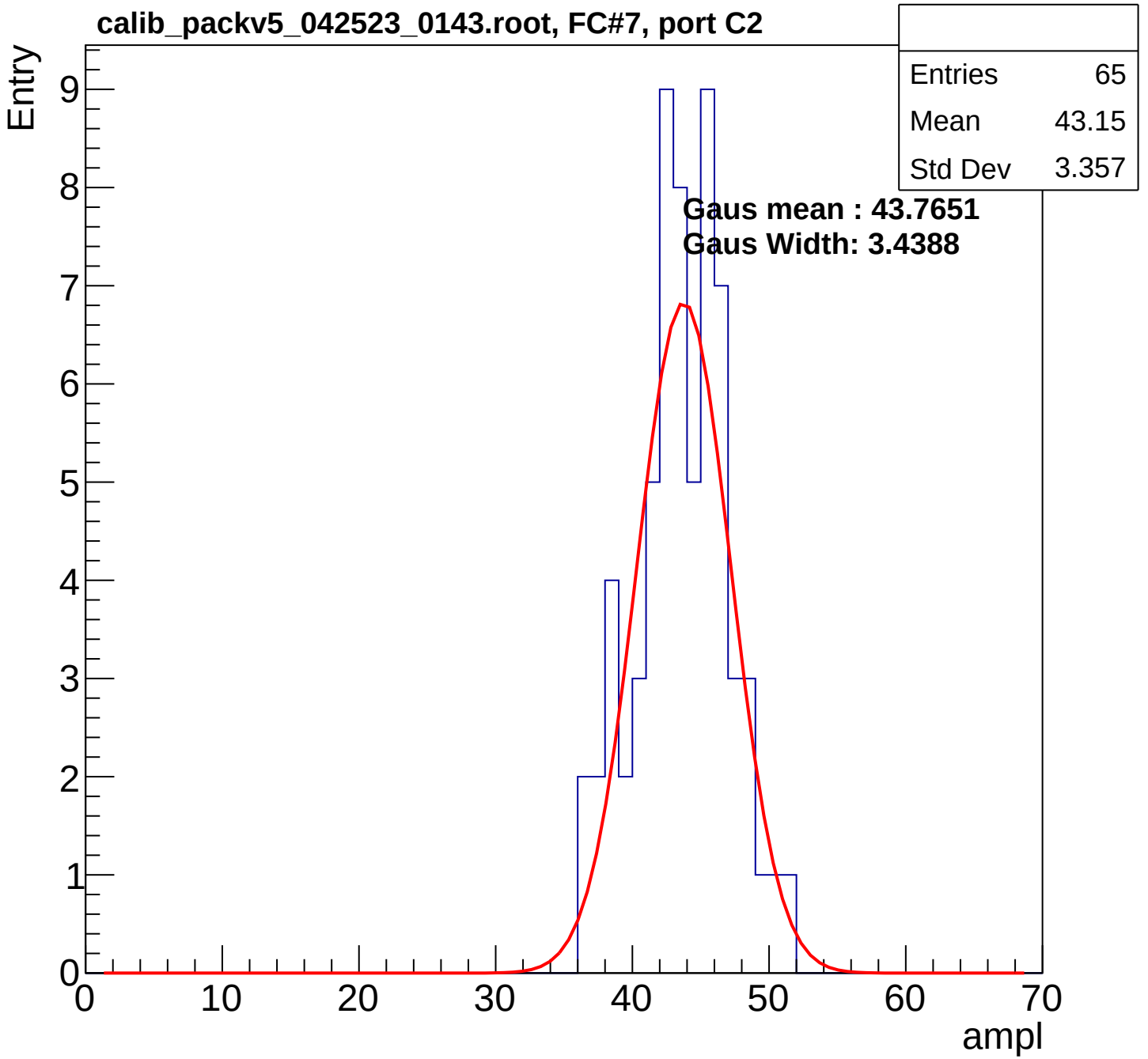
B1L103S, U13-ch41, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.15
Std Dev 3.357
Gaus mean : 43.7651
Gaus Width: 3.4388
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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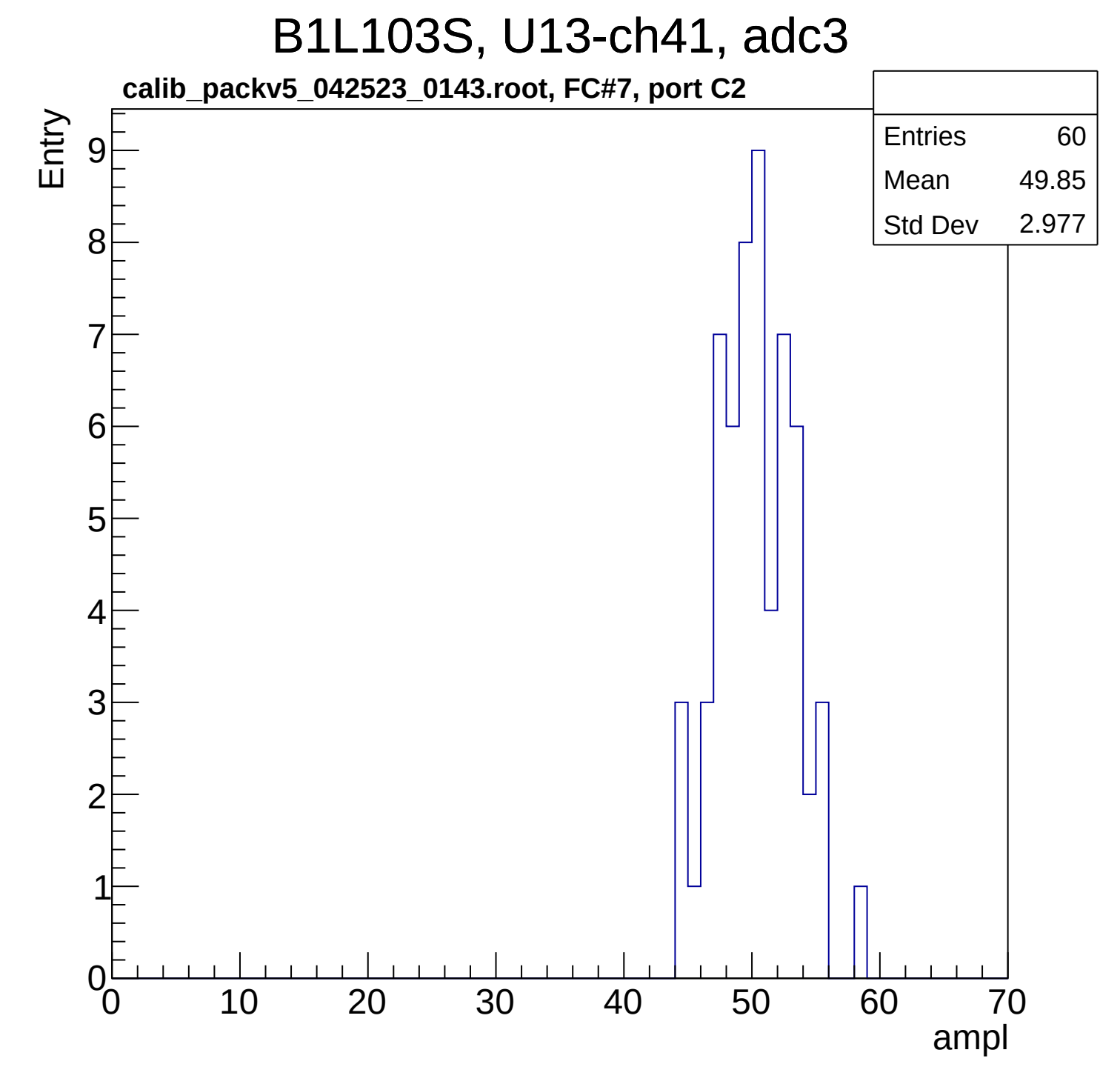
B1L103S, U13-ch41, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.85
Std Dev 2.977
Entry
ampl
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3
4
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6
7
8
9
0
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40
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60
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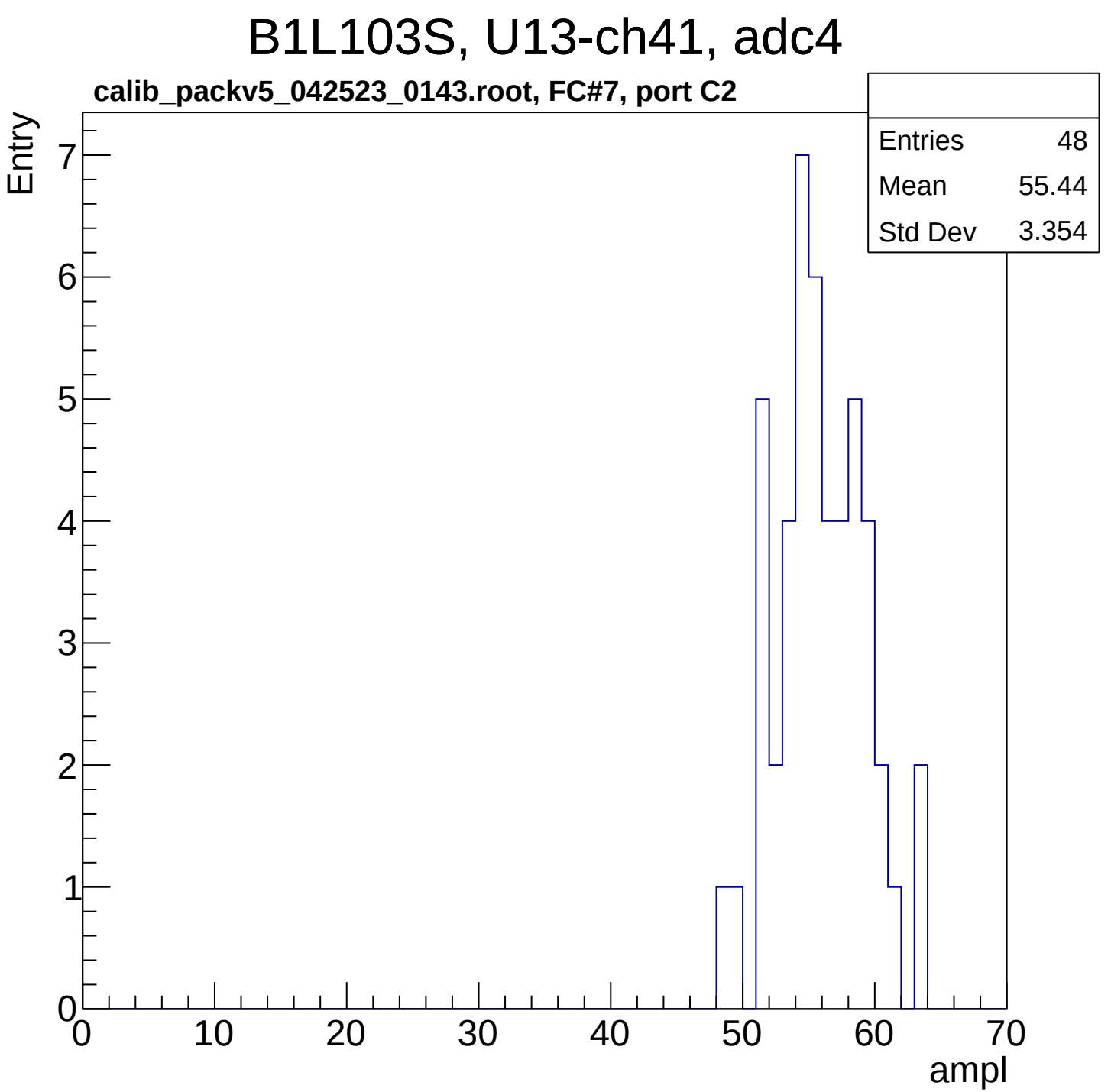
B1L103S, U13-ch41, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 55.44
Std Dev 3.354
Entry
ampl
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3
4
5
6
7
0
10
20
30
40
50
60
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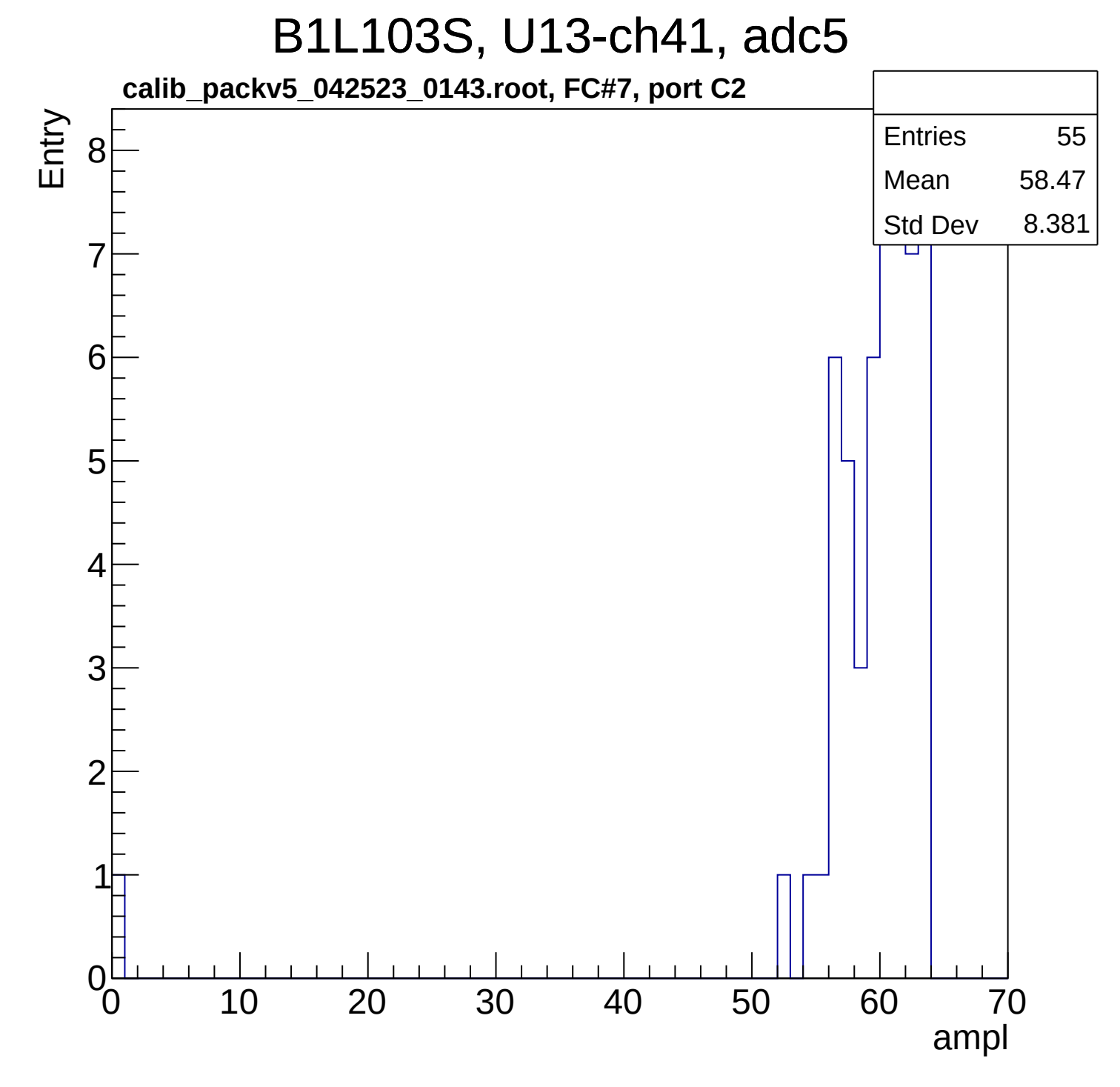
B1L103S, U13-ch41, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.47
Std Dev 8.381
Entry
ampl
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2
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4
5
6
7
8
0
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20
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40
50
60
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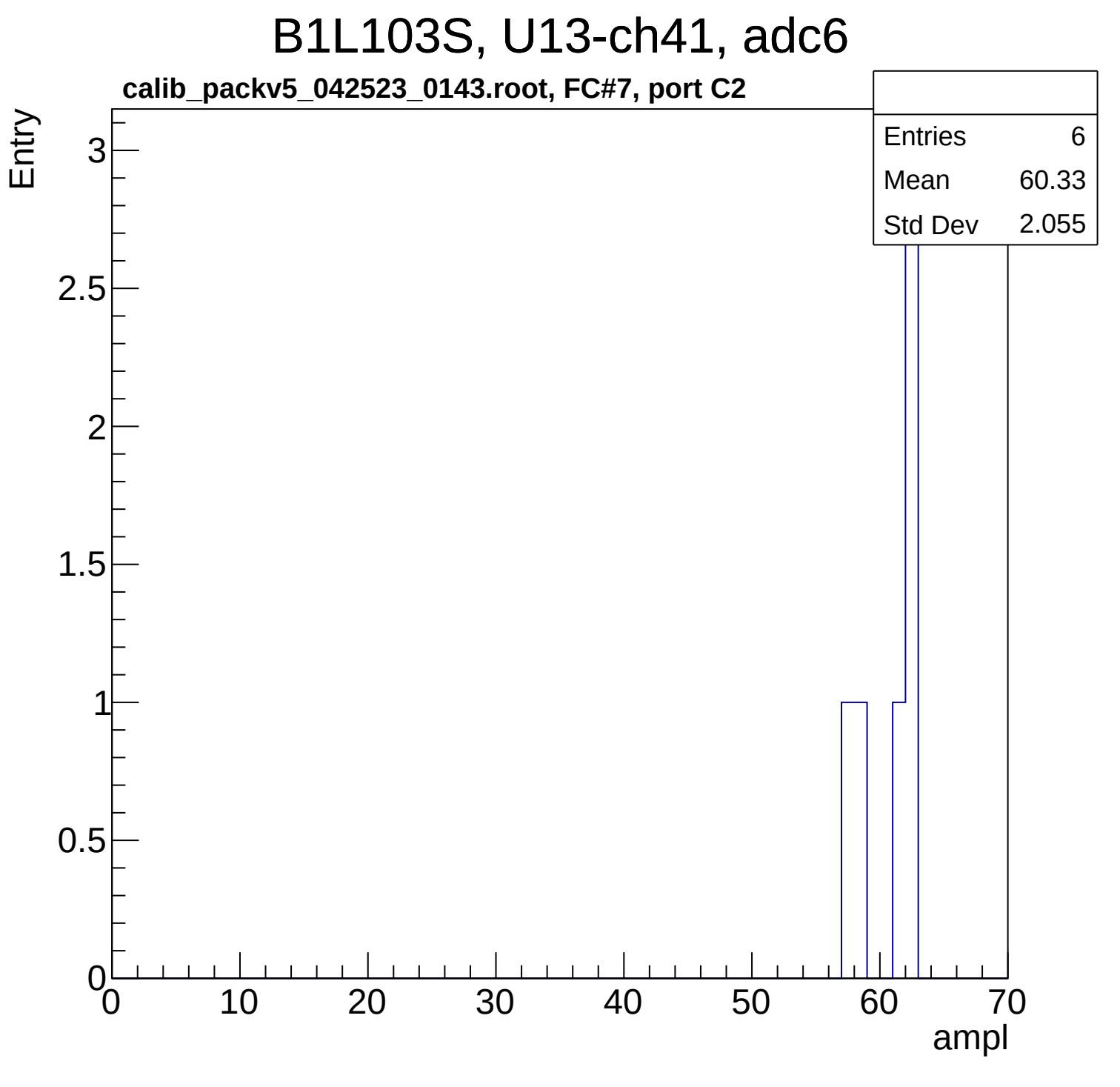
B1L103S, U13-ch41, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 60.33
Std Dev 2.055
Entry
ampl
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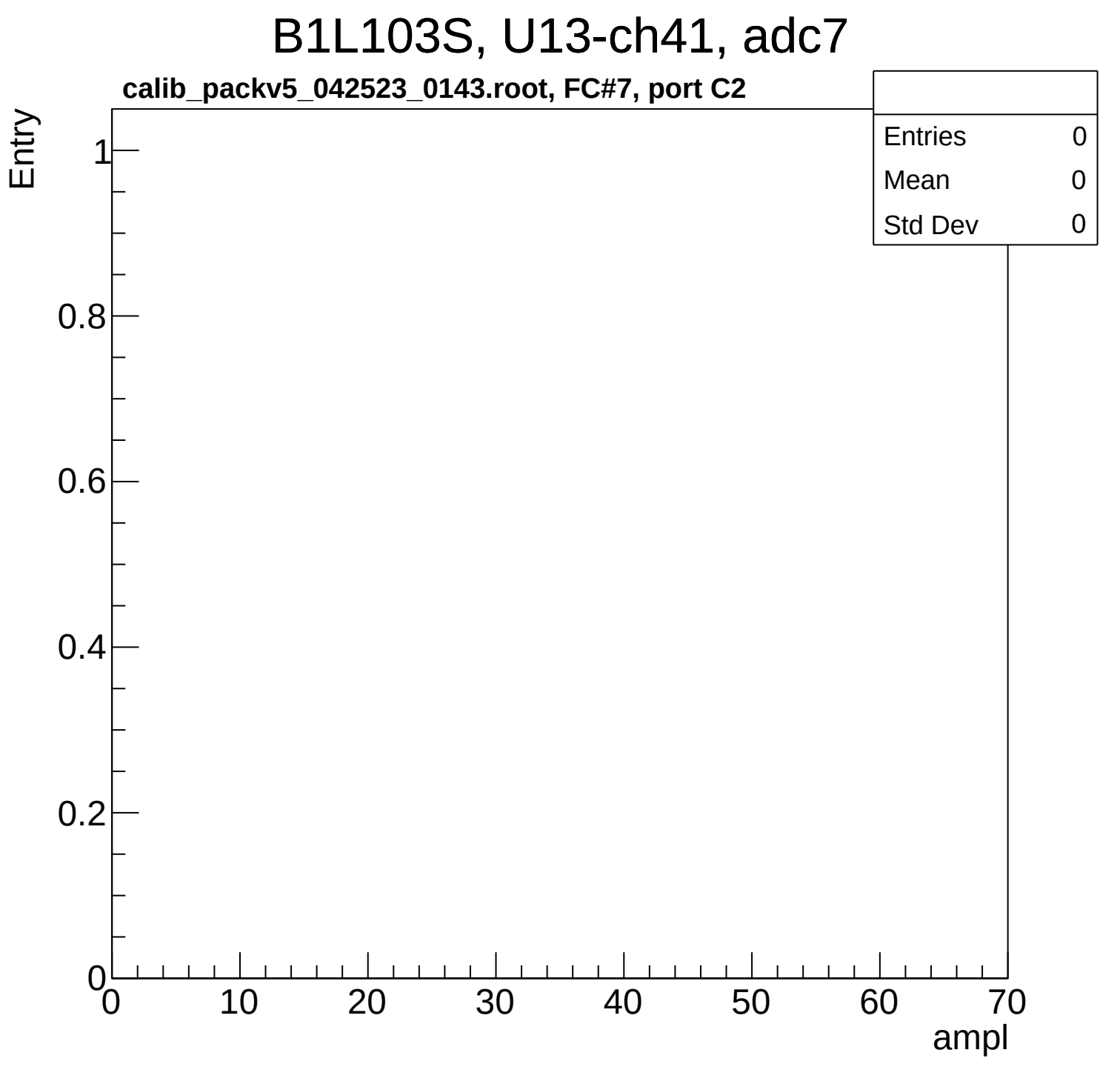
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch41, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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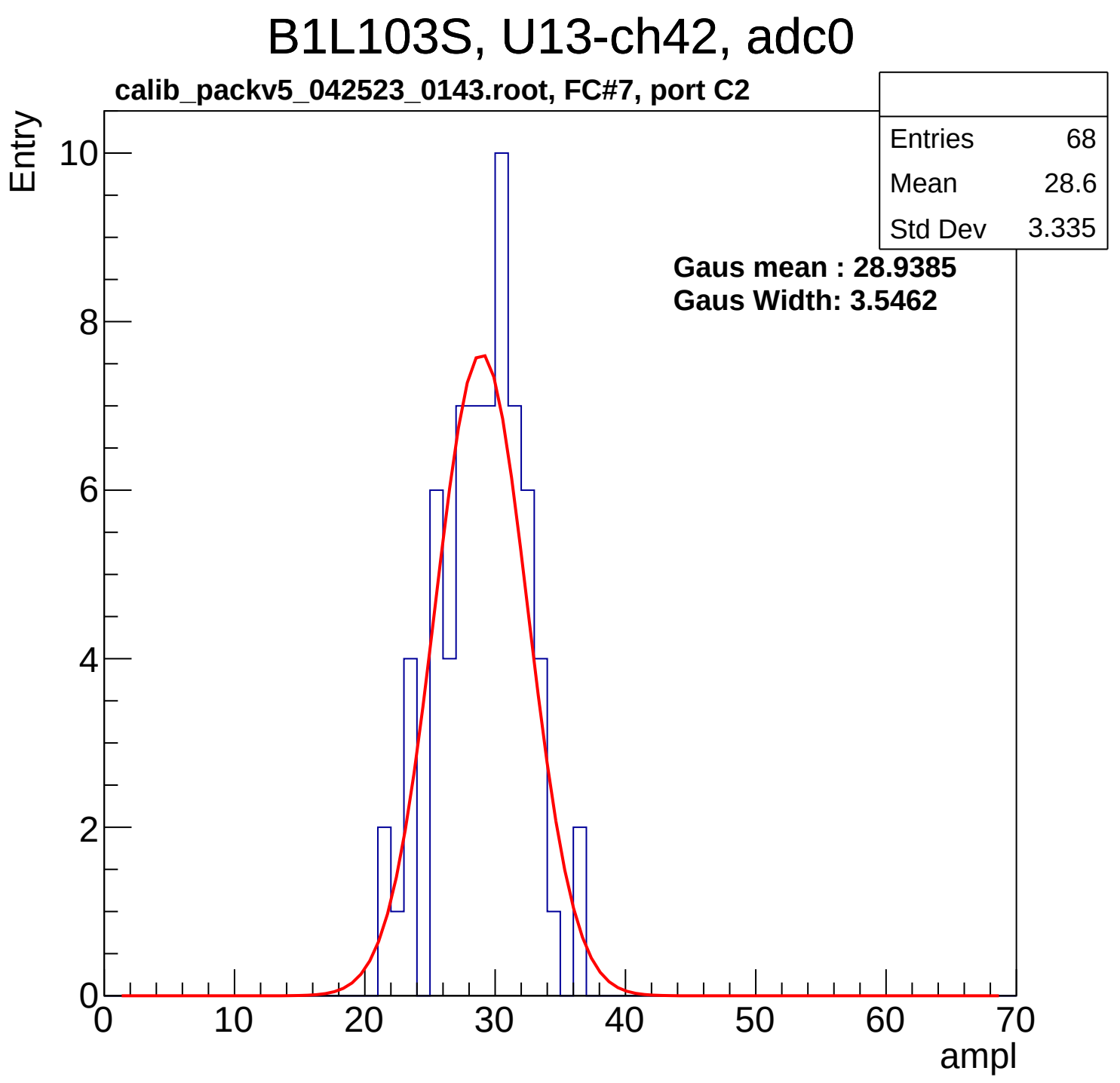
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch42, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 28.6
Std Dev 3.335
Gaus mean : 28.9385
Gaus Width: 3.5462
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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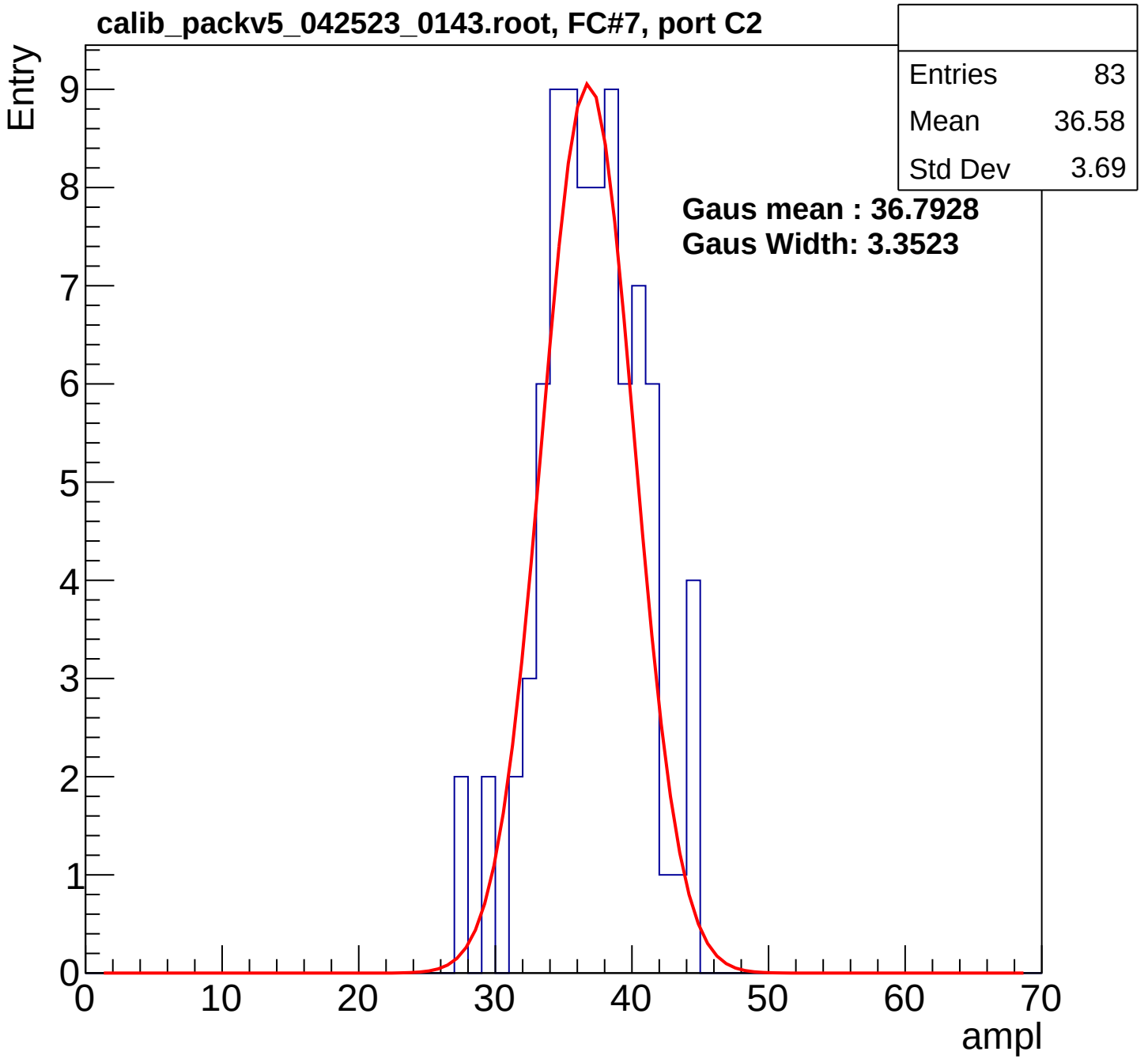
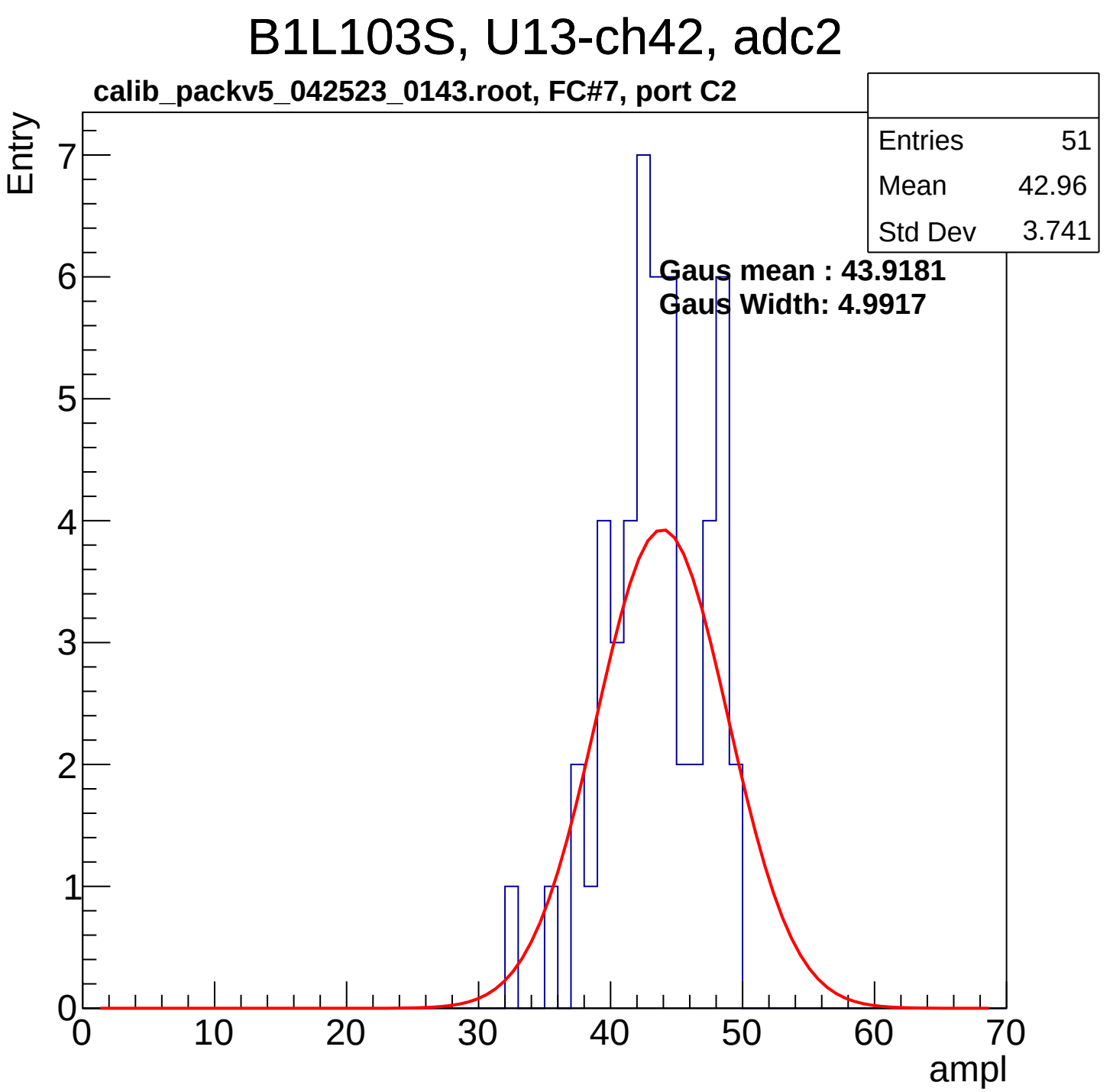

B1L103S, U13-ch42, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 36.58
Std Dev 3.69
Gaus mean : 36.7928
Gaus Width: 3.3523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch42, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 42.96
Std Dev 3.741
Gaus mean : 43.9181
Gaus Width: 4.9917
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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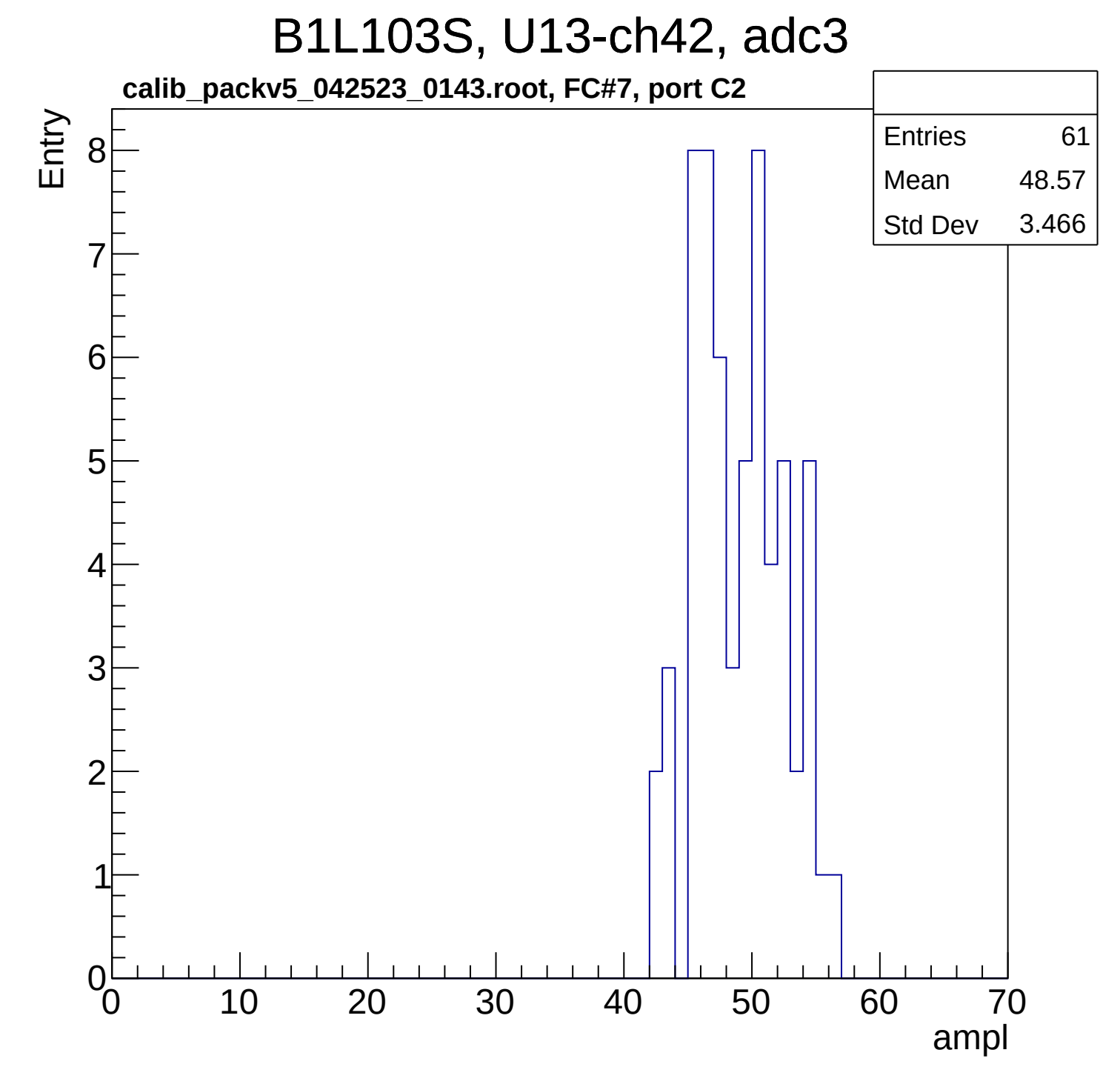
B1L103S, U13-ch42, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 48.57
Std Dev 3.466
Entry
ampl
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4
5
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7
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0
10
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40
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60
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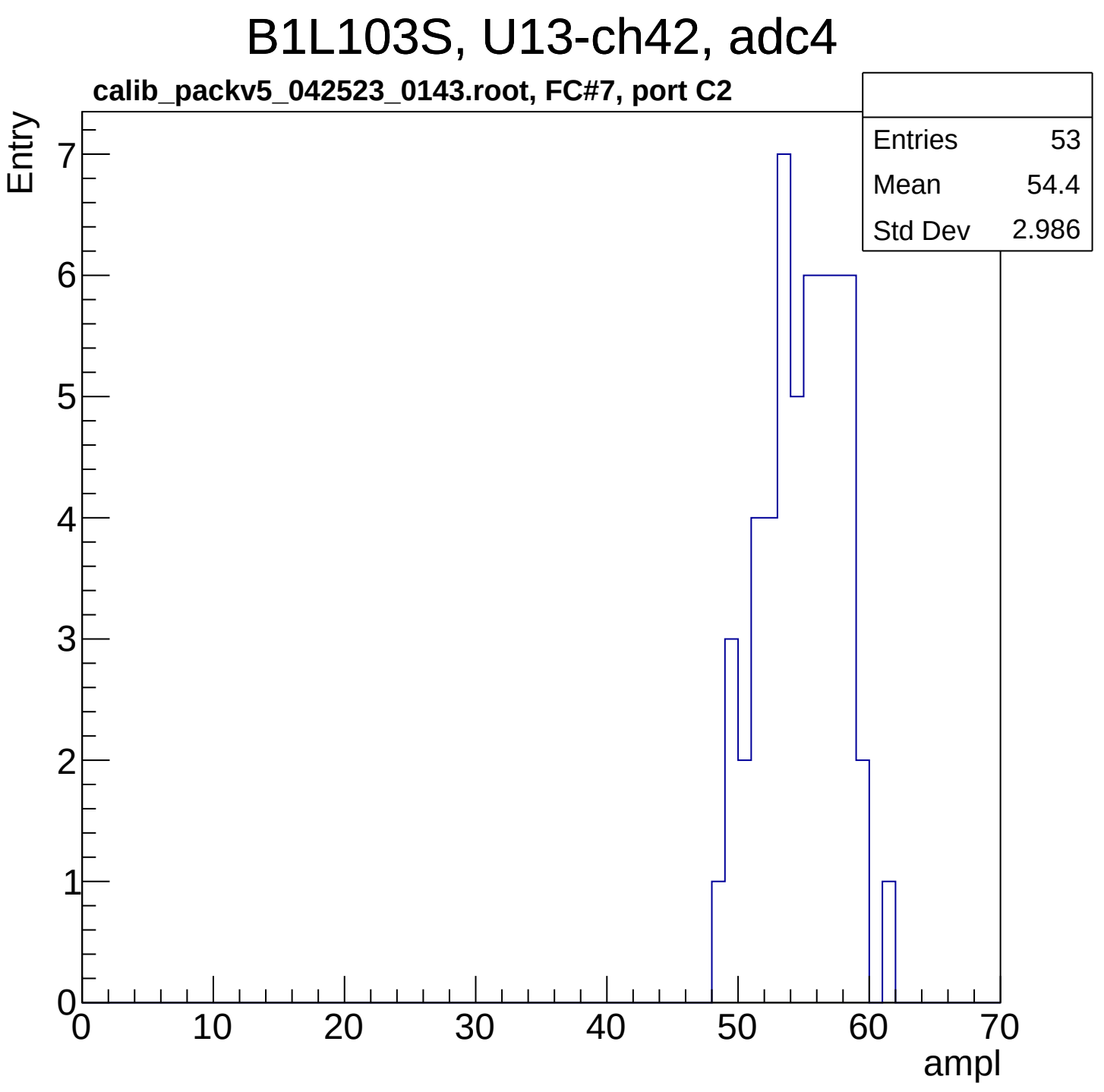
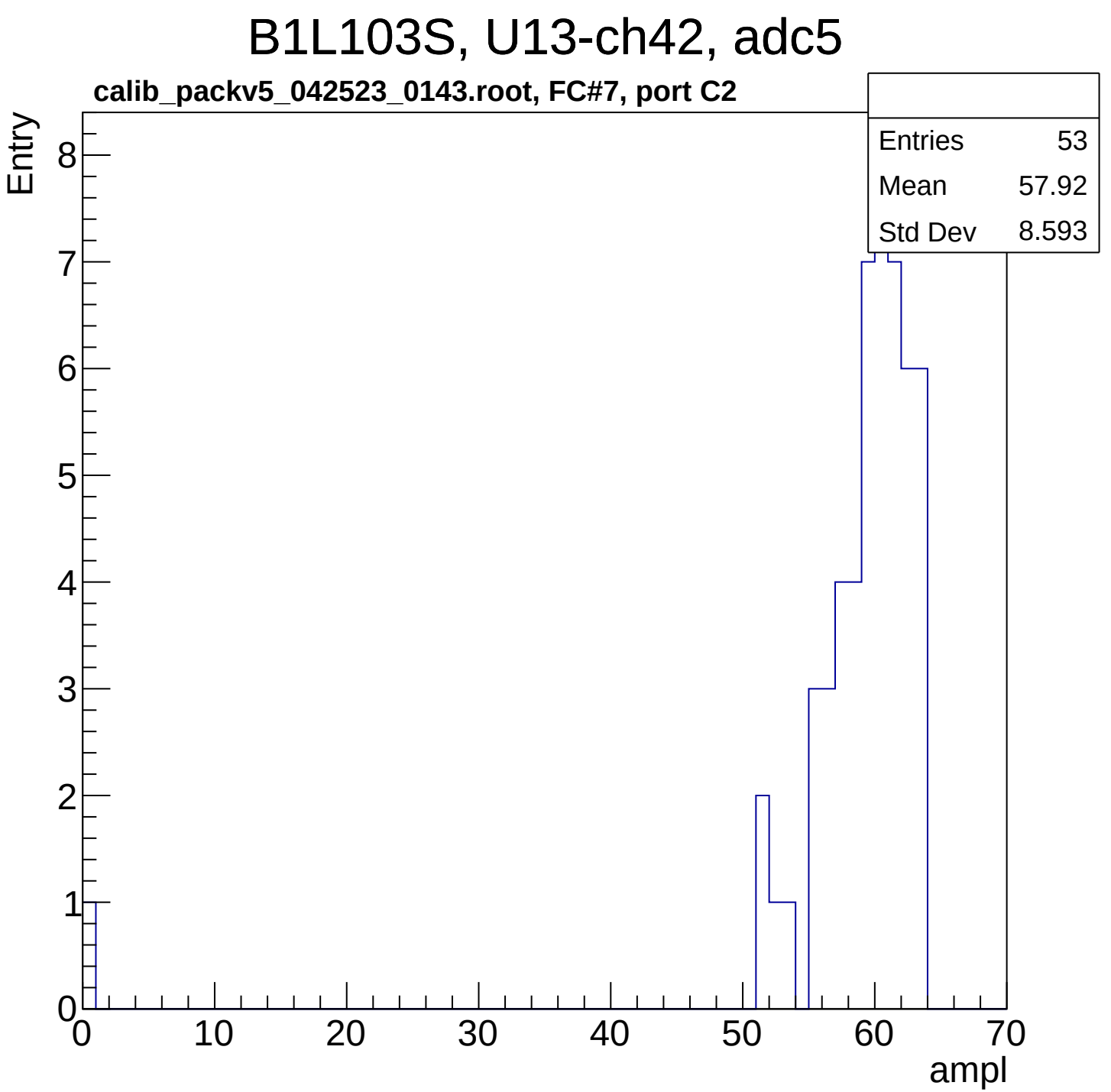

B1L103S, U13-ch42, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 54.4
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch42, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 57.92
Std Dev 8.593
Entry
0
1
2
3
4
5
6
7
8
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10
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30
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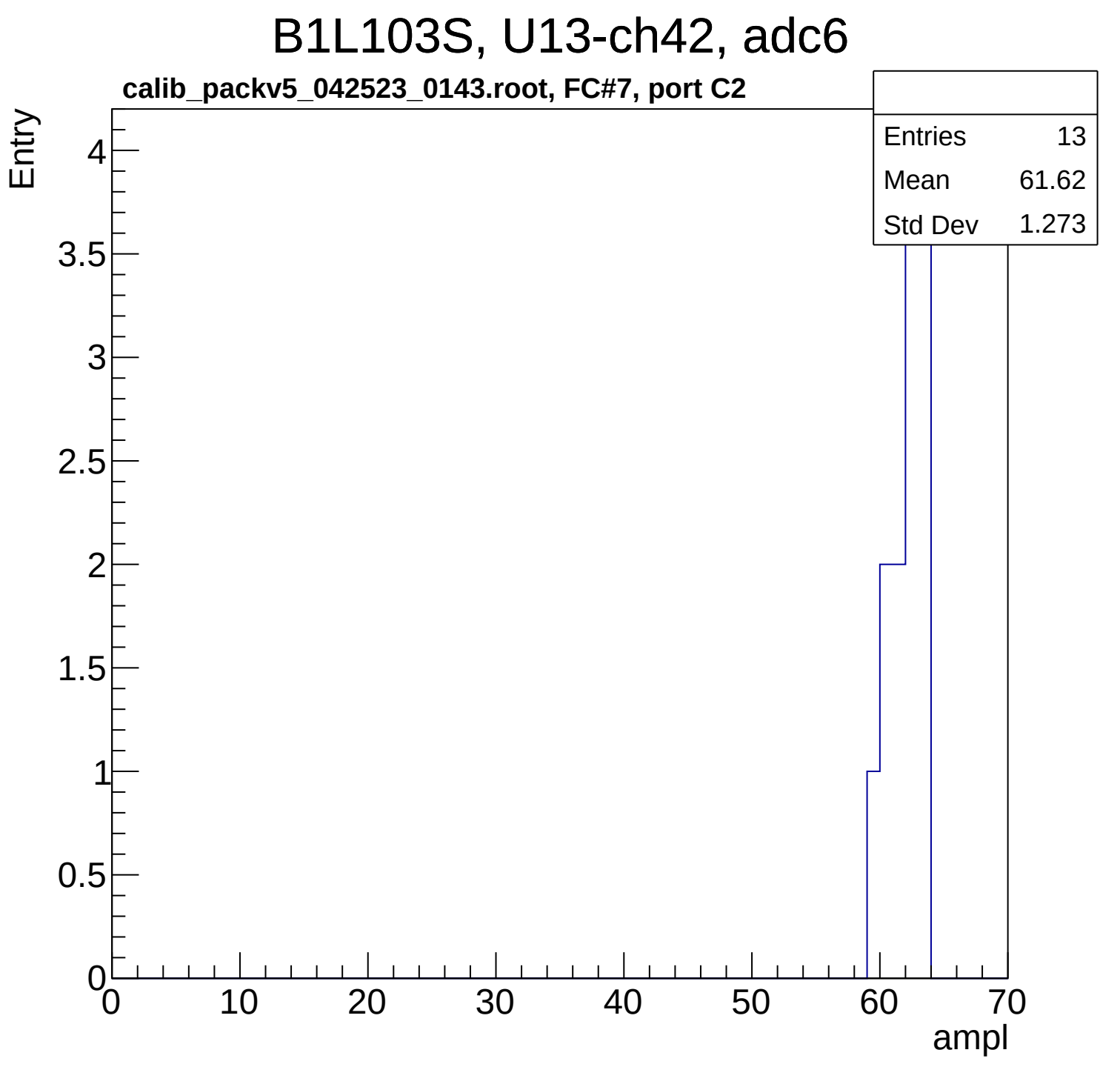
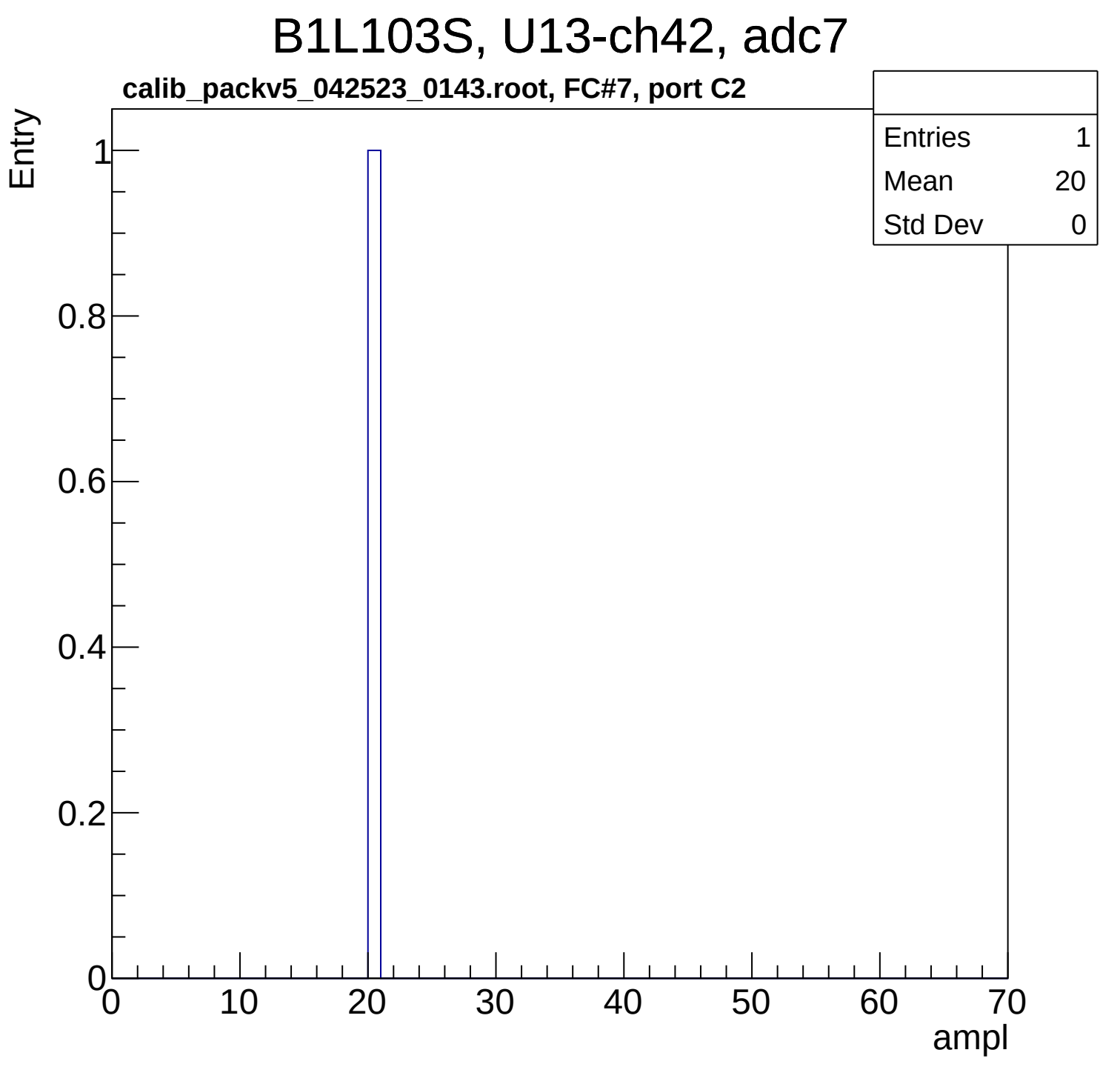
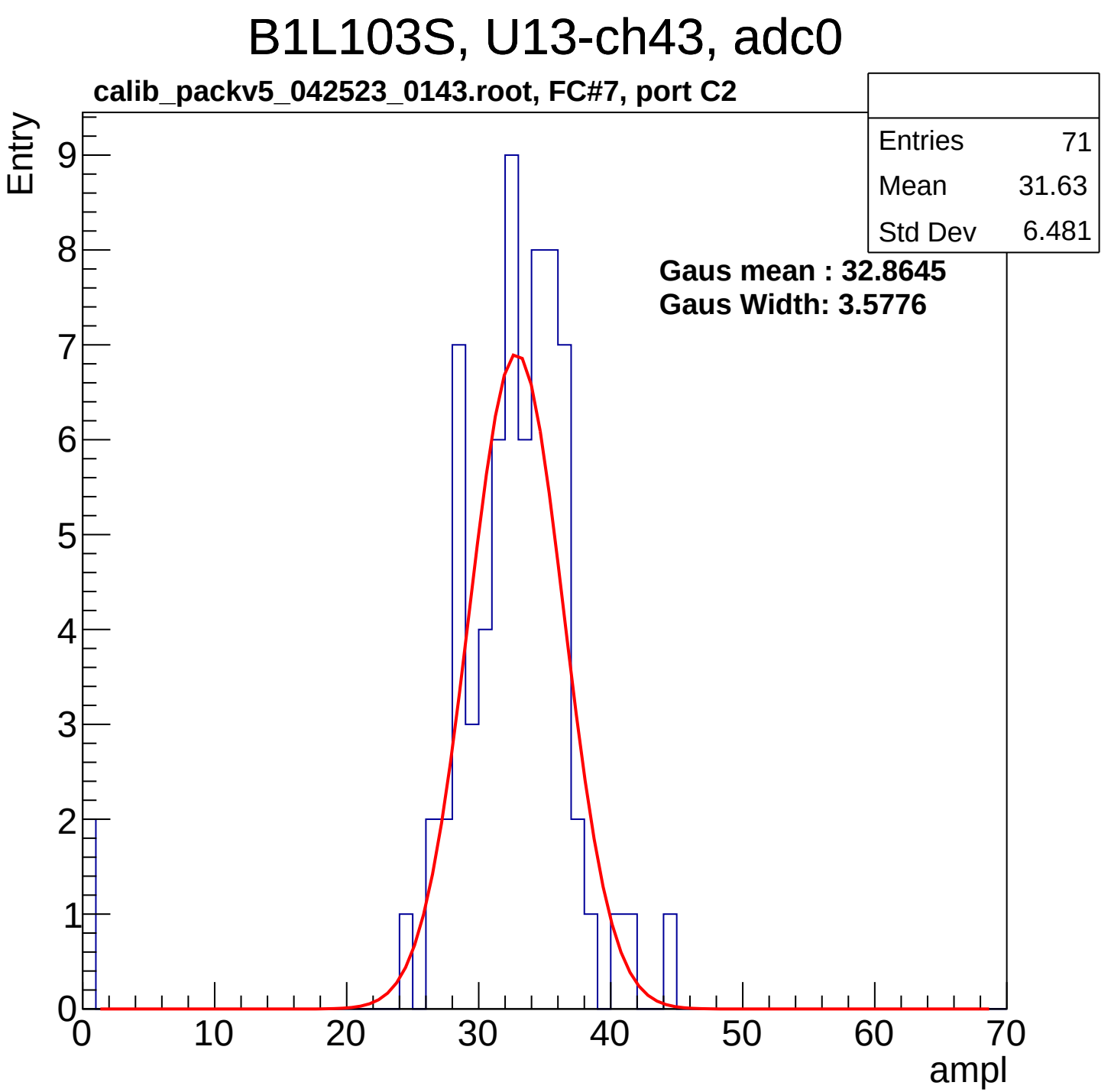
50
60
70
ampl


B1L103S, U13-ch42, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.62
Std Dev 1.273
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U13-ch42, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch43, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 31.63
Std Dev 6.481
Gaus mean : 32.8645
Gaus Width: 3.5776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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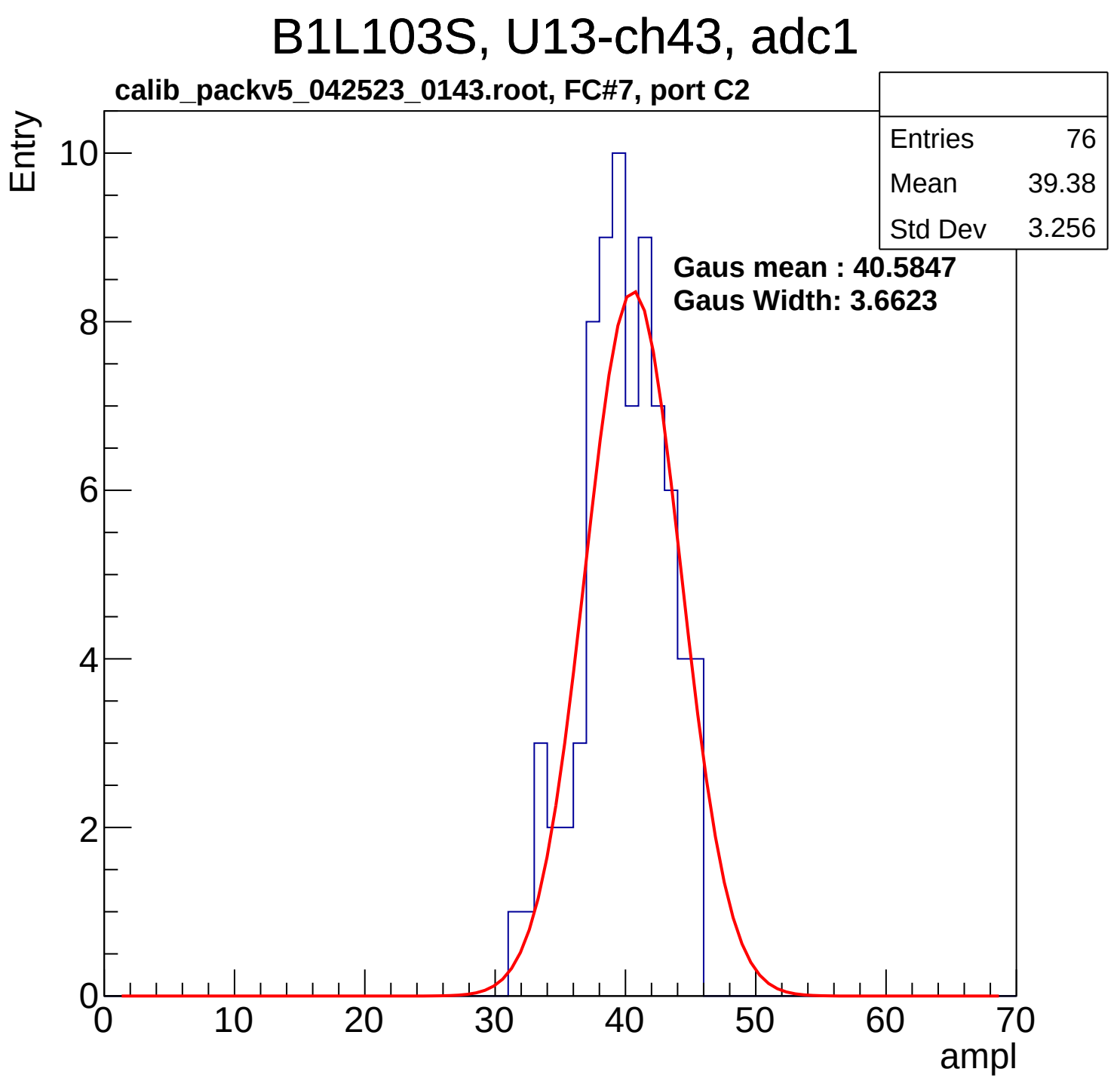
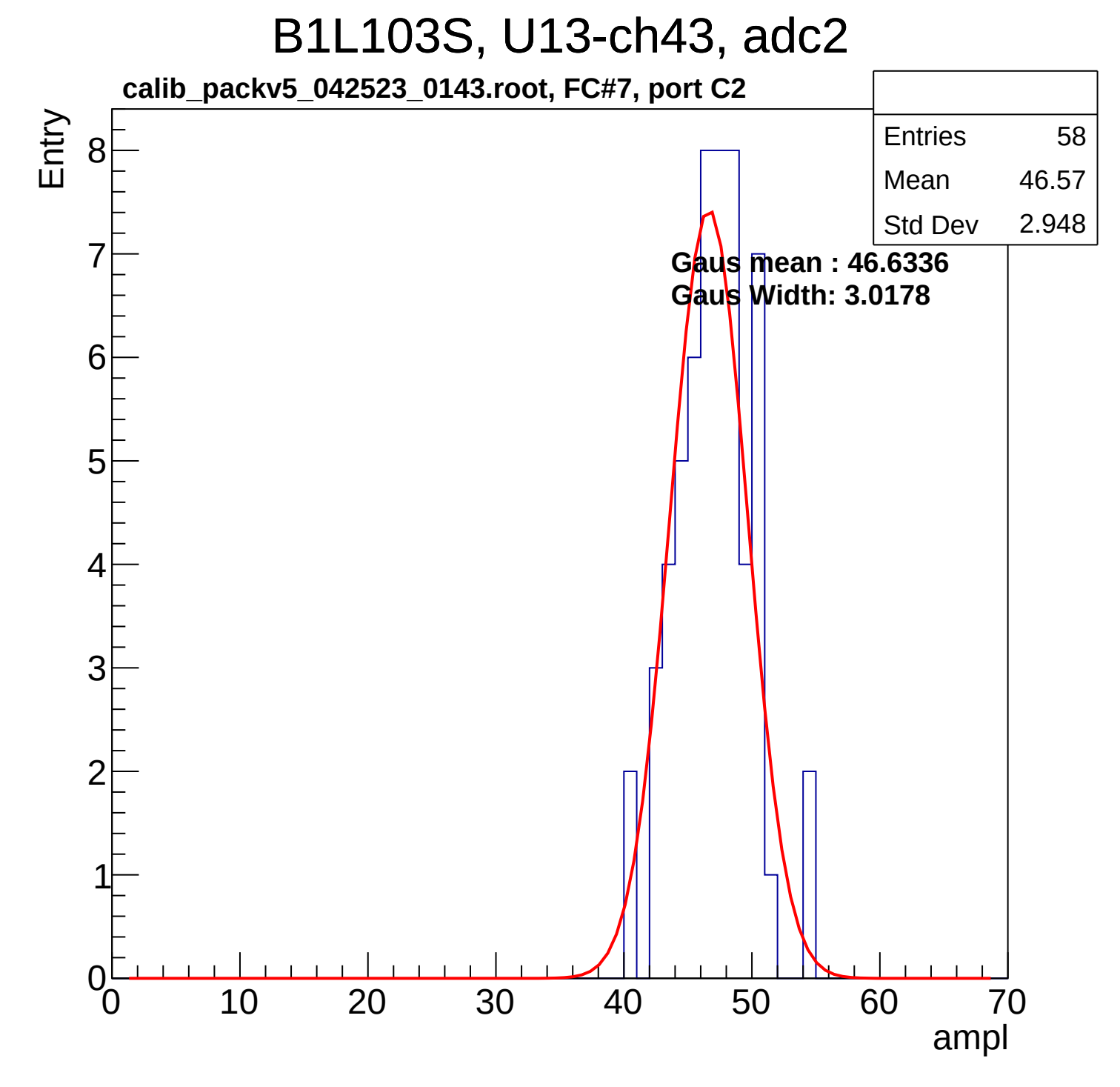

B1L103S, U13-ch43, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 39.38
Std Dev 3.256
Gaus mean : 40.5847
Gaus Width: 3.6623
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch43, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 46.57
Std Dev 2.948
Gaus mean : 46.6336
Gaus Width: 3.0178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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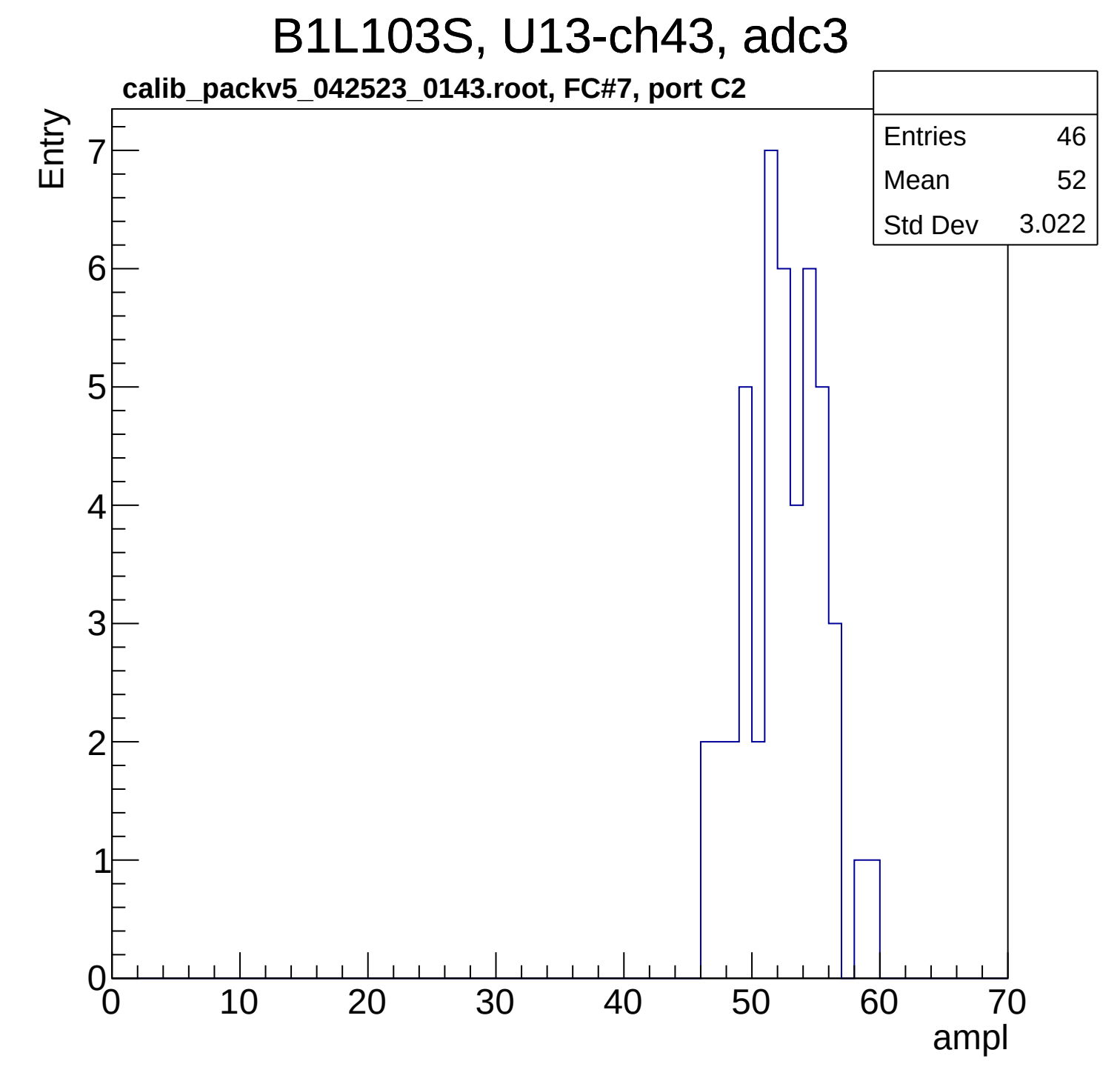
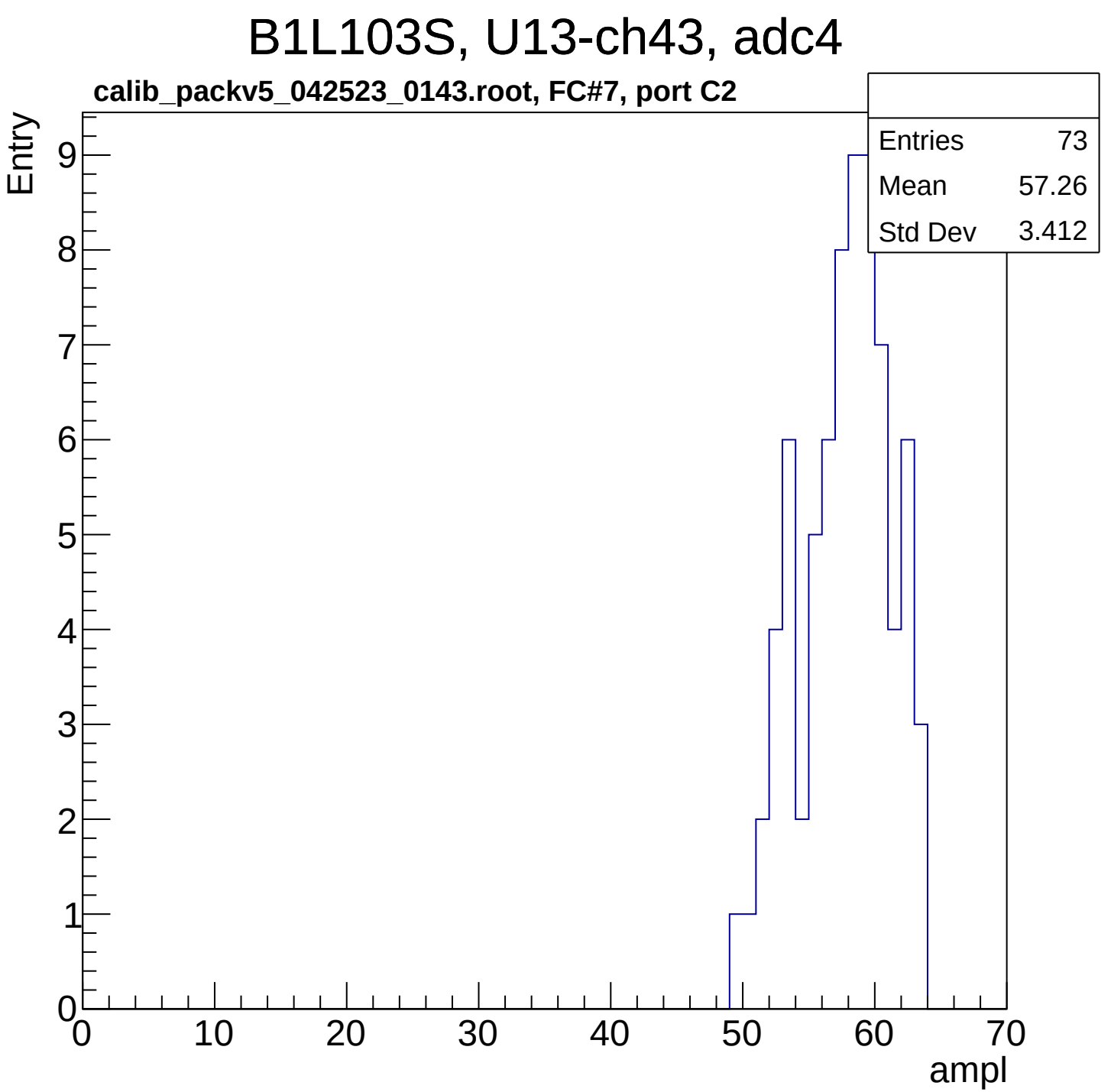

B1L103S, U13-ch43, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 52
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch43, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 57.26
Std Dev 3.412
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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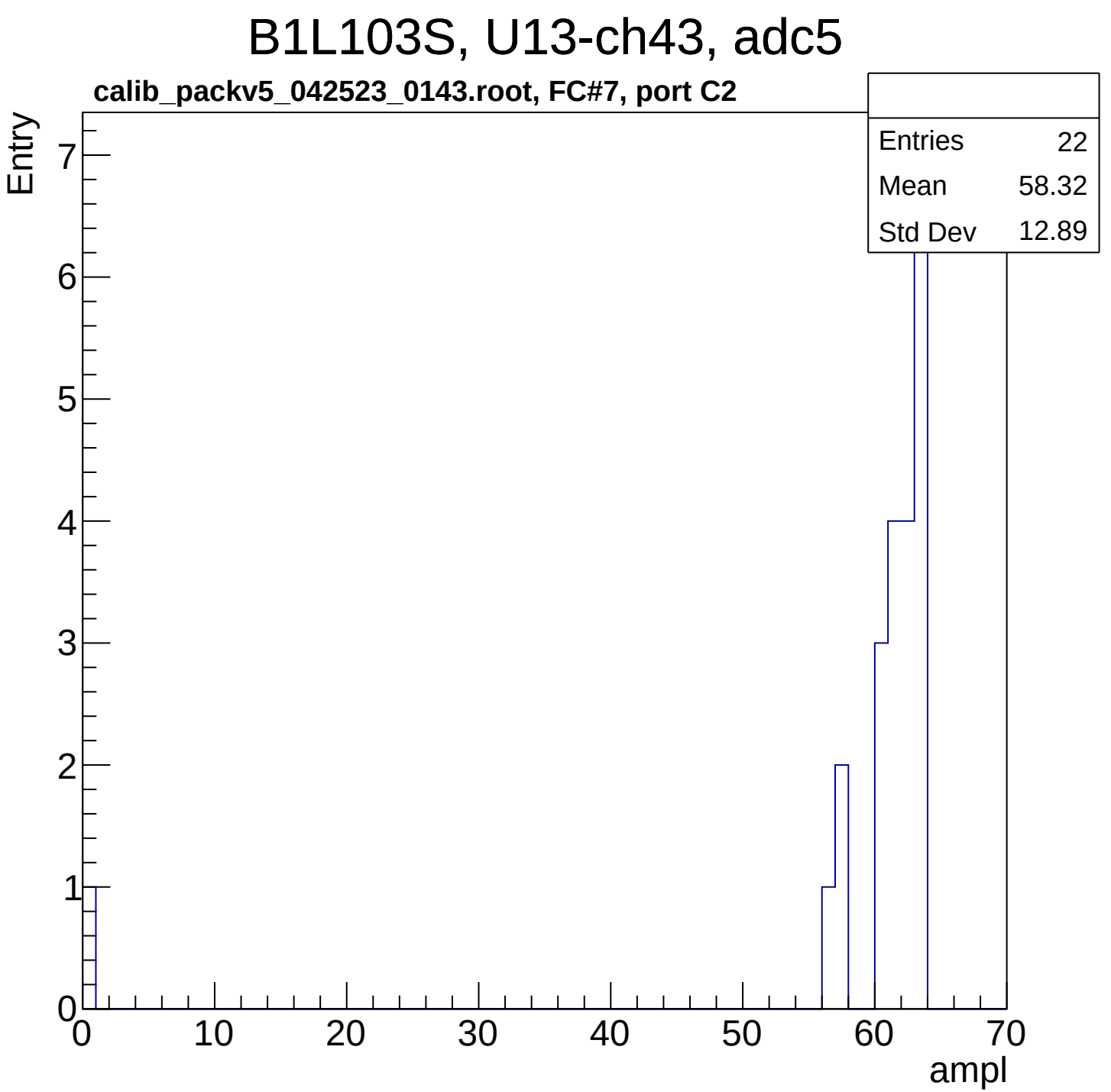
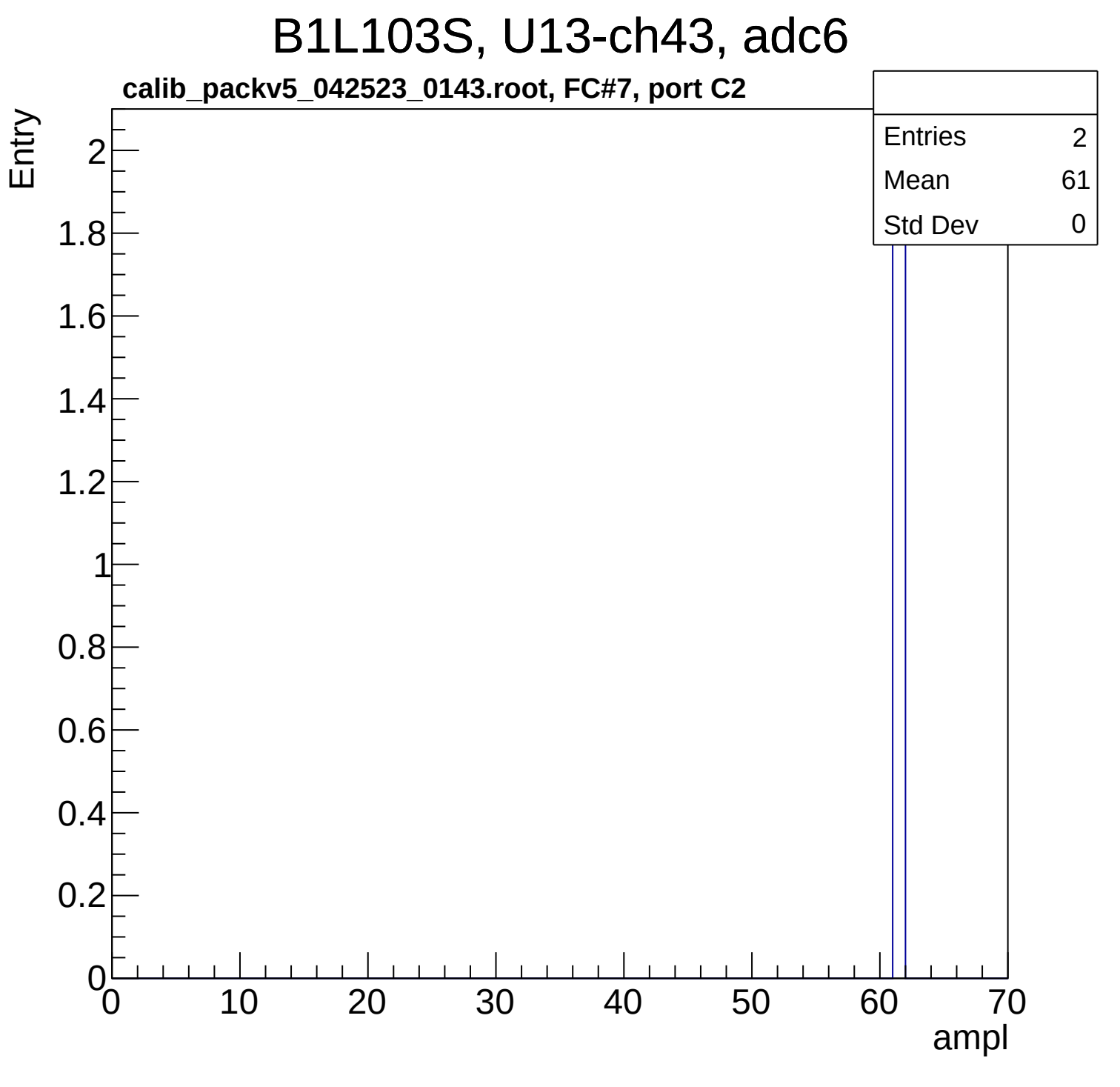
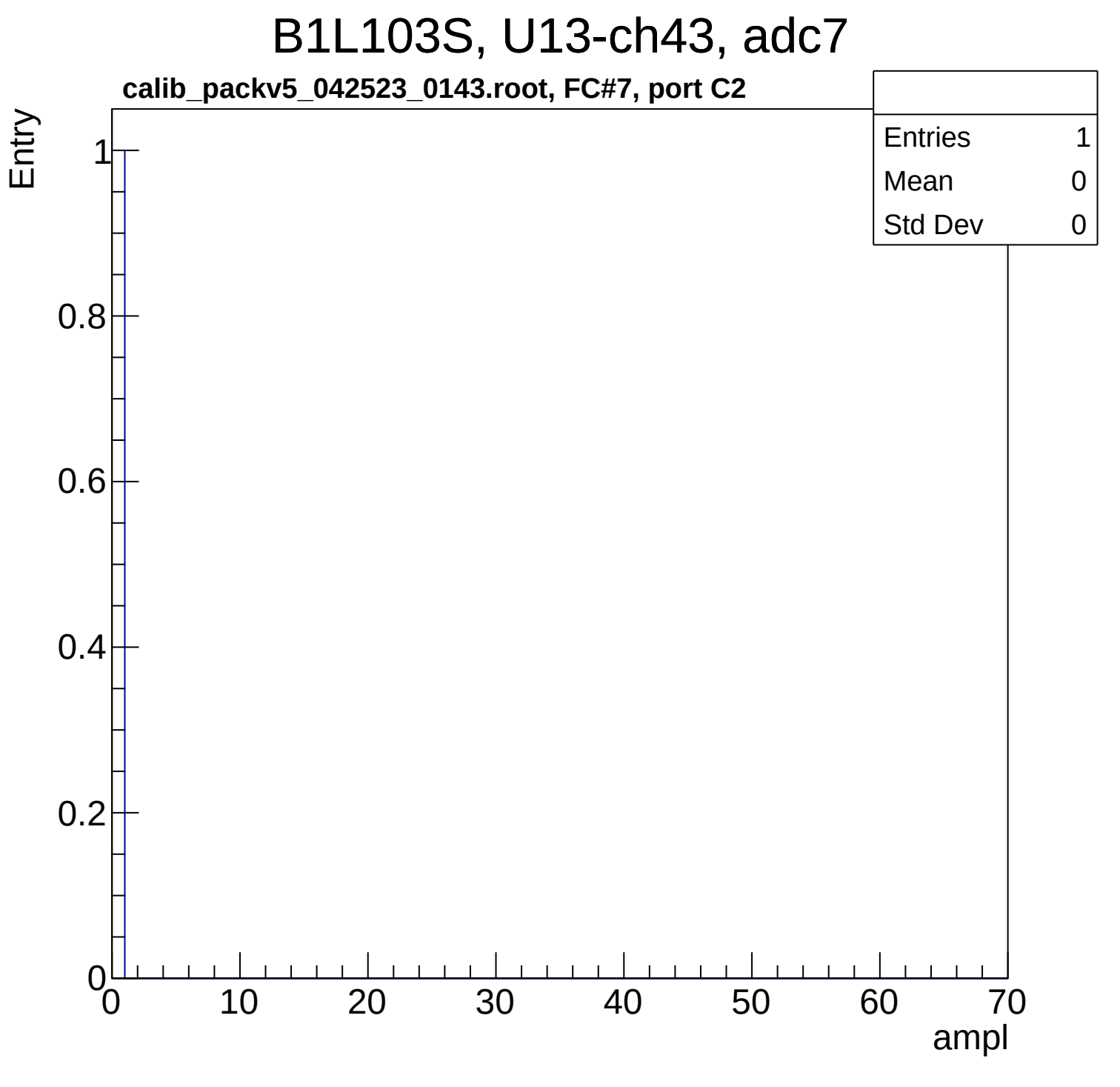
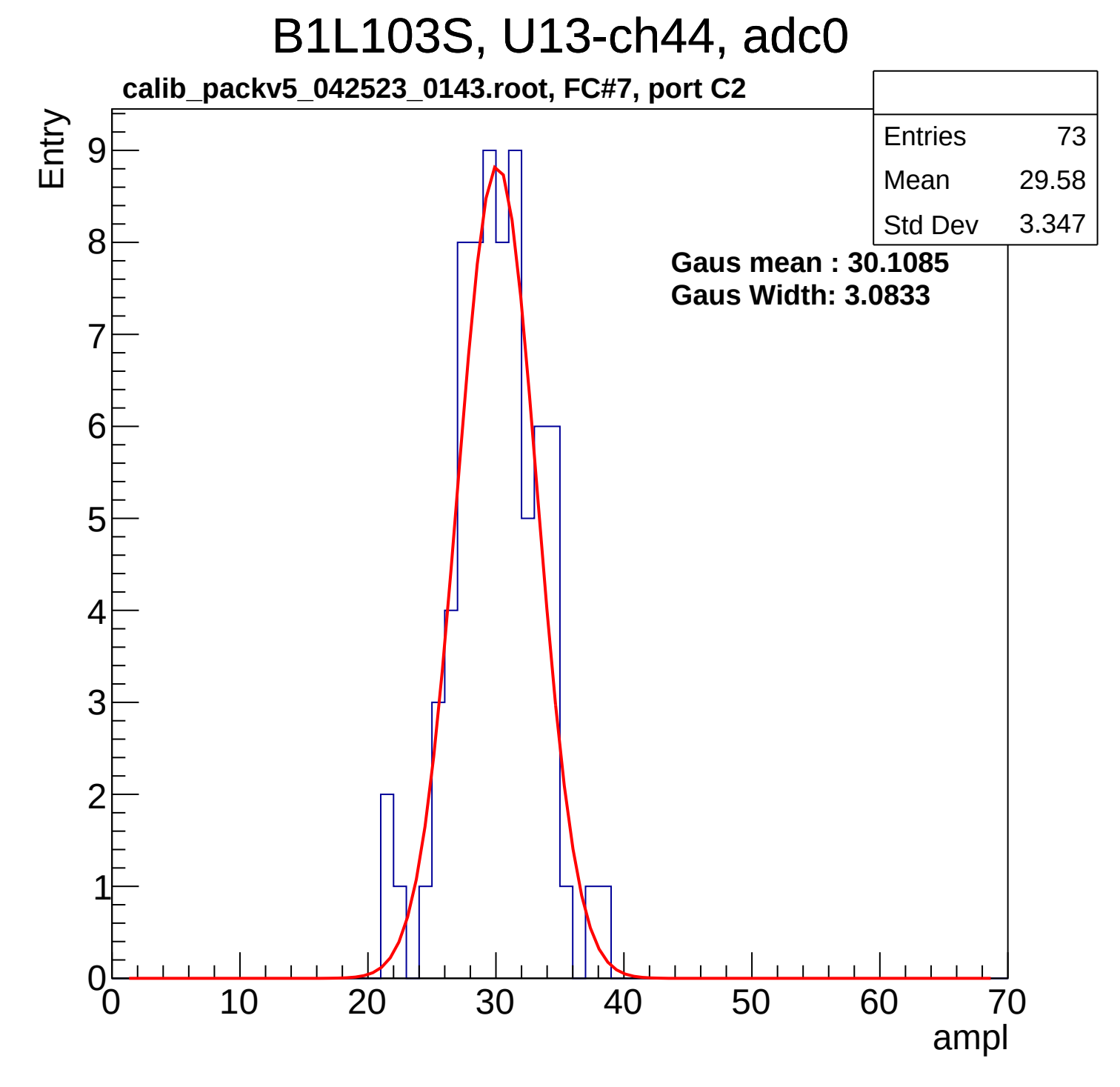
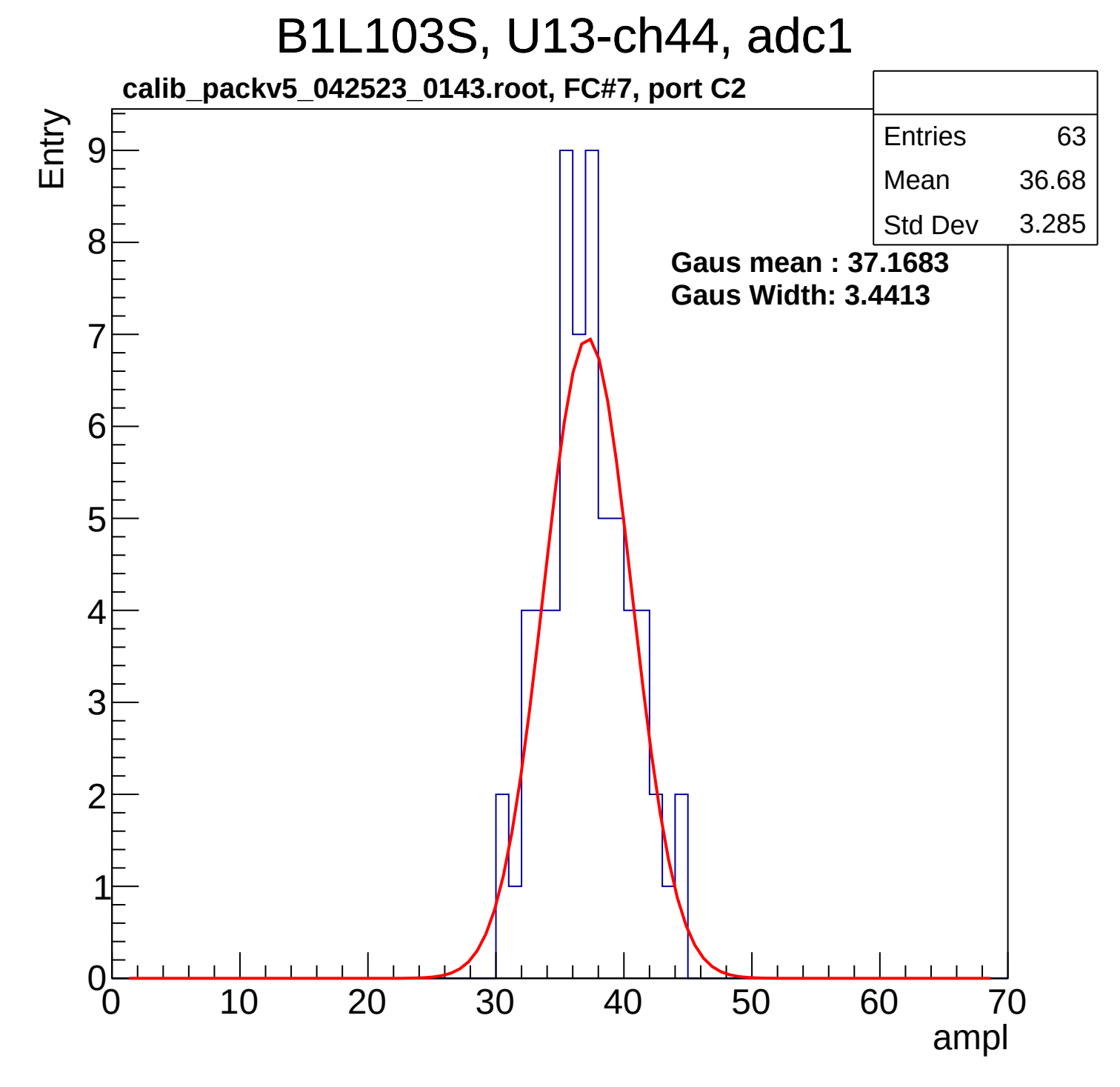
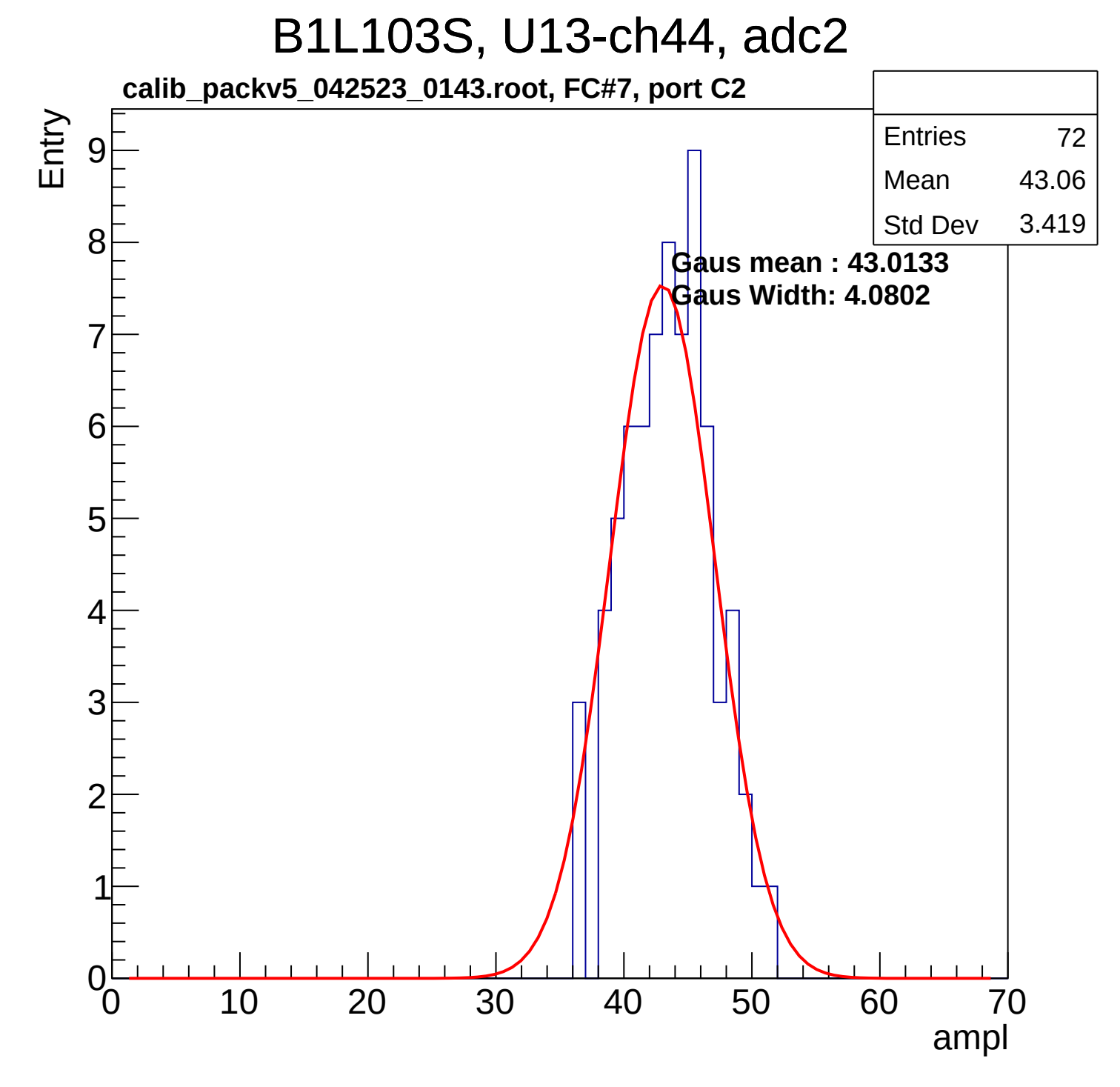

B1L103S, U13-ch43, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.32
Std Dev 12.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch43, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch43, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch44, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.58
Std Dev 3.347
Gaus mean : 30.1085
Gaus Width: 3.0833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch44, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 36.68
Std Dev 3.285
Gaus mean : 37.1683
Gaus Width: 3.4413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch44, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.06
Std Dev 3.419
Gaus mean : 43.0133
Gaus Width: 4.0802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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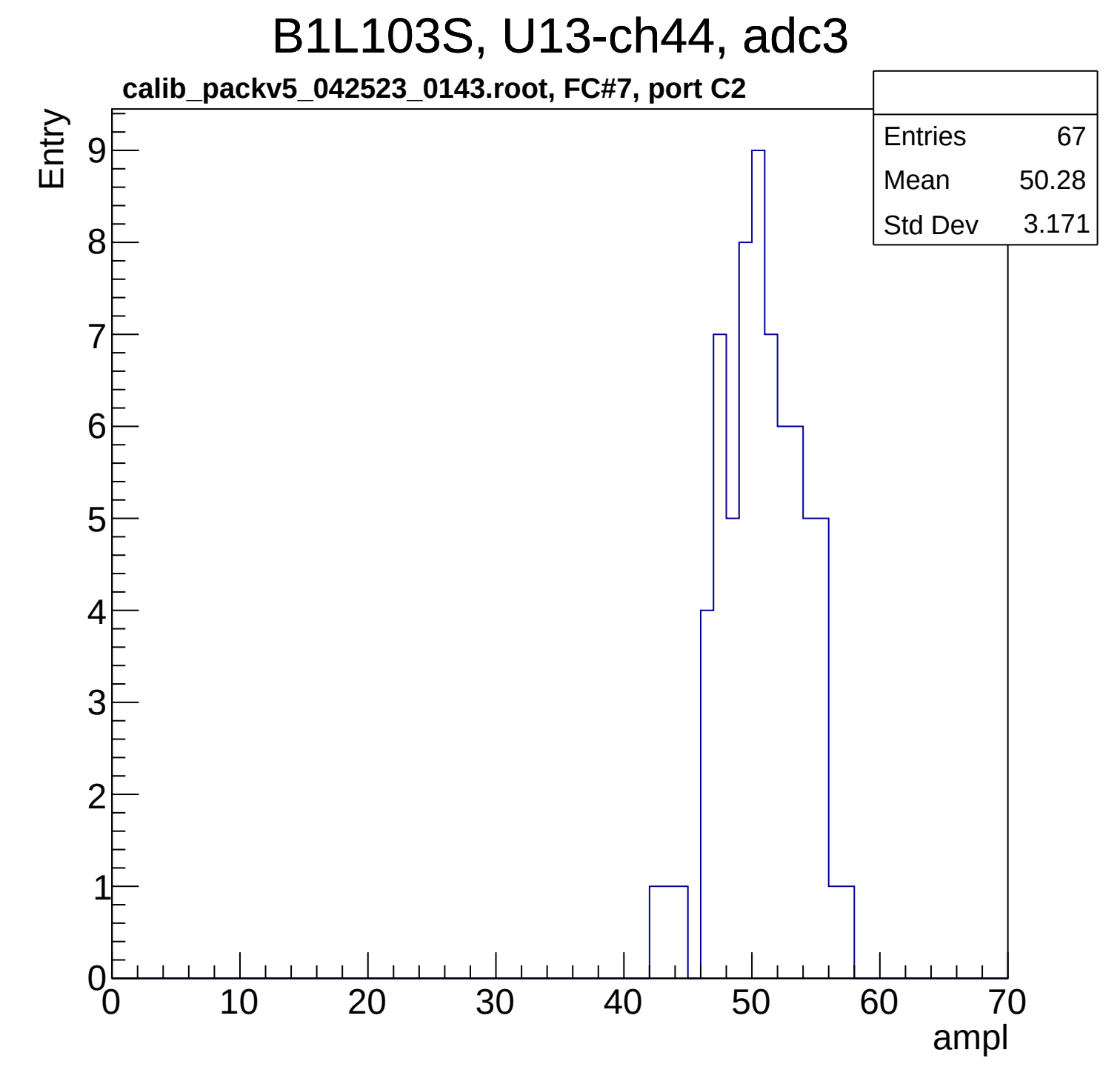
B1L103S, U13-ch44, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 50.28
Std Dev 3.171
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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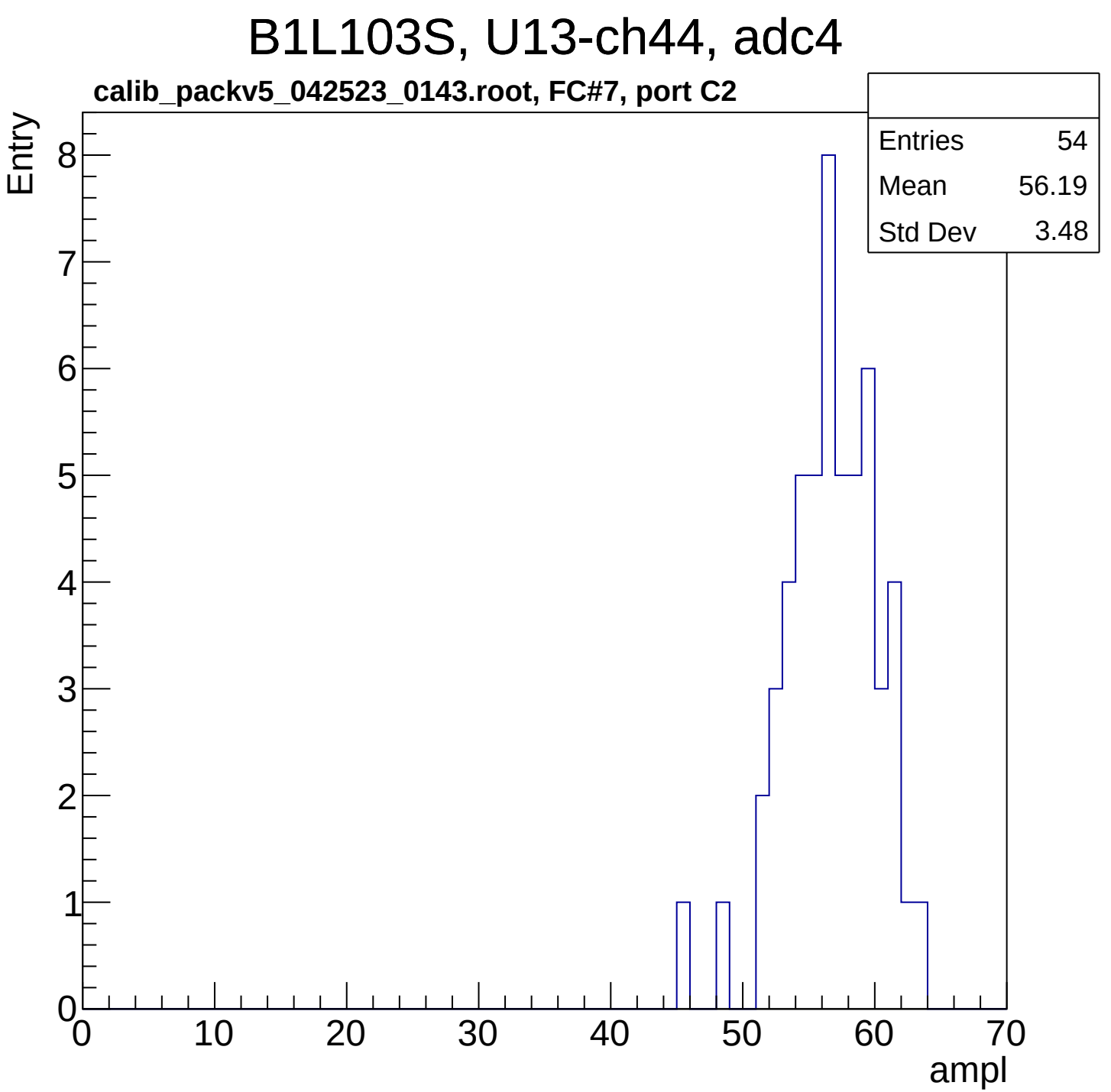
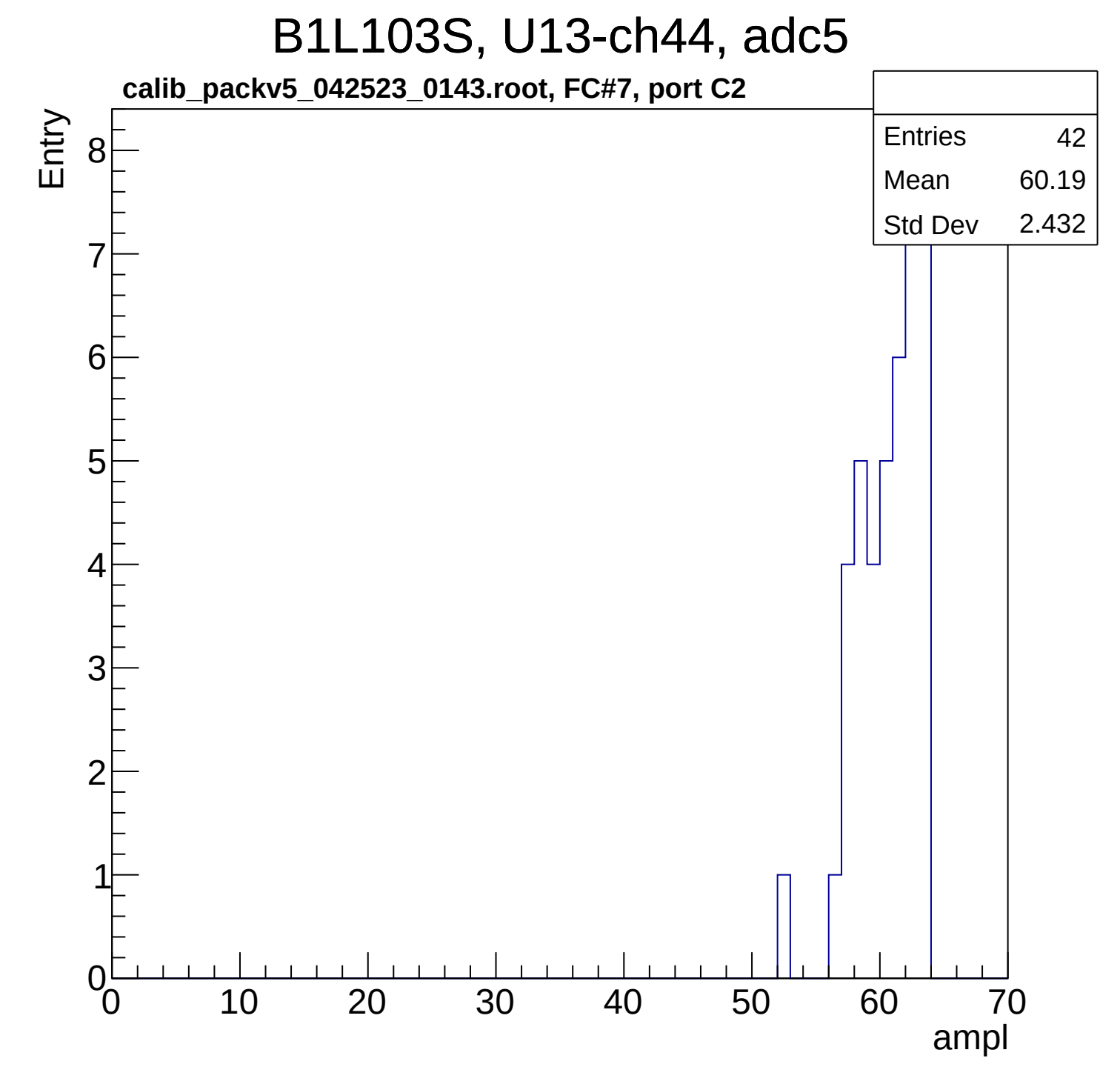
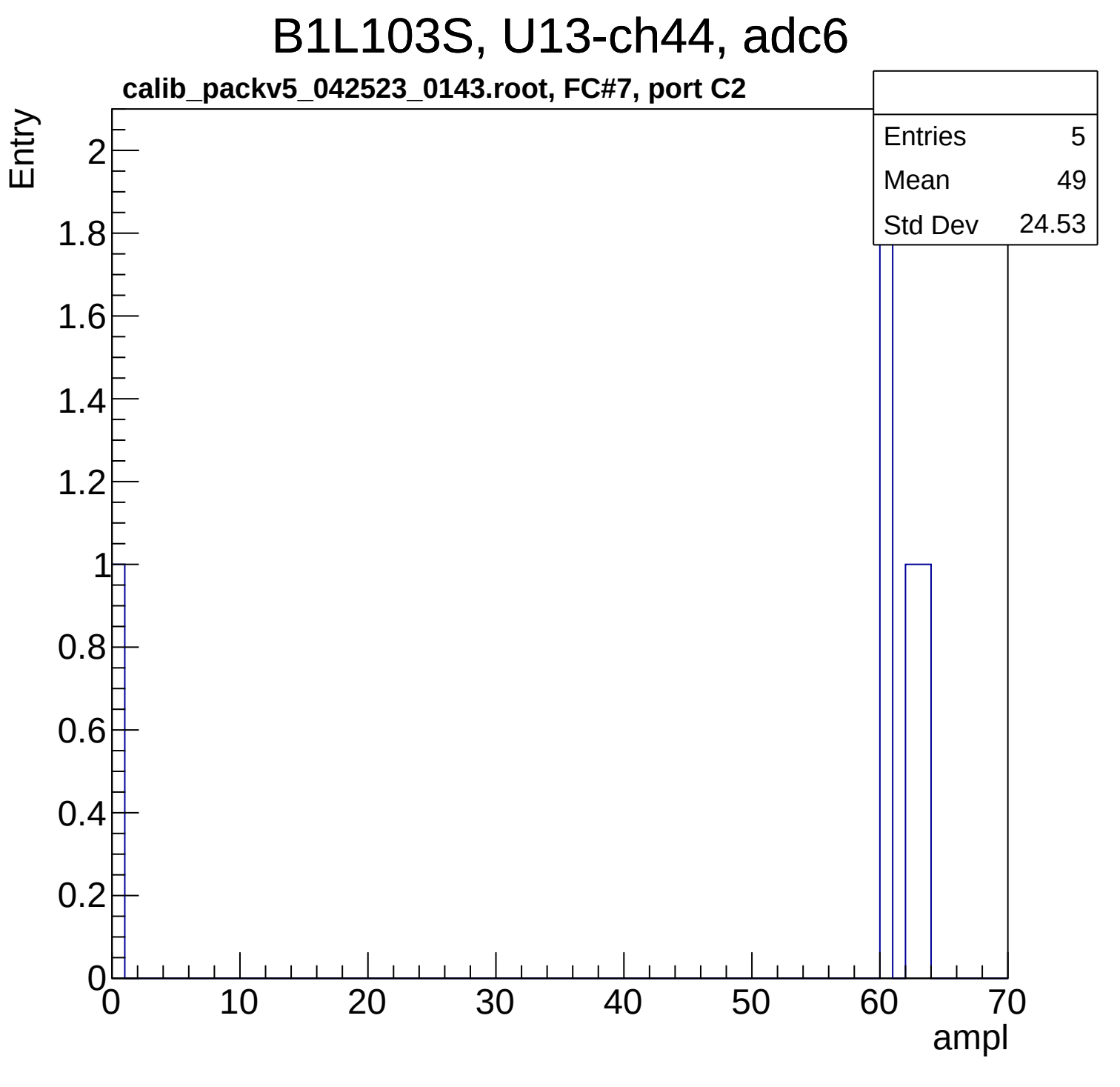
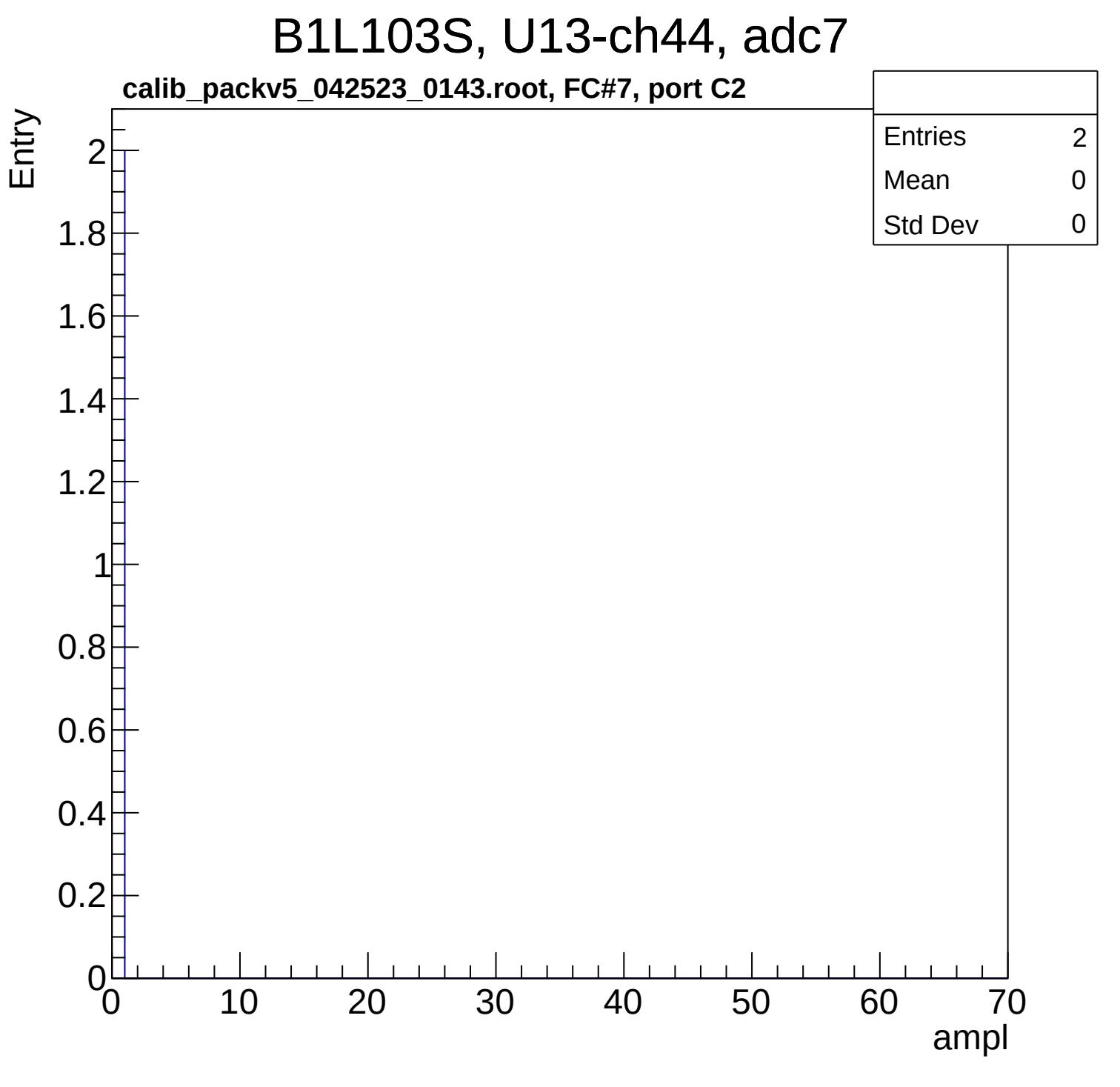
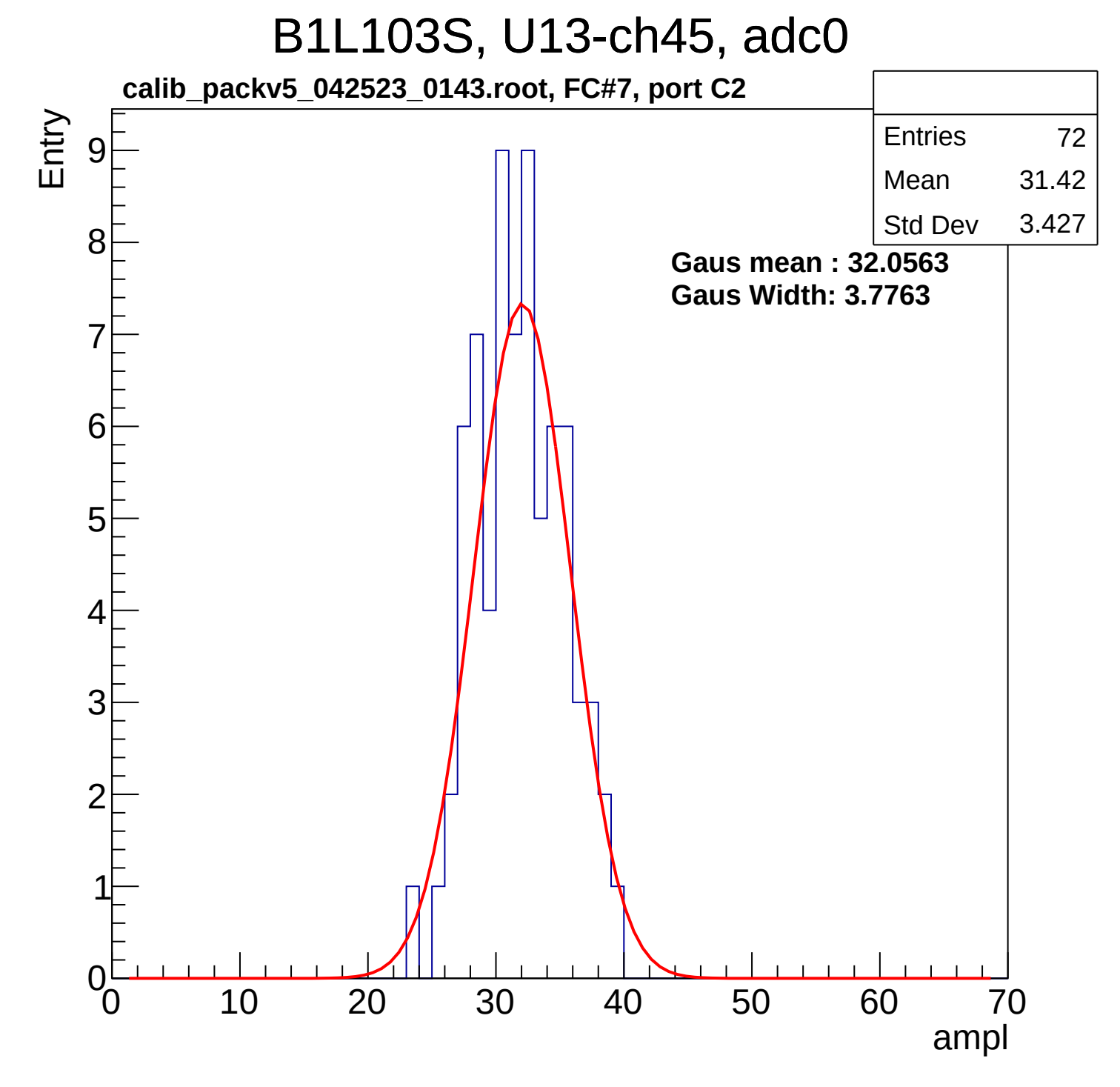
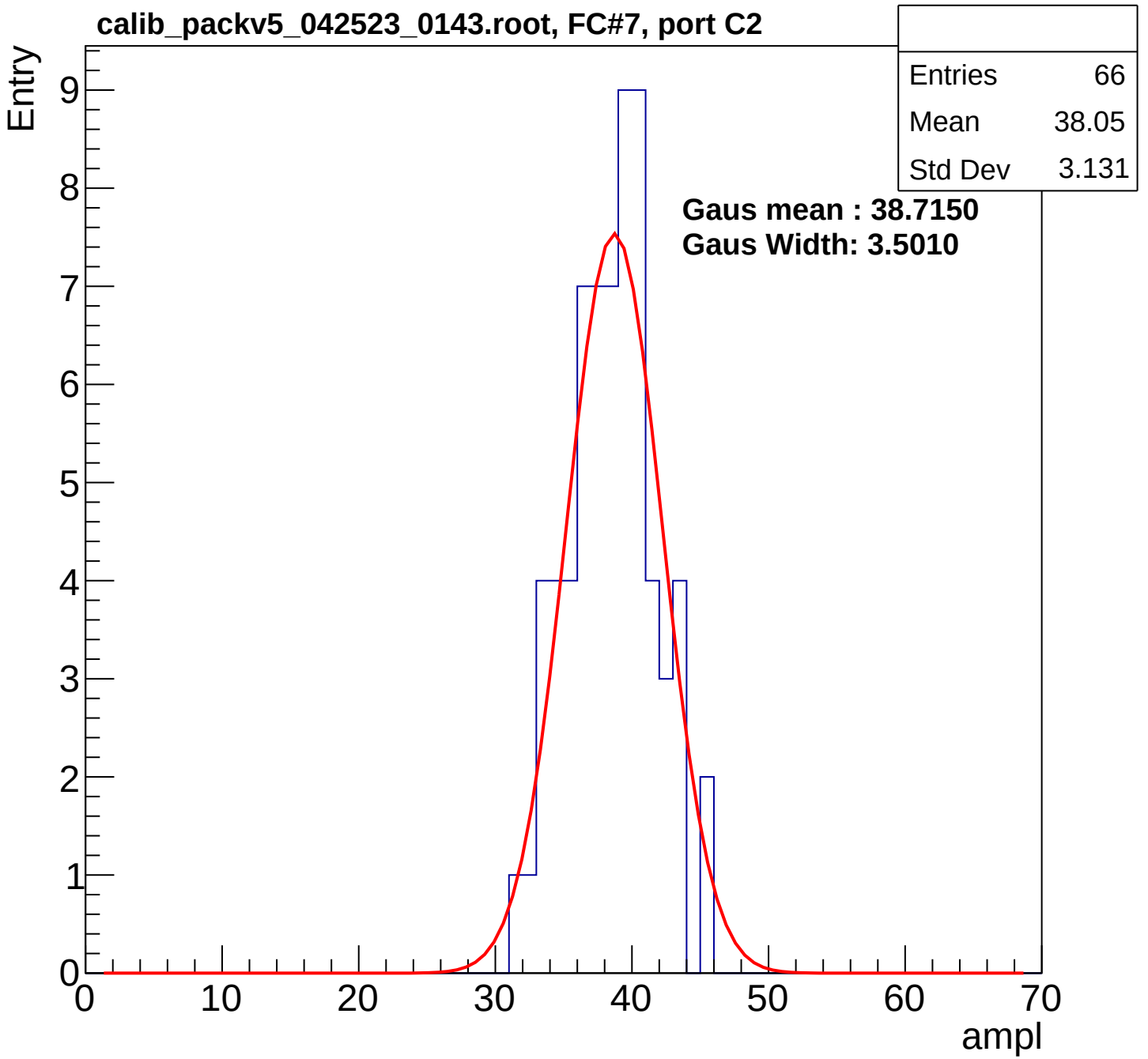

B1L103S, U13-ch44, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.19
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

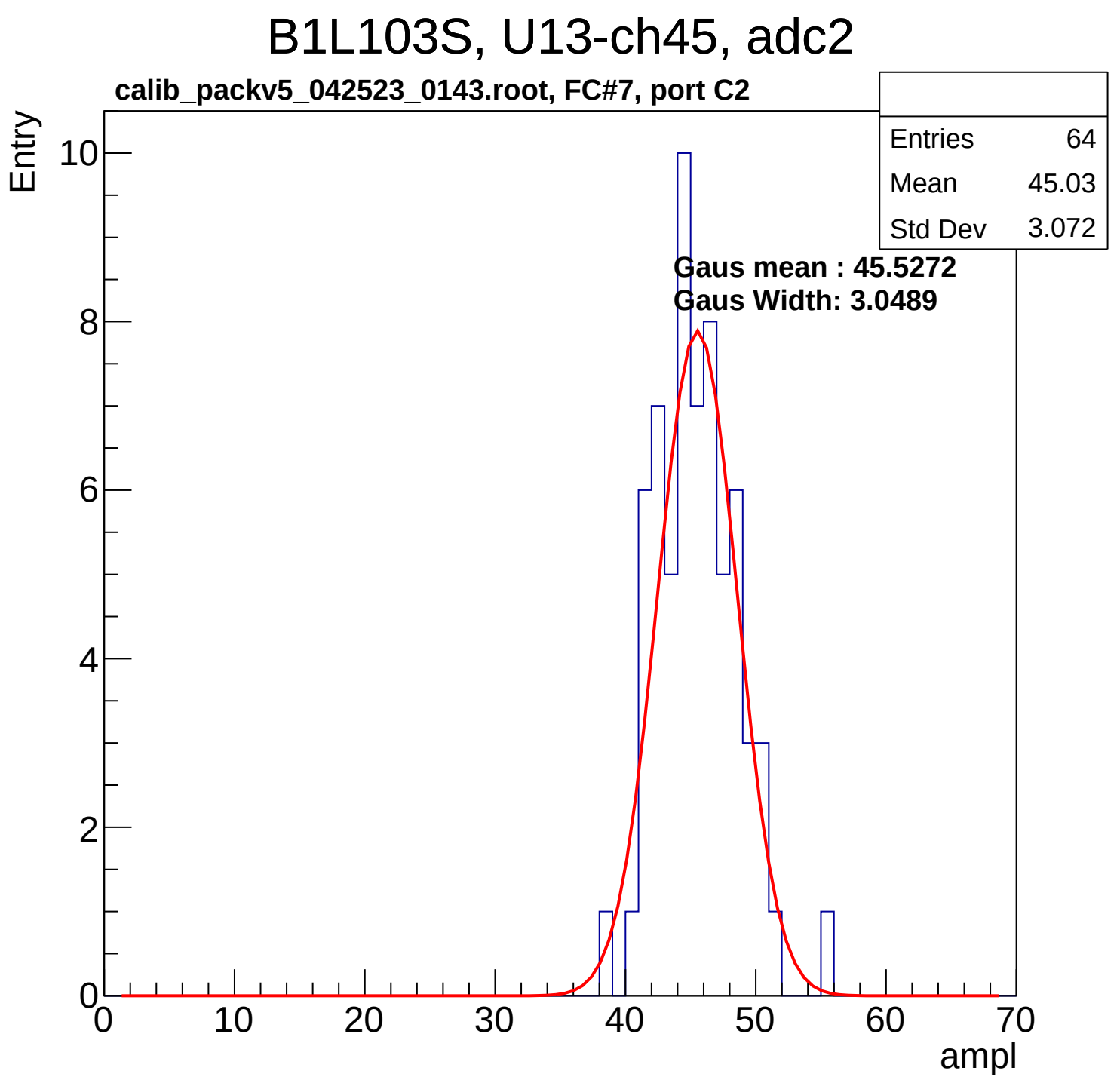

B1L103S, U13-ch44, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 60.19
Std Dev 2.432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

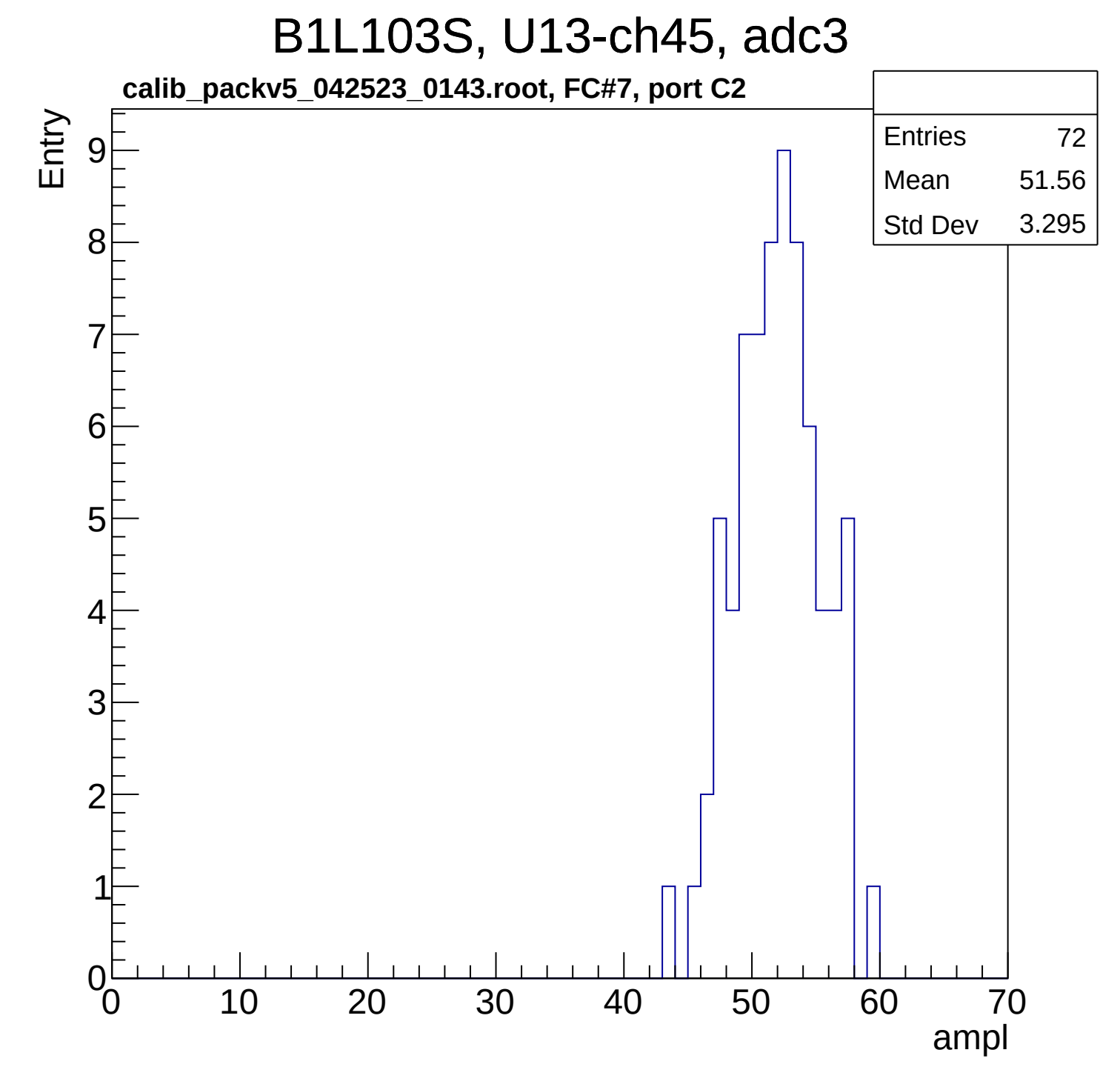

B1L103S, U13-ch44, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49
Std Dev 24.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch44, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch45, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 31.42
Std Dev 3.427
Gaus mean : 32.0563
Gaus Width: 3.7763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch45, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 38.05
Std Dev 3.131
Gaus mean : 38.7150
Gaus Width: 3.5010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch45, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 45.03
Std Dev 3.072
Gaus mean : 45.5272
Gaus Width: 3.0489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch45, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 51.56
Std Dev 3.295
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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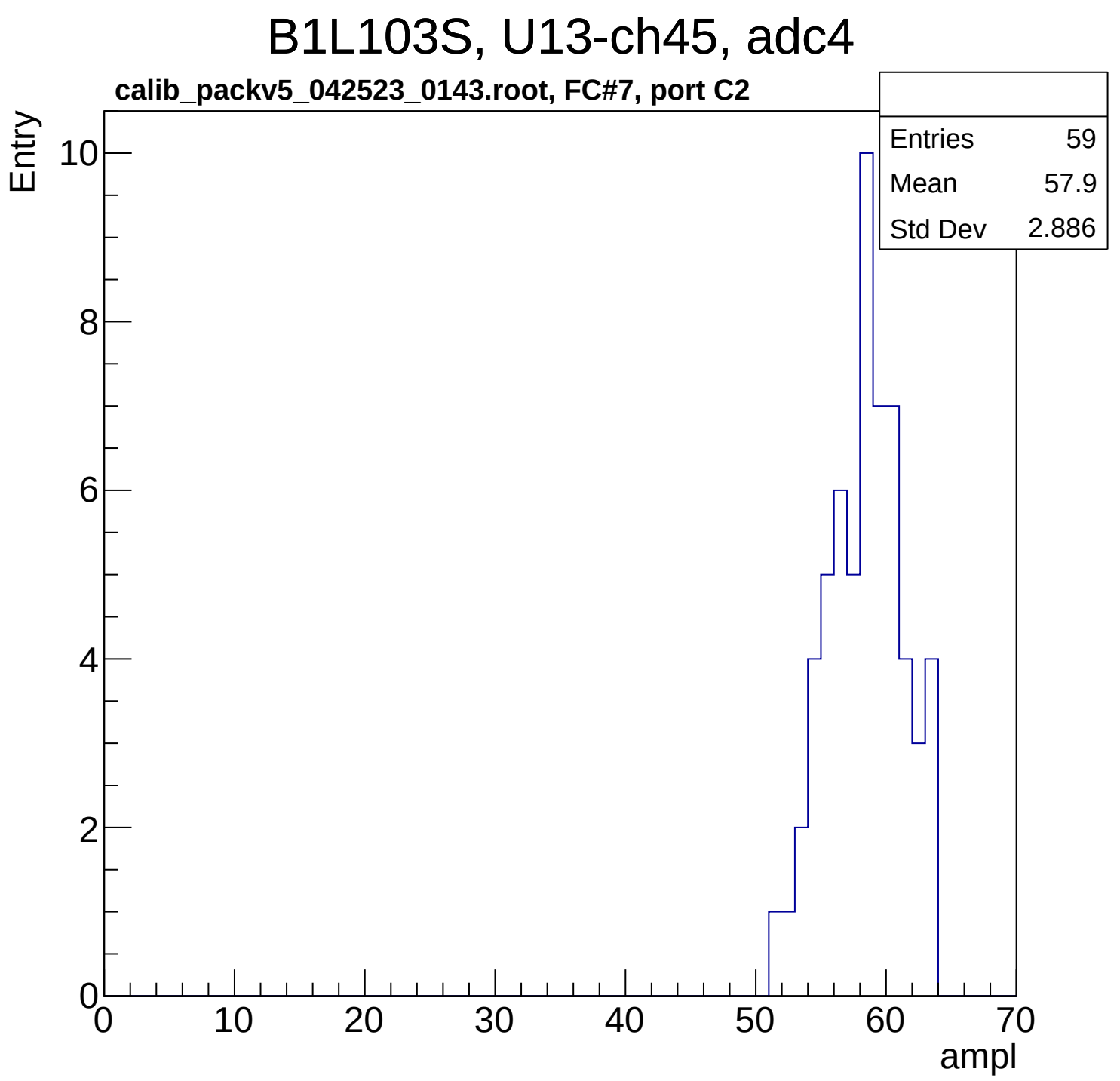
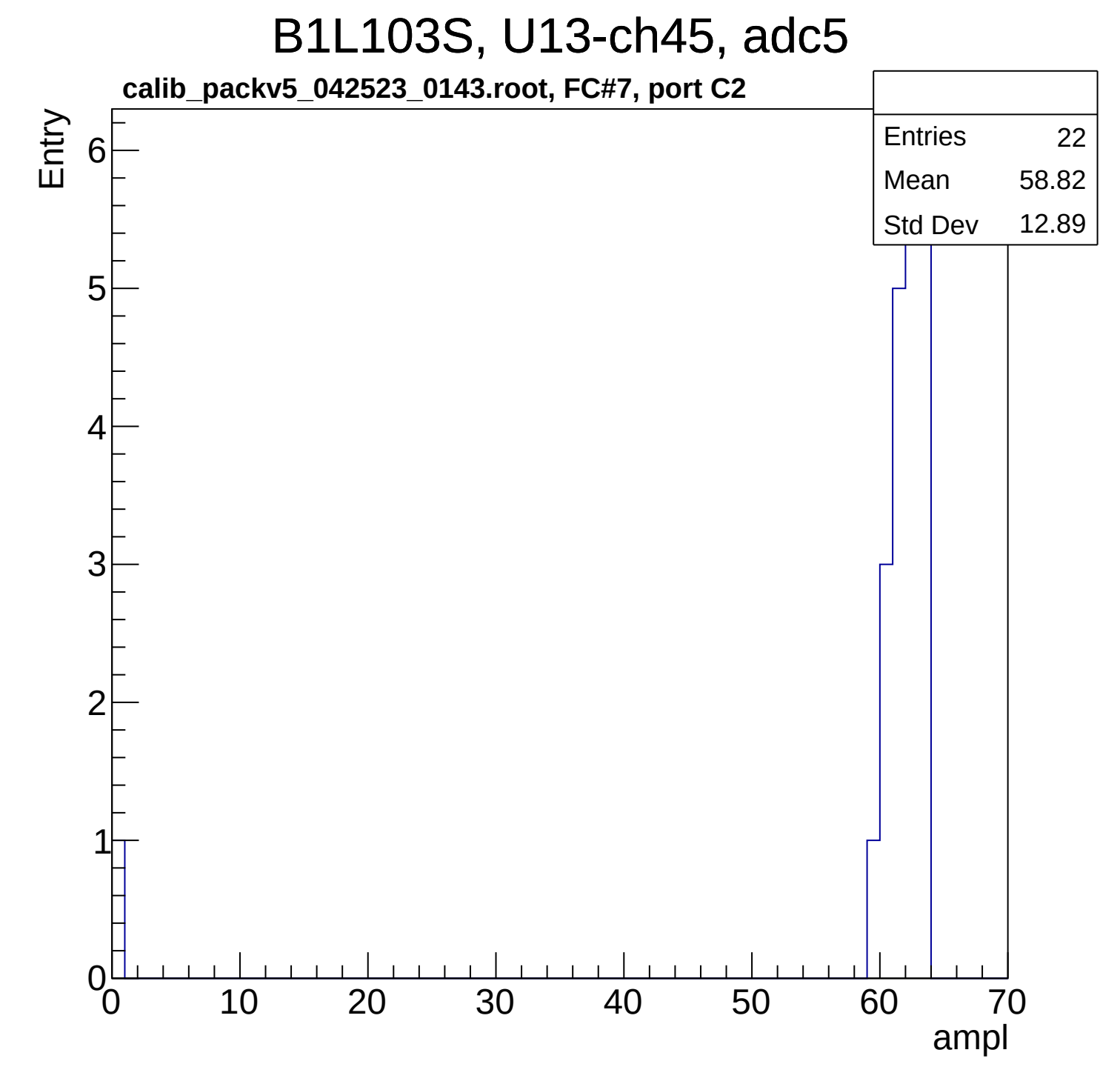
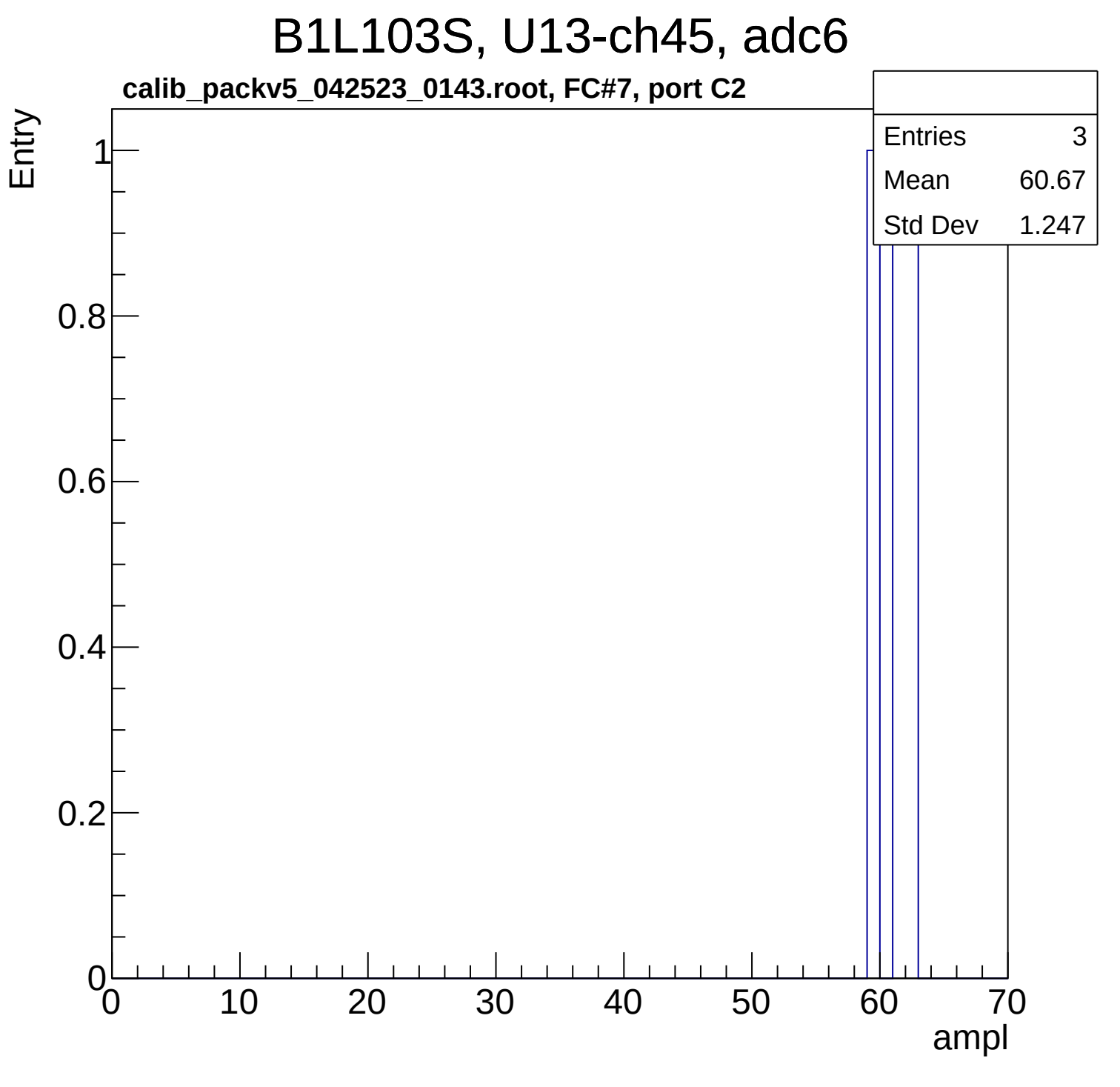
60
70


B1L103S, U13-ch45, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 57.9
Std Dev 2.886
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch45, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.82
Std Dev 12.89
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch45, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 60.67
Std Dev 1.247
Entry
ampl
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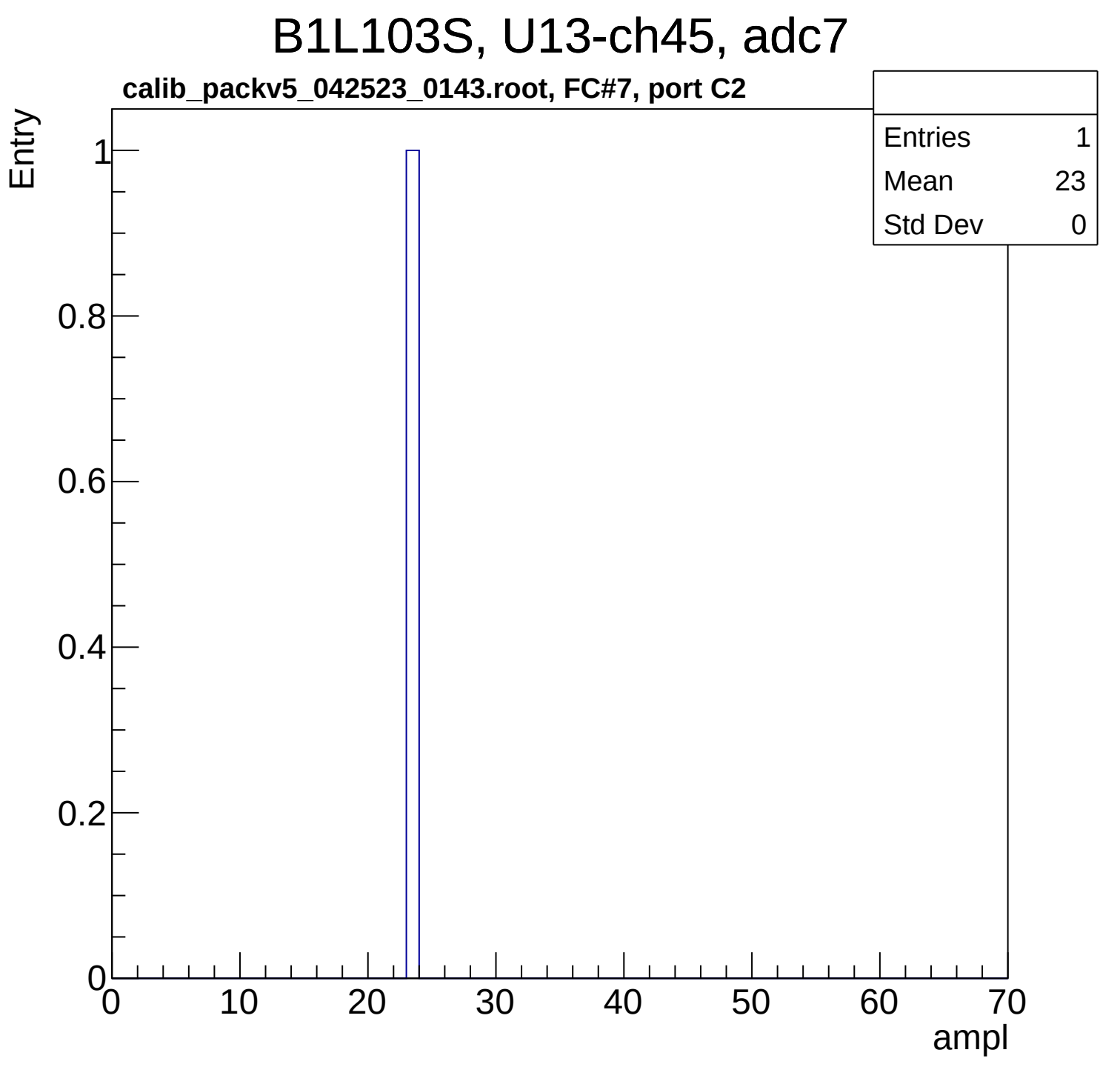
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch45, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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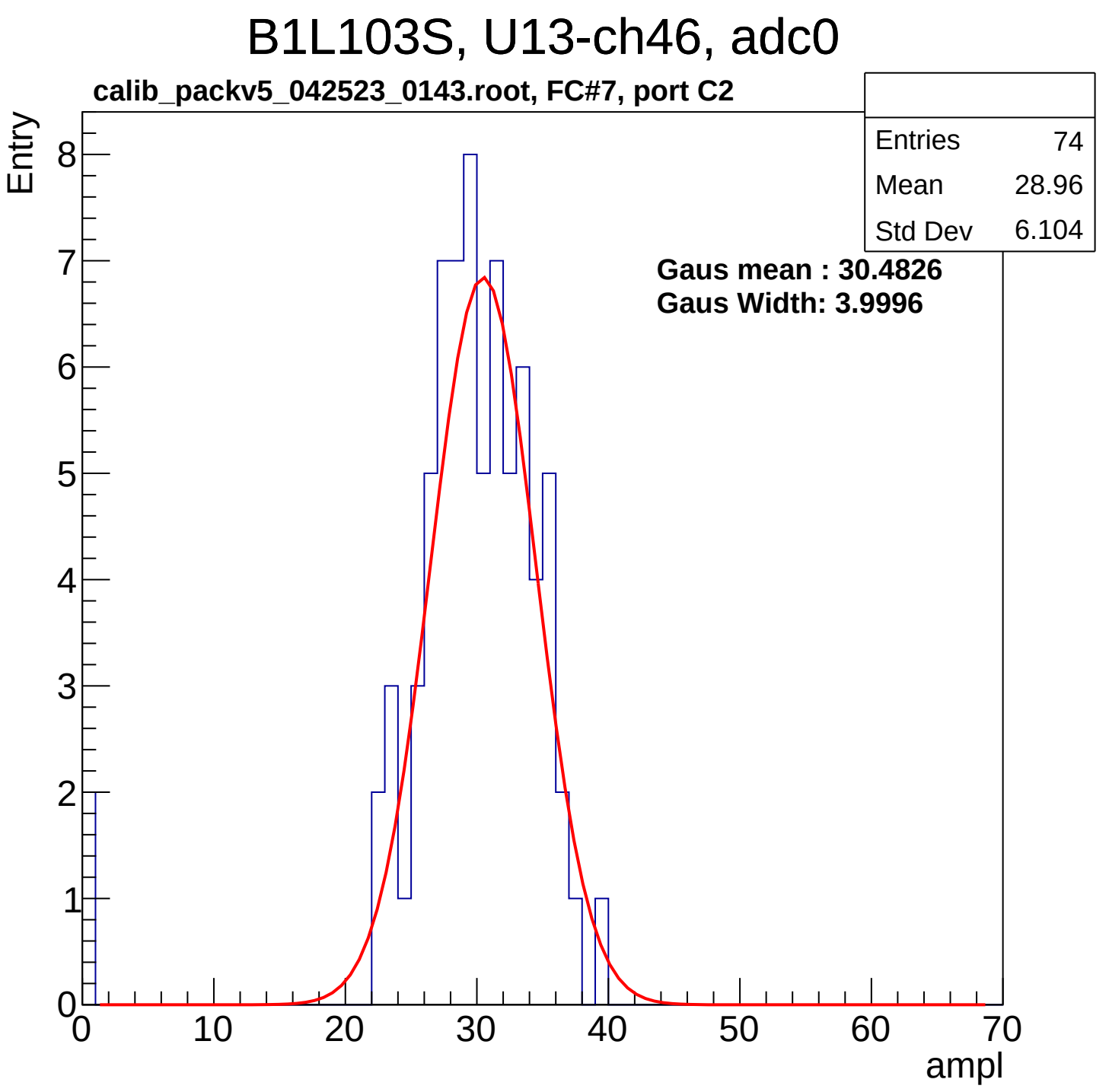
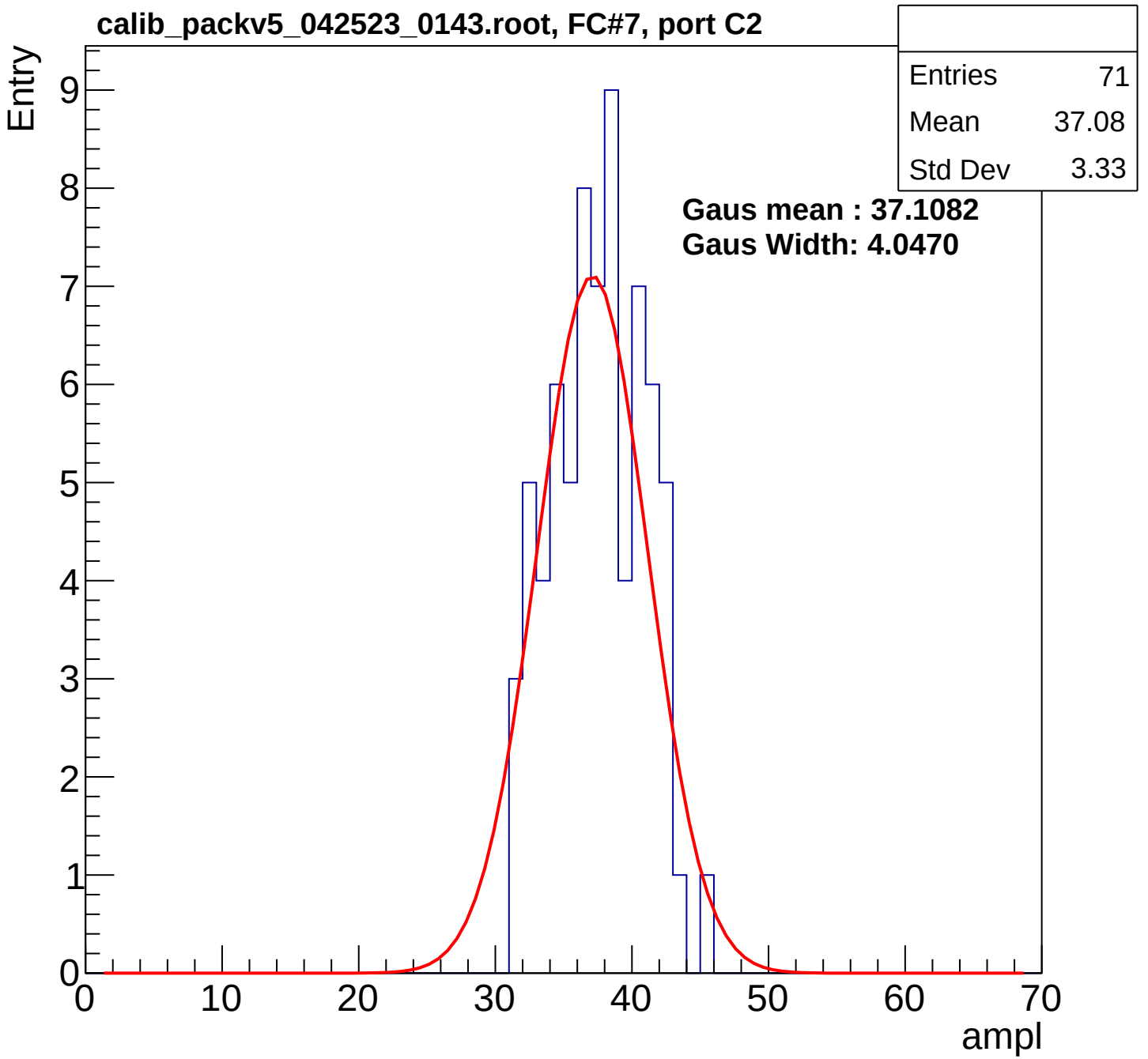
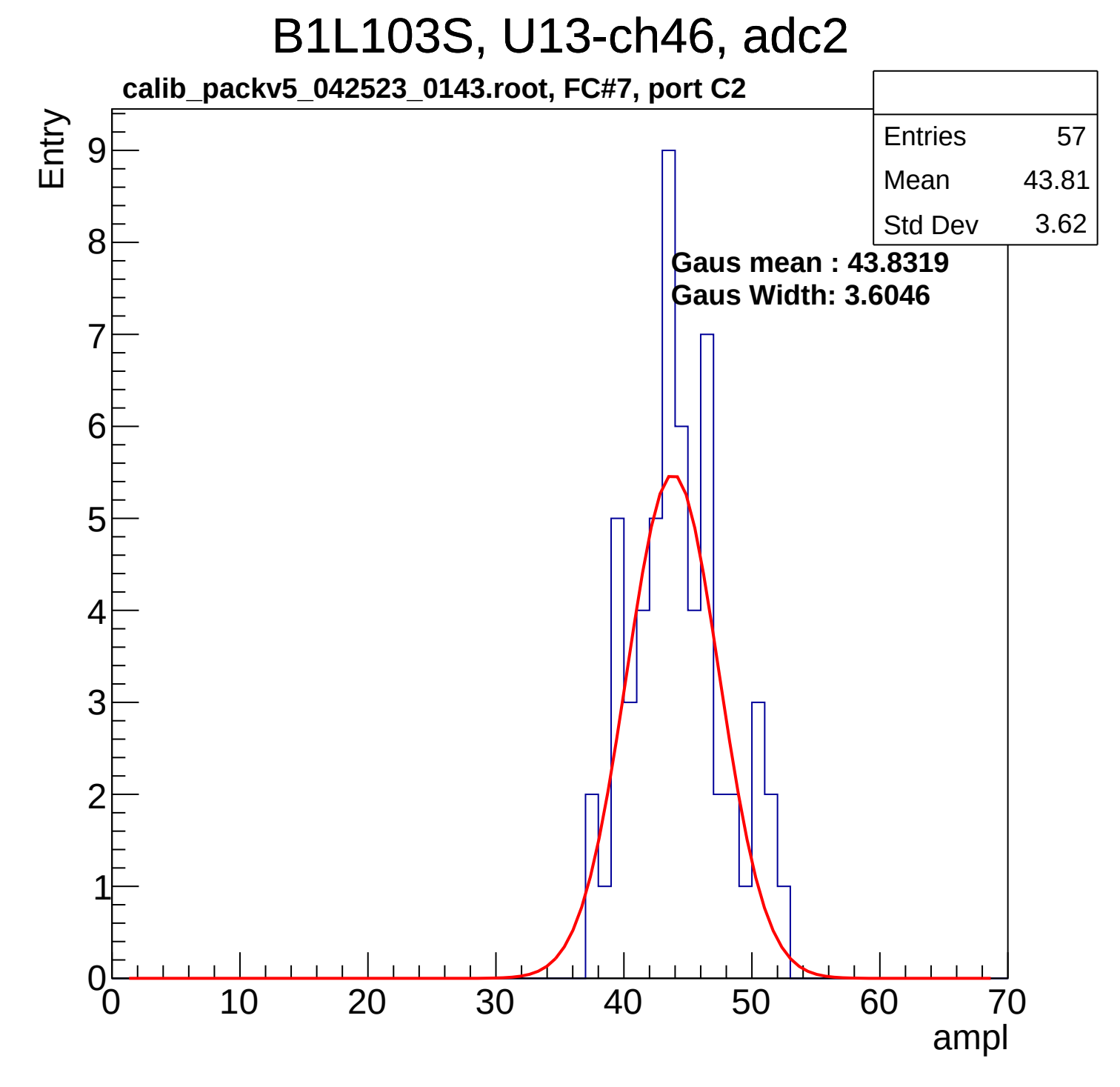
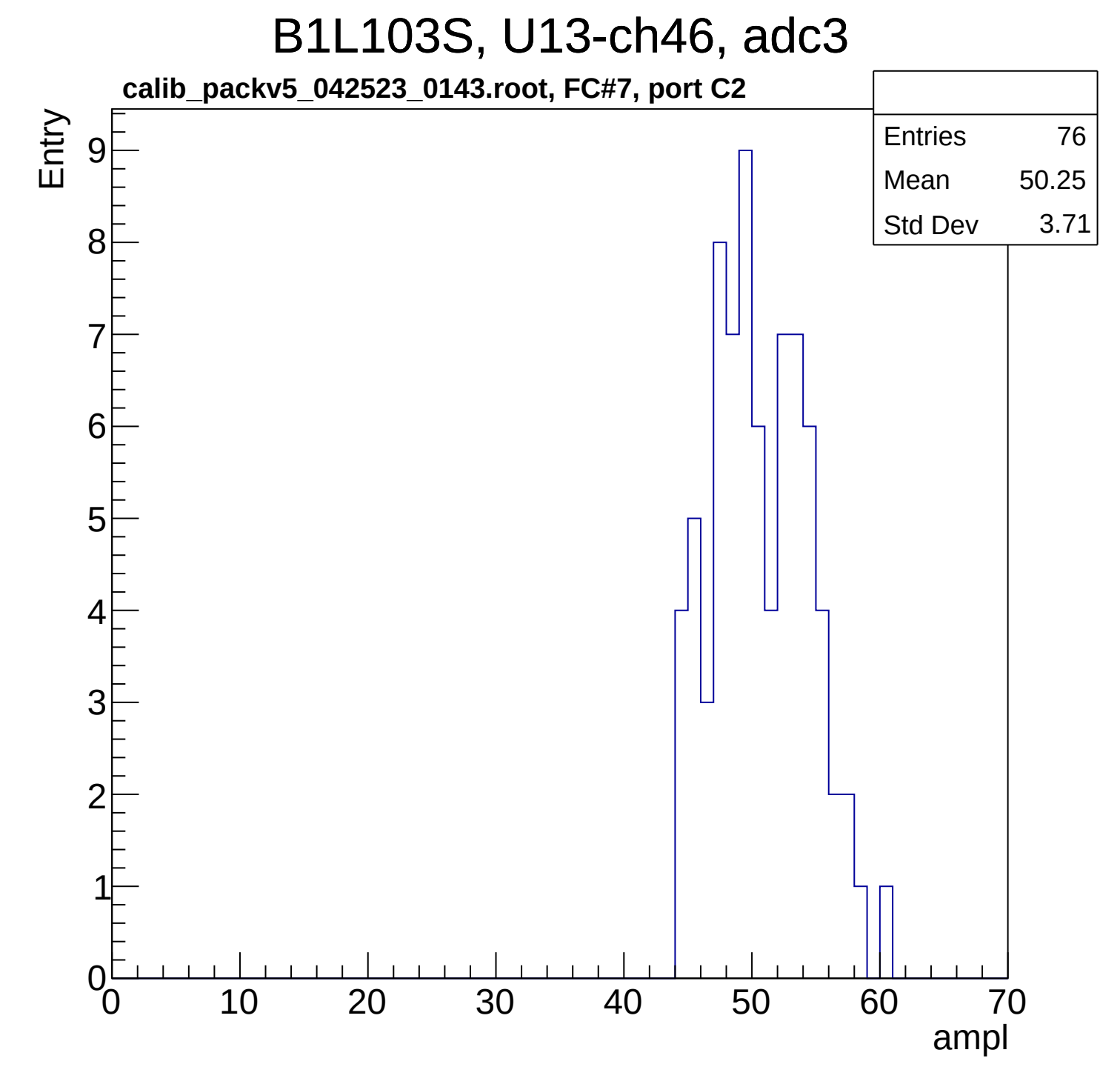
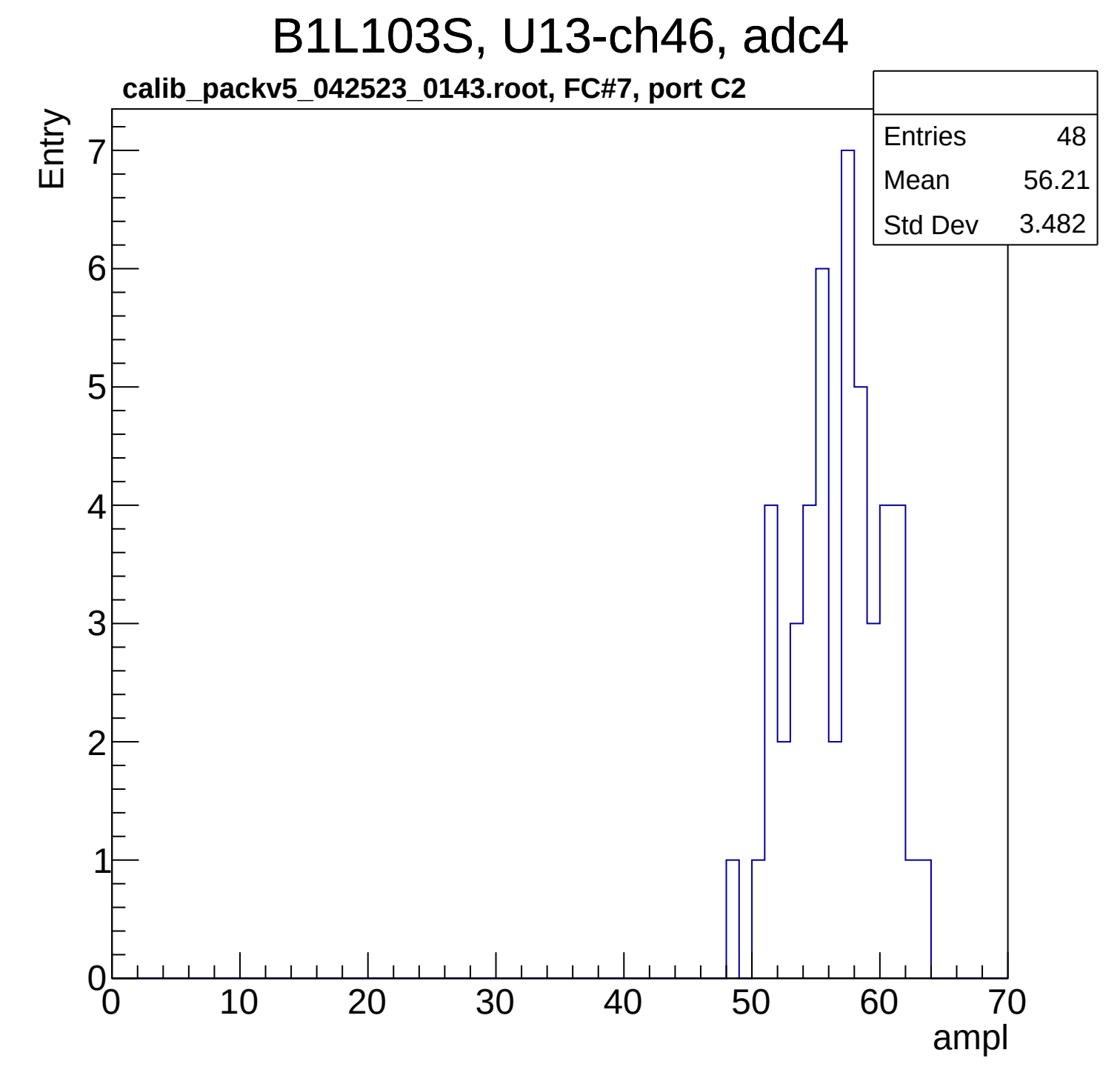
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

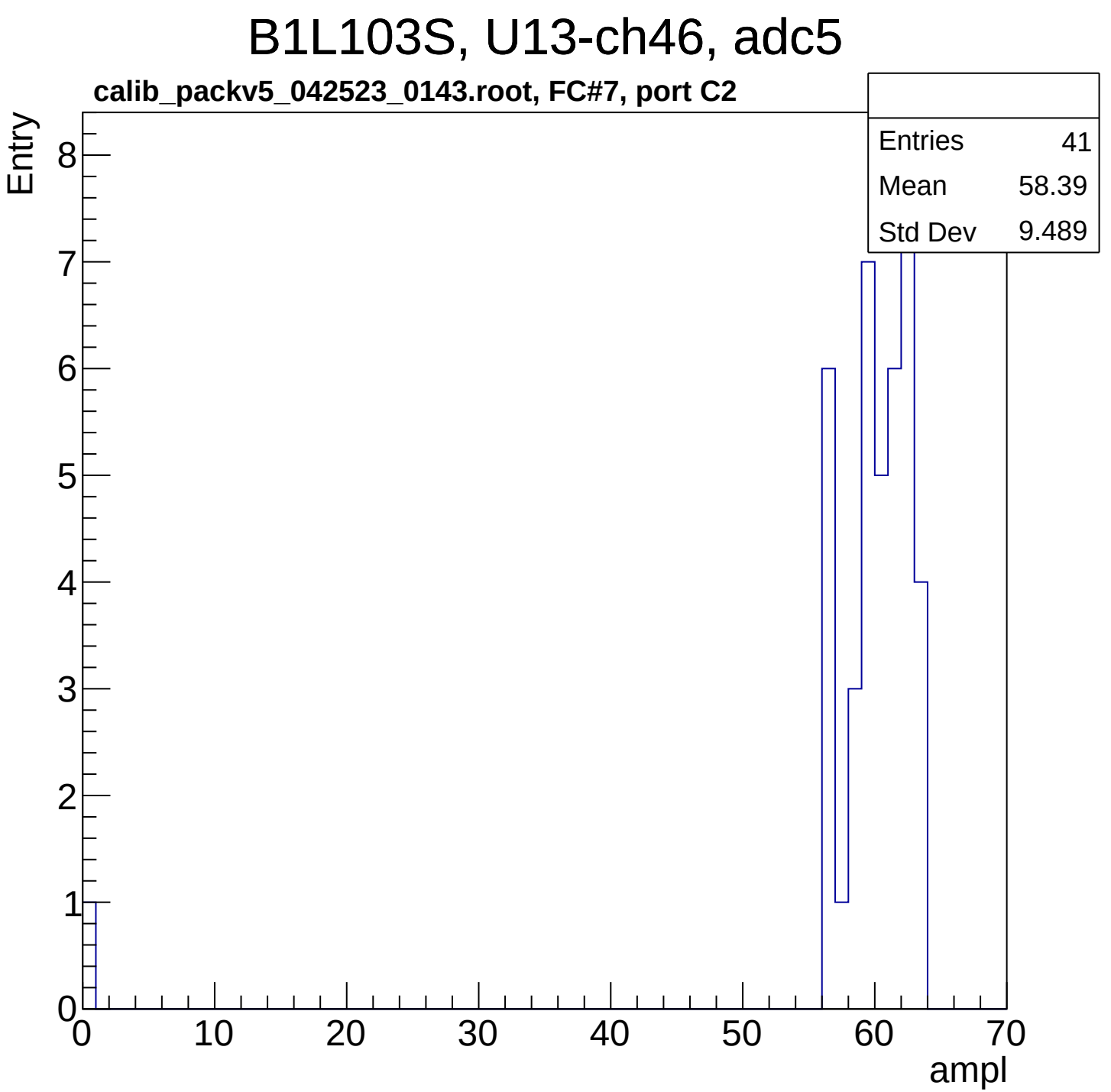

B1L103S, U13-ch46, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 28.96
Std Dev 6.104
Gaus mean : 30.4826
Gaus Width: 3.9996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

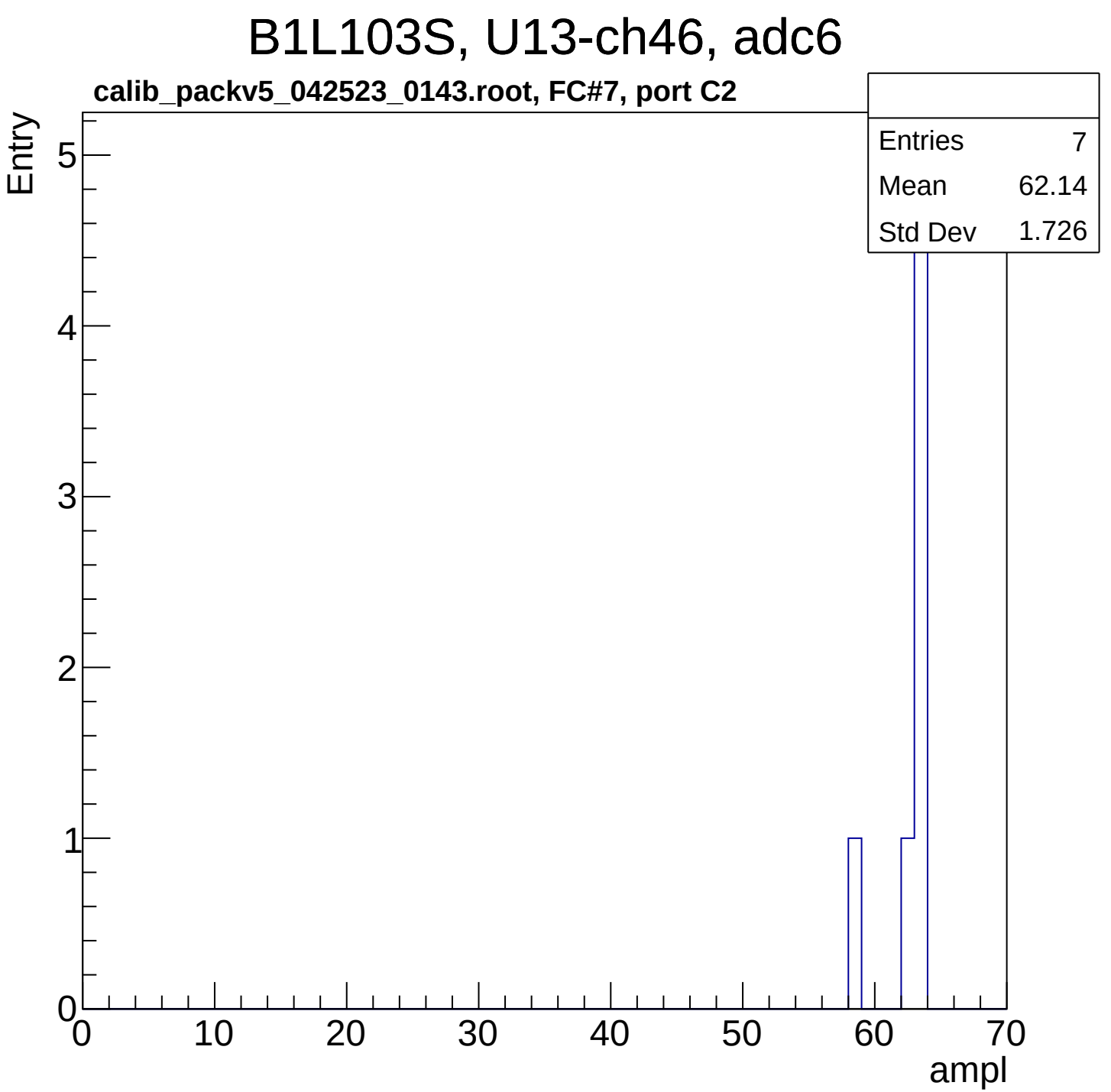

B1L103S, U13-ch46, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 37.08
Std Dev 3.33
Gaus mean : 37.1082
Gaus Width: 4.0470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

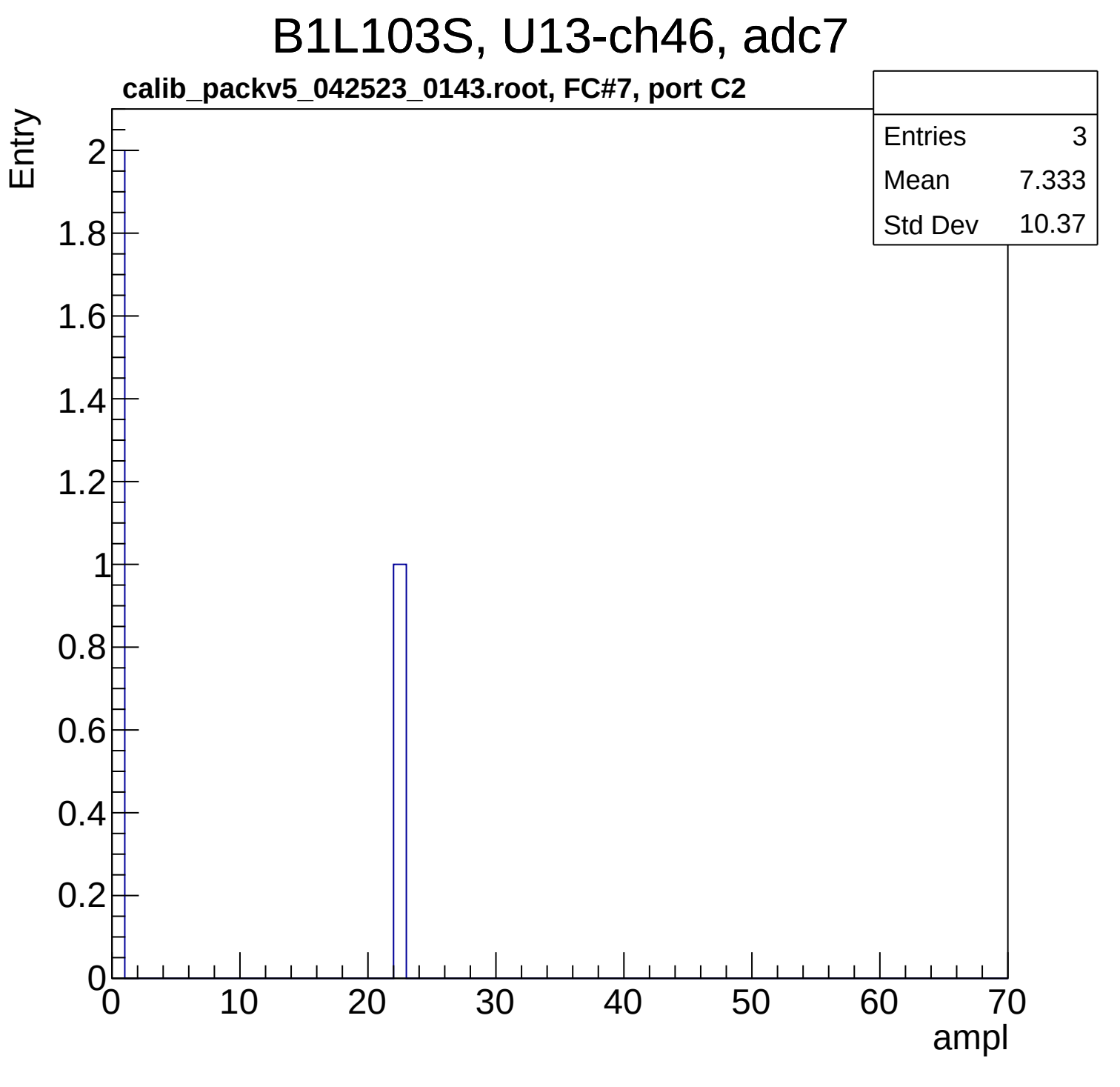

B1L103S, U13-ch46, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.81
Std Dev 3.62
Gaus mean : 43.8319
Gaus Width: 3.6046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch46, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 50.25
Std Dev 3.71
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch46, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 56.21
Std Dev 3.482
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch46, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 58.39
Std Dev 9.489
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch46, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.14
Std Dev 1.726
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch46, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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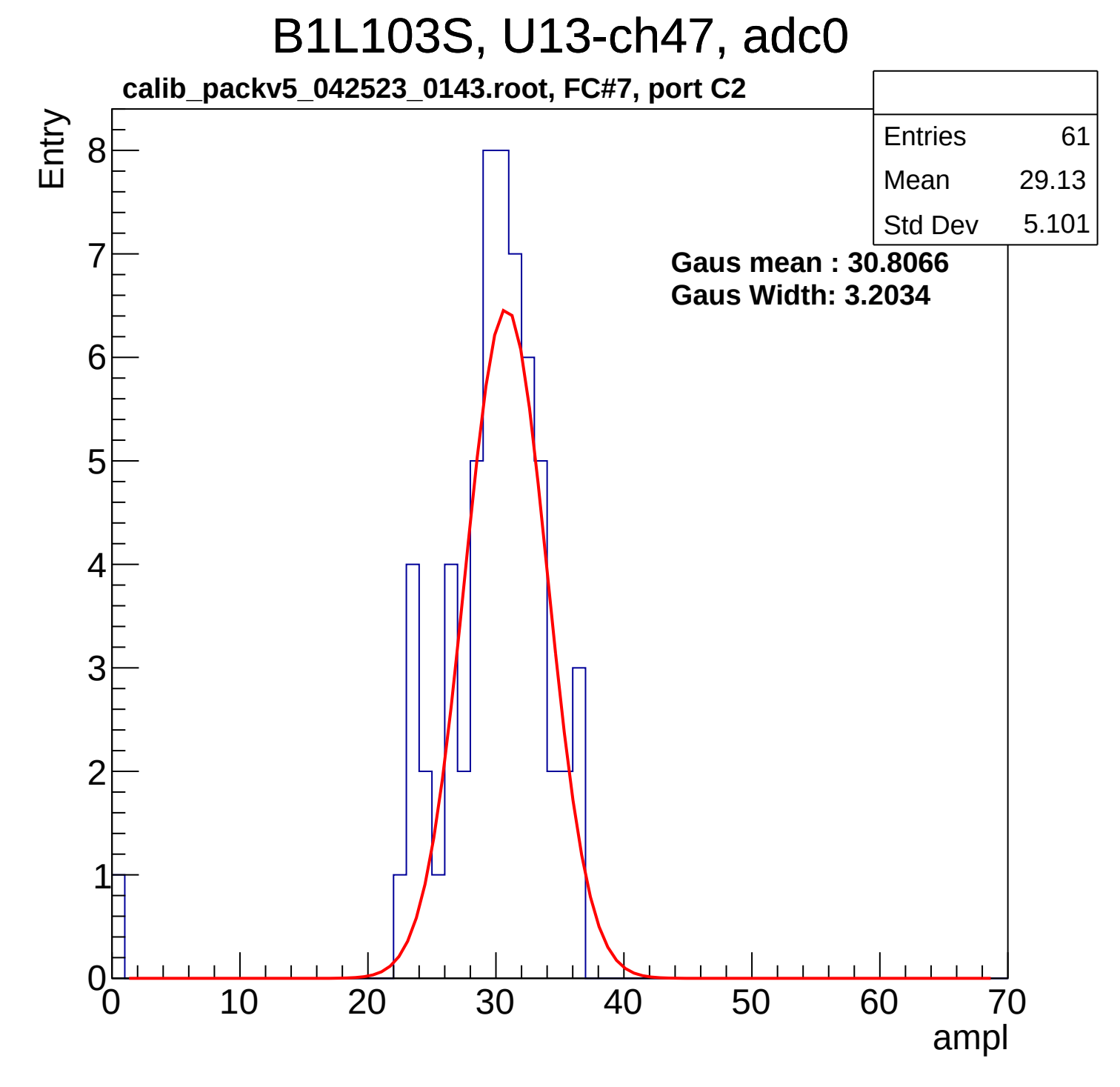
B1L103S, U13-ch47, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 29.13
Std Dev 5.101
Gaus mean : 30.8066
Gaus Width: 3.2034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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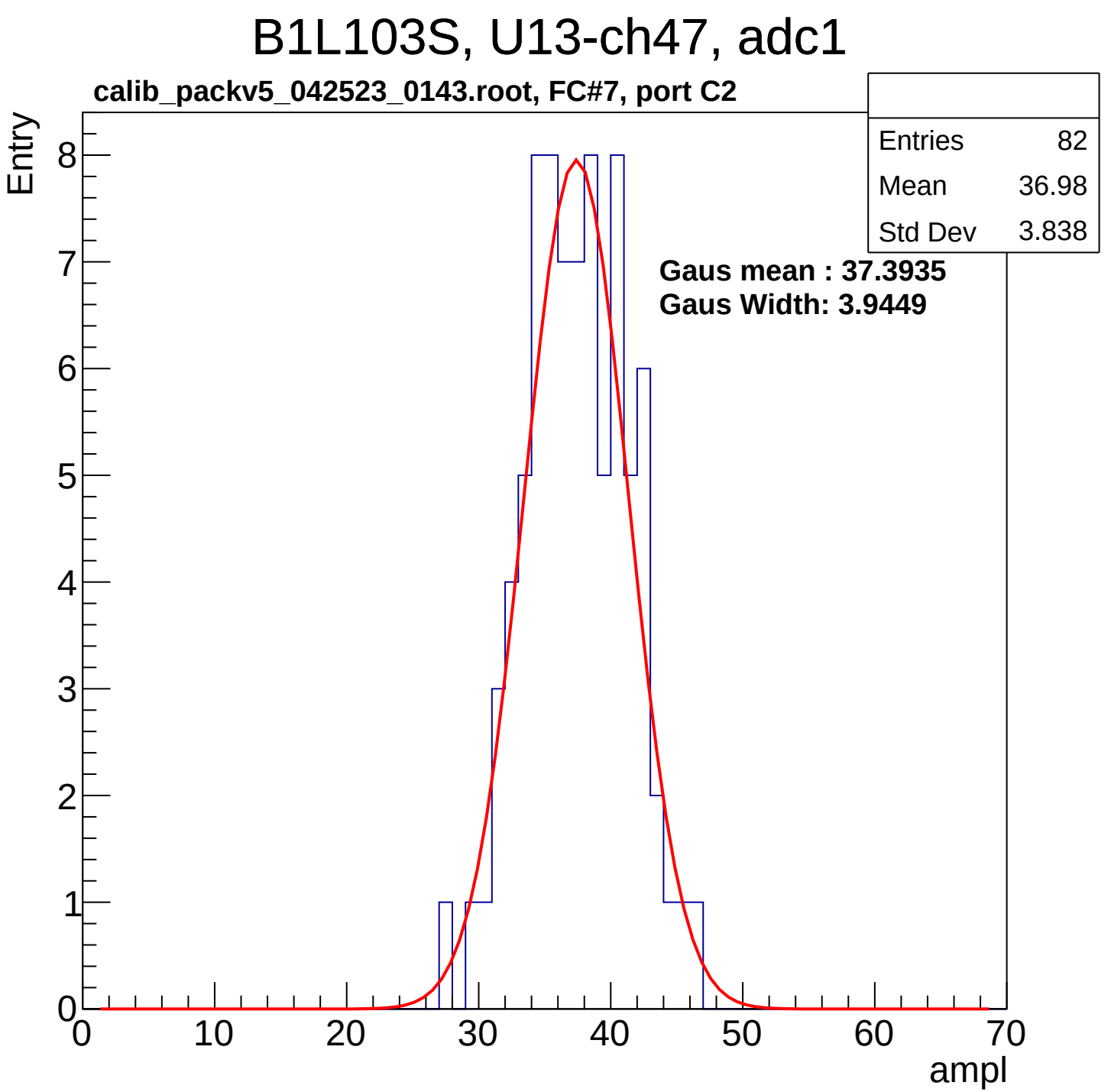
B1L103S, U13-ch47, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 36.98
Std Dev 3.838
Gaus mean : 37.3935
Gaus Width: 3.9449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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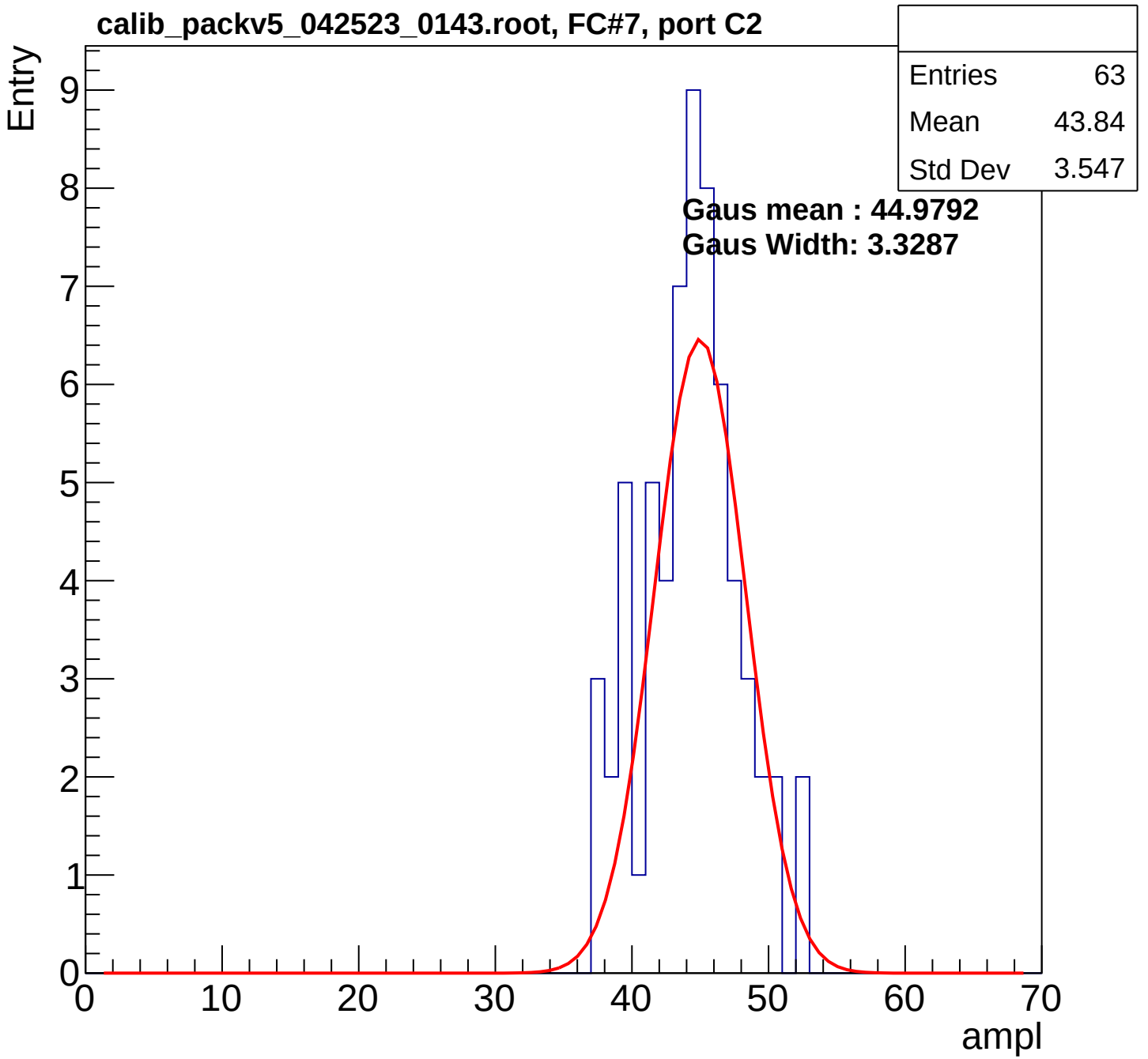
B1L103S, U13-ch47, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.84
Std Dev 3.547
Gaus mean : 44.9792
Gaus Width: 3.3287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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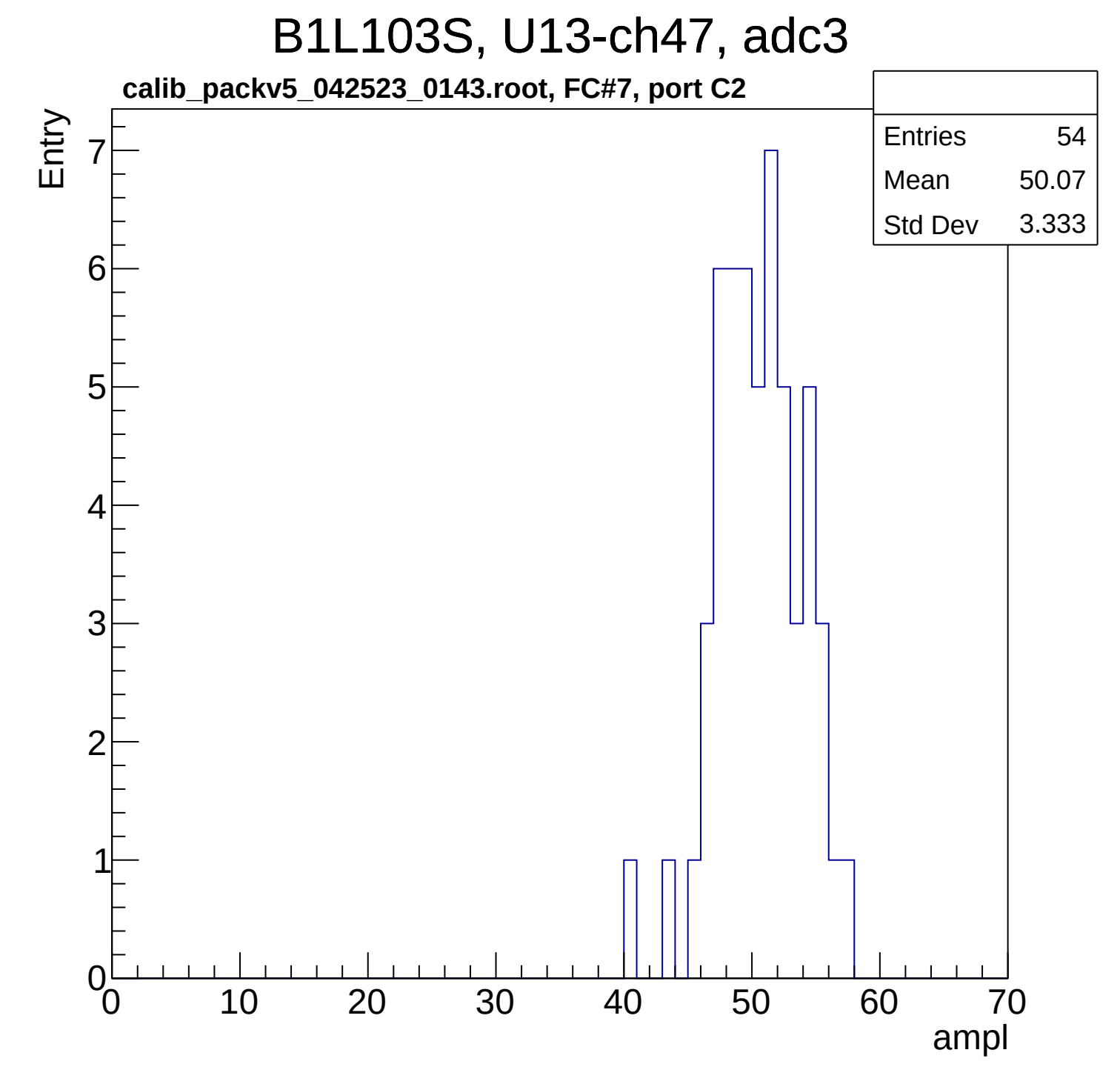
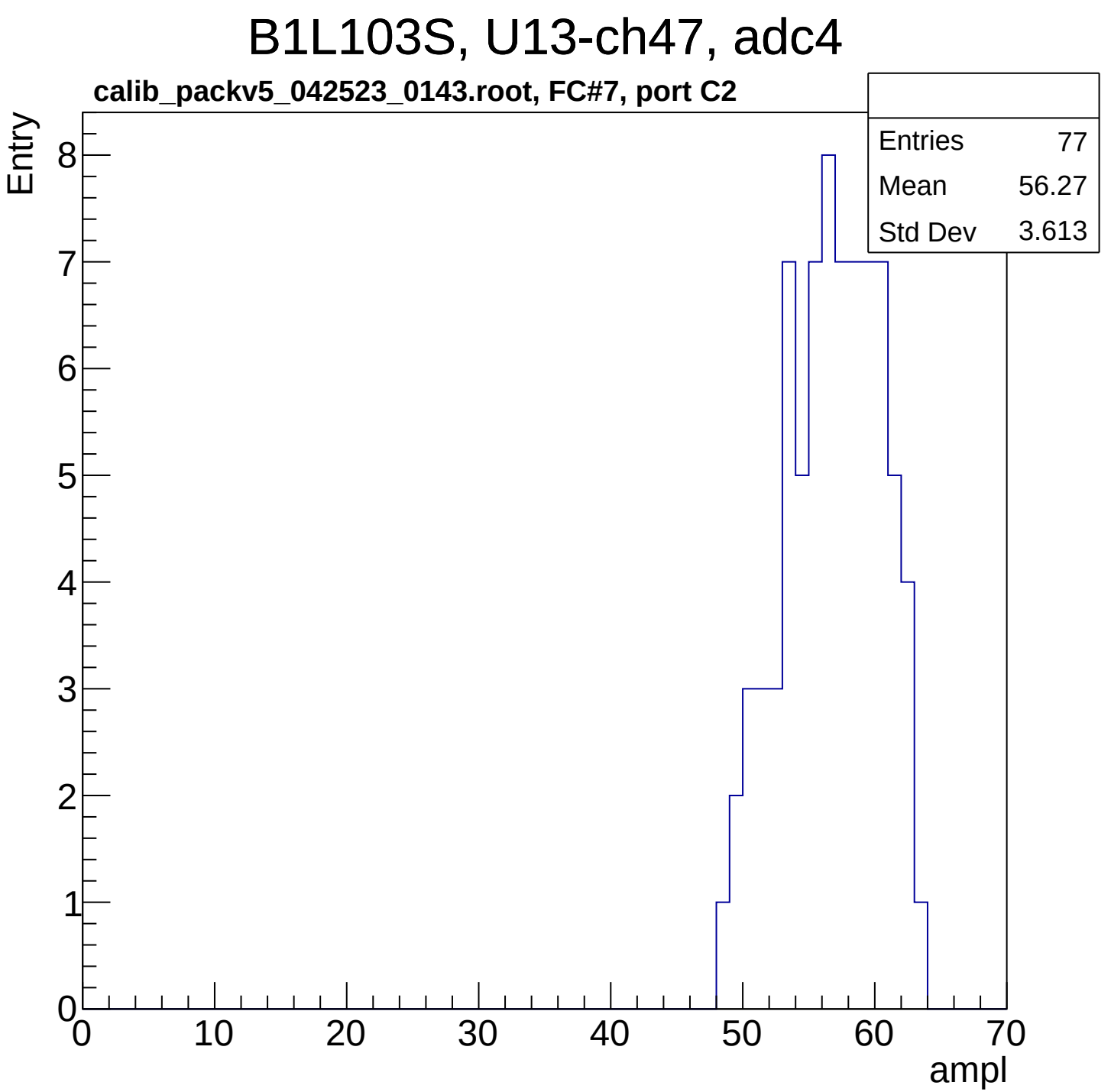
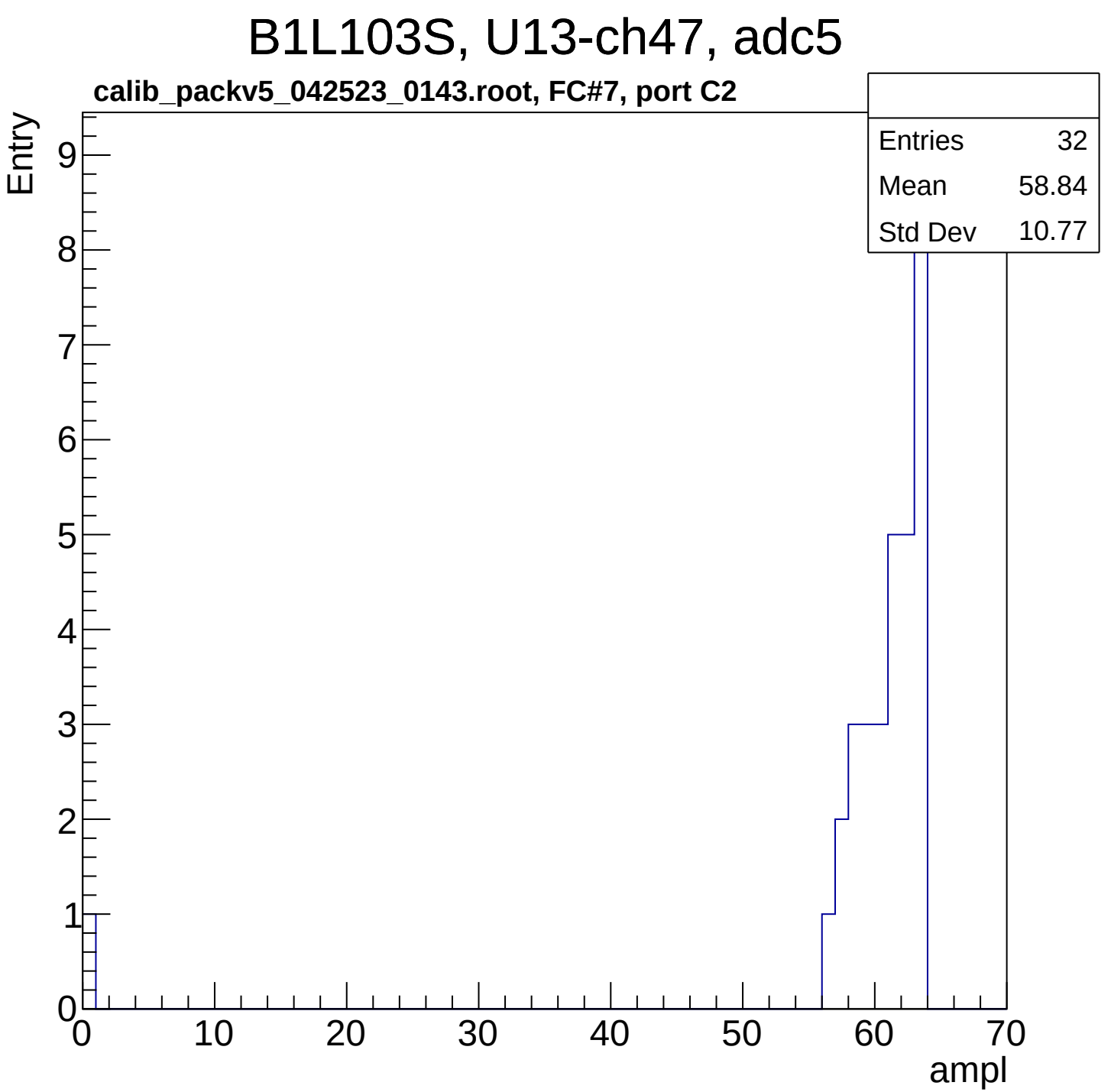
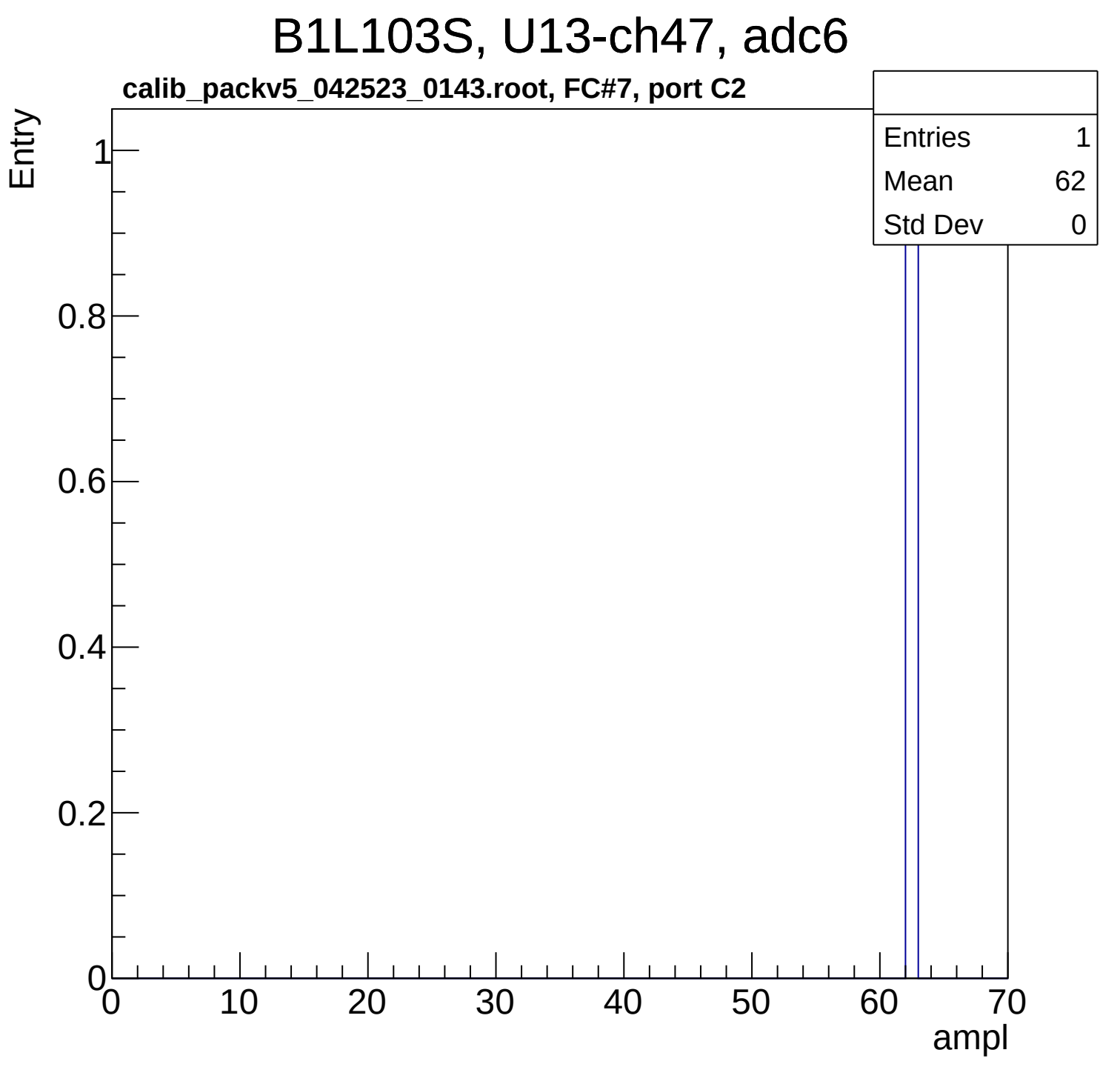
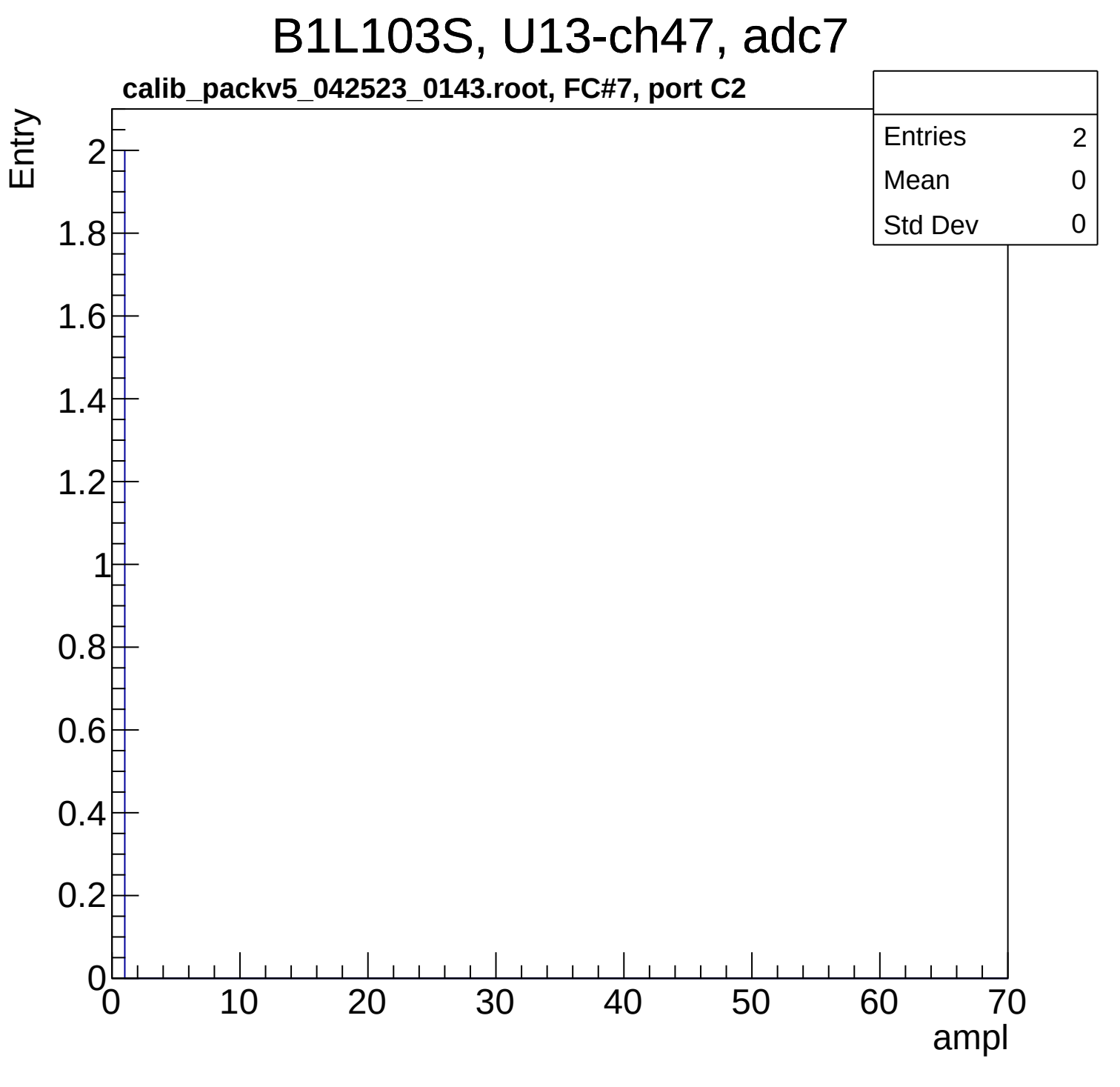
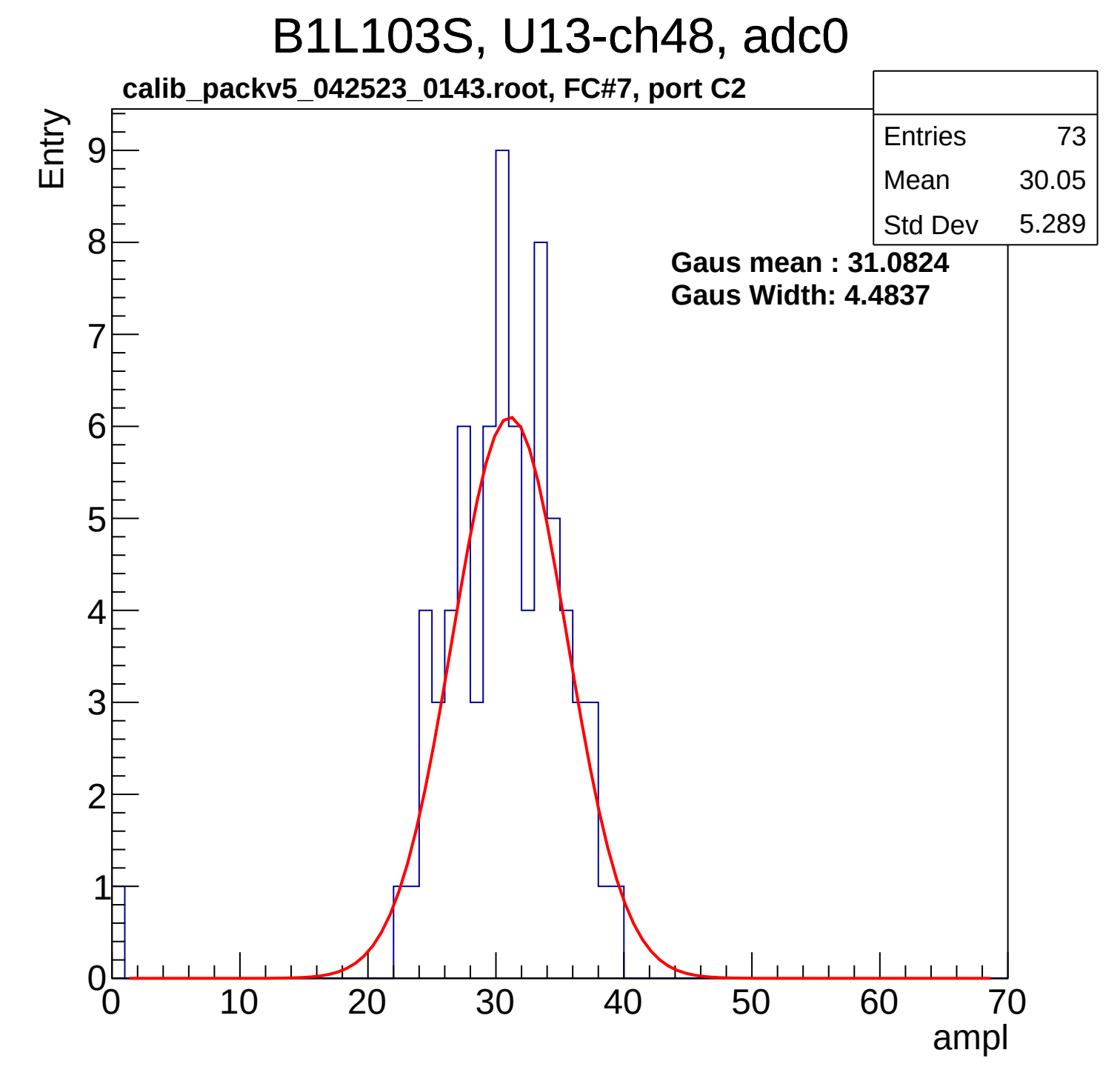

B1L103S, U13-ch47, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 50.07
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

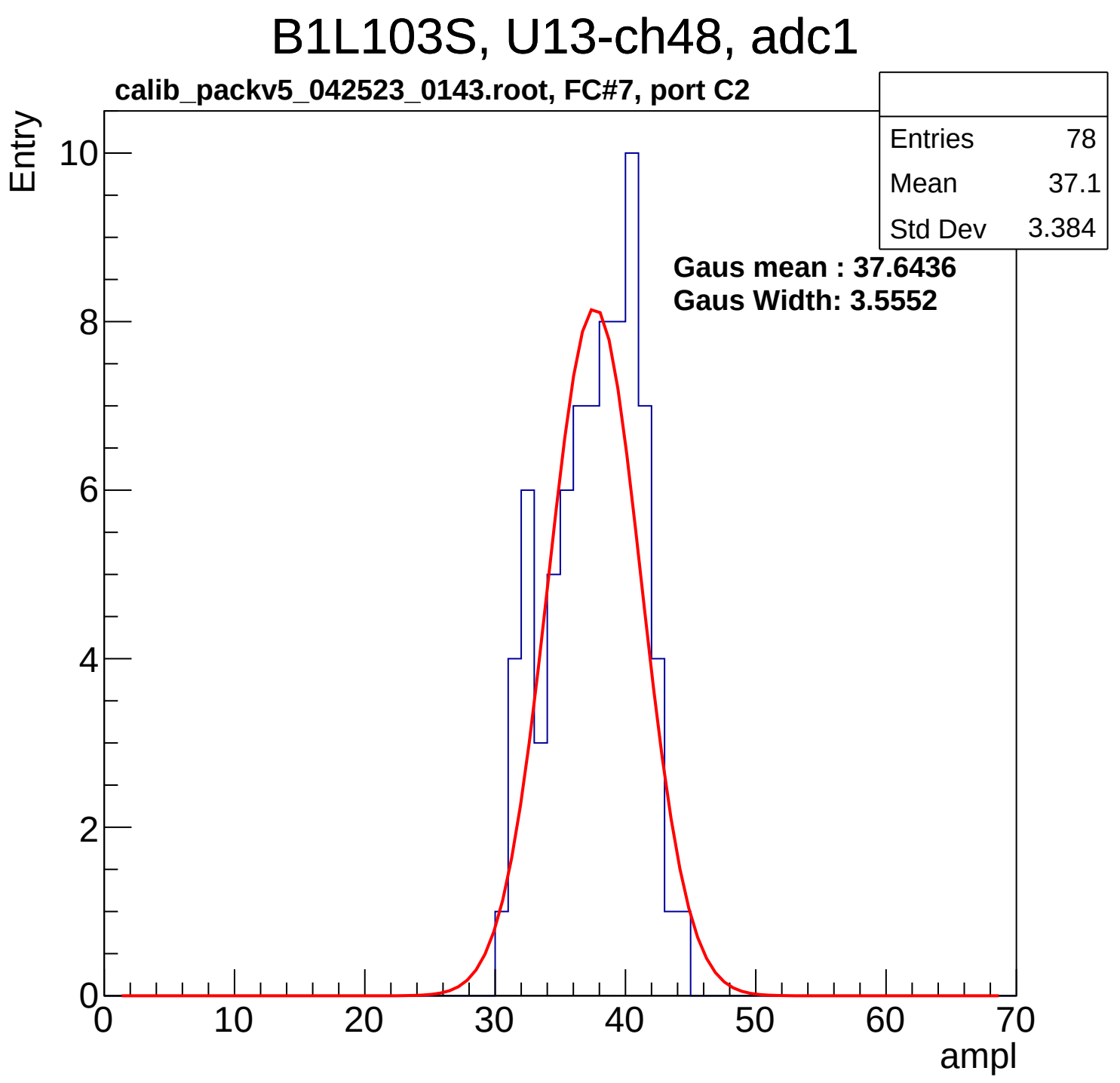

B1L103S, U13-ch47, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 56.27
Std Dev 3.613
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

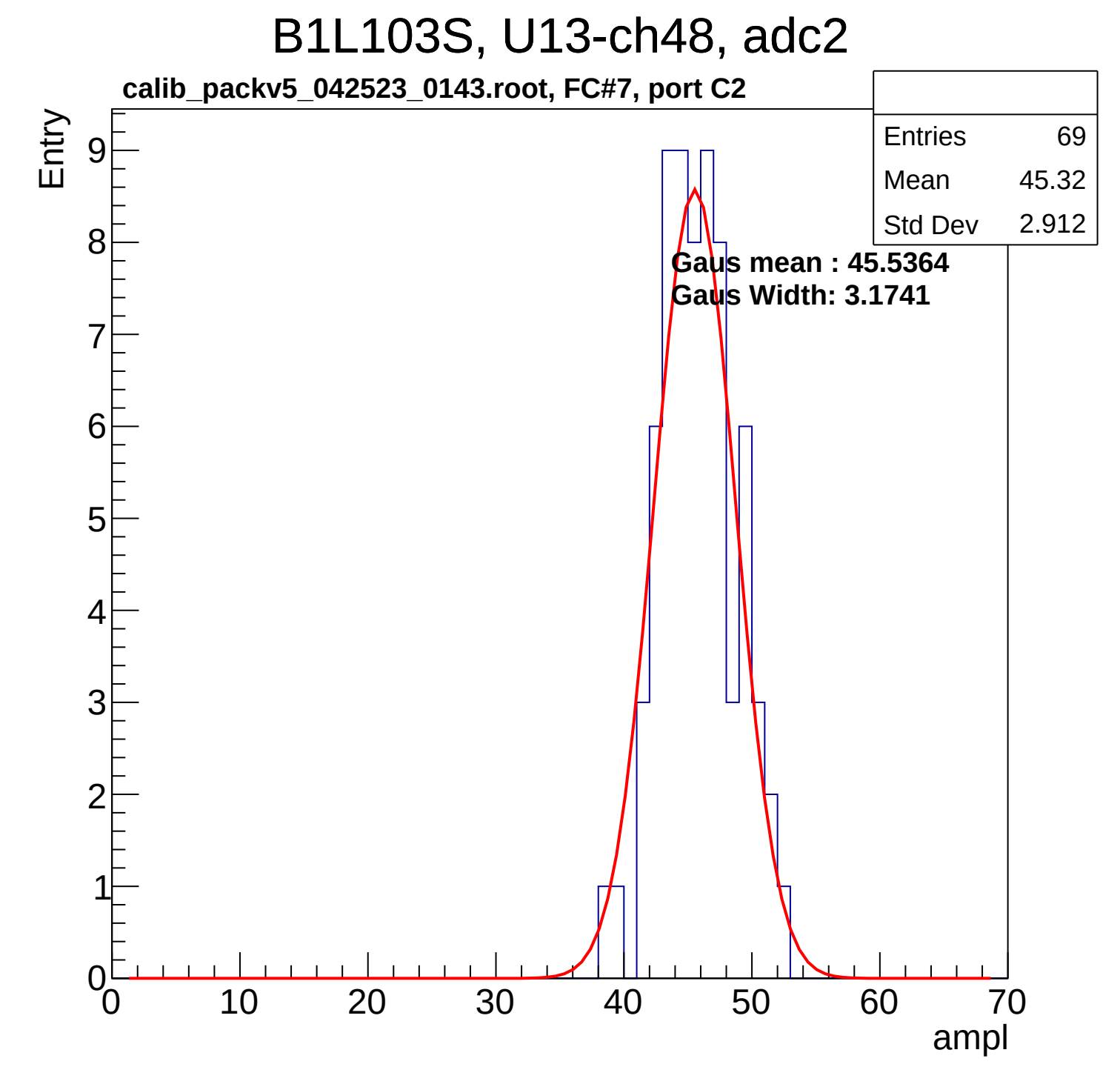

B1L103S, U13-ch47, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.84
Std Dev 10.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch47, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch47, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch48, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 30.05
Std Dev 5.289
Gaus mean : 31.0824
Gaus Width: 4.4837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch48, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 37.1
Std Dev 3.384
Gaus mean : 37.6436
Gaus Width: 3.5552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch48, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 45.32
Std Dev 2.912
Gaus mean : 45.5364
Gaus Width: 3.1741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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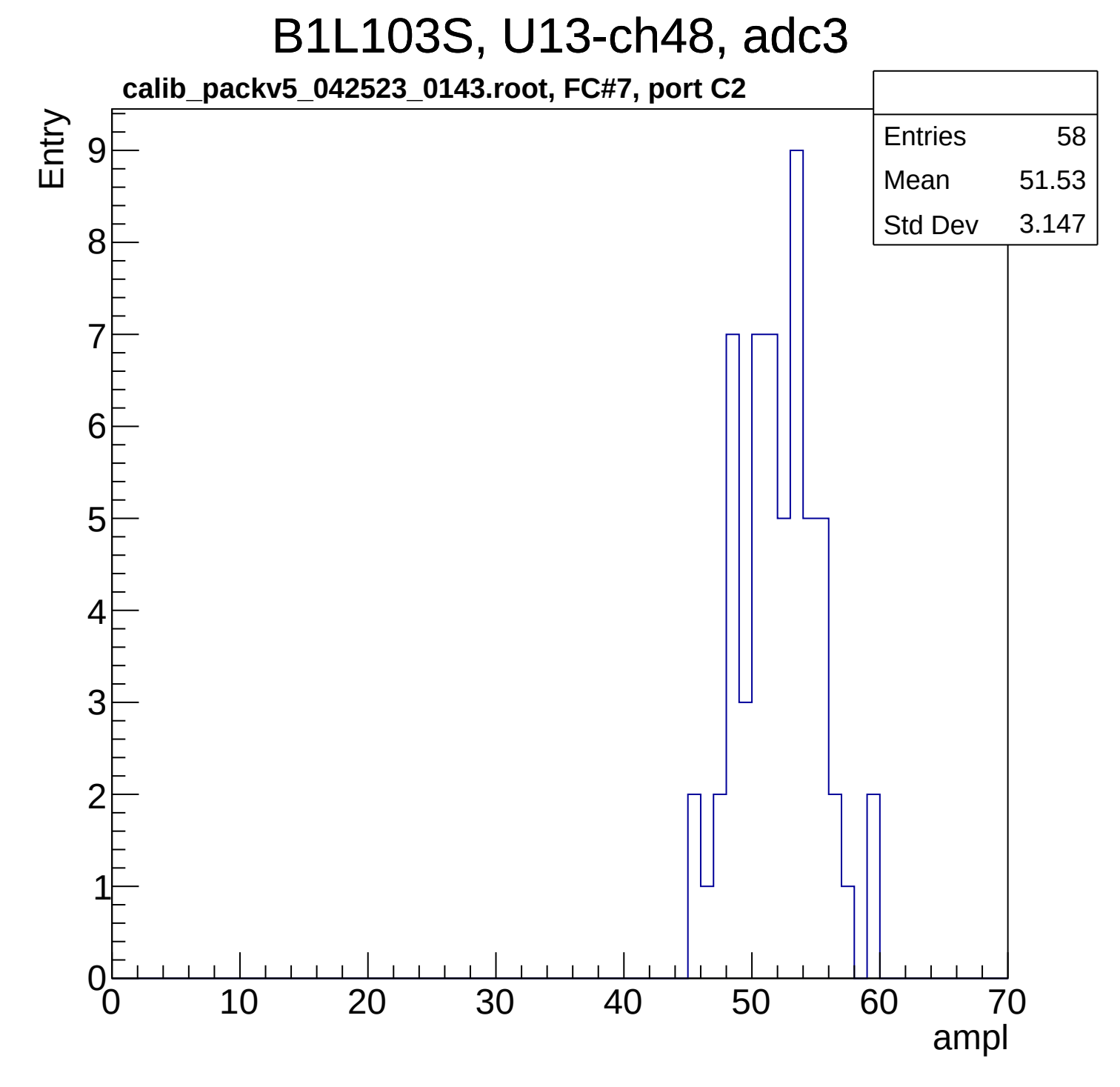
B1L103S, U13-ch48, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 51.53
Std Dev 3.147
Entry
ampl
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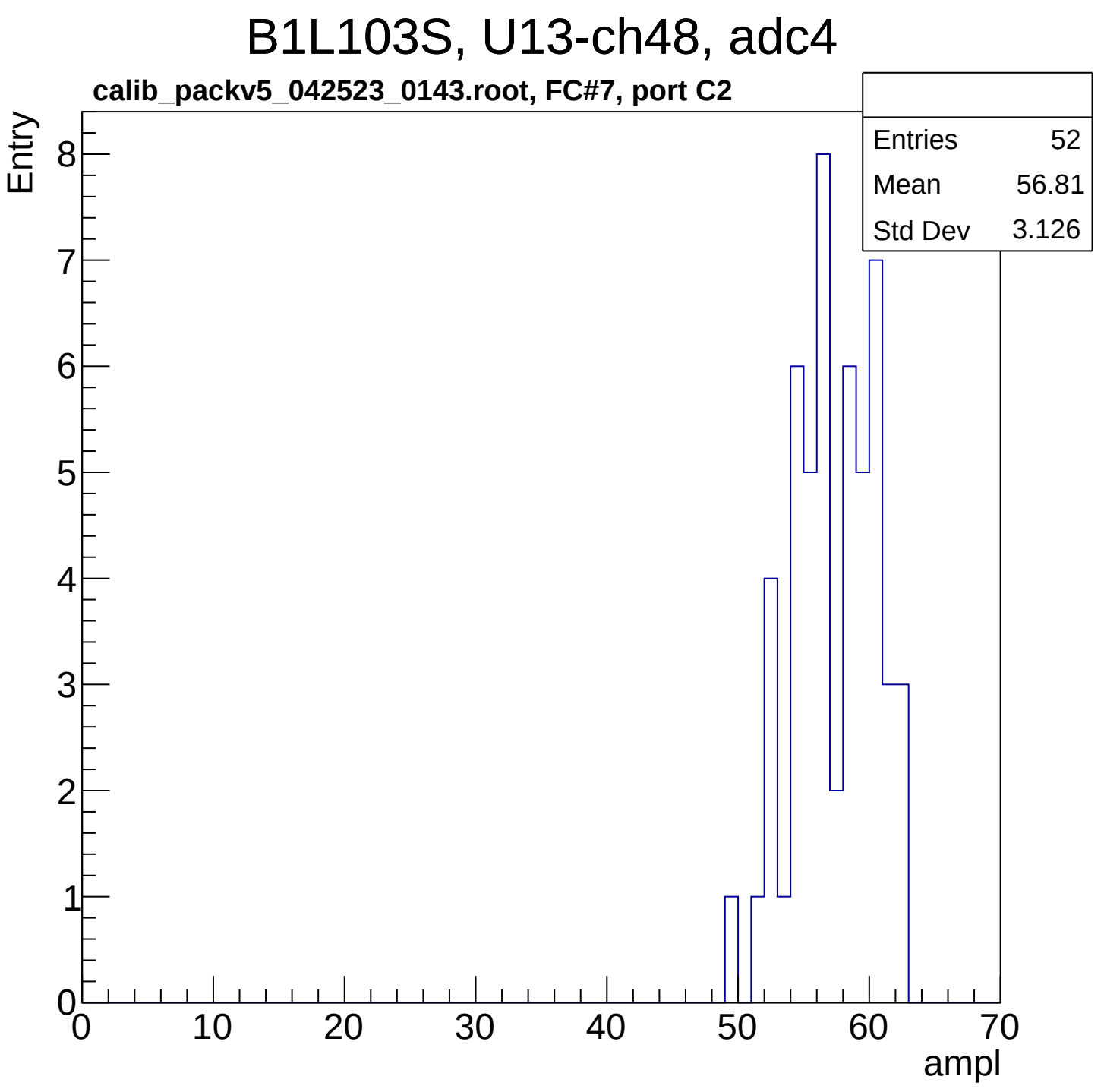
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch48, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 56.81
Std Dev 3.126
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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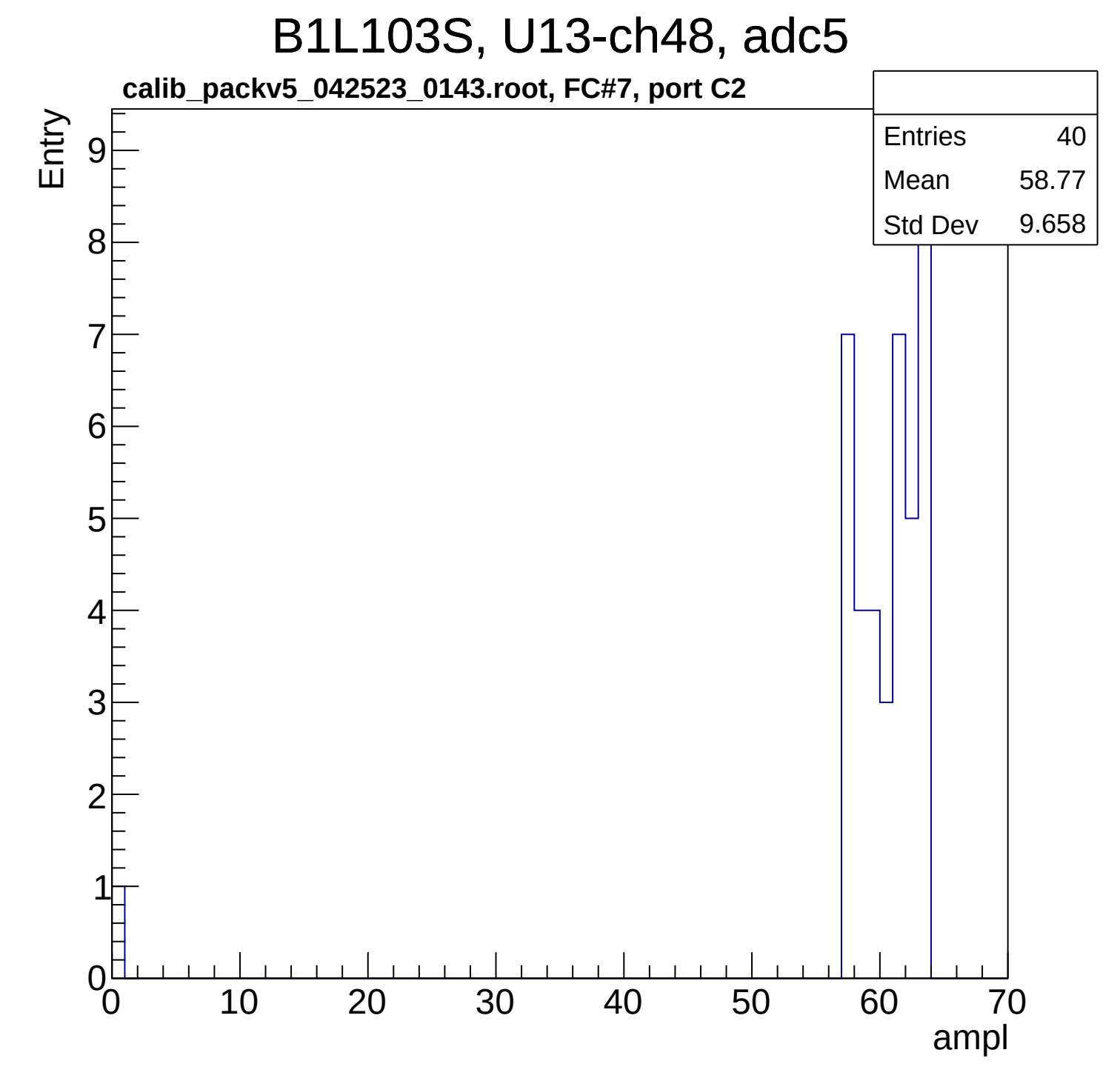
B1L103S, U13-ch48, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.77
Std Dev 9.658
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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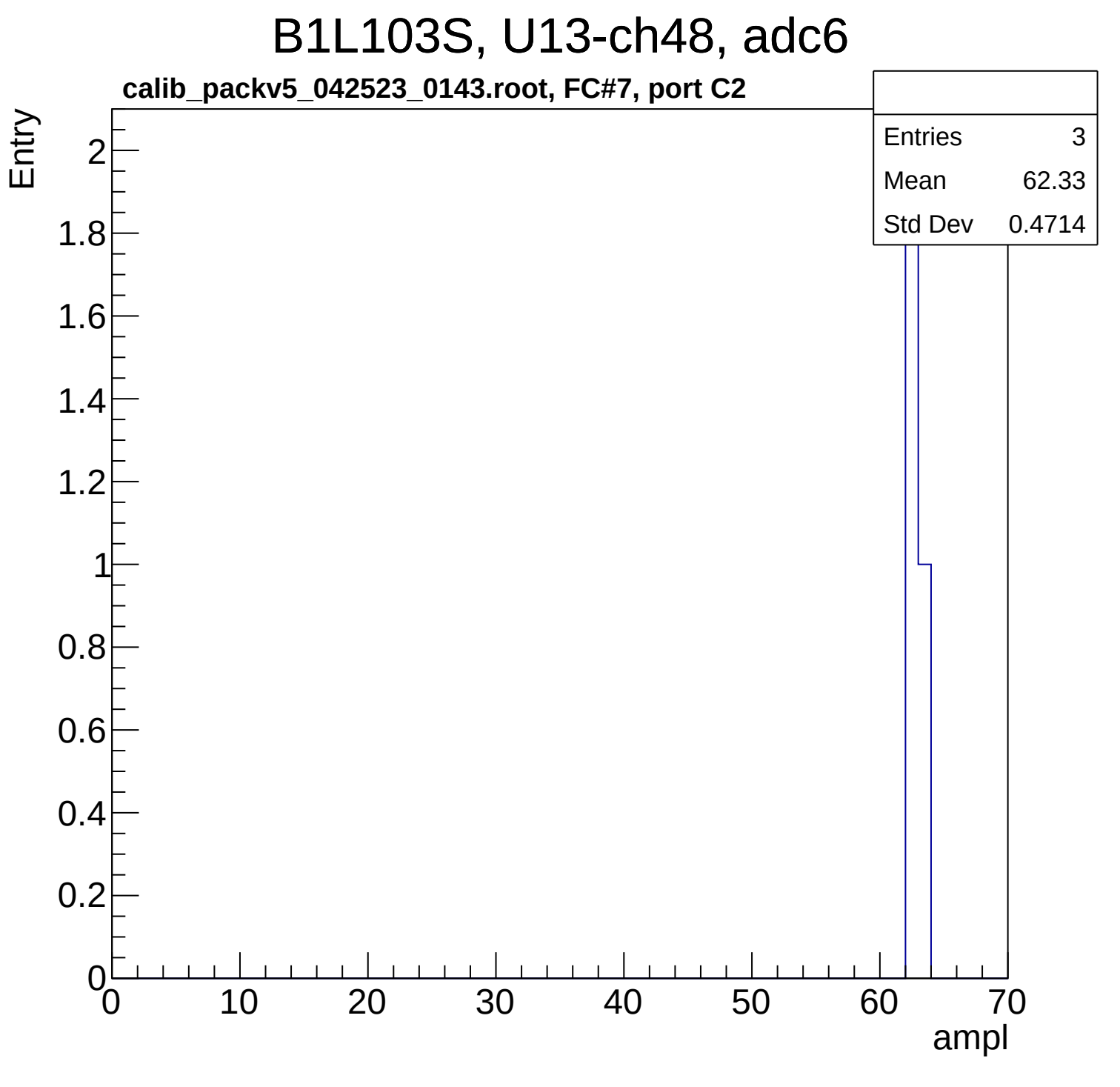
B1L103S, U13-ch48, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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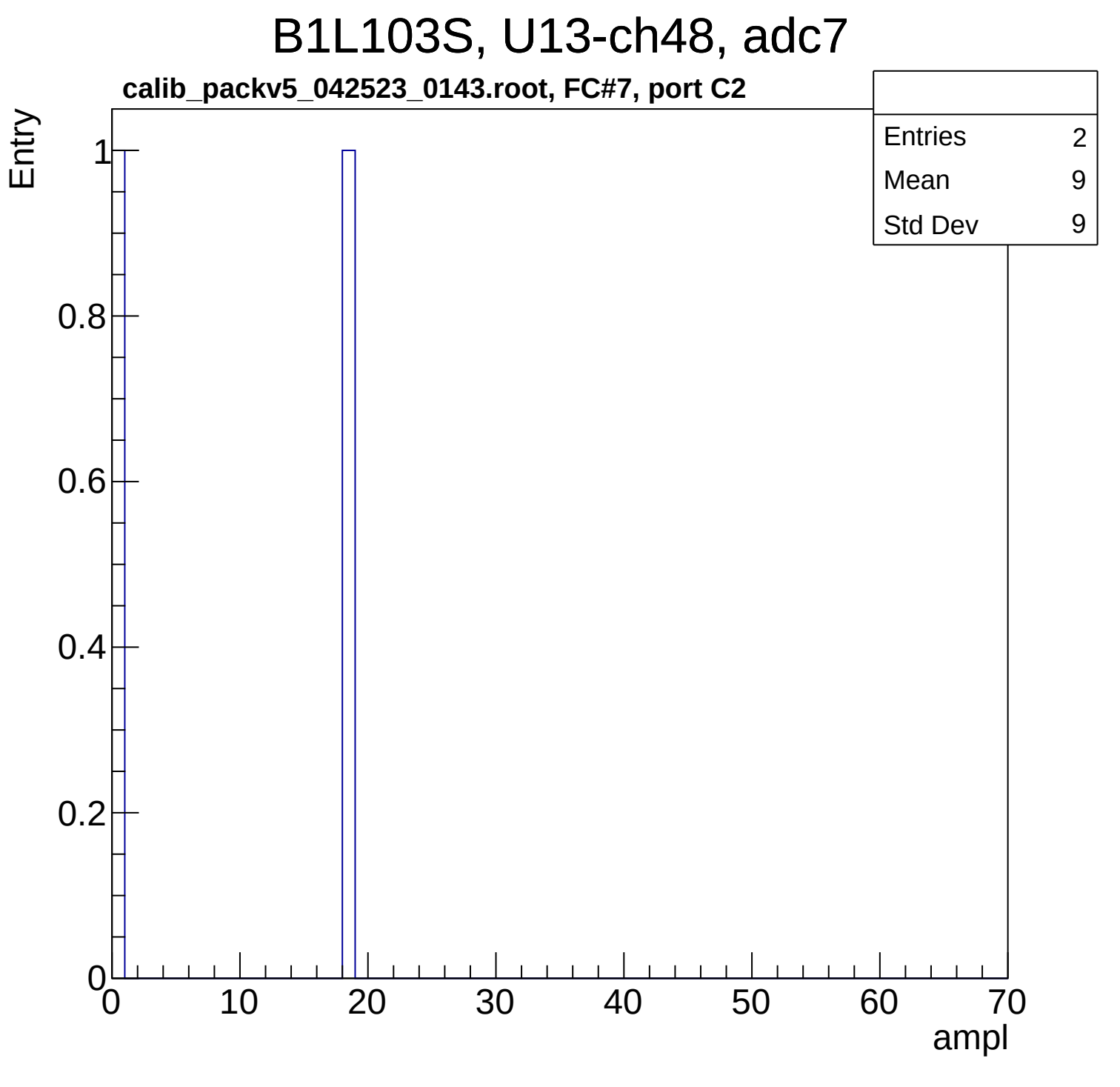
B1L103S, U13-ch48, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 9
Std Dev 9
Entry
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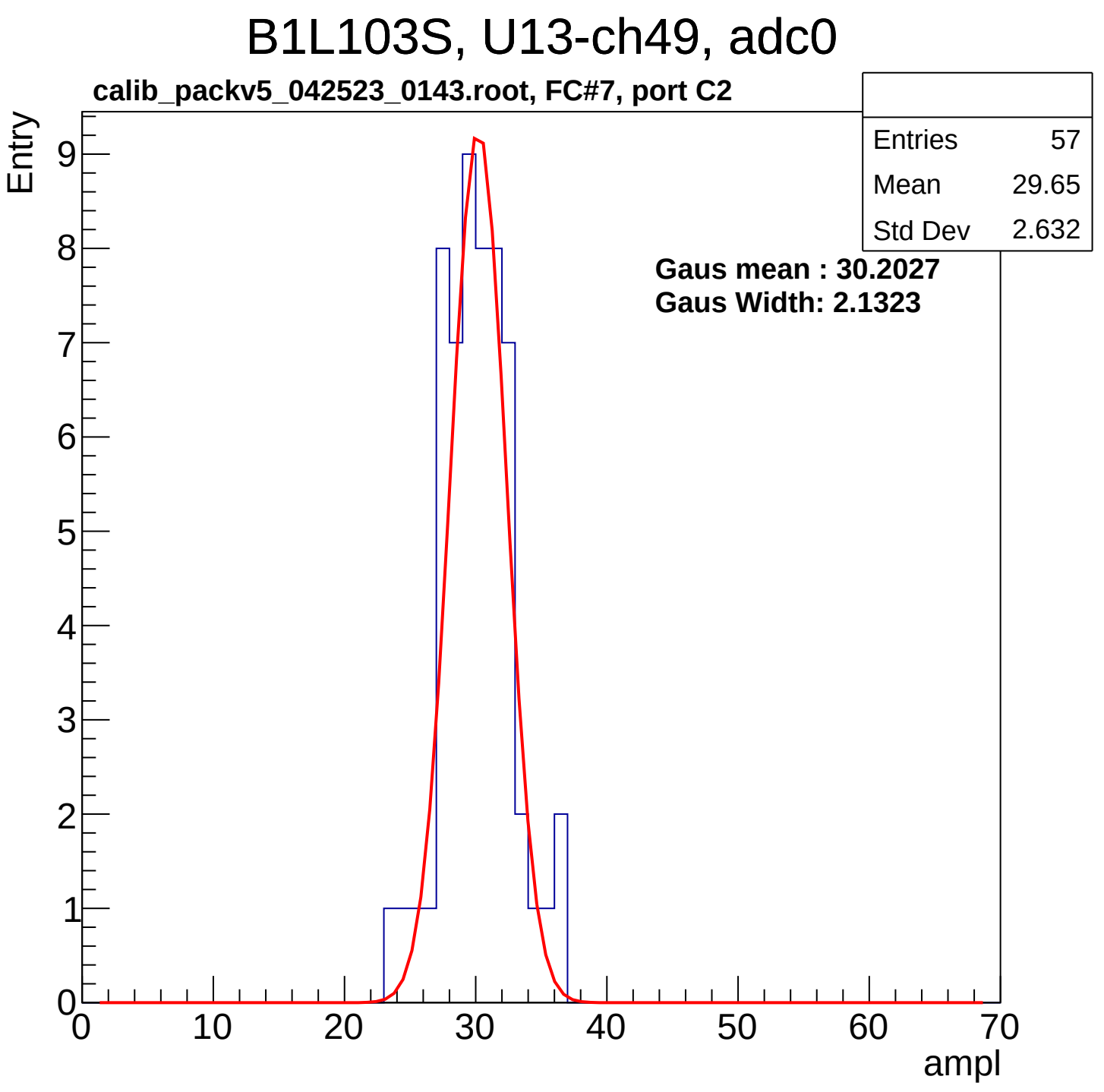
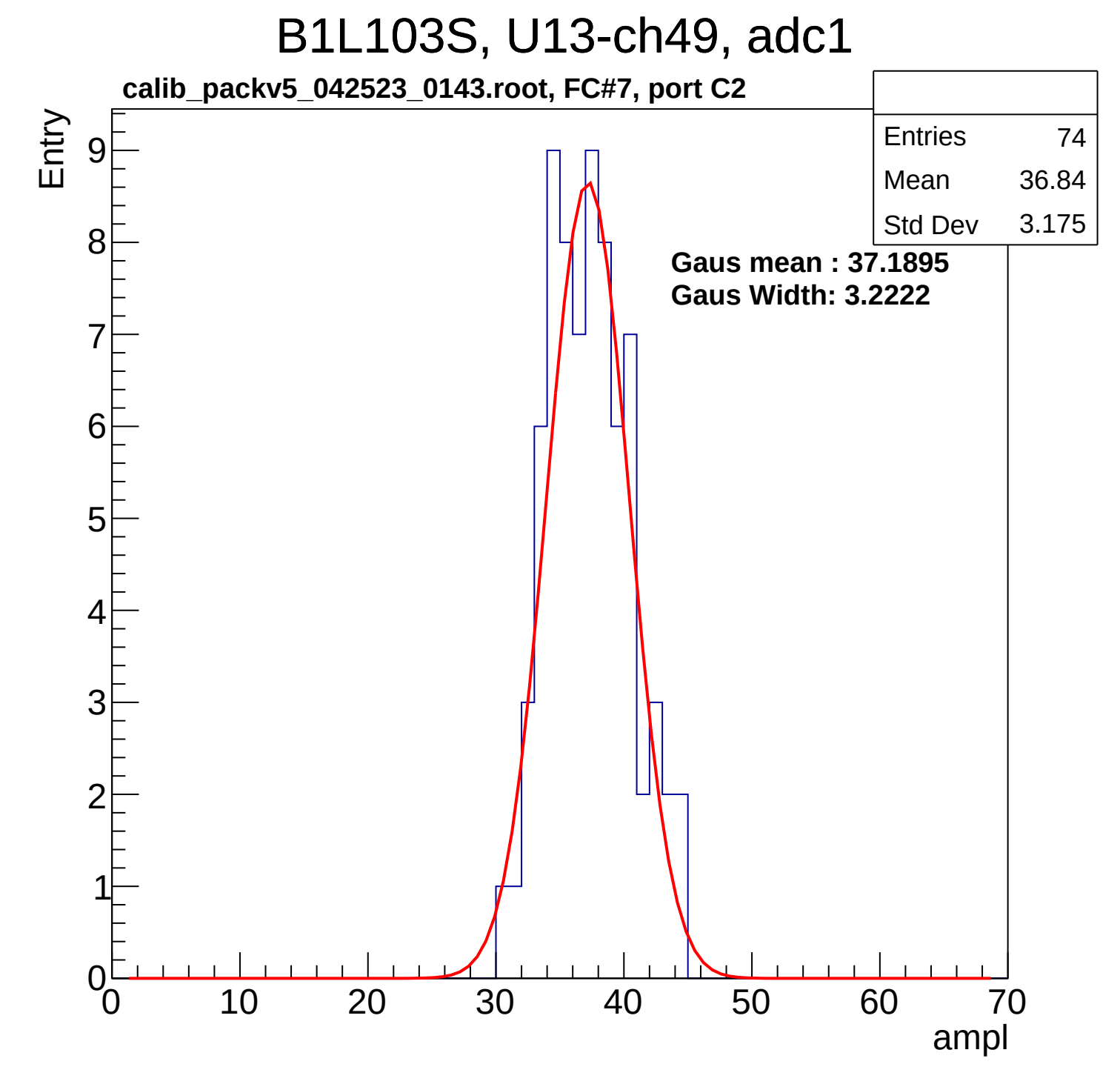
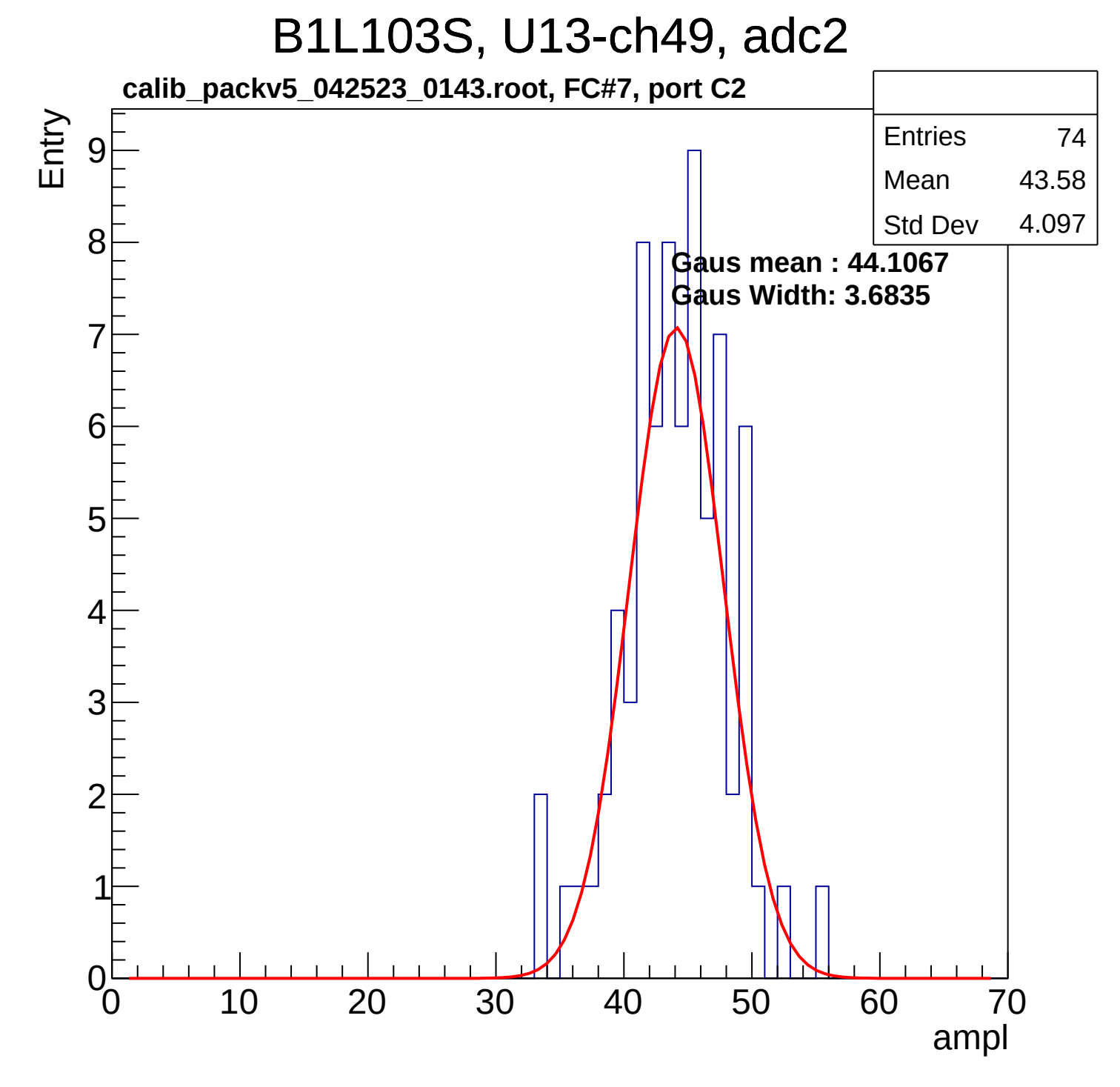
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch49, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 29.65
Std Dev 2.632
Gaus mean : 30.2027
Gaus Width: 2.1323
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch49, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 36.84
Std Dev 3.175
Gaus mean : 37.1895
Gaus Width: 3.2222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch49, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 43.58
Std Dev 4.097
Gaus mean : 44.1067
Gaus Width: 3.6835
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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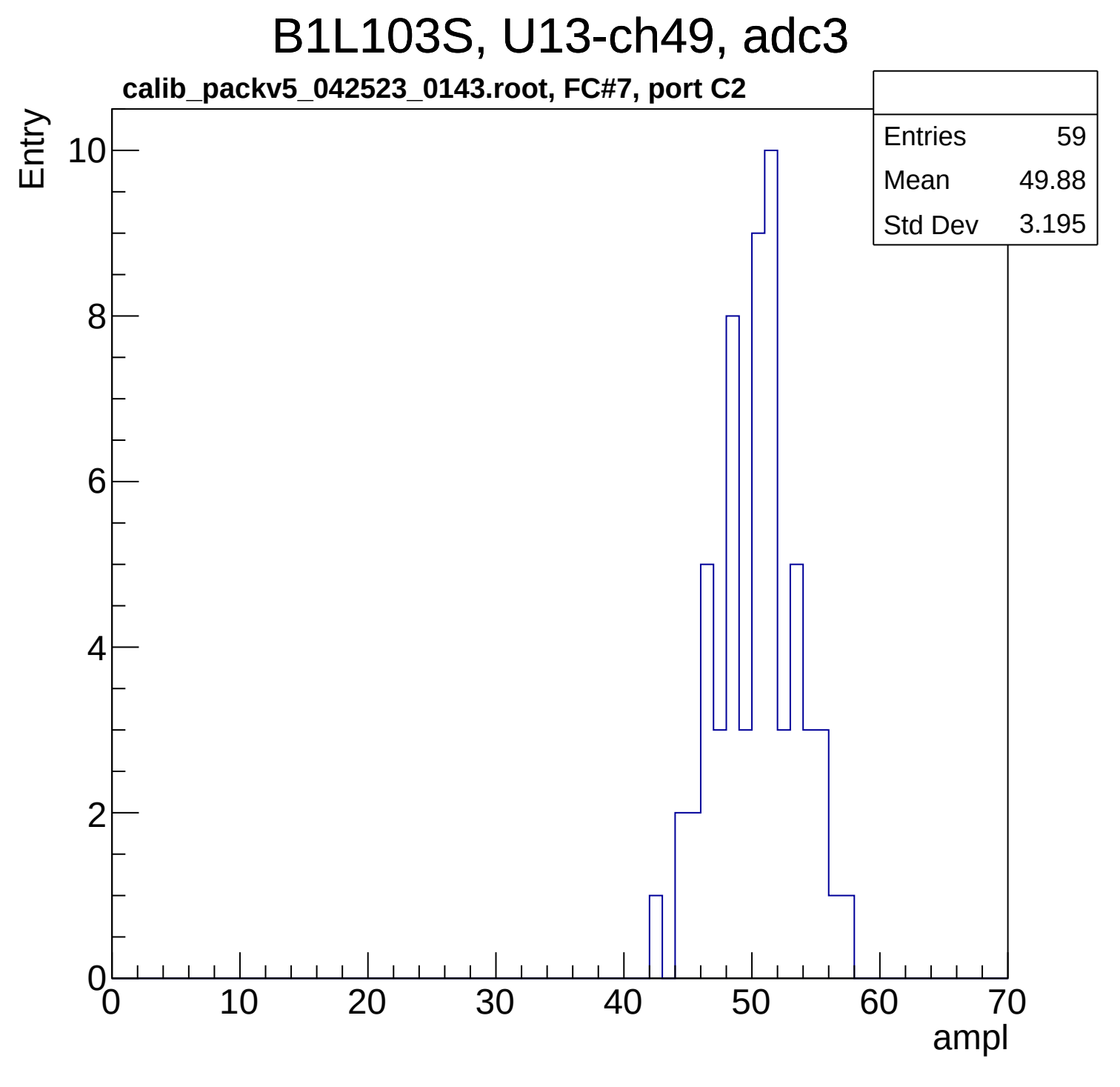
B1L103S, U13-ch49, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 49.88
Std Dev 3.195
Entry
ampl
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6
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10
0
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20
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40
50
60
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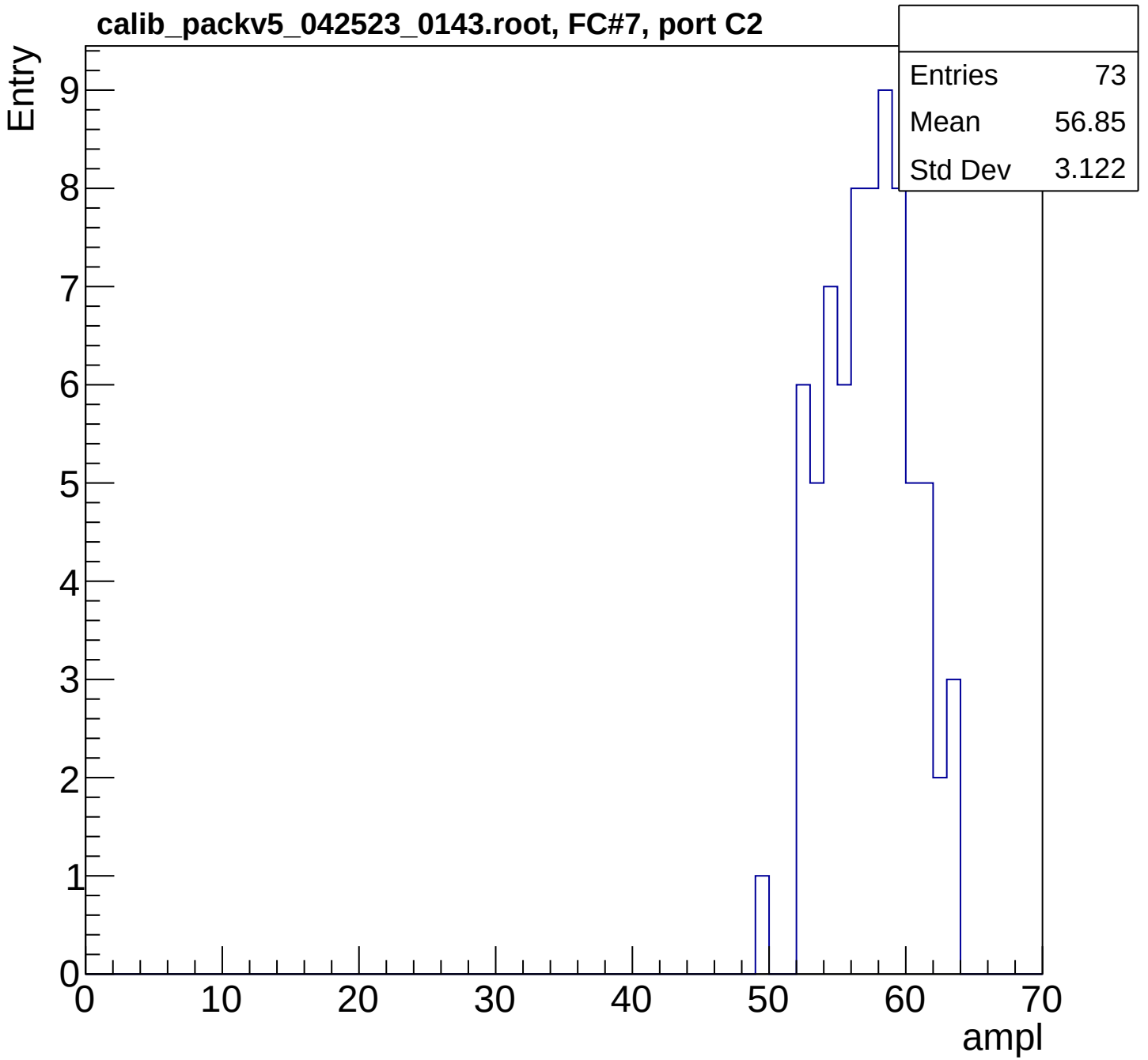
B1L103S, U13-ch49, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 56.85
Std Dev 3.122
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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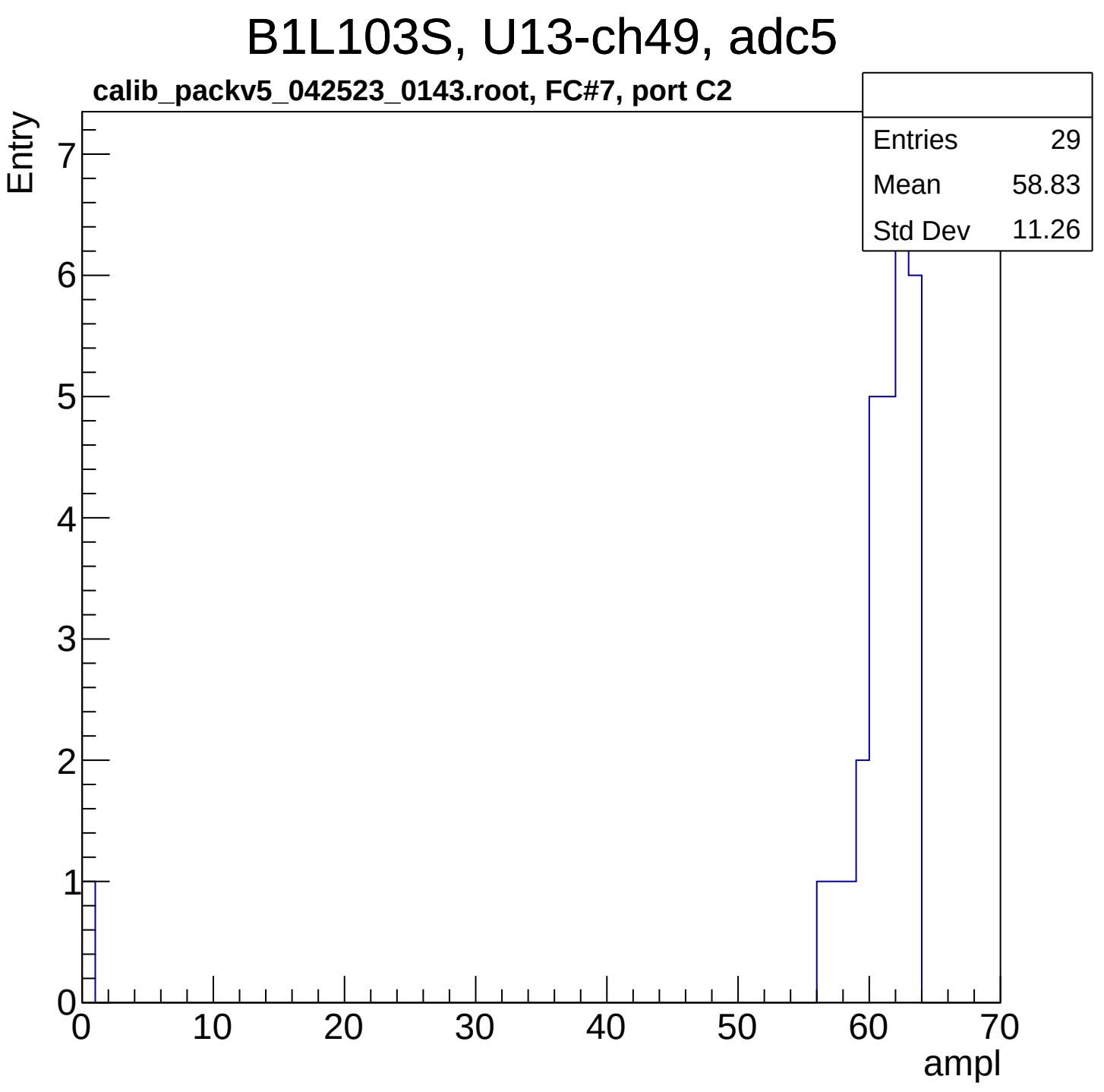
B1L103S, U13-ch49, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 58.83
Std Dev 11.26
Entry
ampl
0
1
2
3
4
5
6
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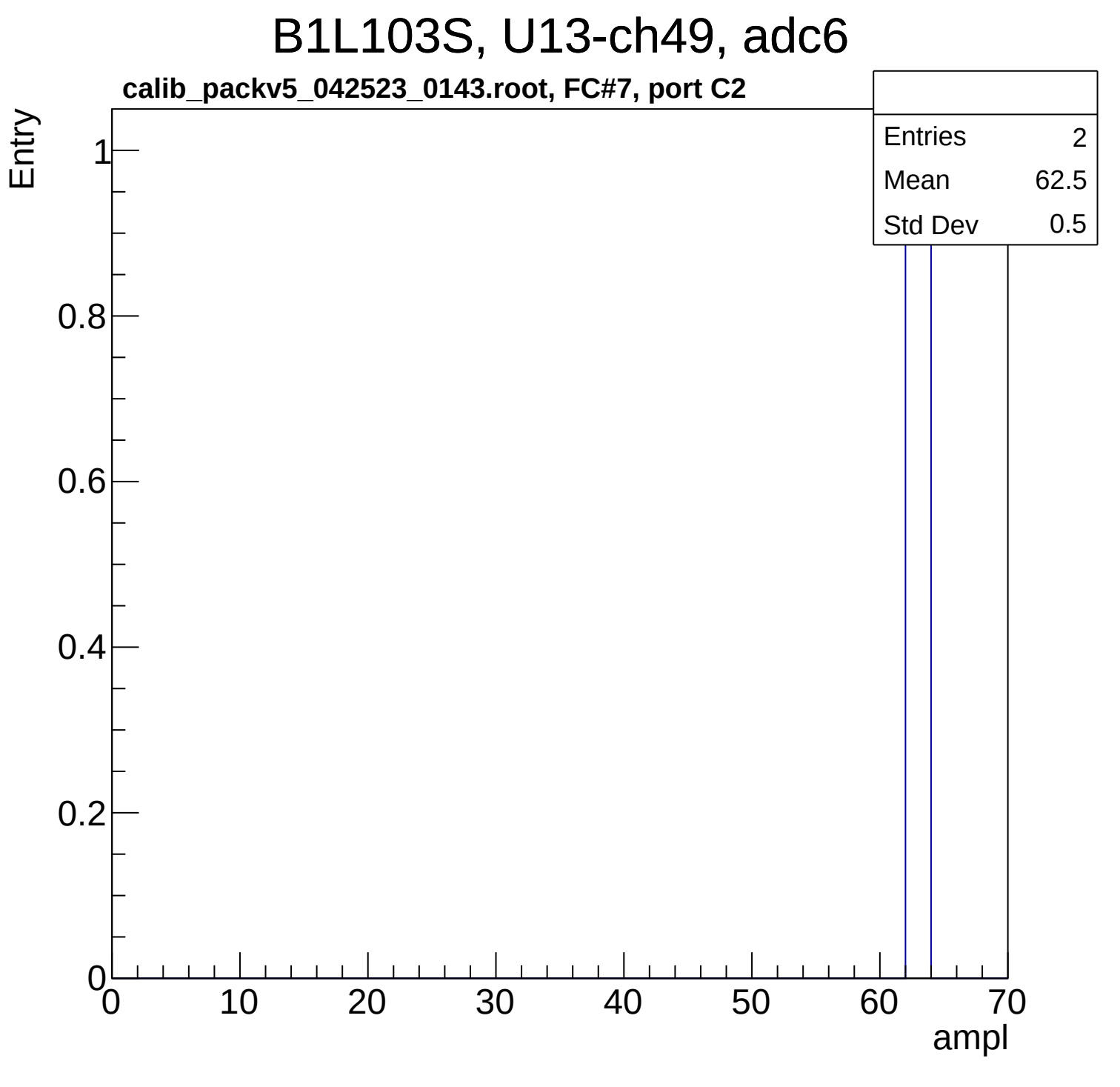
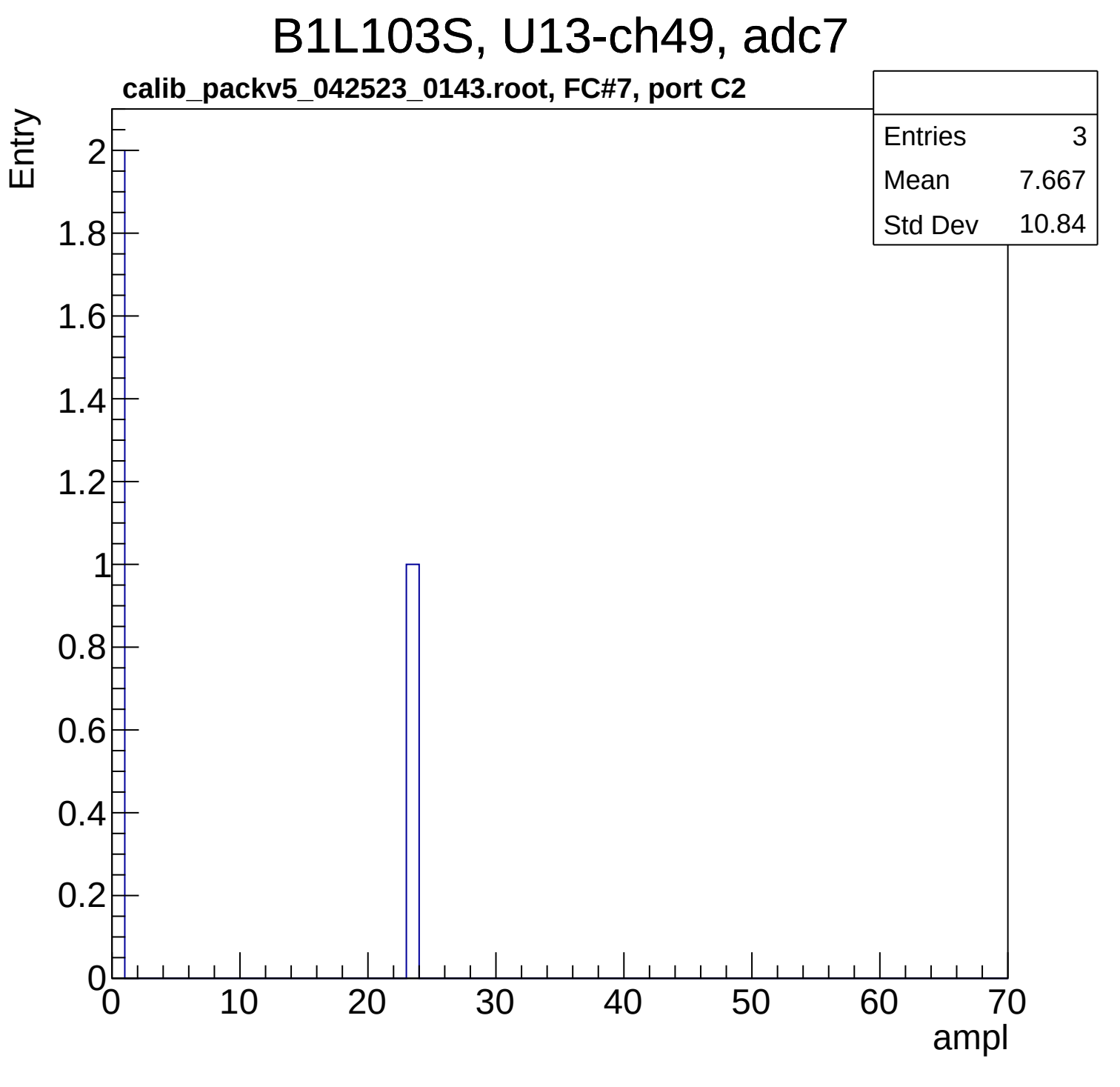
0
10
20
30
40
50
60
70


B1L103S, U13-ch49, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch49, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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10
20
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50
60
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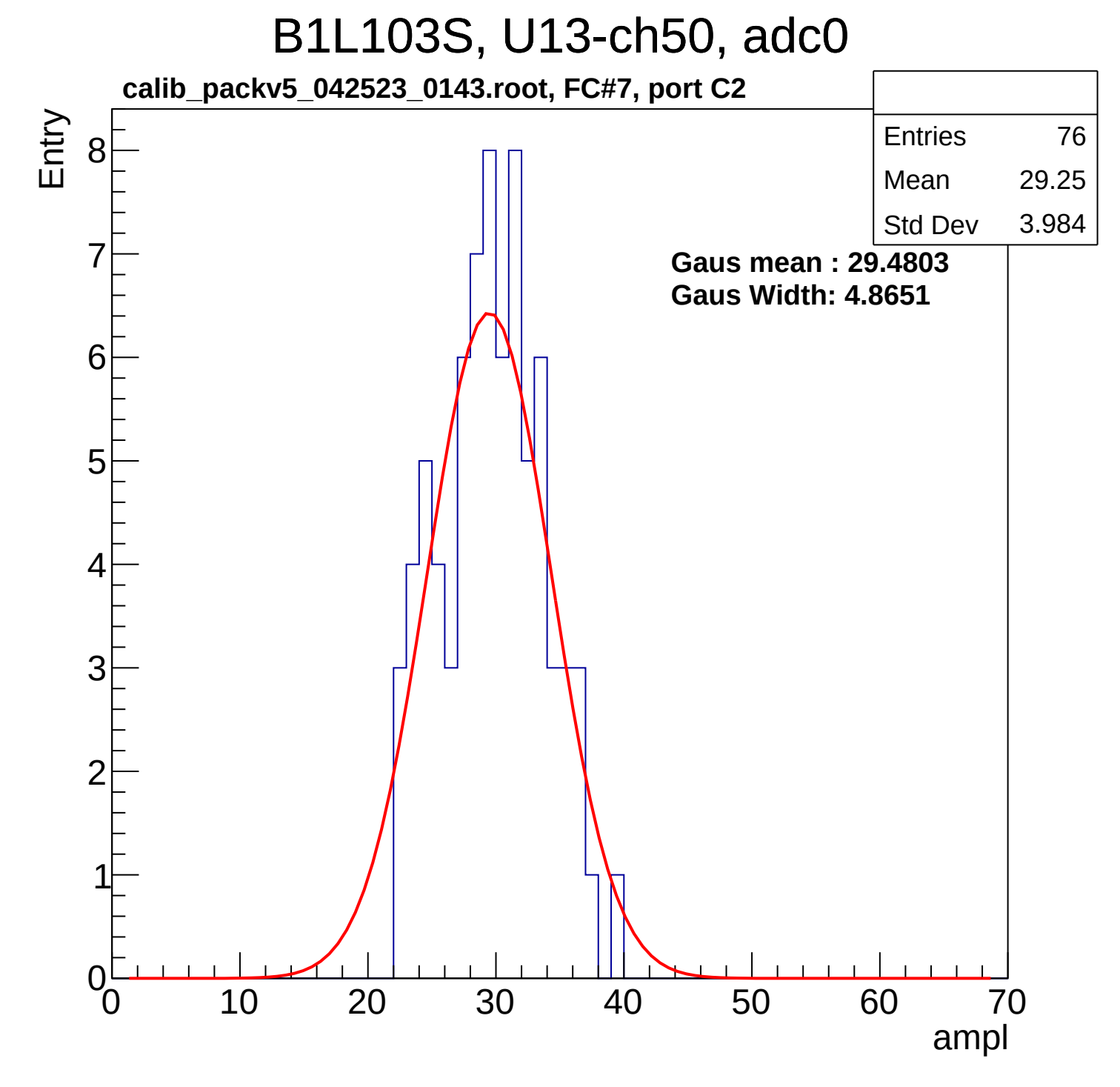
B1L103S, U13-ch50, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.25
Std Dev 3.984
Gaus mean : 29.4803
Gaus Width: 4.8651
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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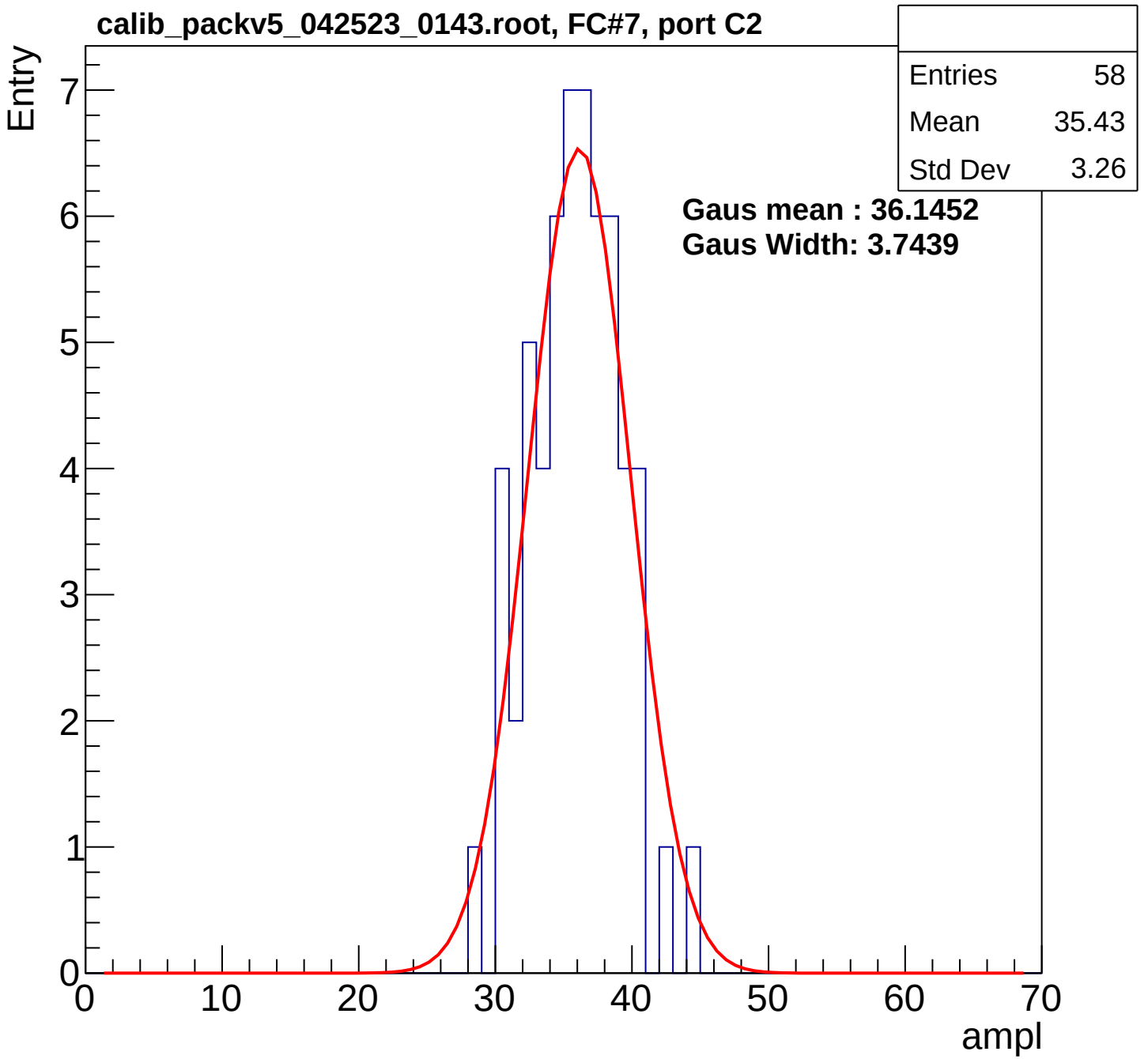
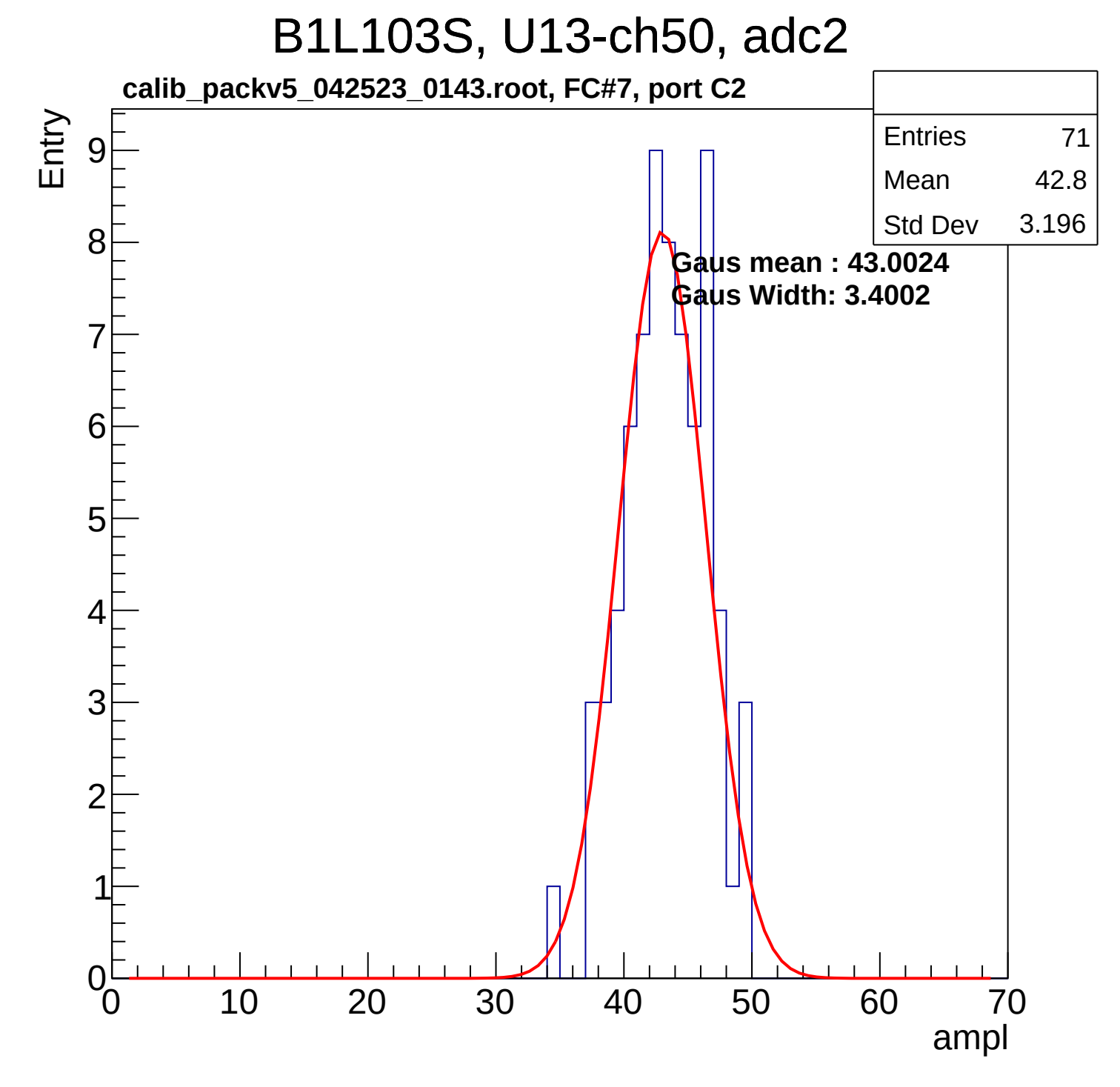
40
50
60
70


B1L103S, U13-ch50, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 35.43
Std Dev 3.26
Gaus mean : 36.1452
Gaus Width: 3.7439
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch50, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.8
Std Dev 3.196
Gaus mean : 43.0024
Gaus Width: 3.4002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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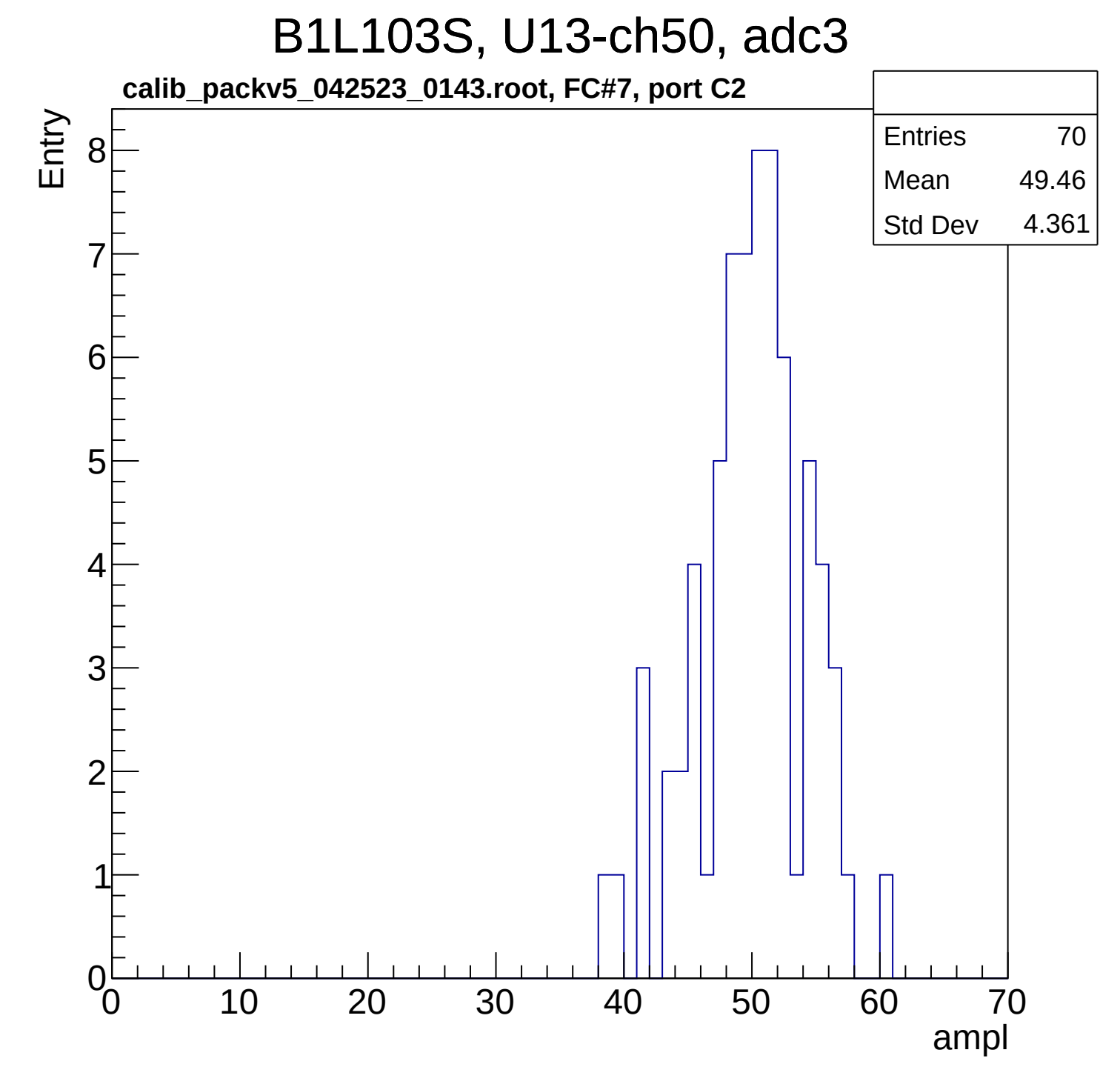
B1L103S, U13-ch50, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.46
Std Dev 4.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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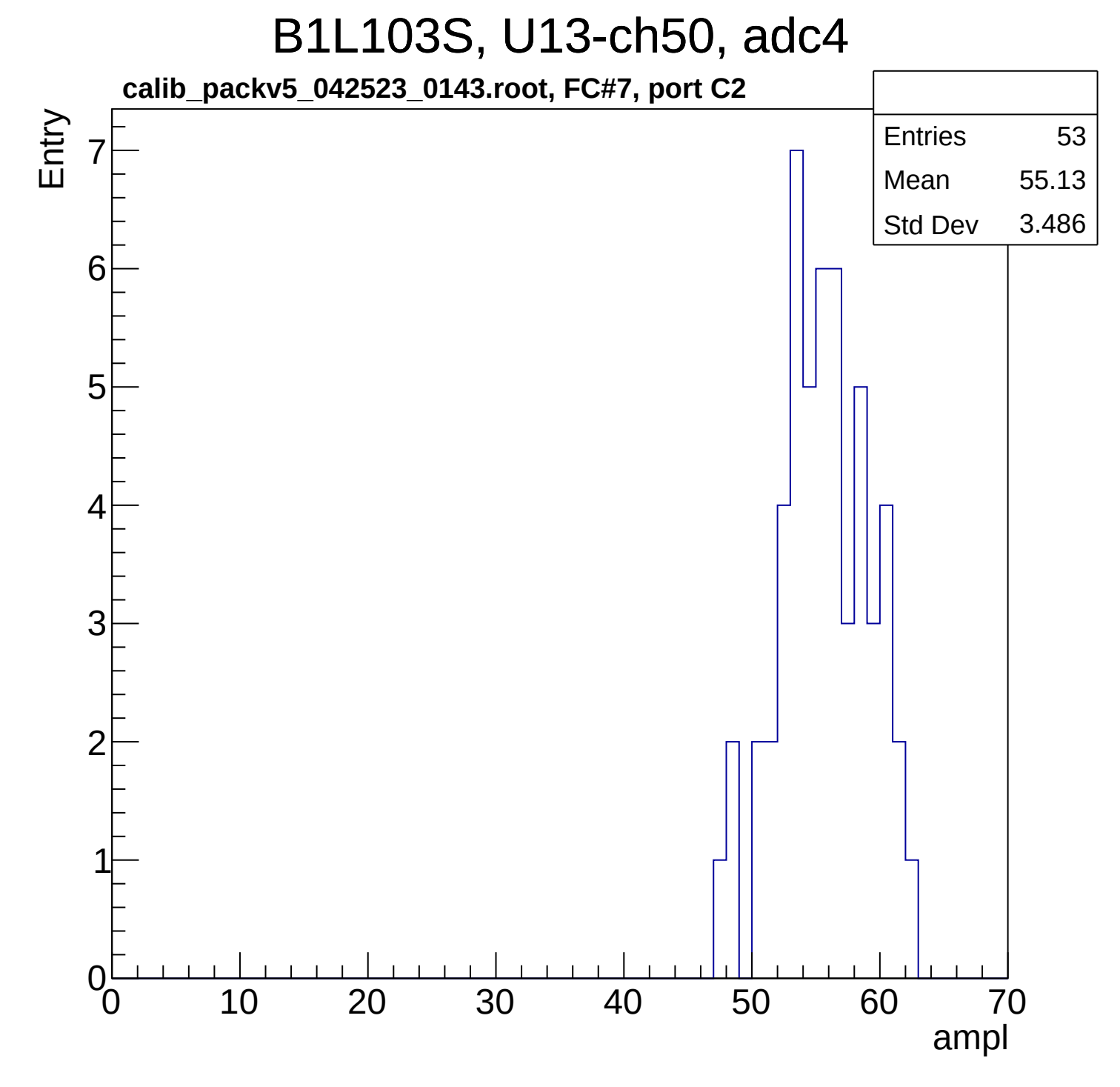
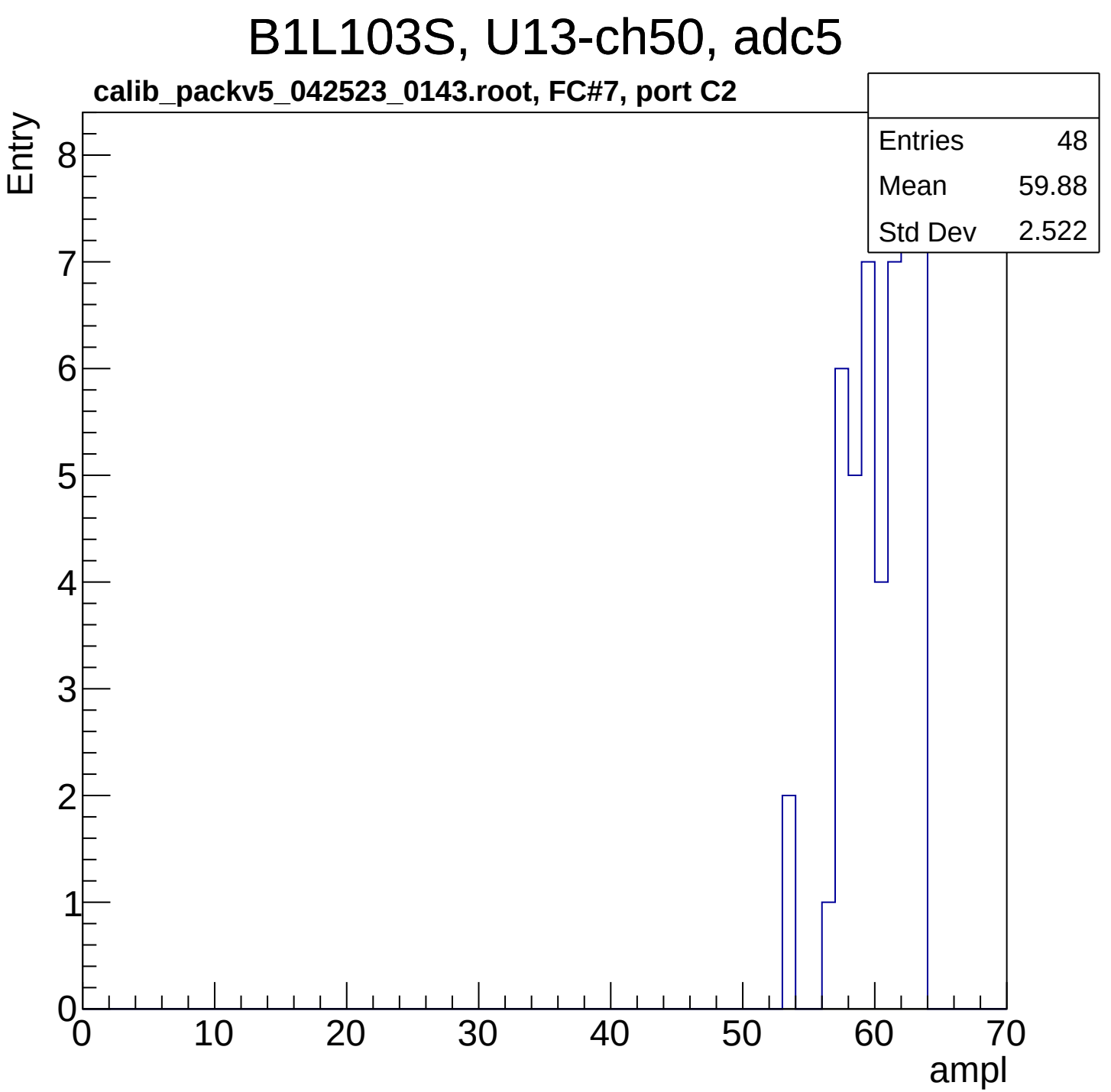
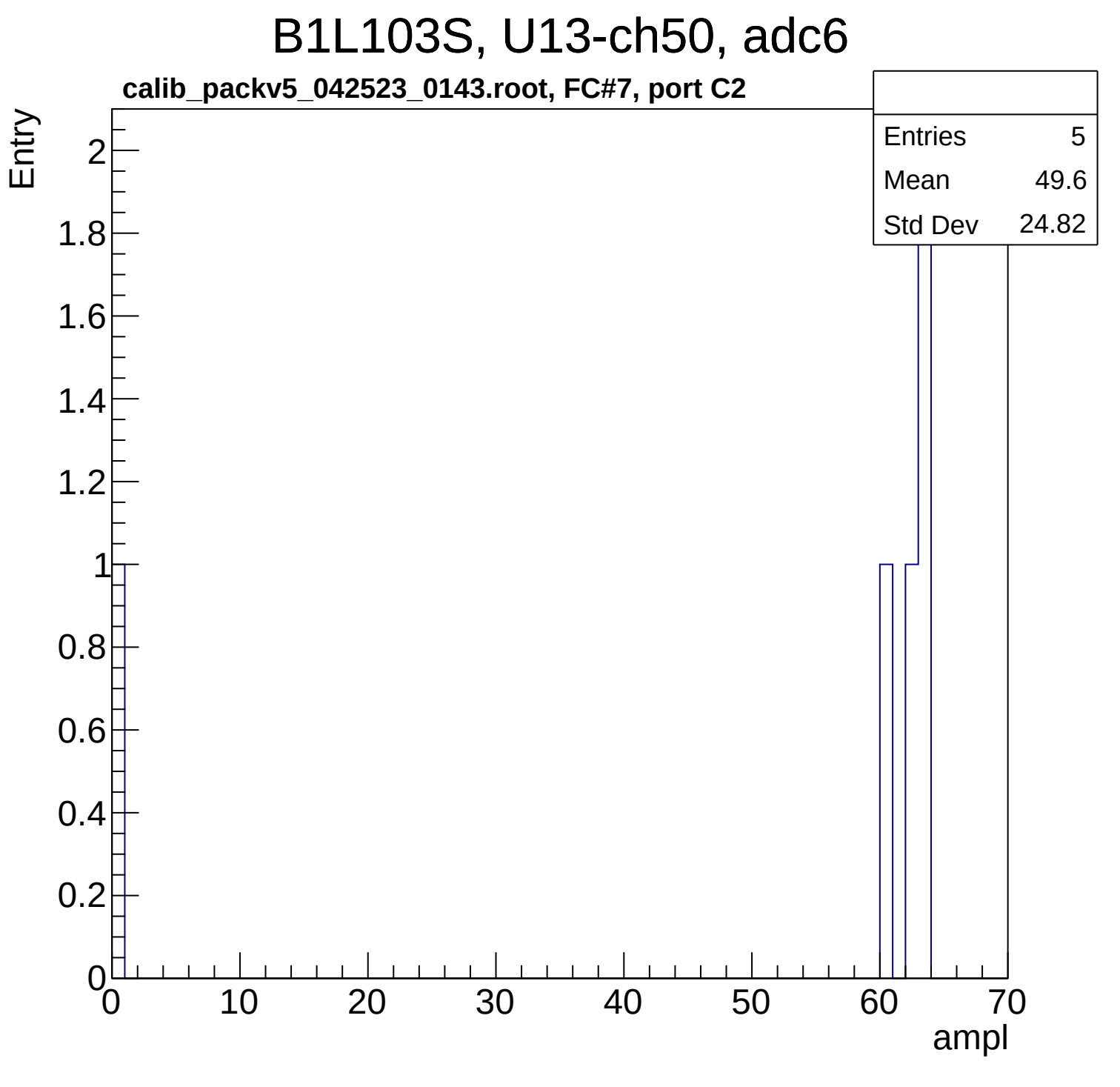
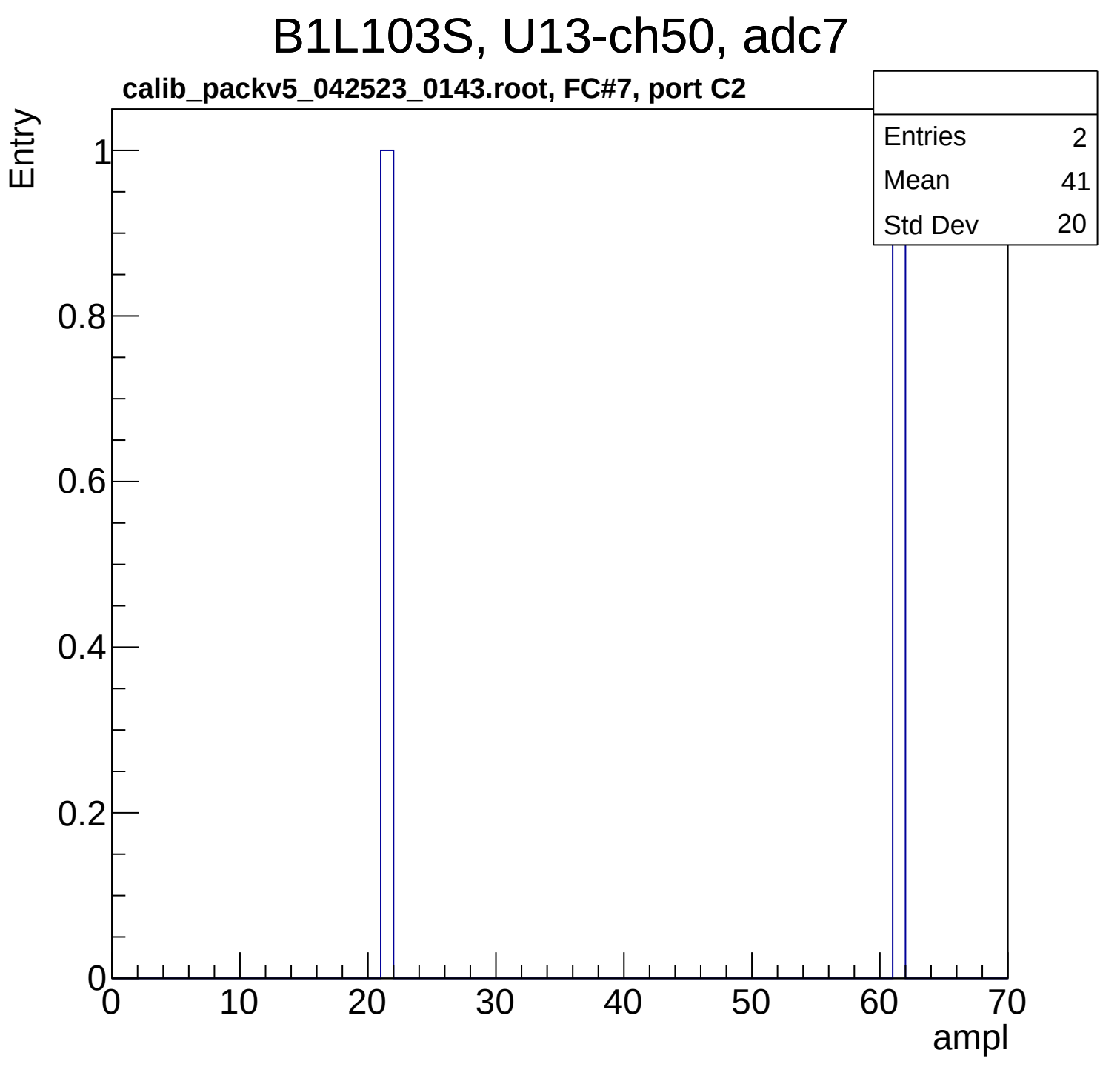
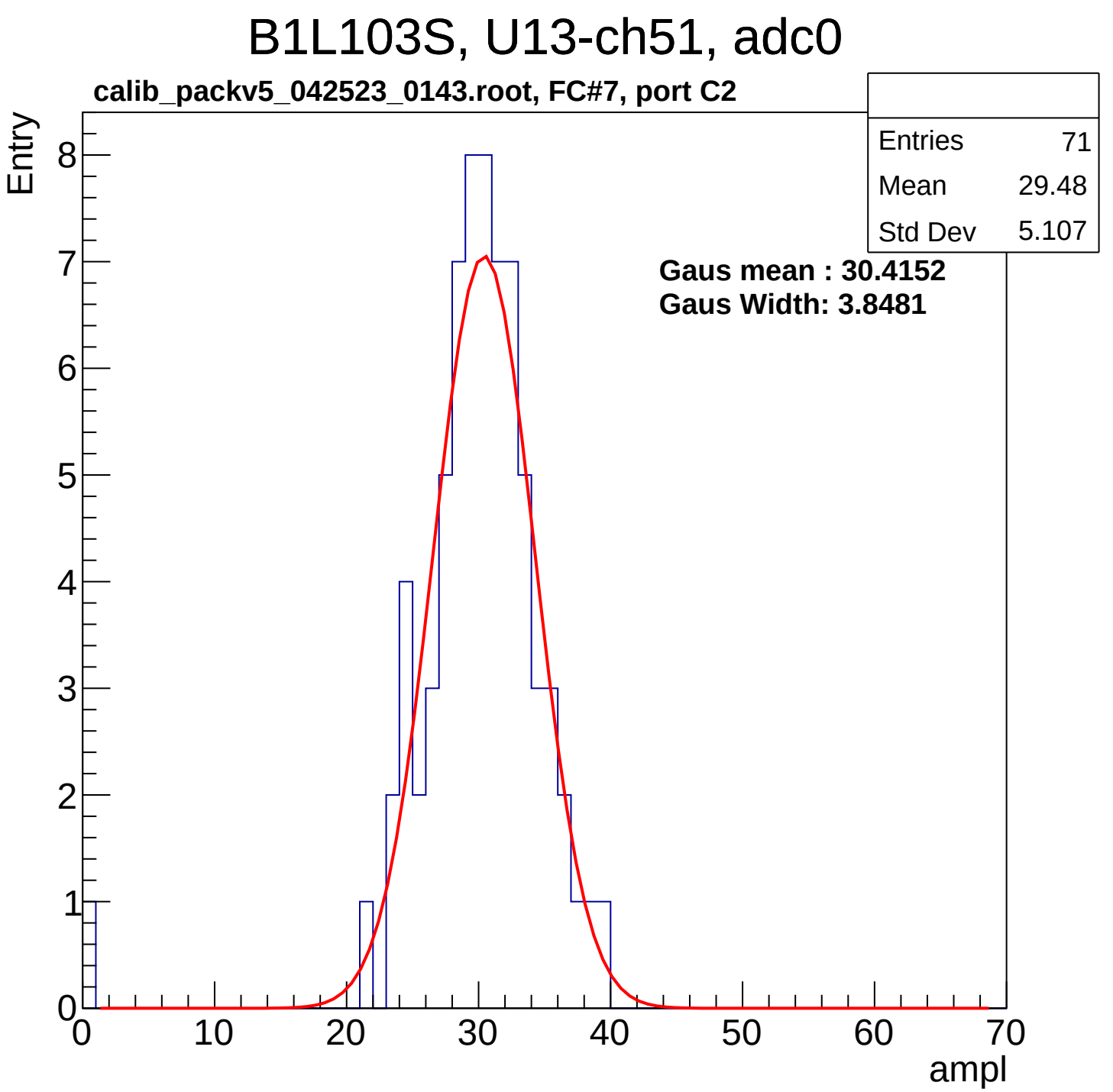
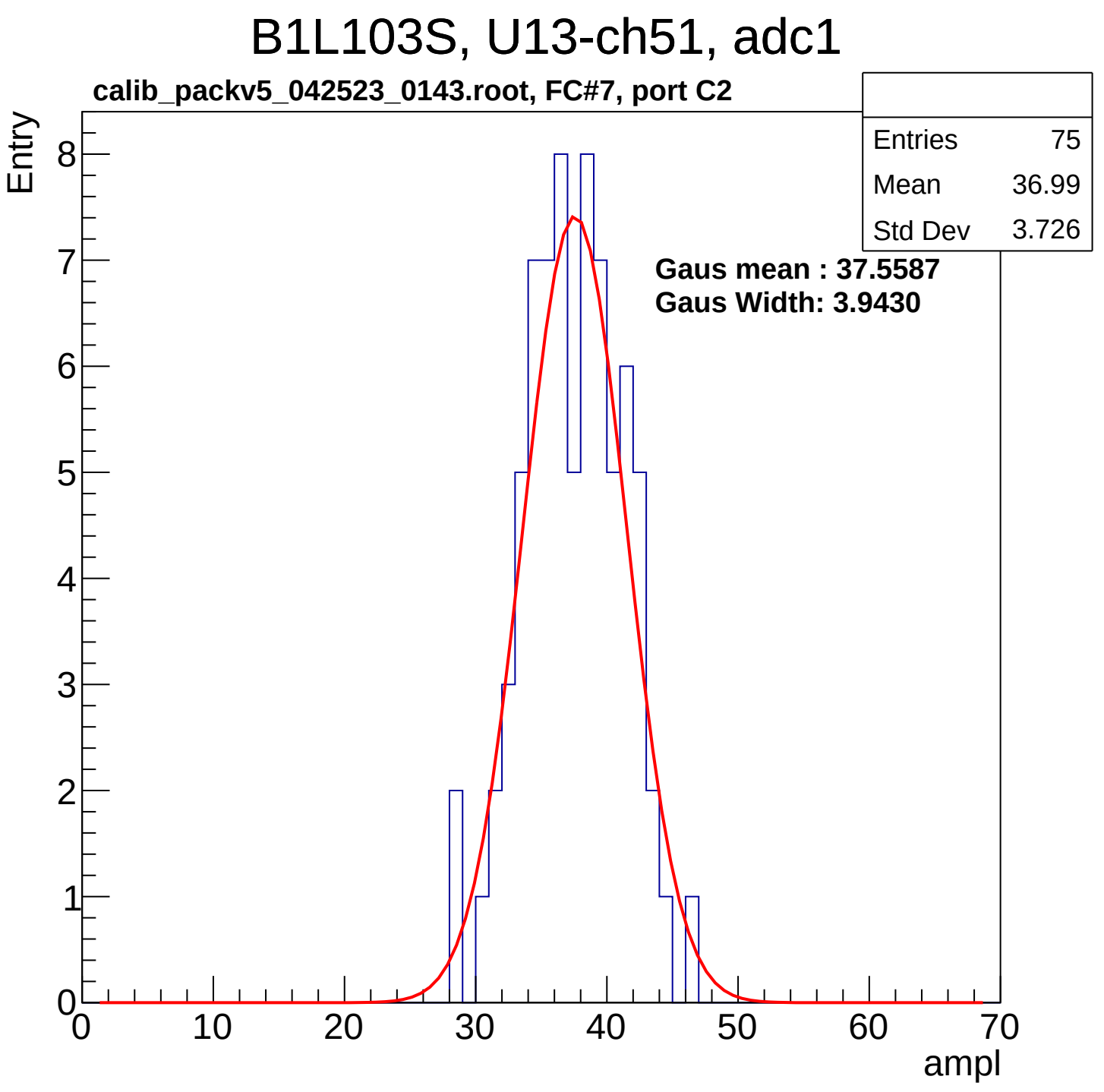

B1L103S, U13-ch50, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.13
Std Dev 3.486
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

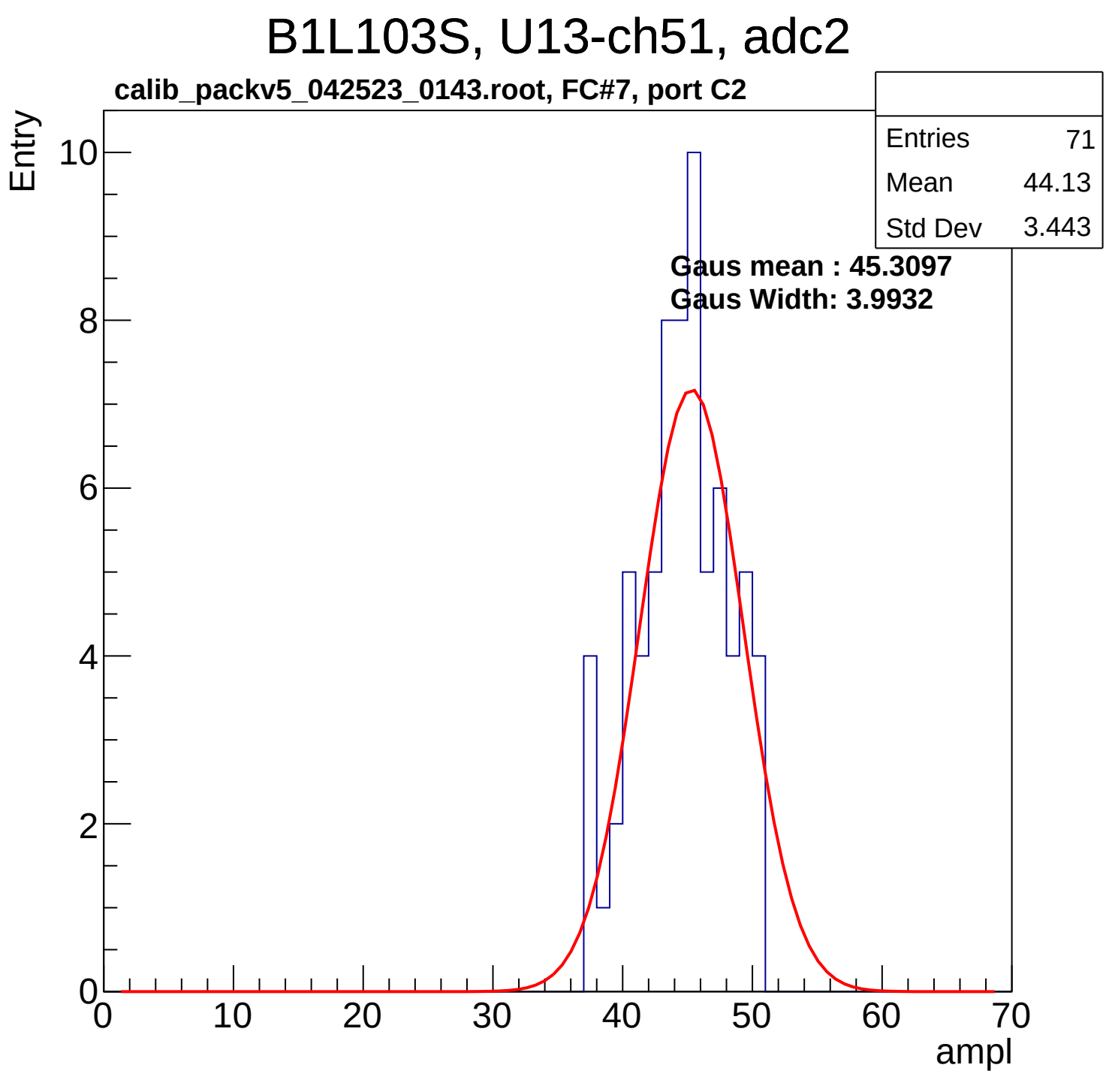

B1L103S, U13-ch50, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.88
Std Dev 2.522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

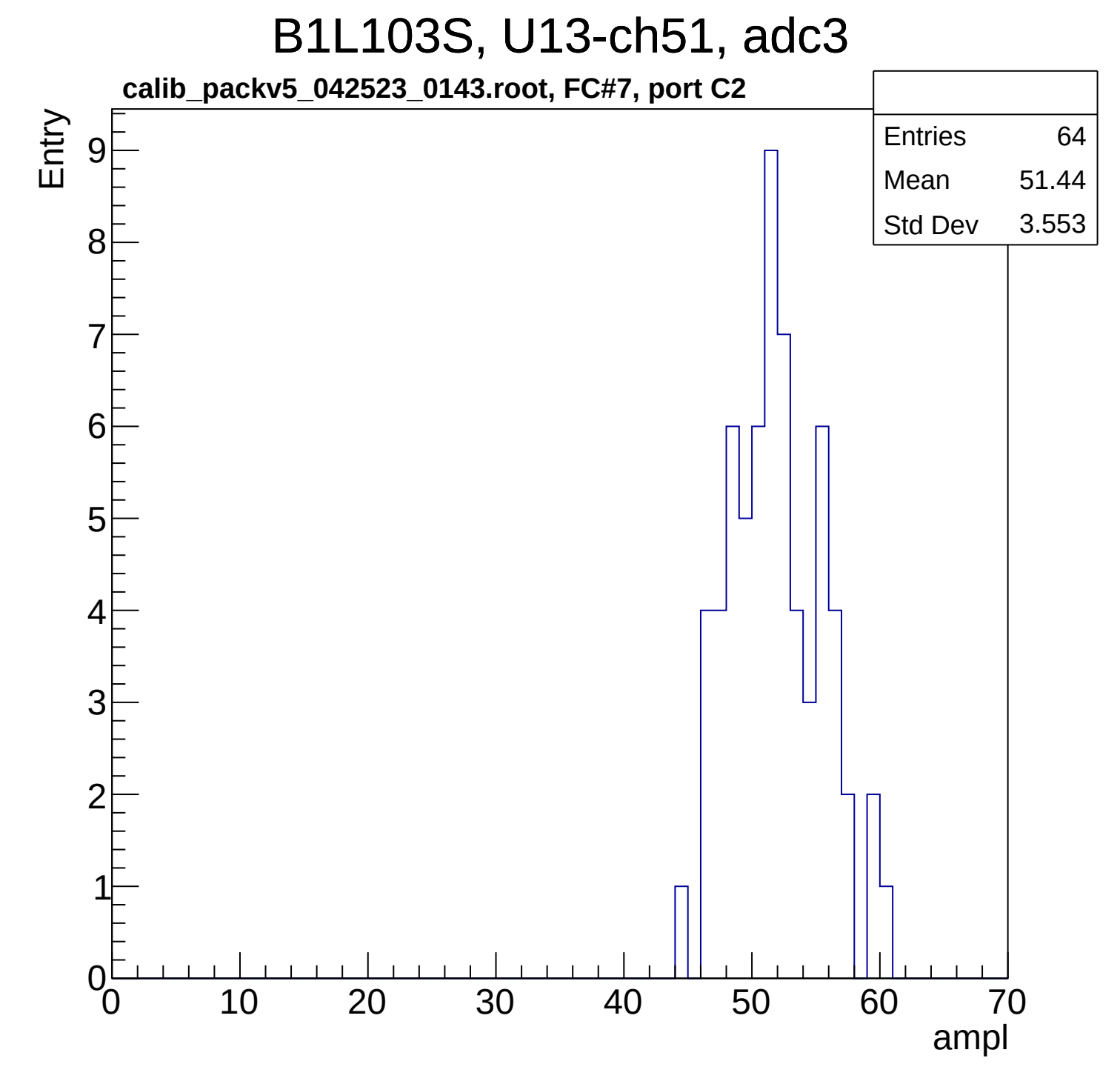

B1L103S, U13-ch50, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

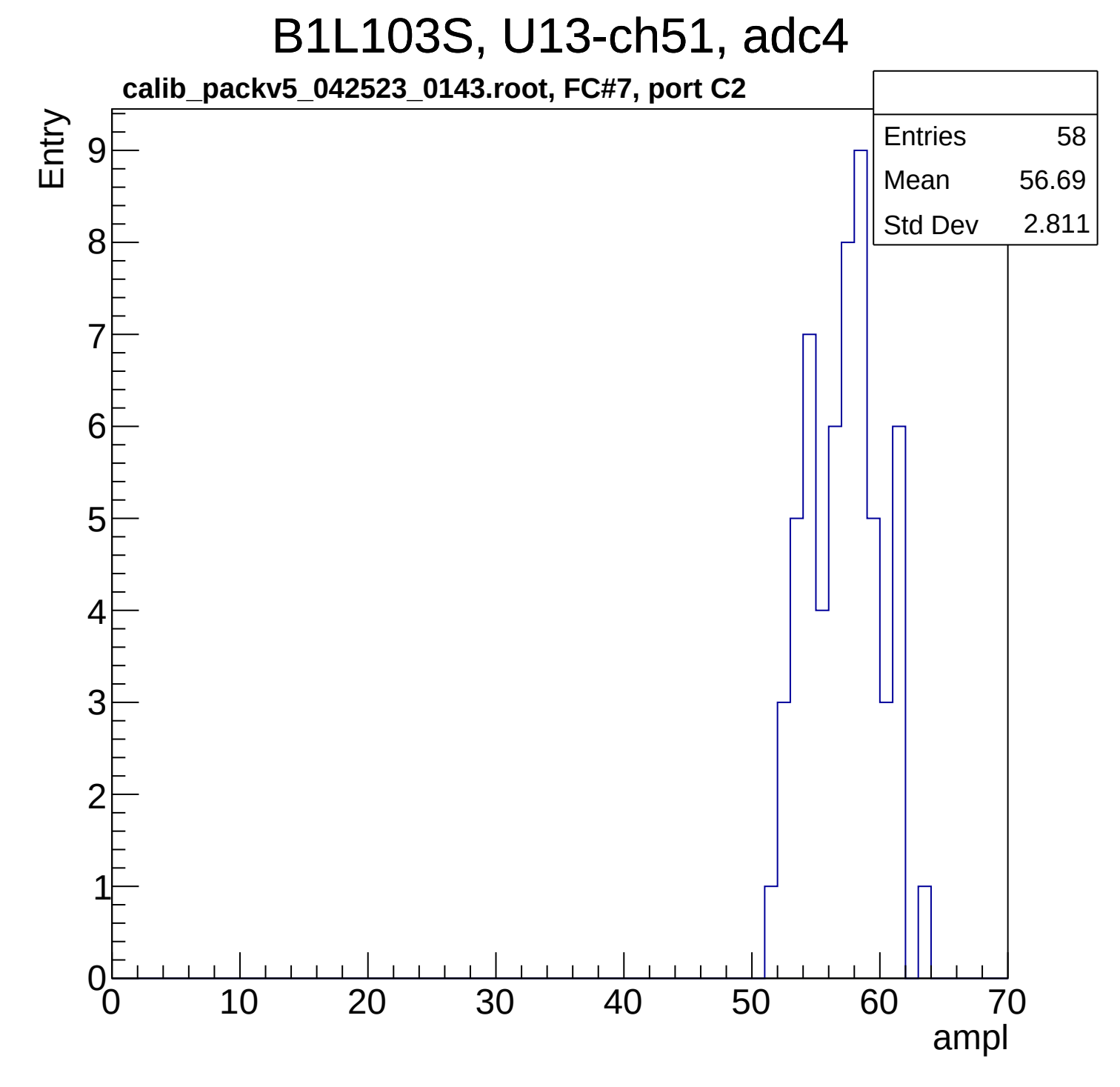

B1L103S, U13-ch50, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 41
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

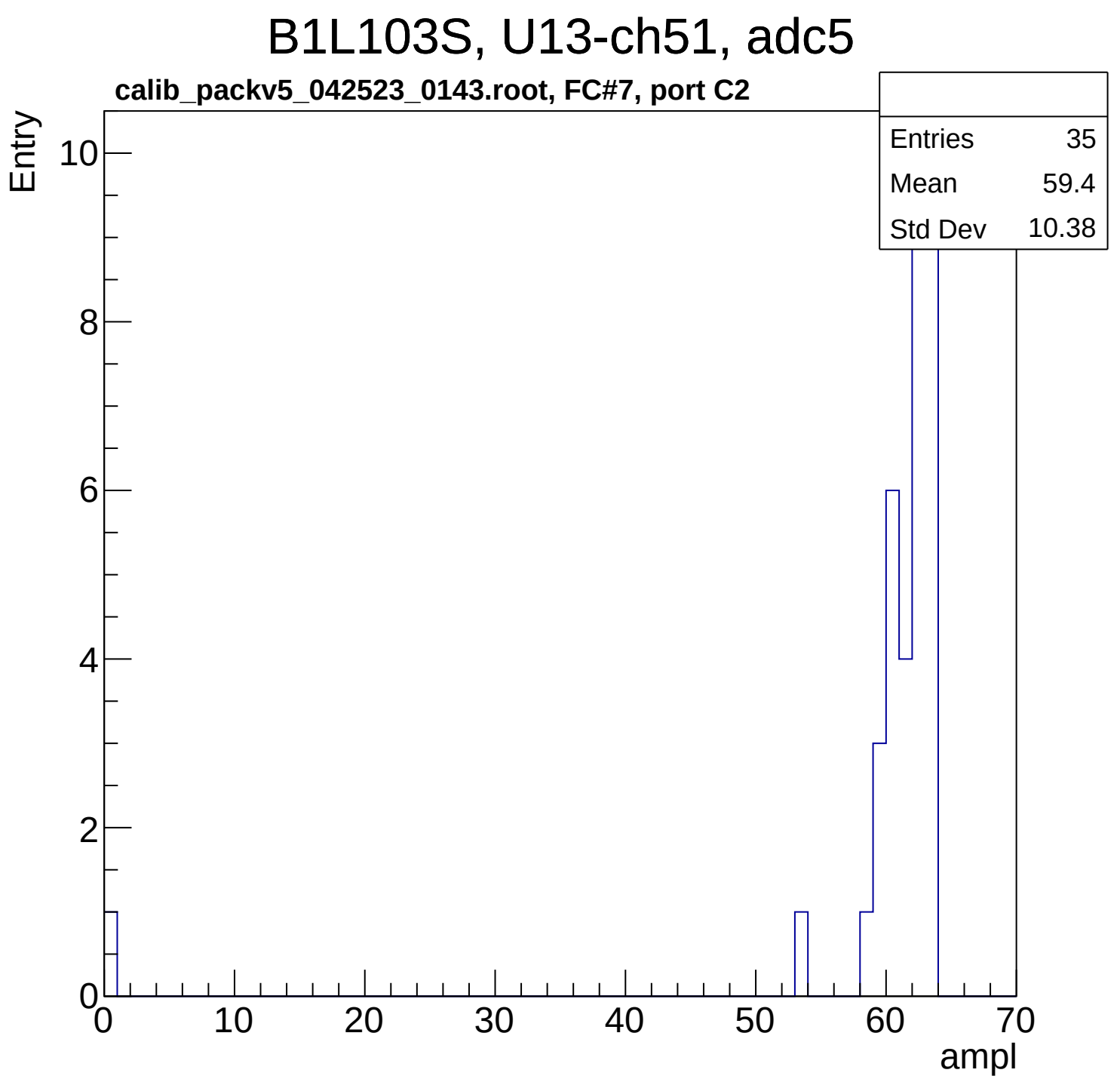

B1L103S, U13-ch51, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 29.48
Std Dev 5.107
Gaus mean : 30.4152
Gaus Width: 3.8481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

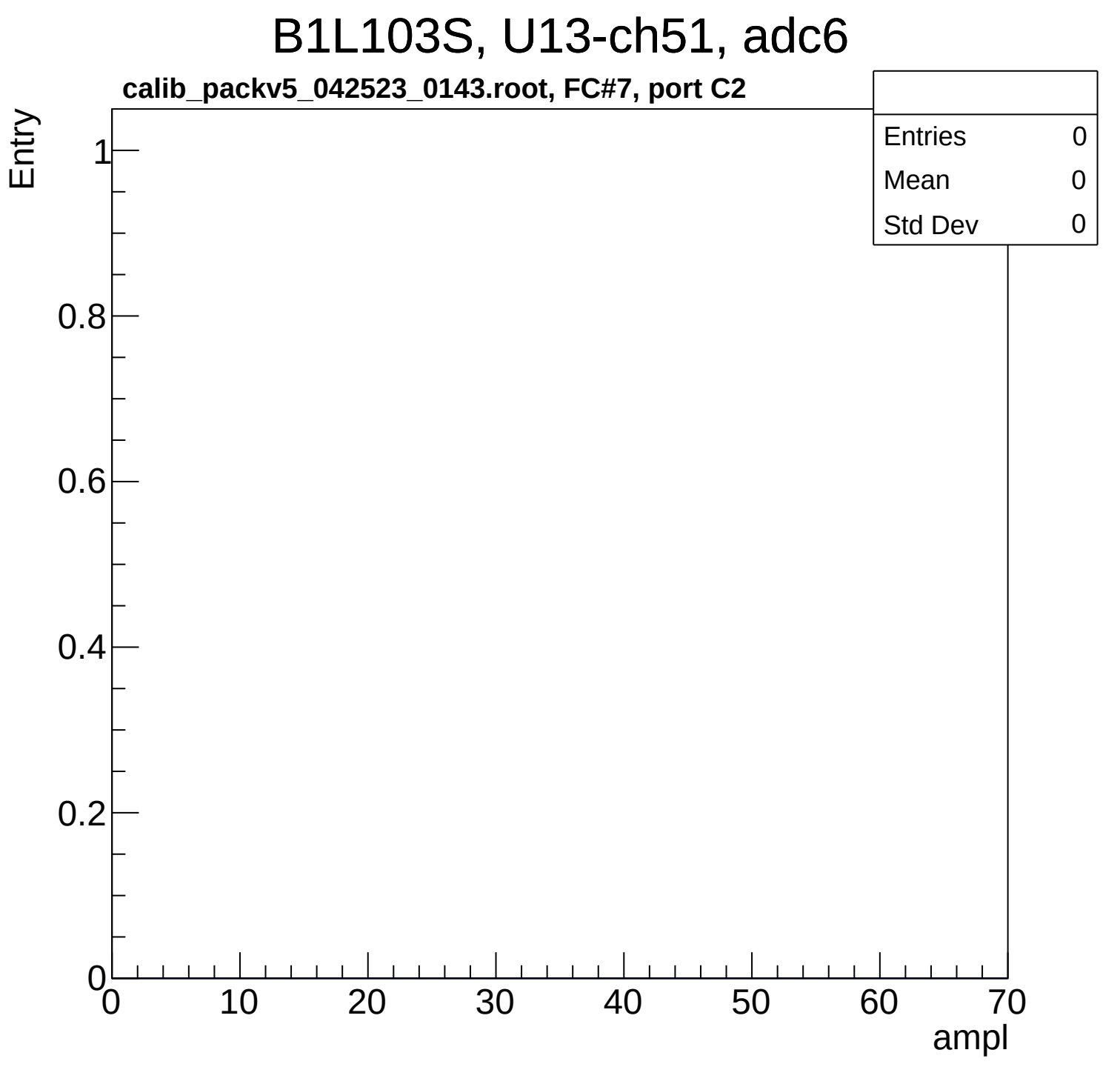

B1L103S, U13-ch51, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 36.99
Std Dev 3.726
Gaus mean : 37.5587
Gaus Width: 3.9430
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

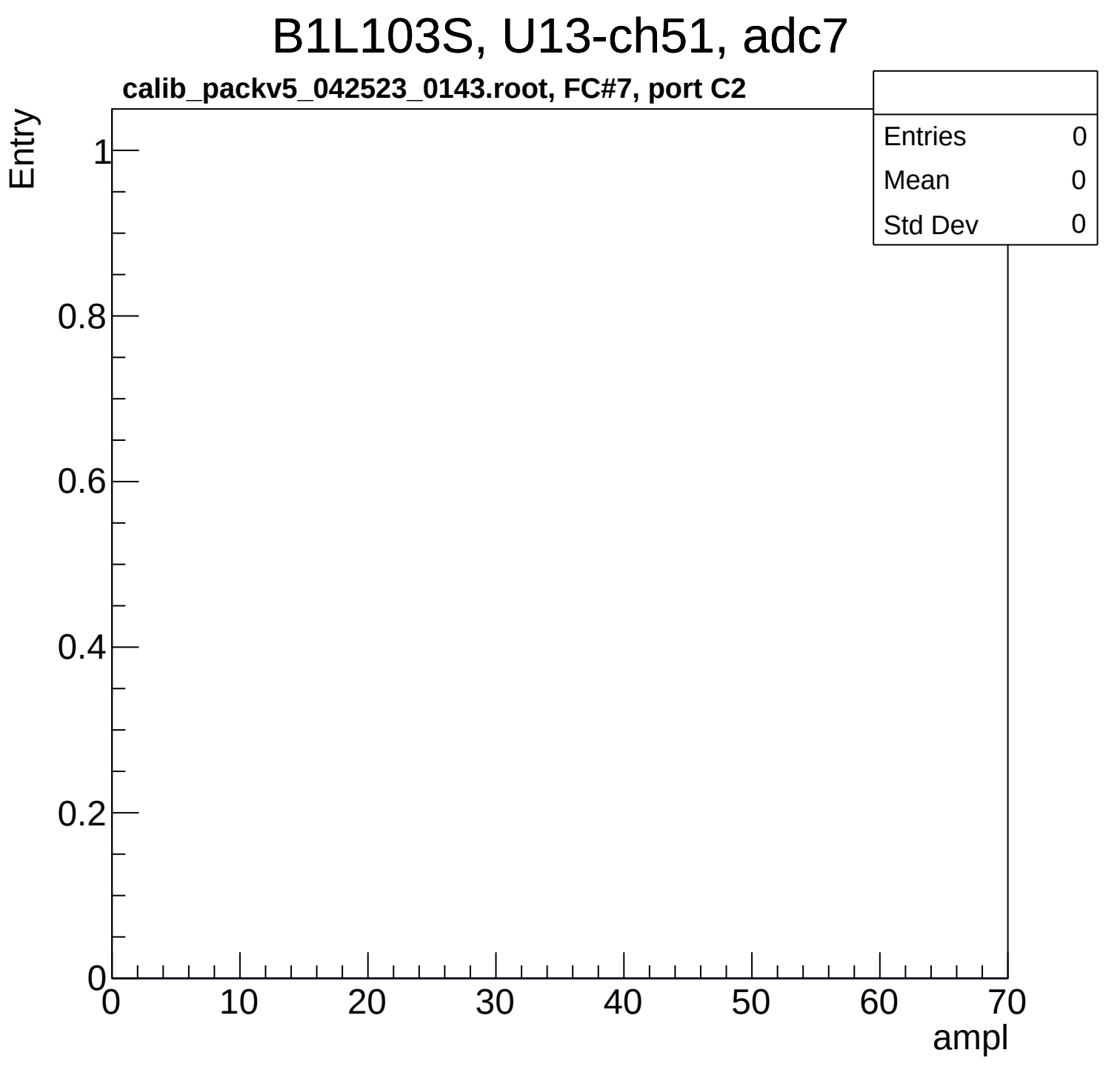

B1L103S, U13-ch51, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 44.13
Std Dev 3.443
Gaus mean : 45.3097
Gaus Width: 3.9932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

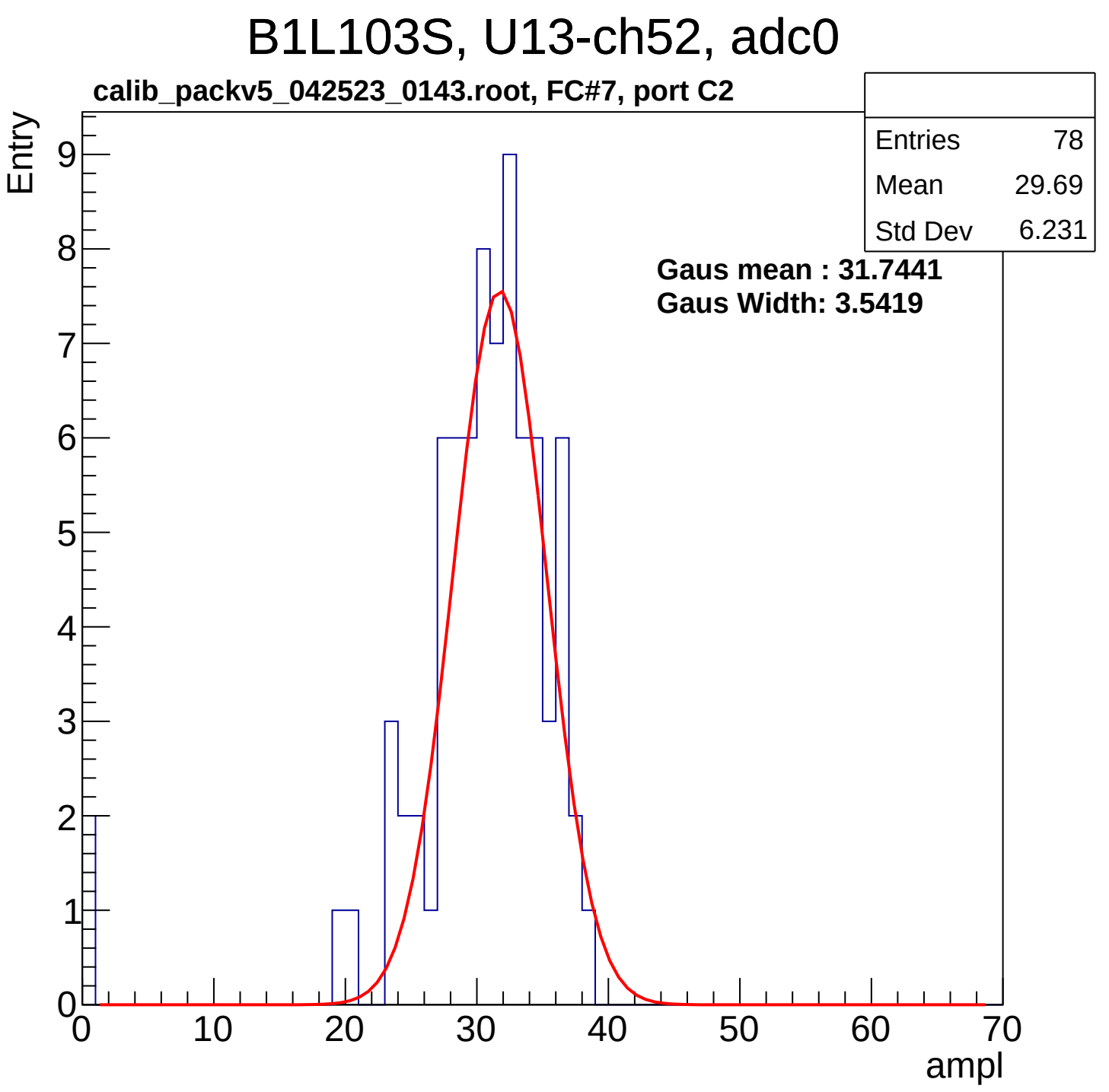

B1L103S, U13-ch51, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 51.44
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

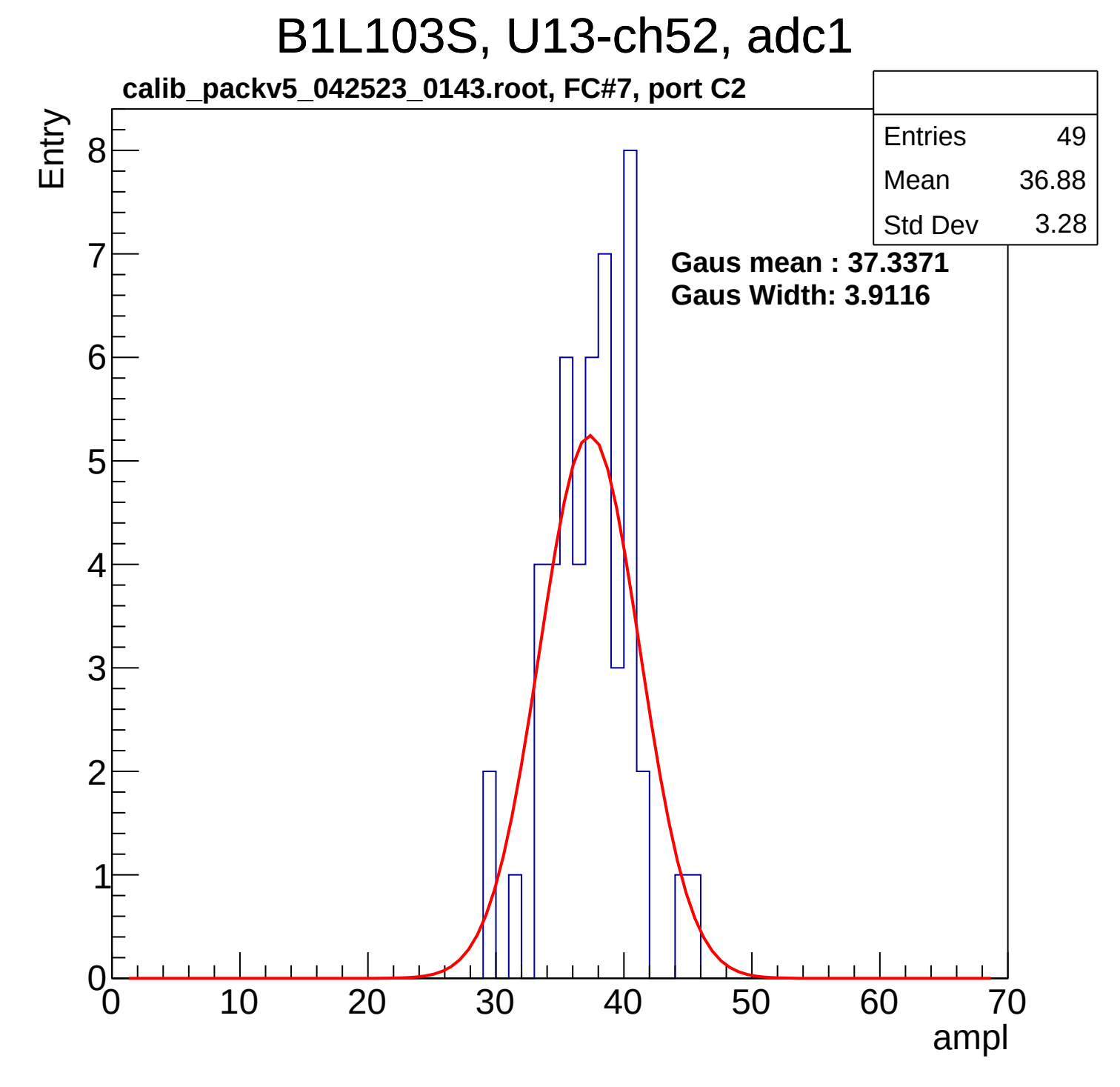

B1L103S, U13-ch51, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.69
Std Dev 2.811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

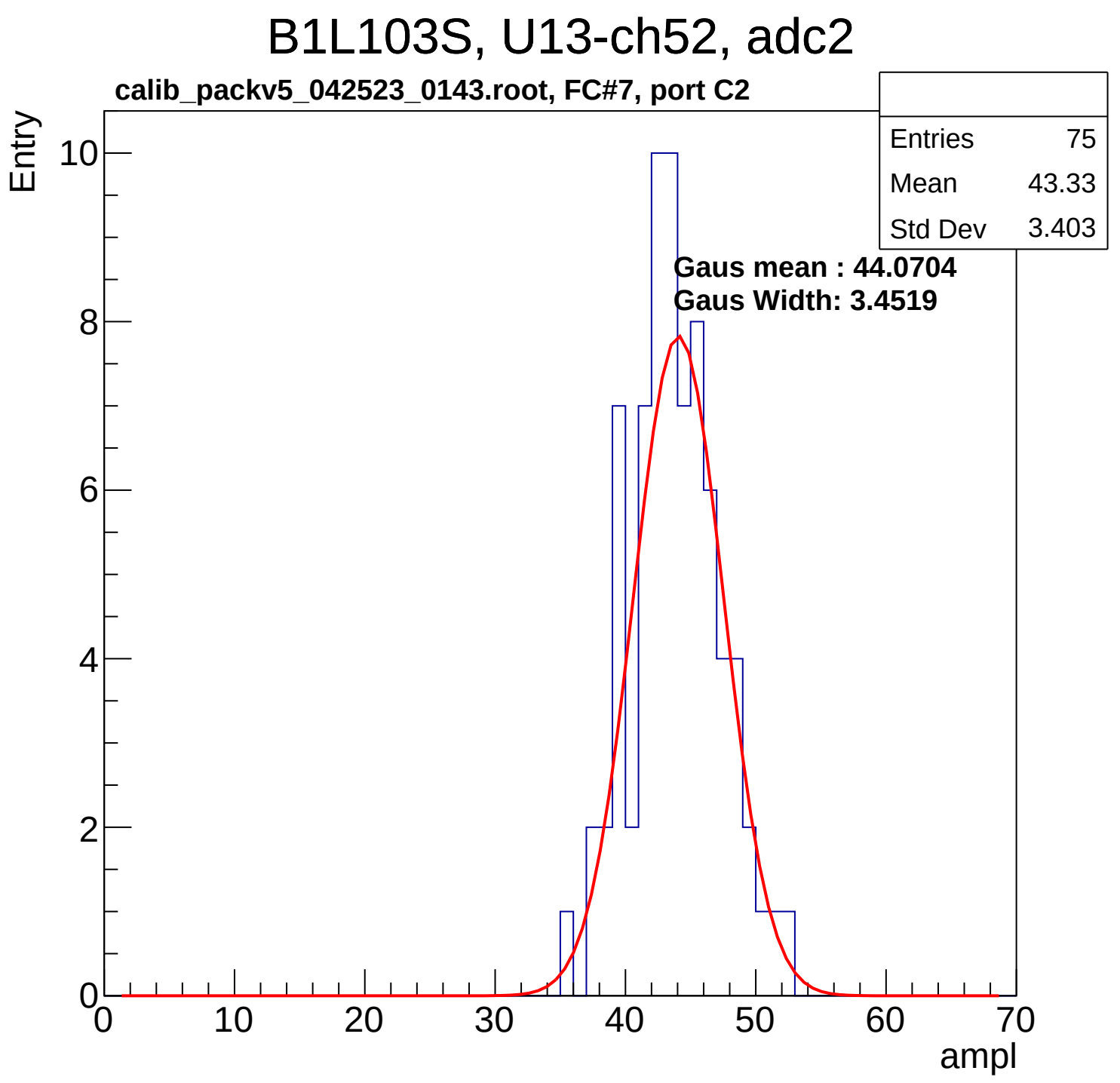

B1L103S, U13-ch51, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 59.4
Std Dev 10.38
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

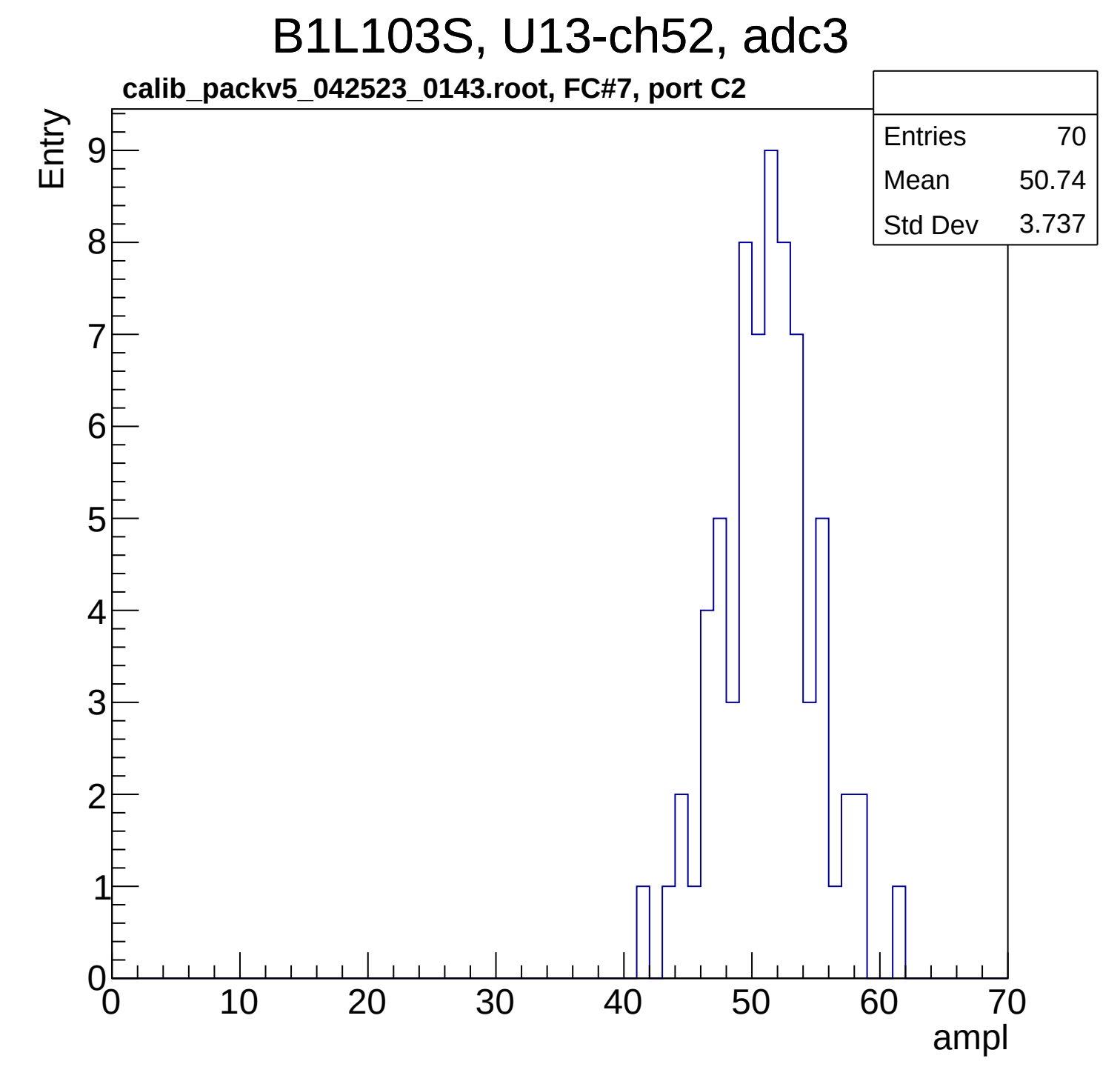

B1L103S, U13-ch51, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

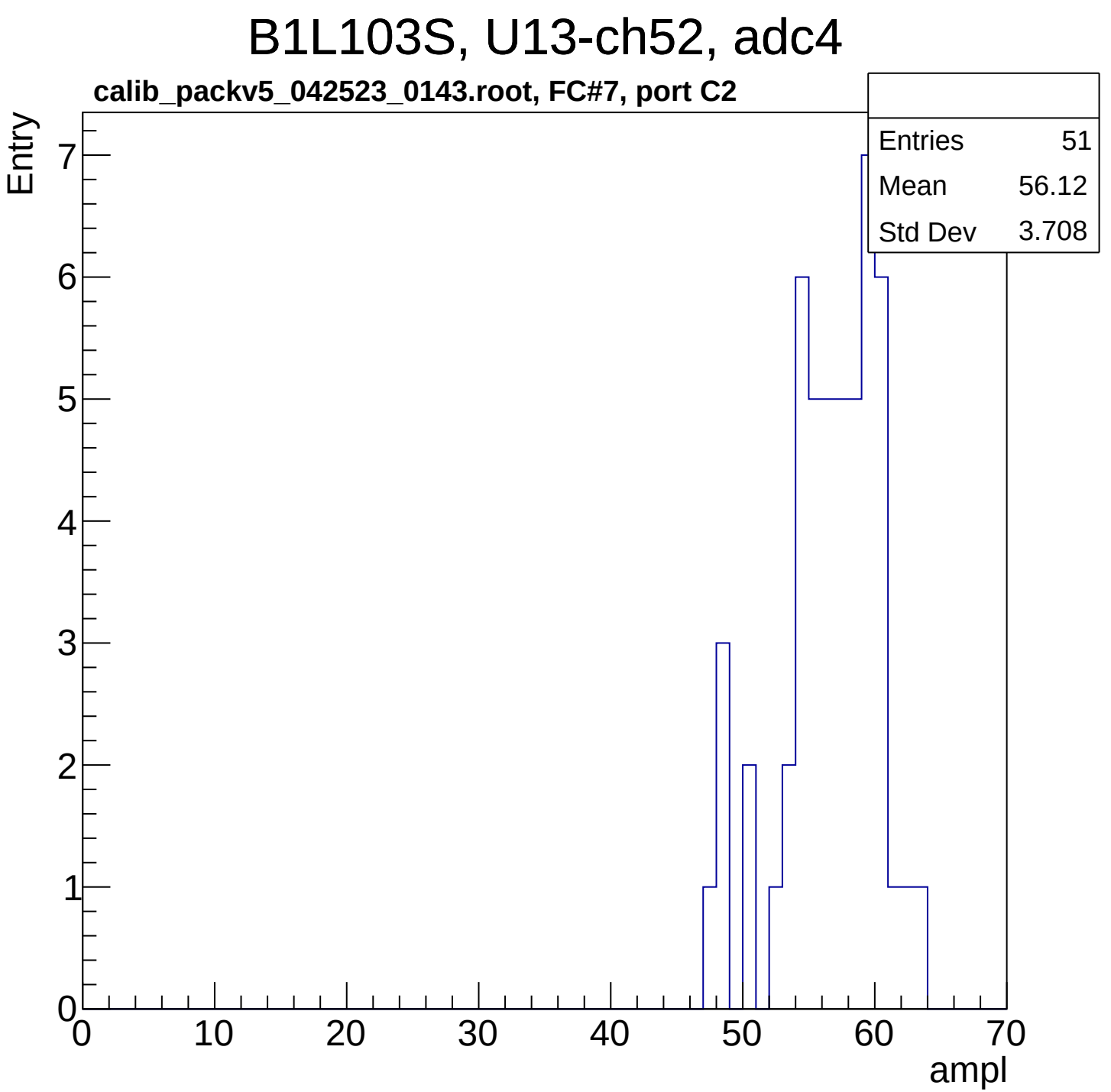

B1L103S, U13-ch51, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

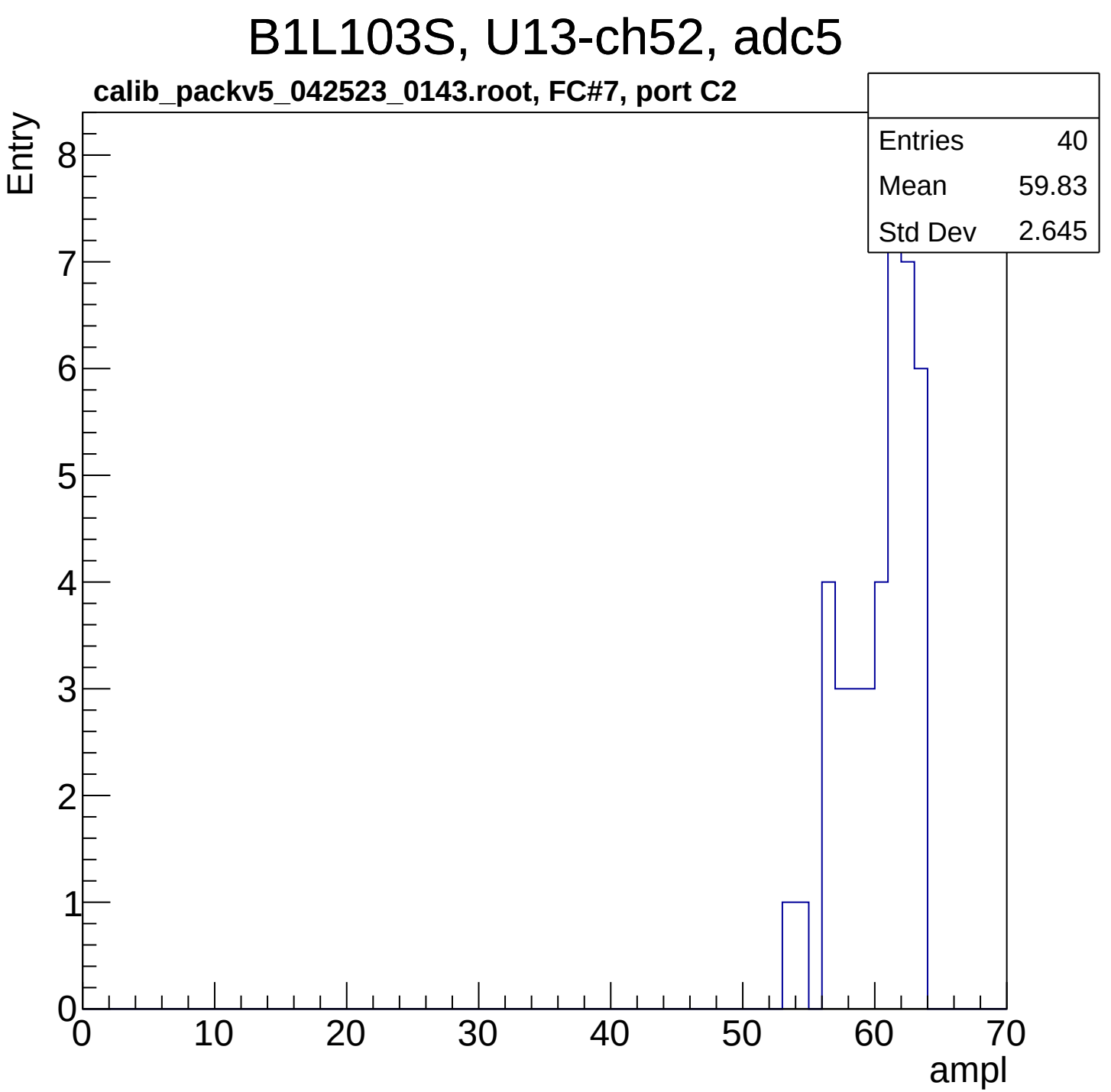

B1L103S, U13-ch52, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 29.69
Std Dev 6.231
Gaus mean : 31.7441
Gaus Width: 3.5419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 36.88
Std Dev 3.28
Gaus mean : 37.3371
Gaus Width: 3.9116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 43.33
Std Dev 3.403
Gaus mean : 44.0704
Gaus Width: 3.4519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 50.74
Std Dev 3.737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 56.12
Std Dev 3.708
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 59.83
Std Dev 2.645
Entry
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2
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4
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8
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10
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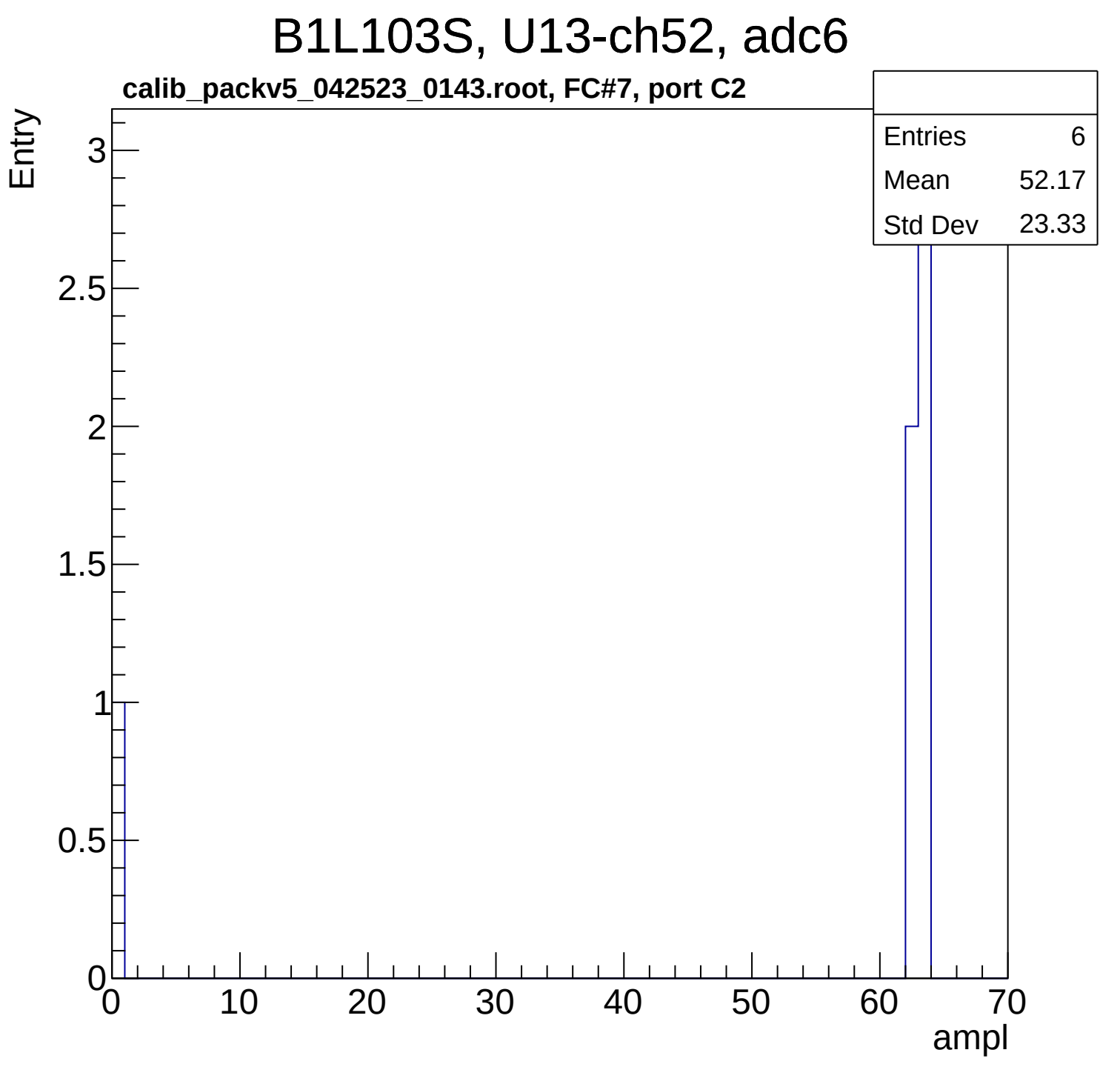
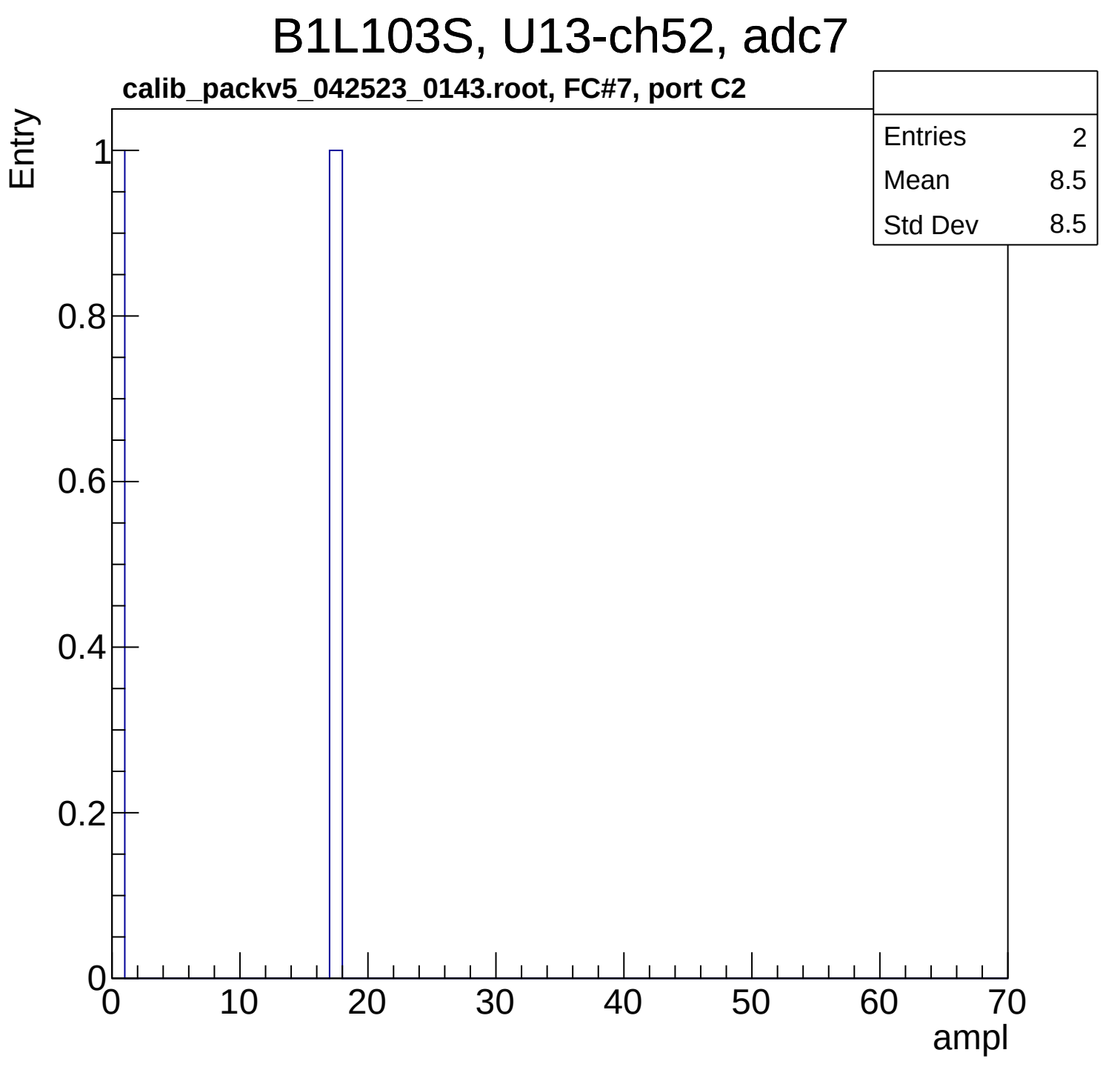
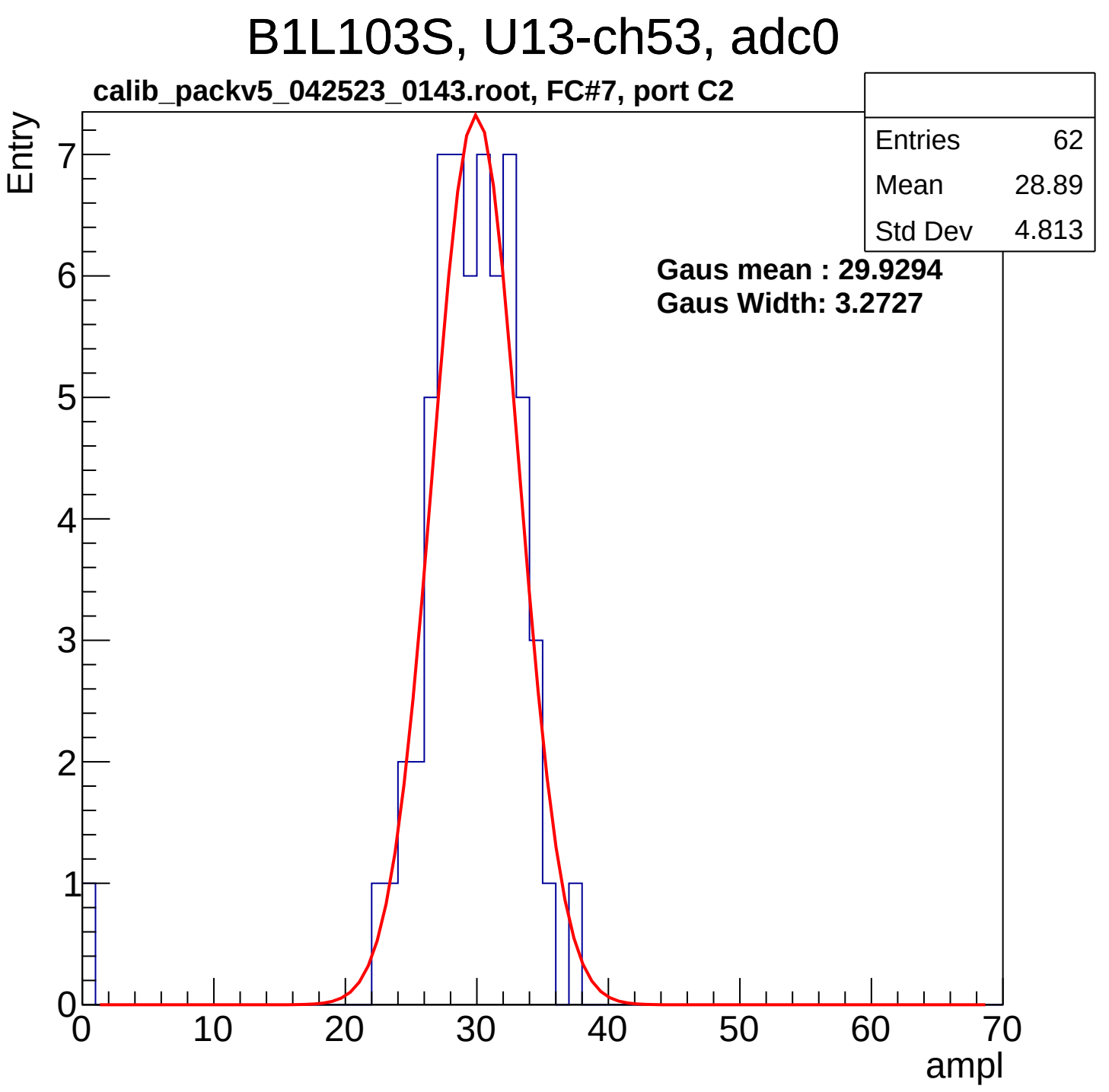
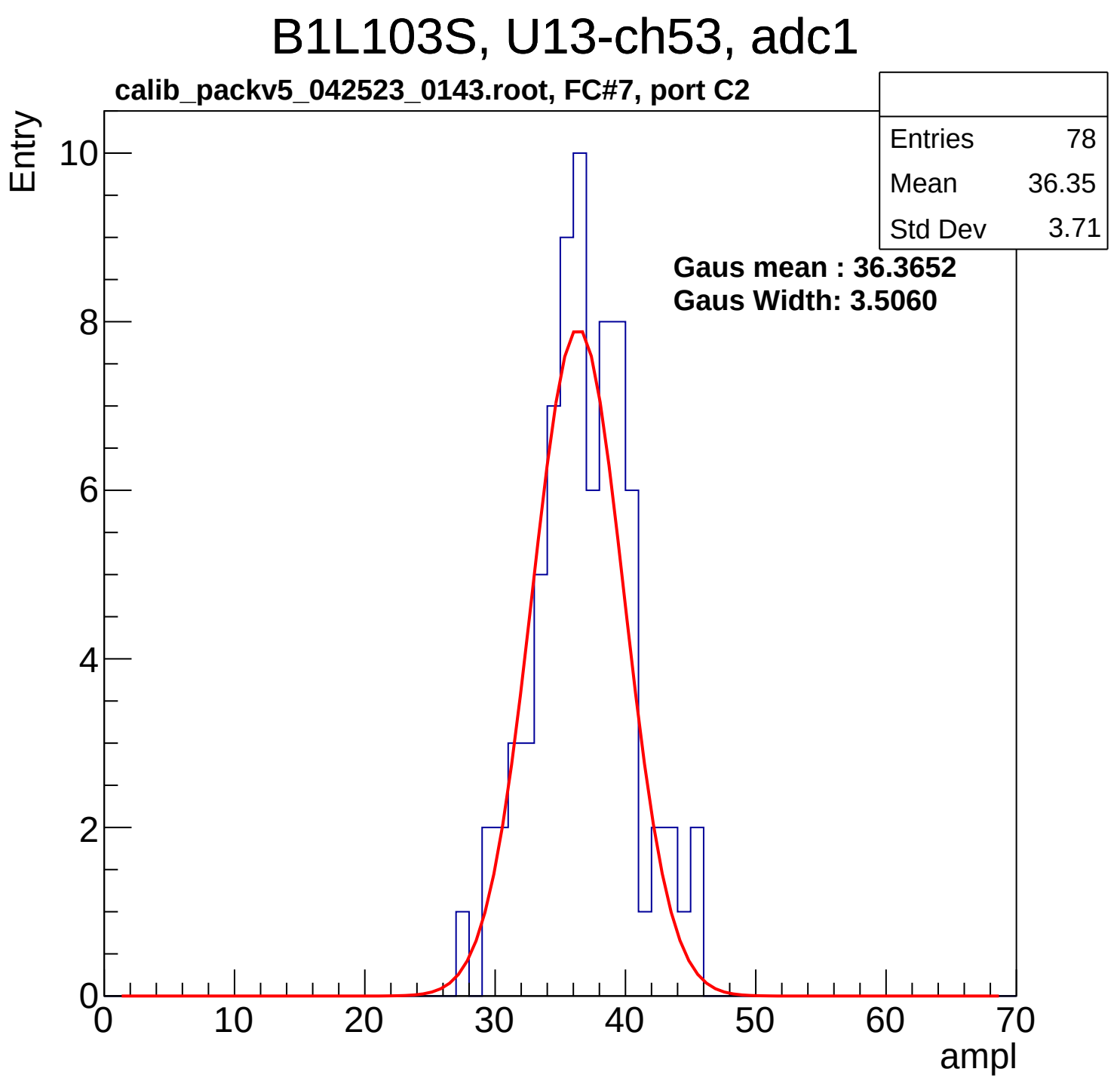
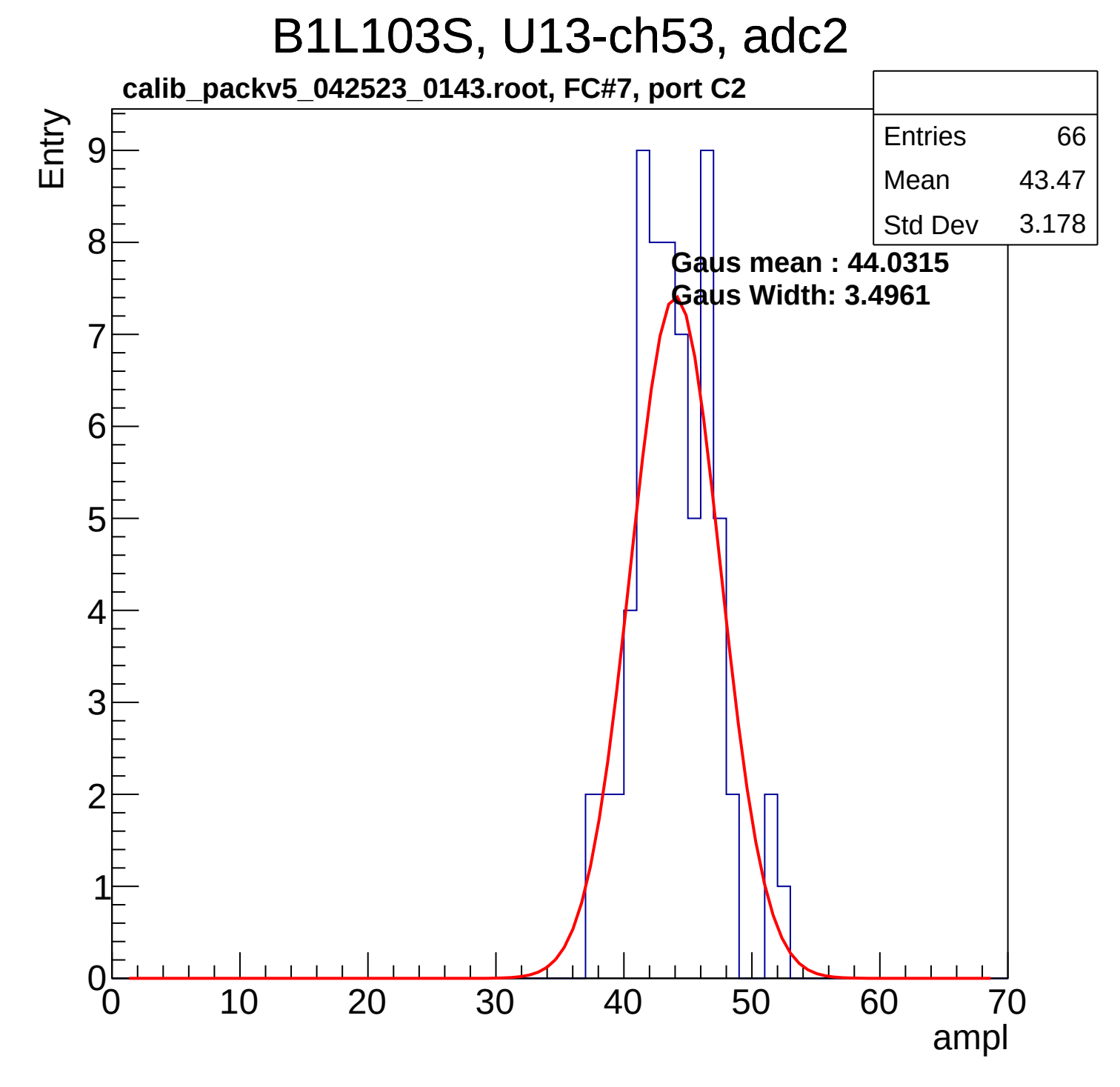
30
40
50
60
70
ampl


B1L103S, U13-ch52, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch52, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch53, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28.89
Std Dev 4.813
Gaus mean : 29.9294
Gaus Width: 3.2727
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch53, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.35
Std Dev 3.71
Gaus mean : 36.3652
Gaus Width: 3.5060
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch53, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43.47
Std Dev 3.178
Gaus mean : 44.0315
Gaus Width: 3.4961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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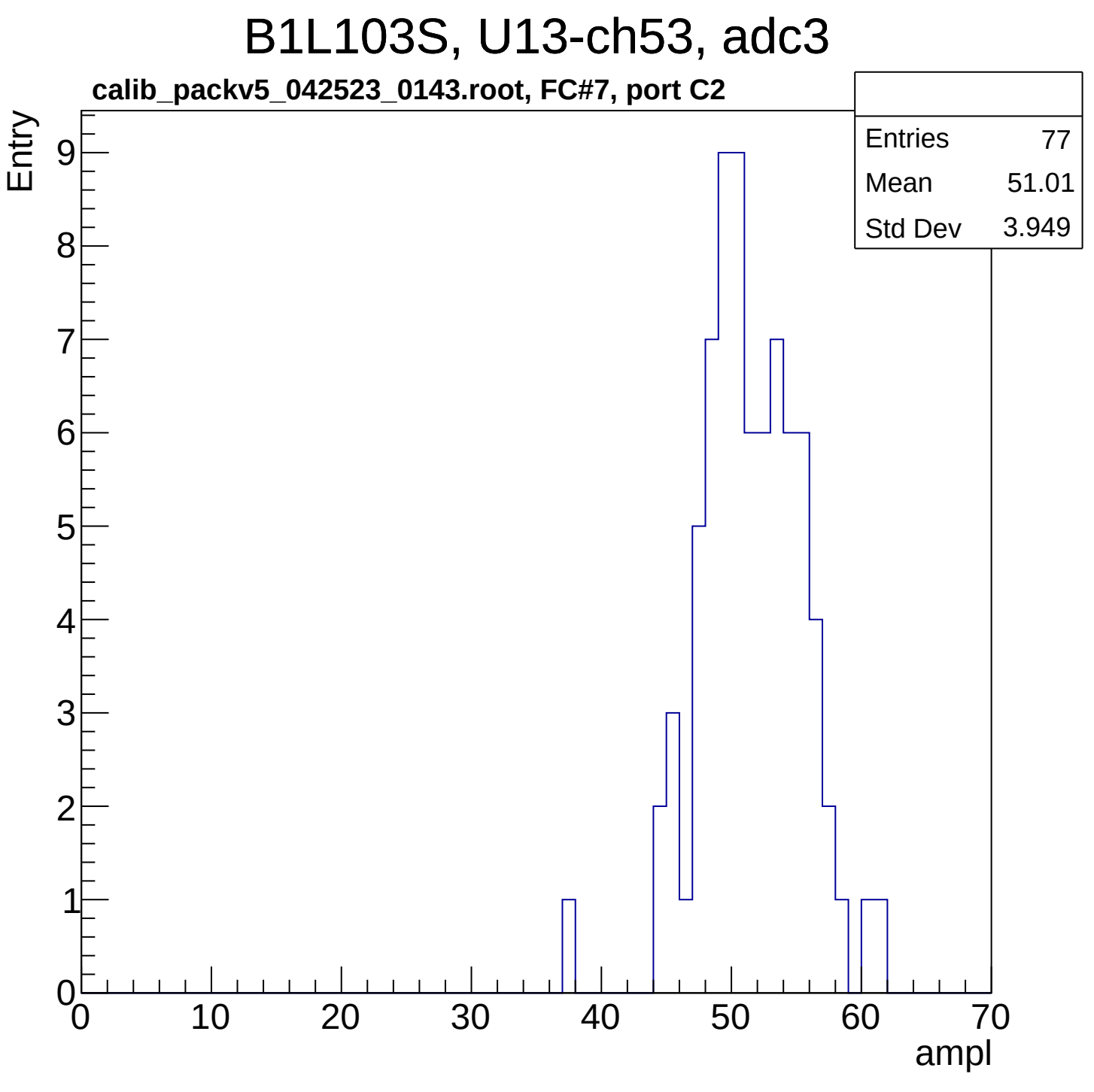
B1L103S, U13-ch53, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 51.01
Std Dev 3.949
Entry
ampl
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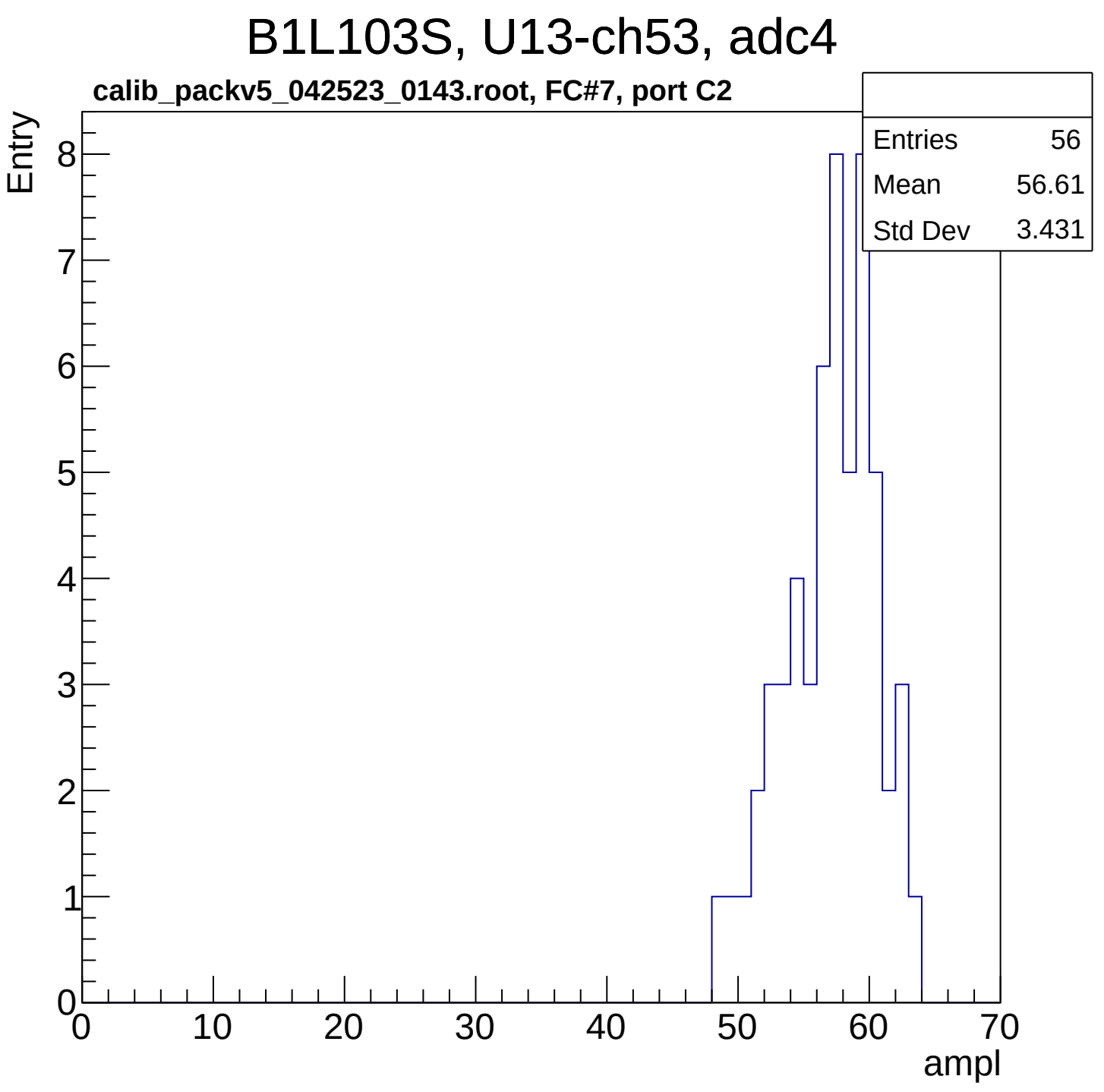
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch53, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 56.61
Std Dev 3.431
Entry
ampl
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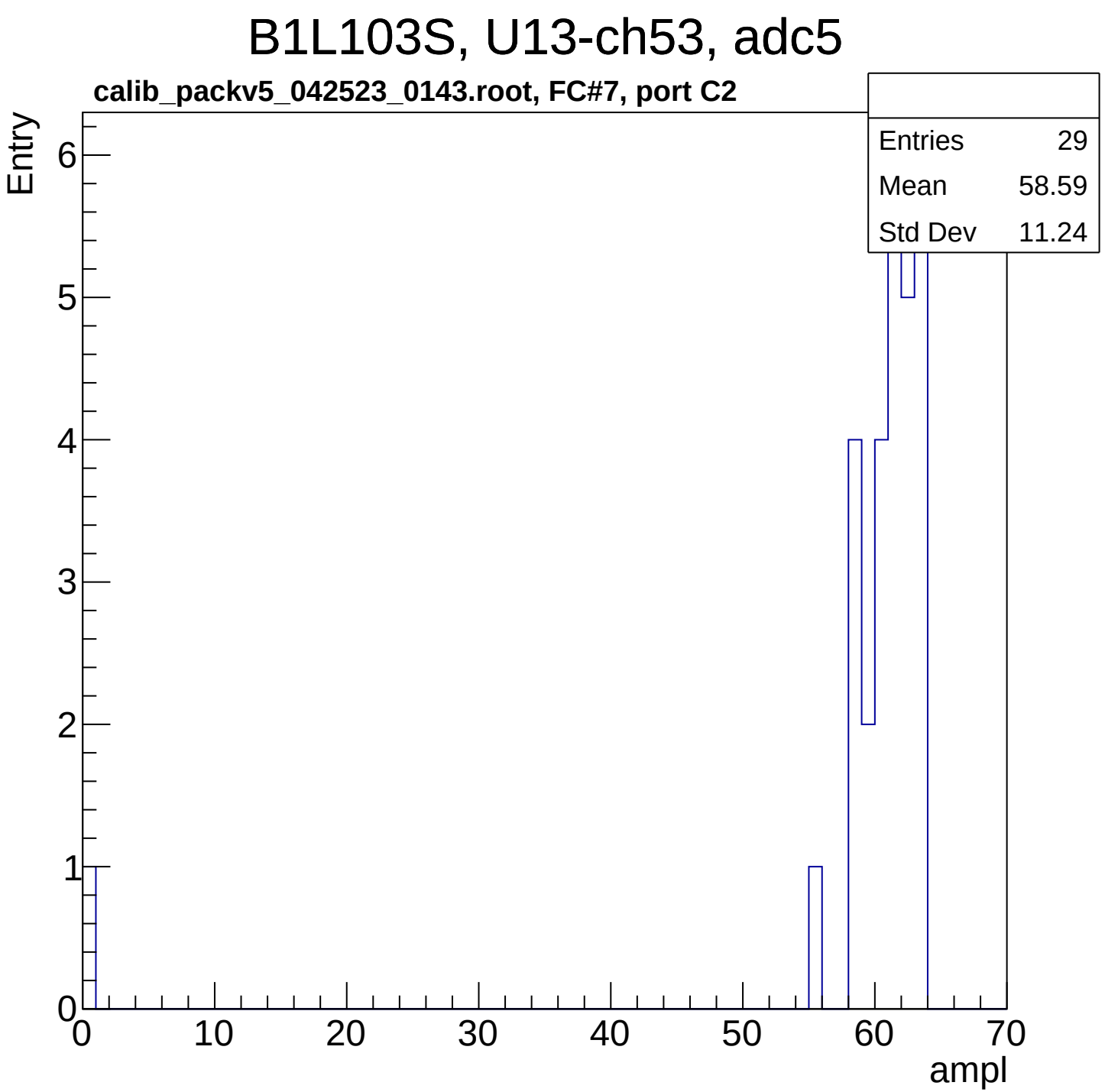
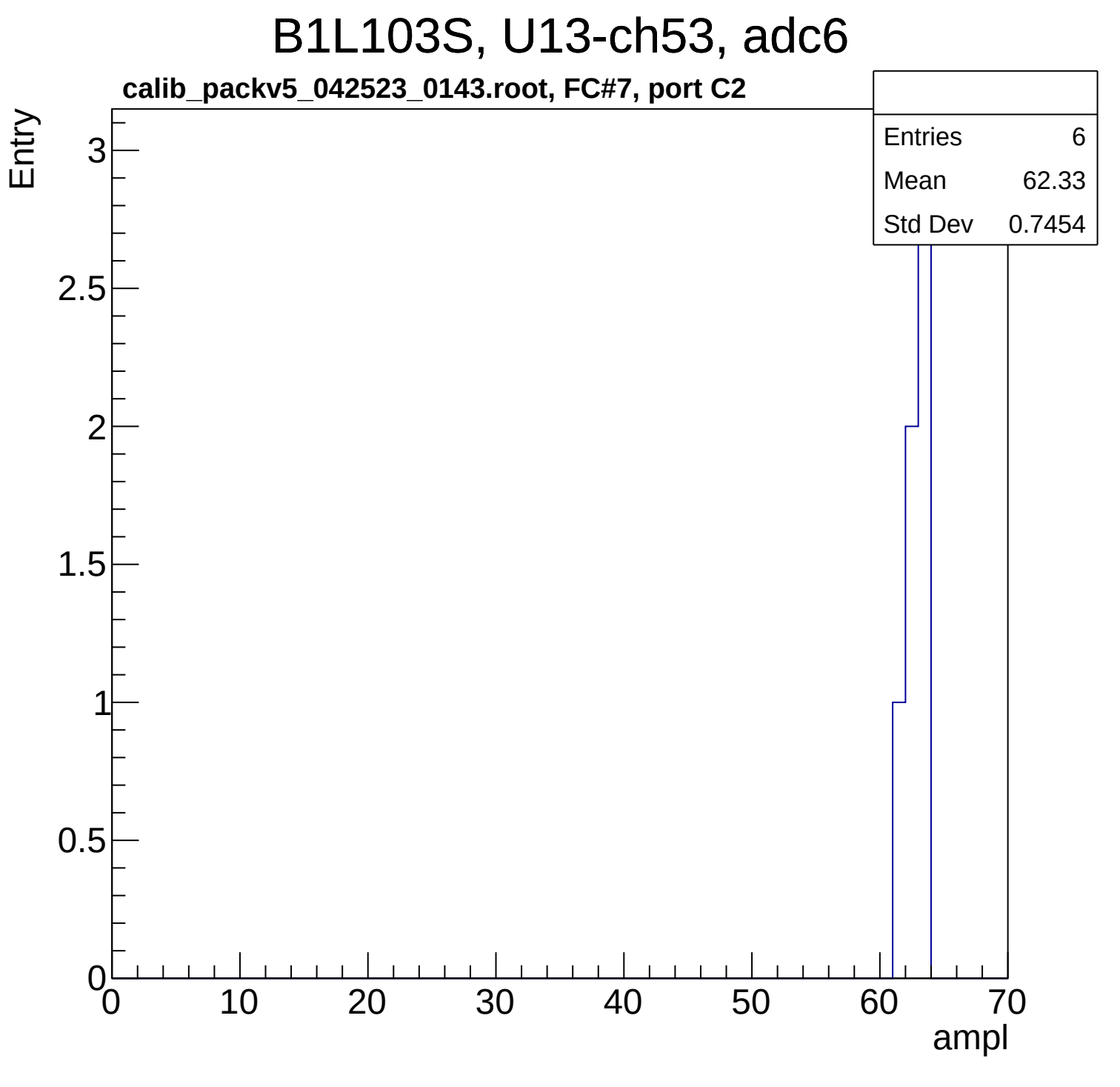
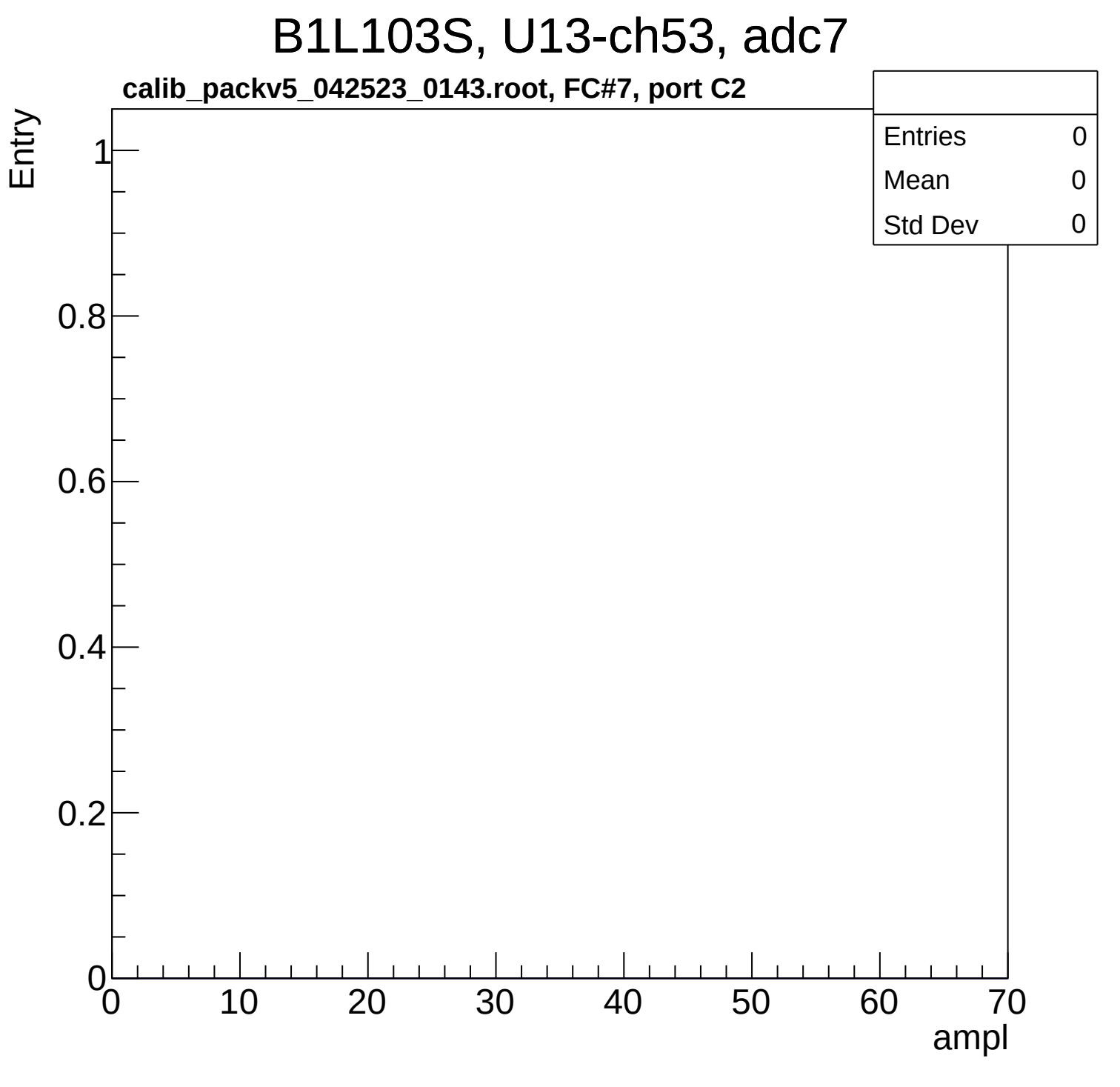
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch53, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 58.59
Std Dev 11.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch53, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch53, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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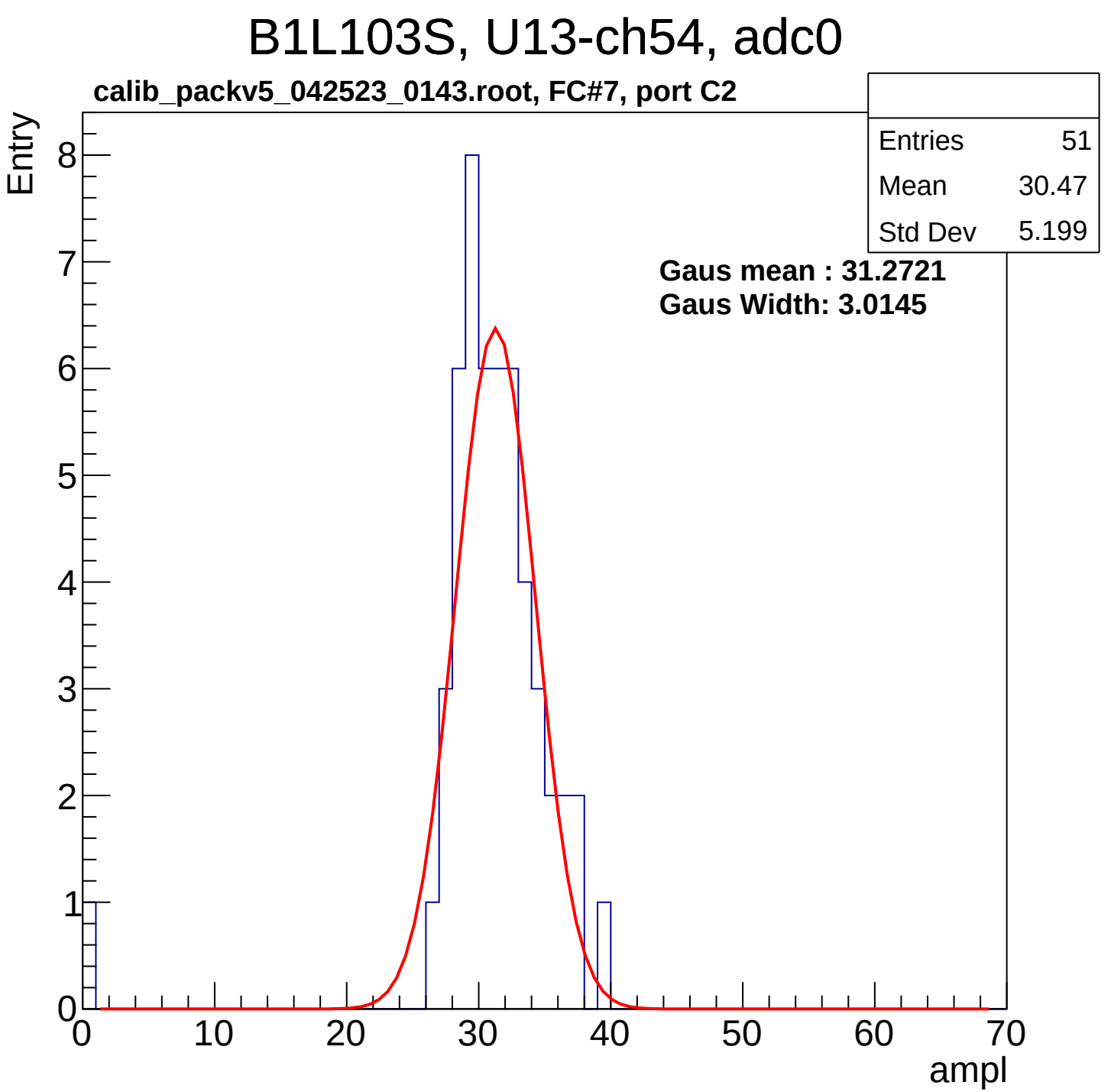
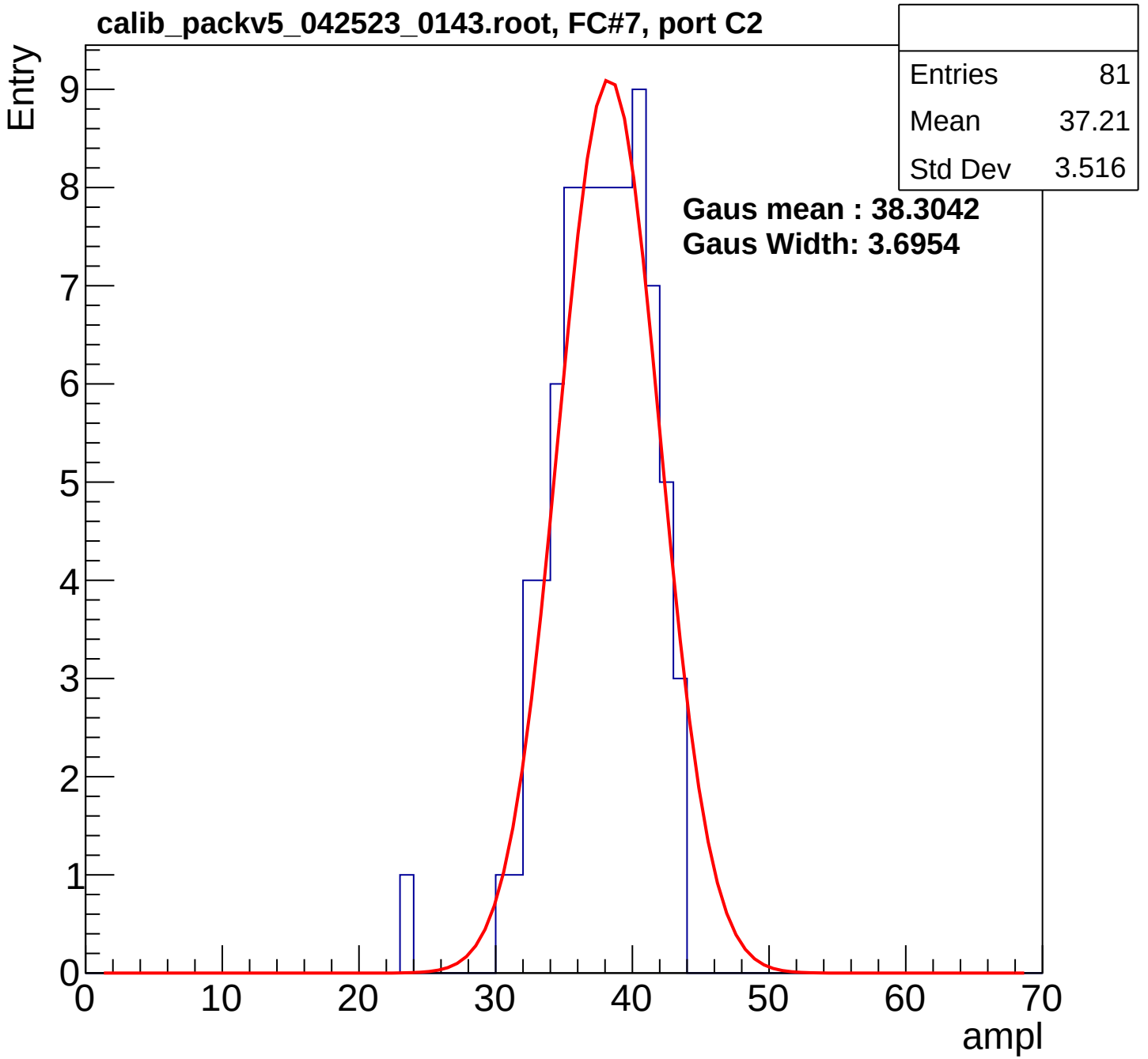
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch54, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 30.47
Std Dev 5.199
Gaus mean : 31.2721
Gaus Width: 3.0145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch54, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 37.21
Std Dev 3.516
Gaus mean : 38.3042
Gaus Width: 3.6954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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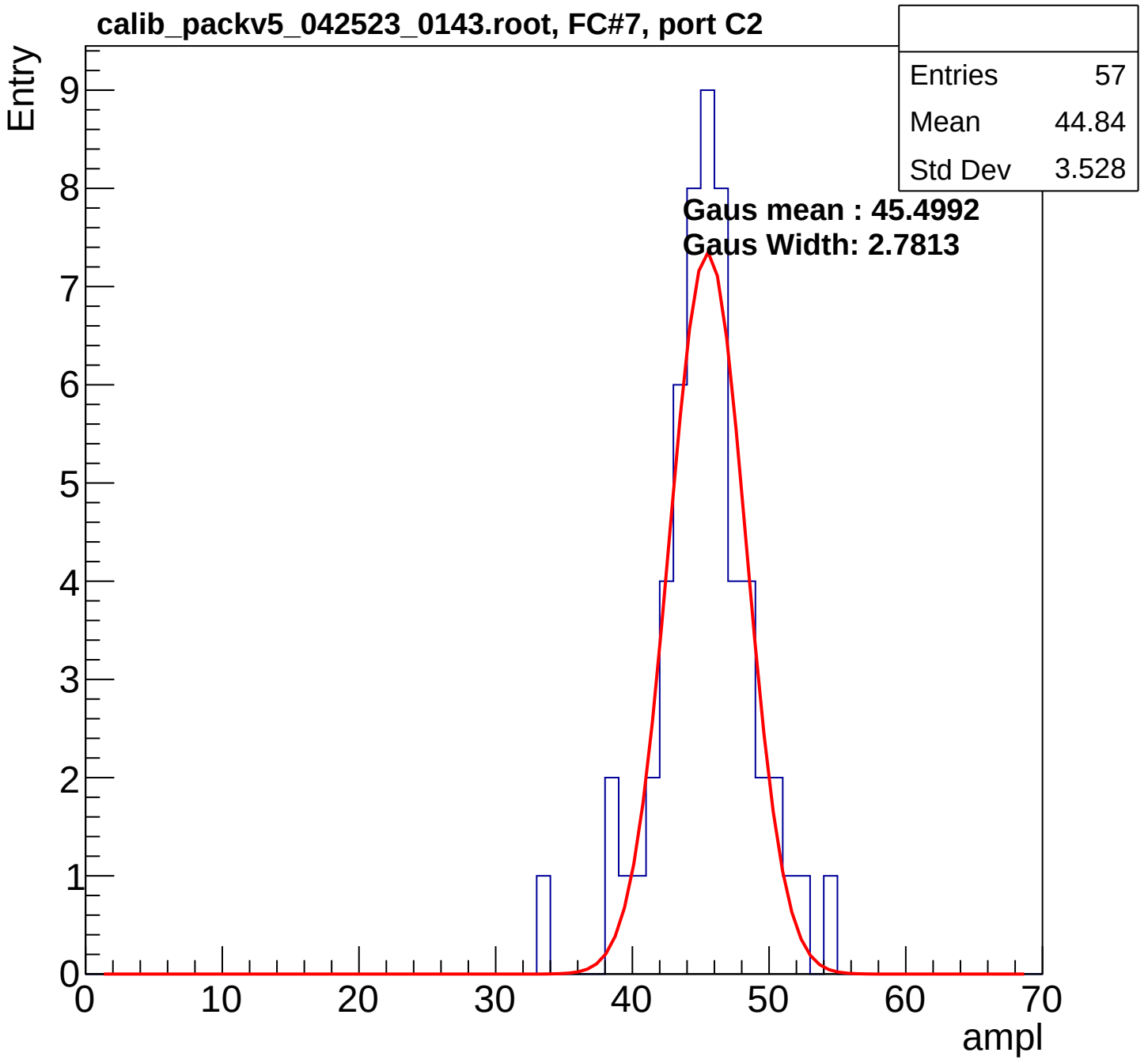
B1L103S, U13-ch54, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 44.84
Std Dev 3.528
Gaus mean : 45.4992
Gaus Width: 2.7813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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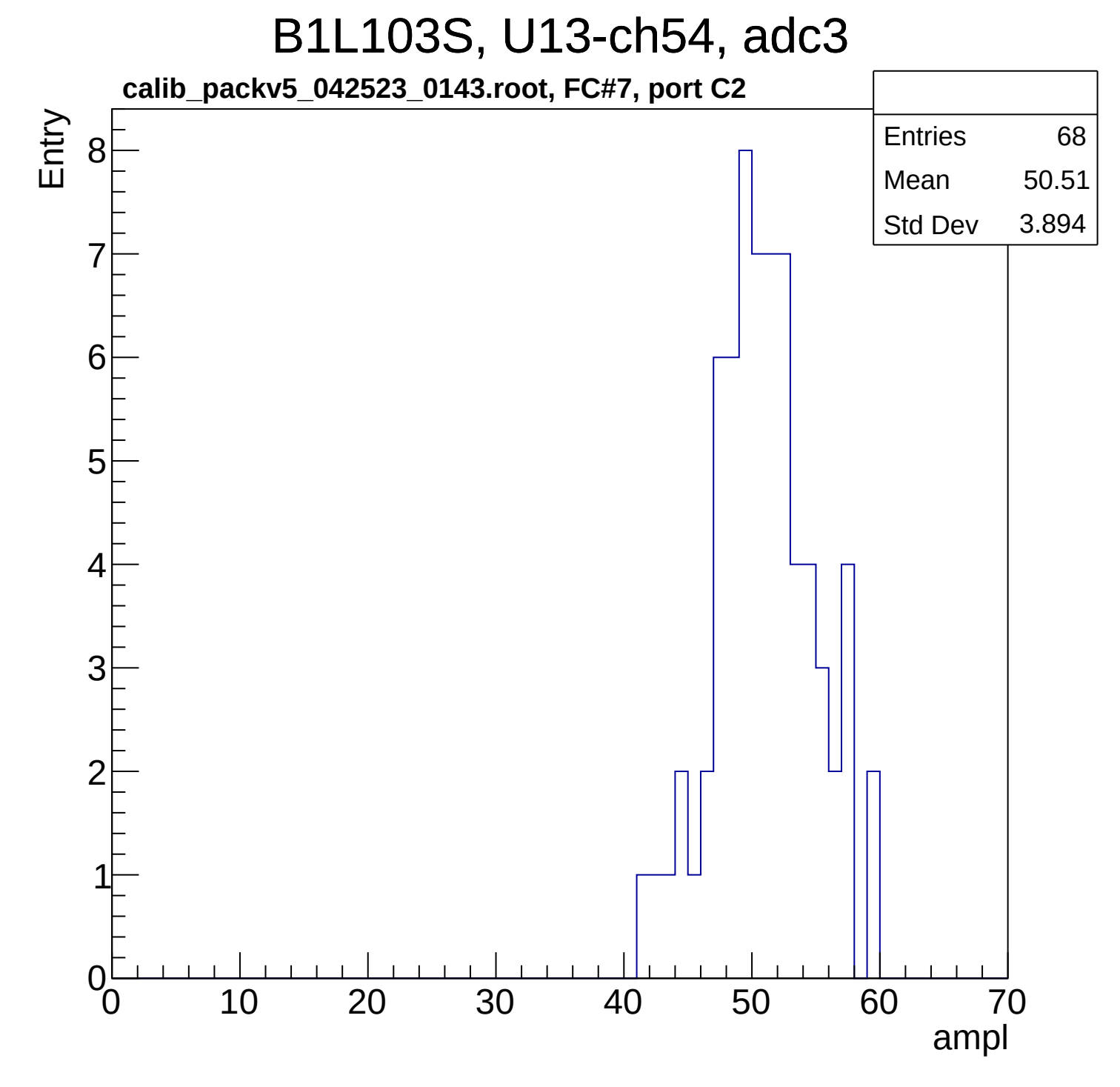
B1L103S, U13-ch54, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 50.51
Std Dev 3.894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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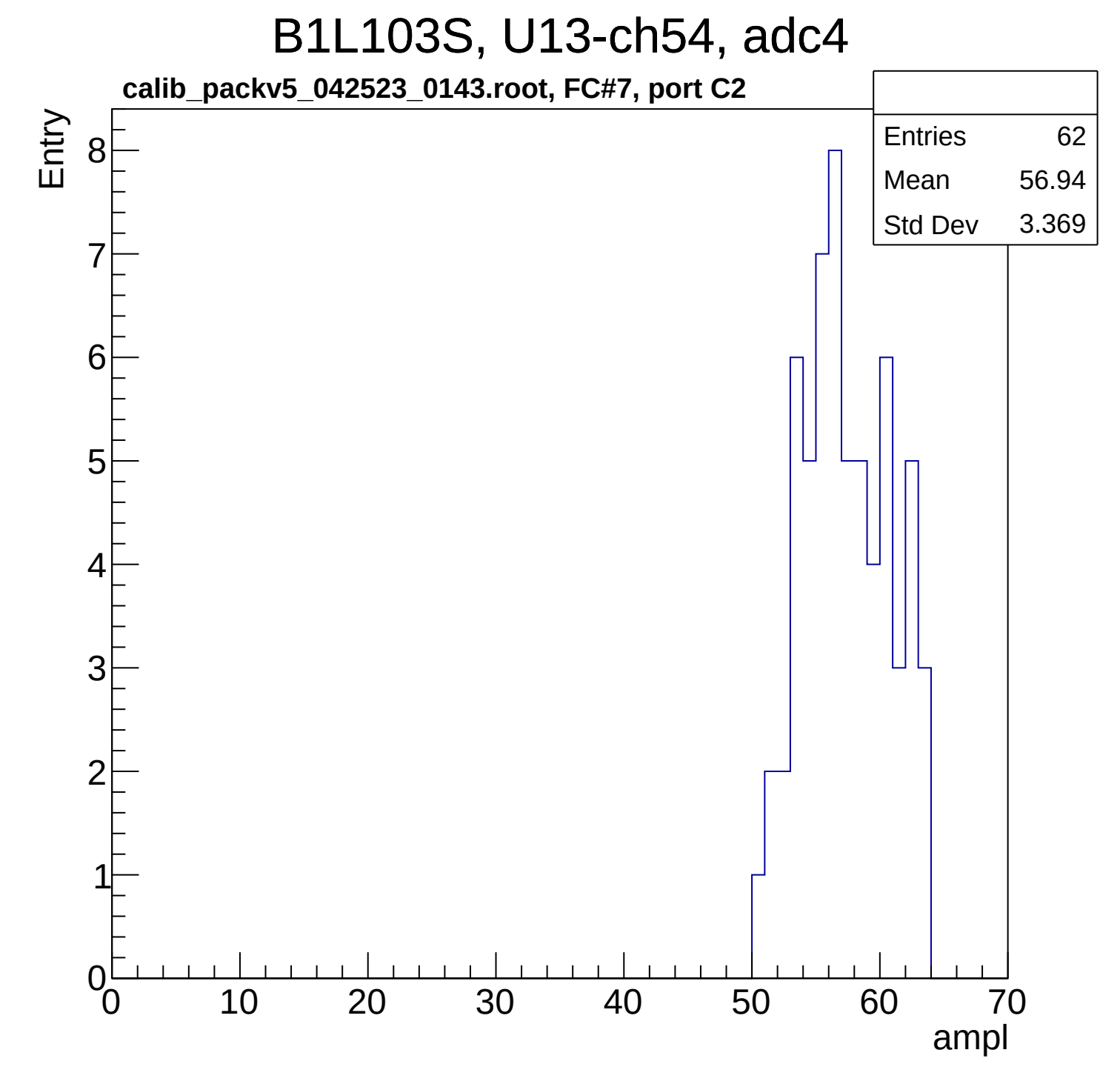
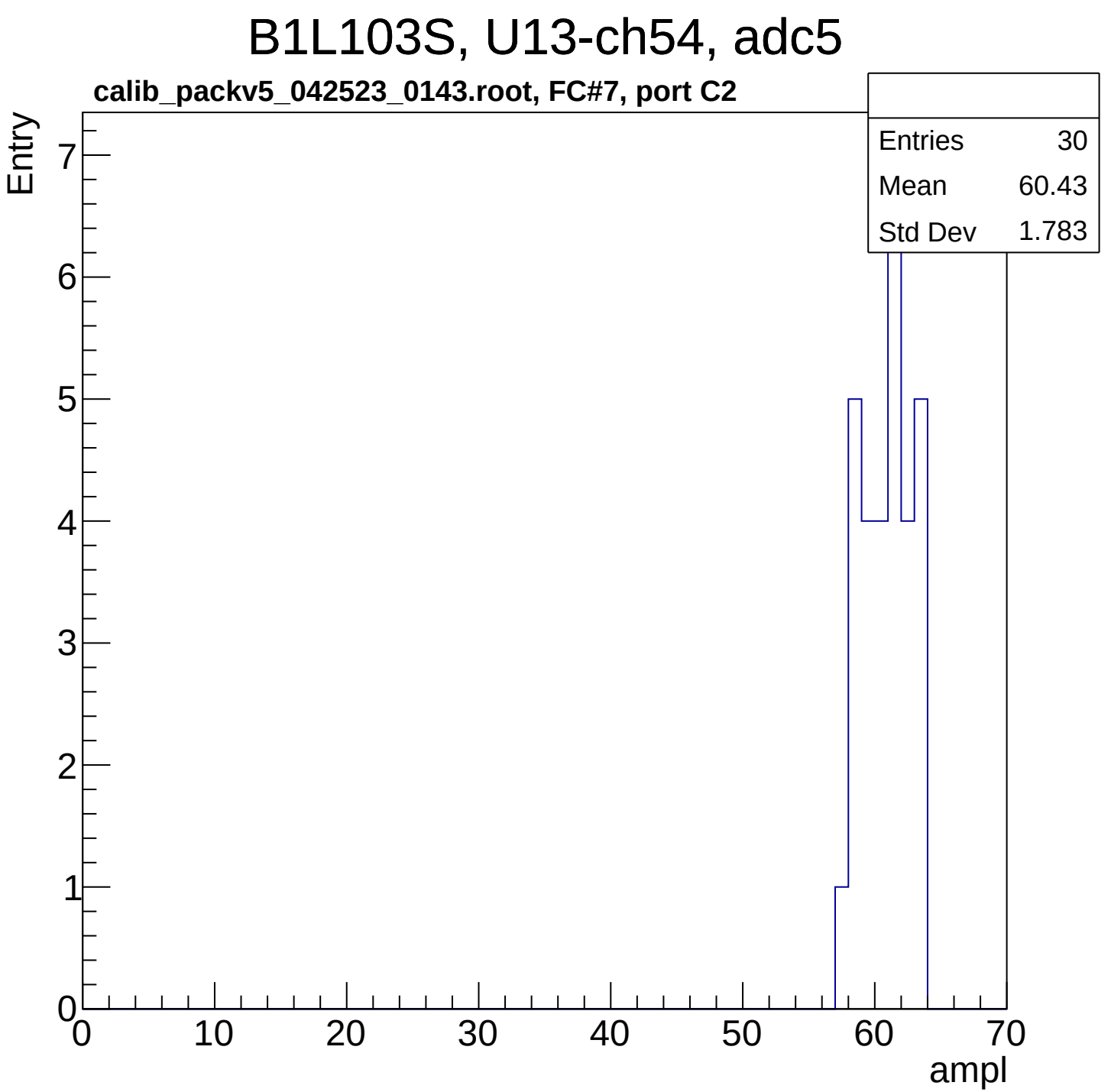
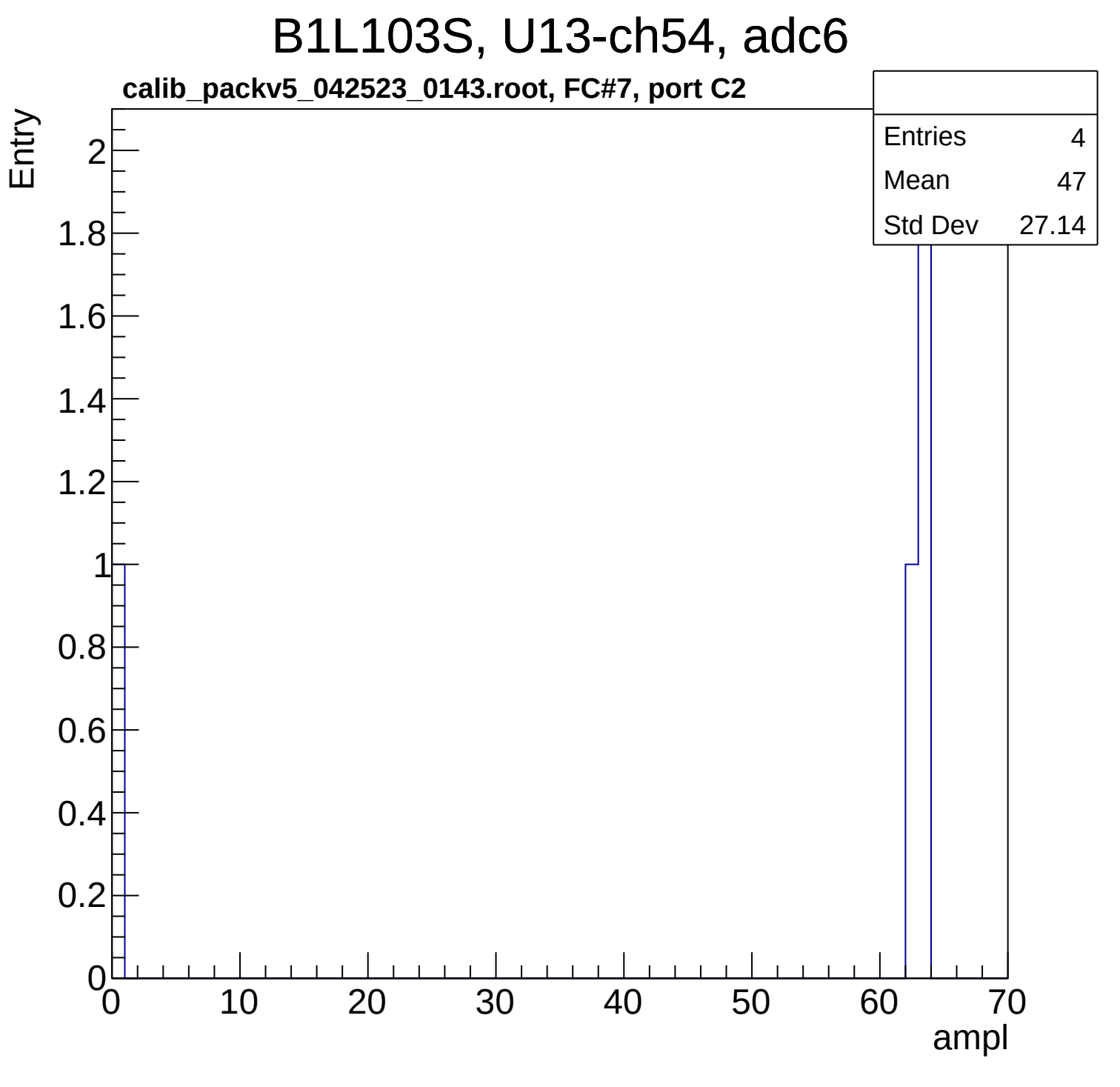

B1L103S, U13-ch54, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 56.94
Std Dev 3.369
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch54, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 60.43
Std Dev 1.783
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch54, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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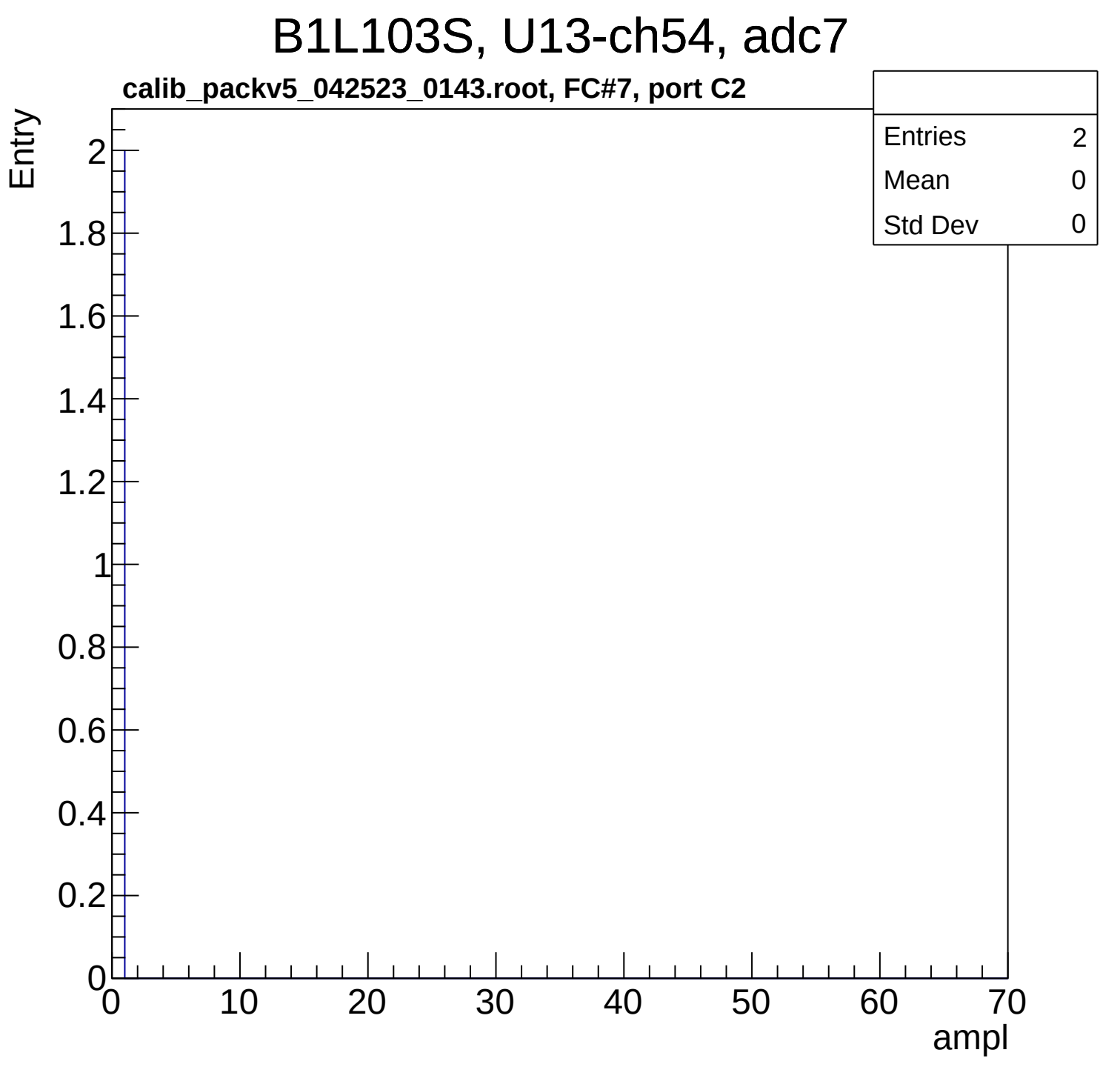
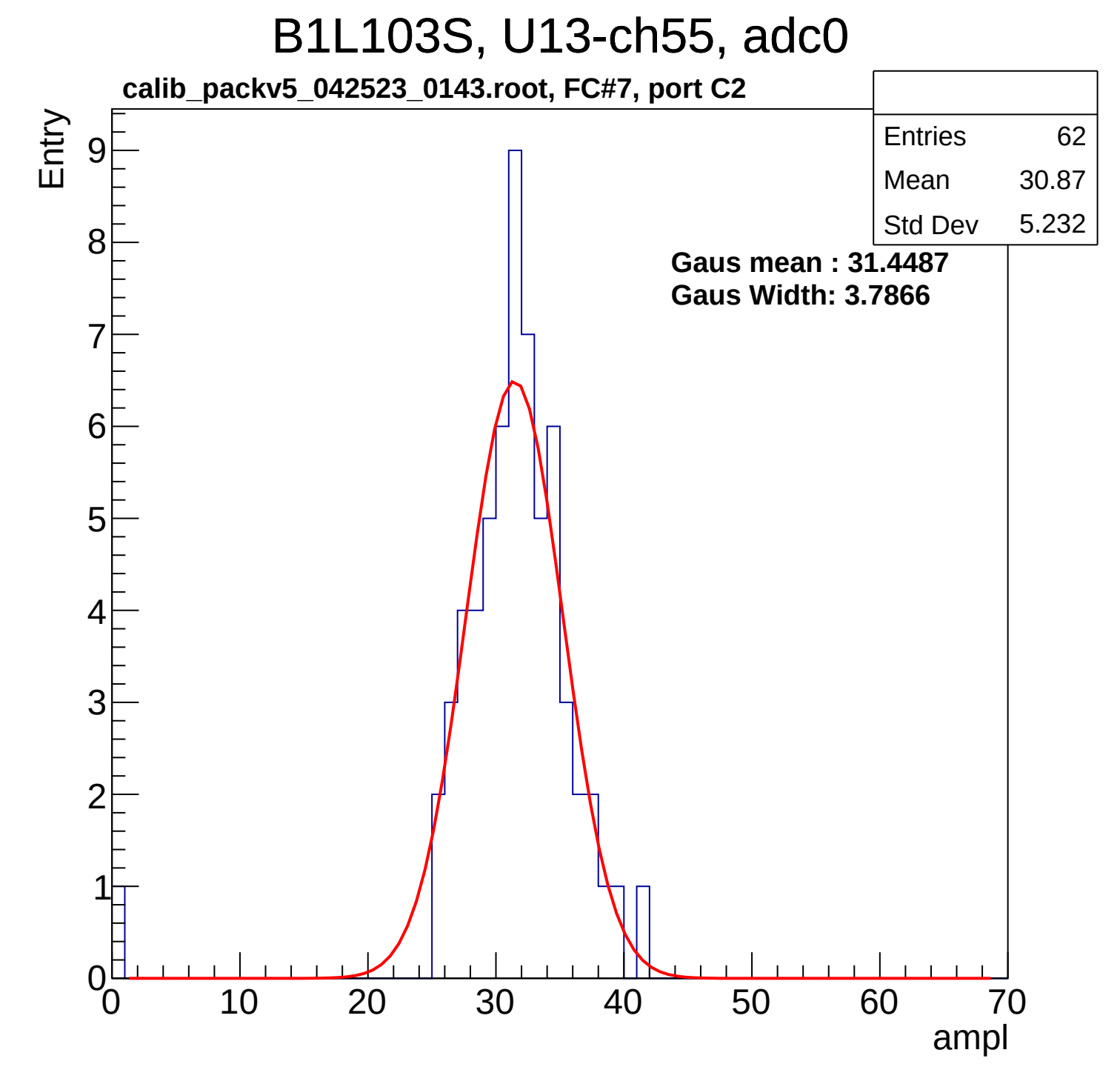

B1L103S, U13-ch54, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch55, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 30.87
Std Dev 5.232
Gaus mean : 31.4487
Gaus Width: 3.7866
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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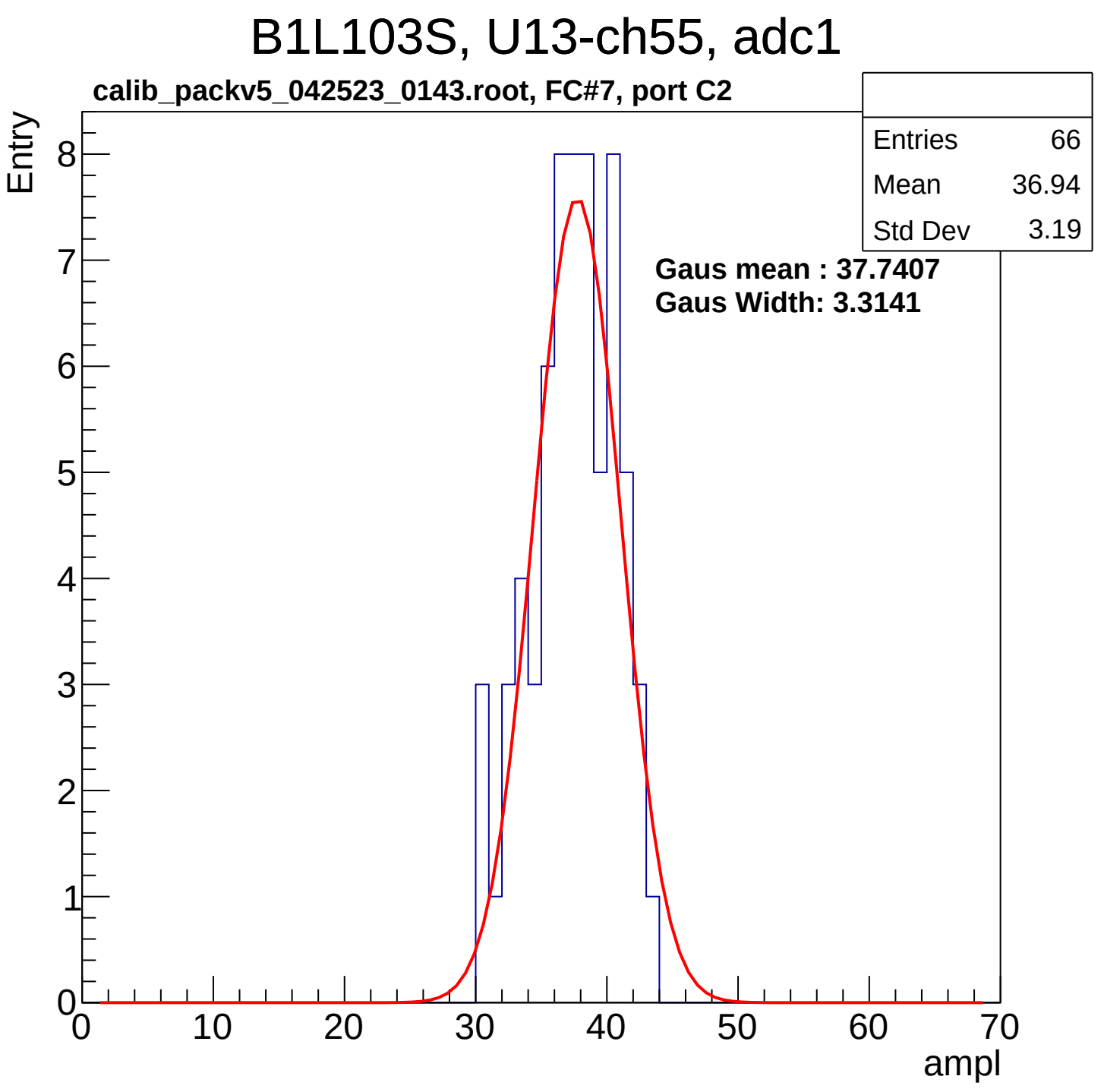
B1L103S, U13-ch55, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 36.94
Std Dev 3.19
Gaus mean : 37.7407
Gaus Width: 3.3141
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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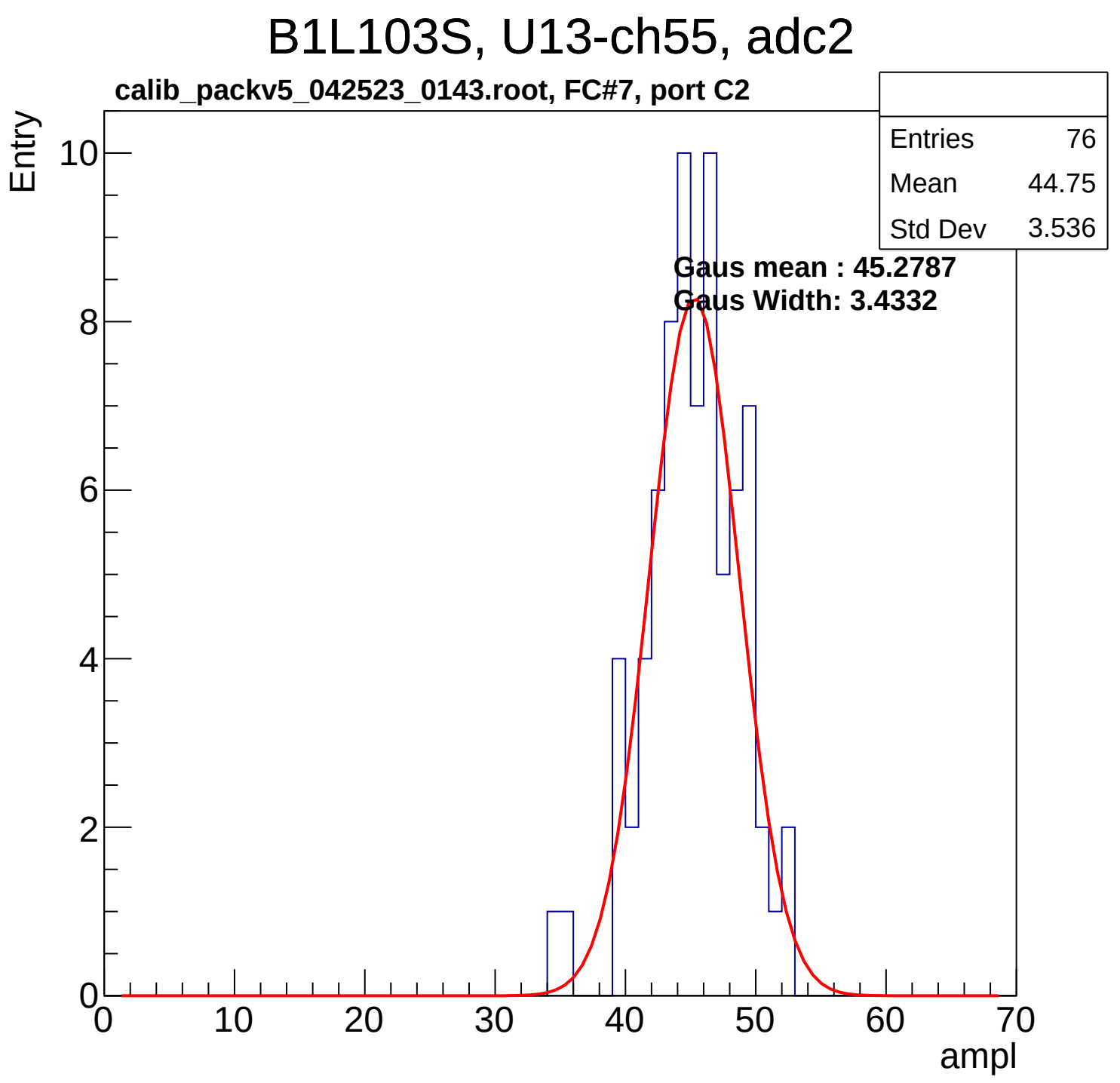
B1L103S, U13-ch55, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 44.75
Std Dev 3.536
Gaus mean : 45.2787
Gaus Width: 3.4332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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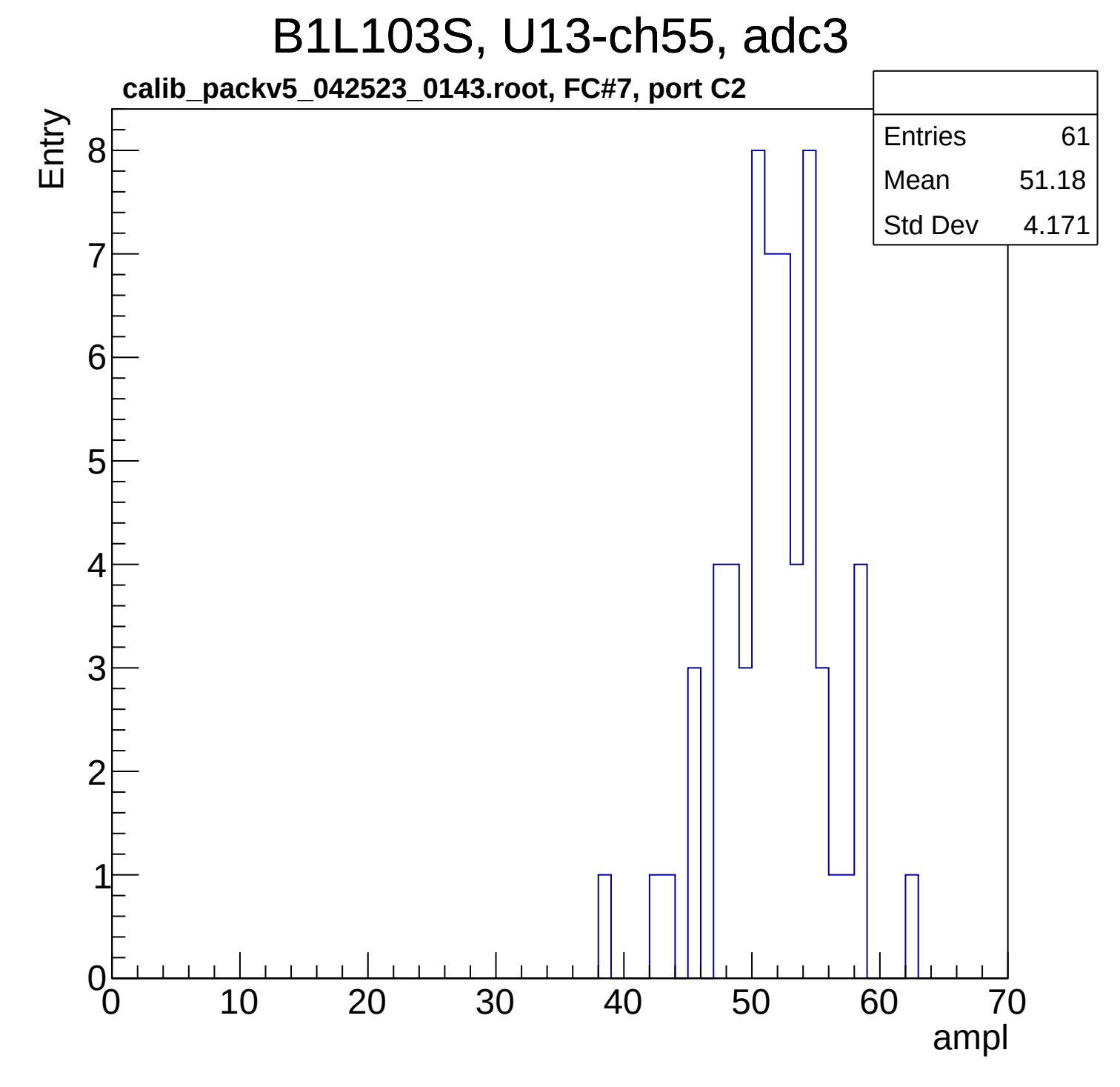
B1L103S, U13-ch55, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 51.18
Std Dev 4.171
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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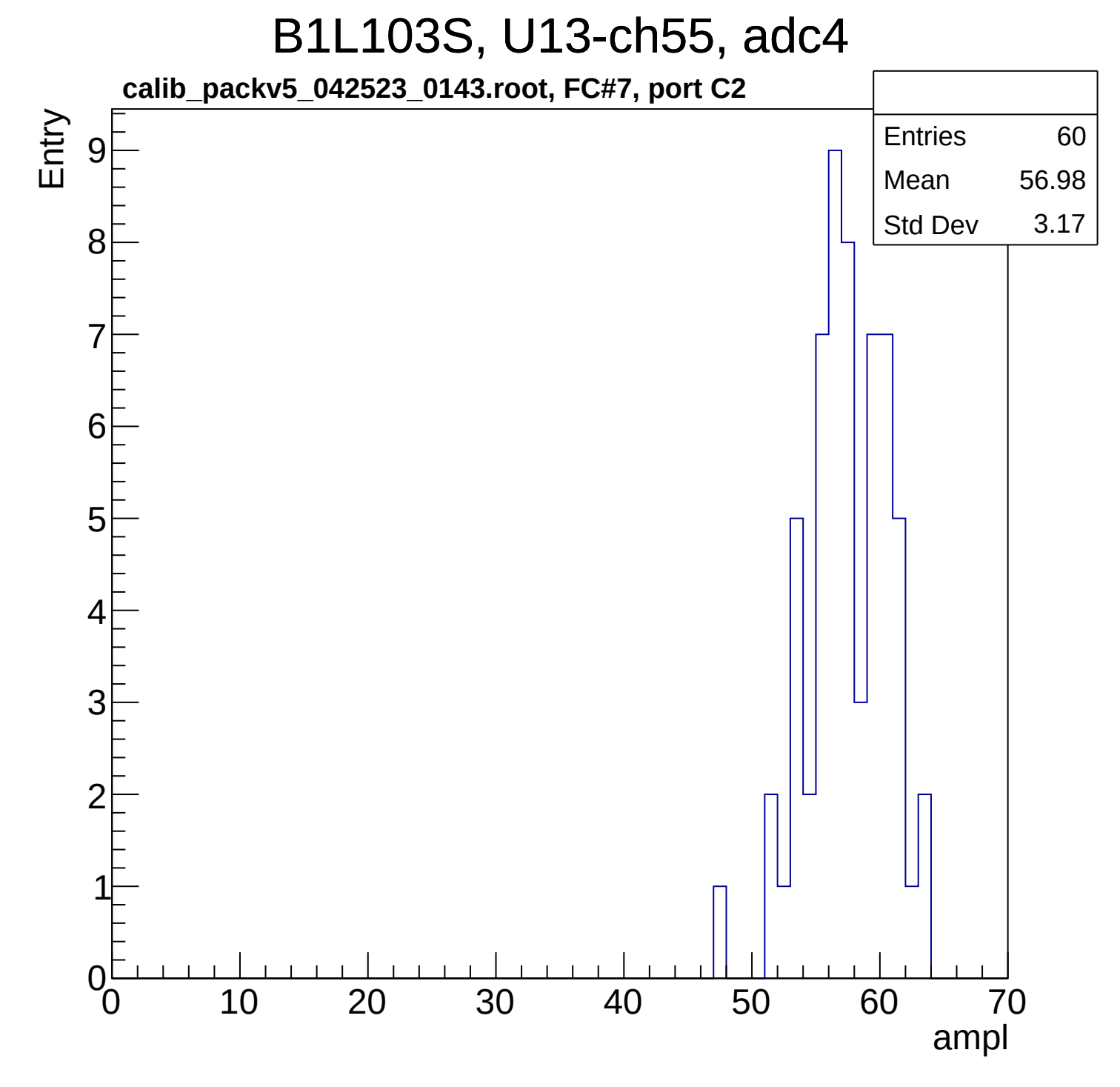
B1L103S, U13-ch55, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.98
Std Dev 3.17
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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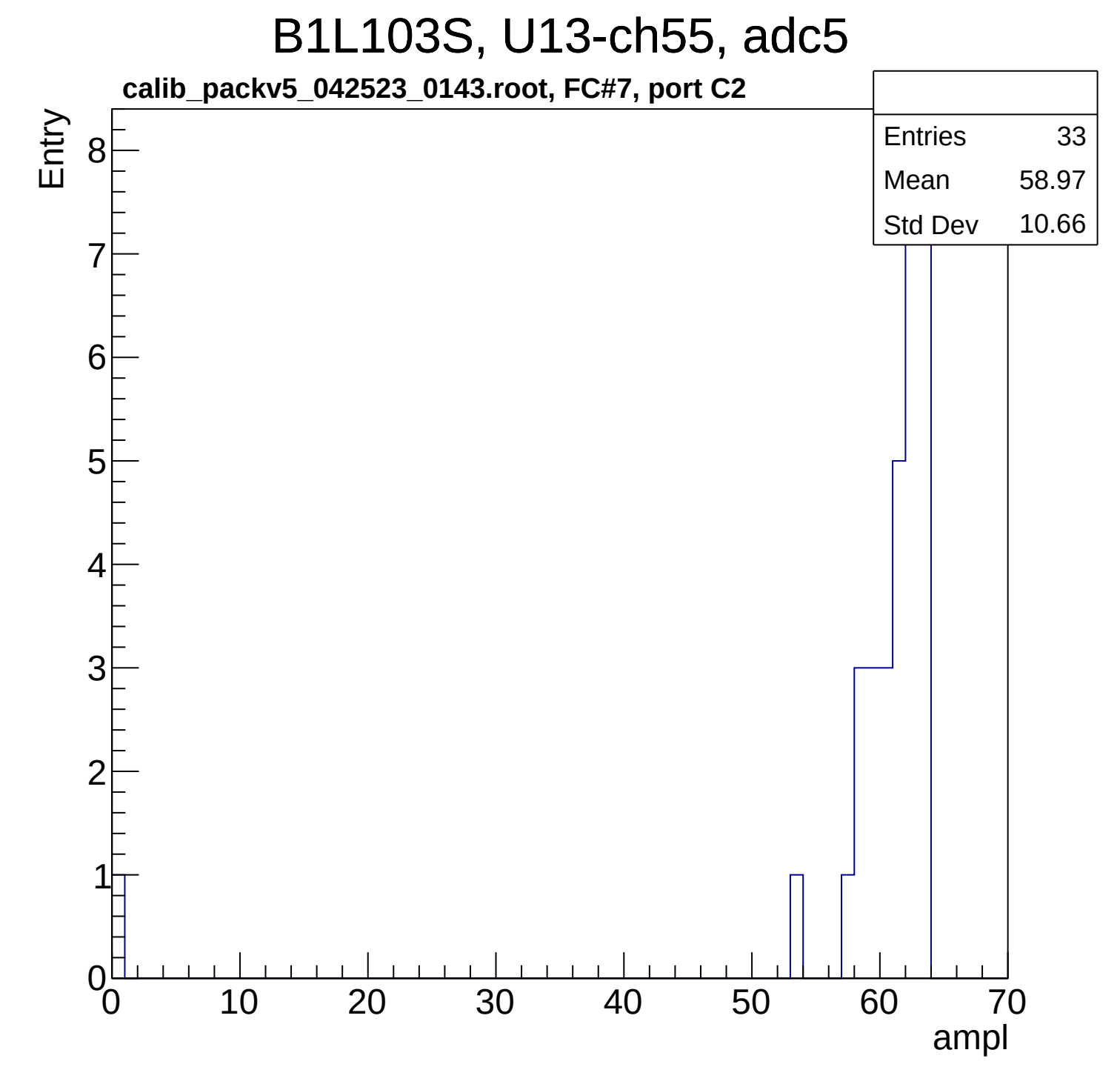
B1L103S, U13-ch55, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.97
Std Dev 10.66
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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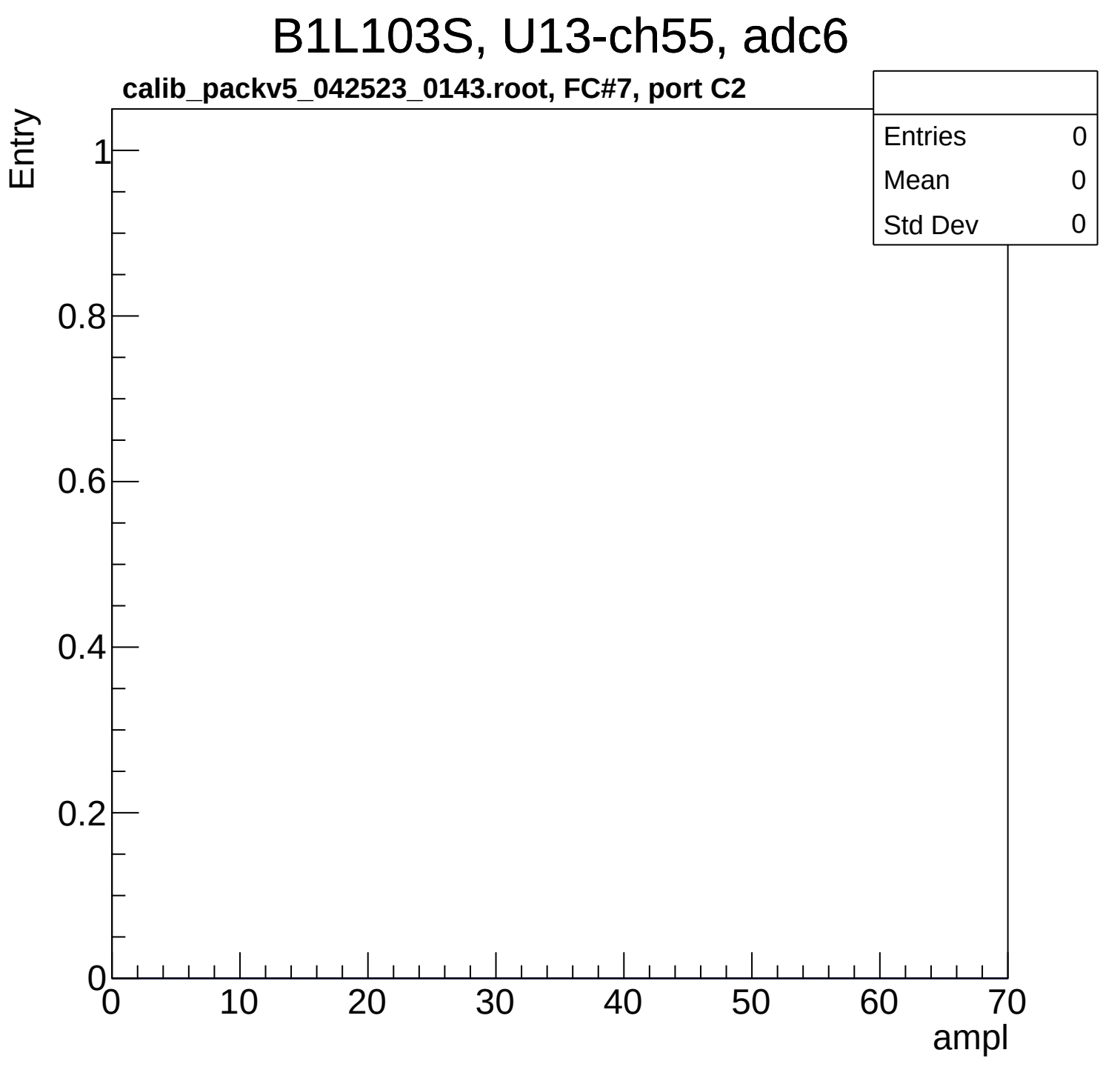
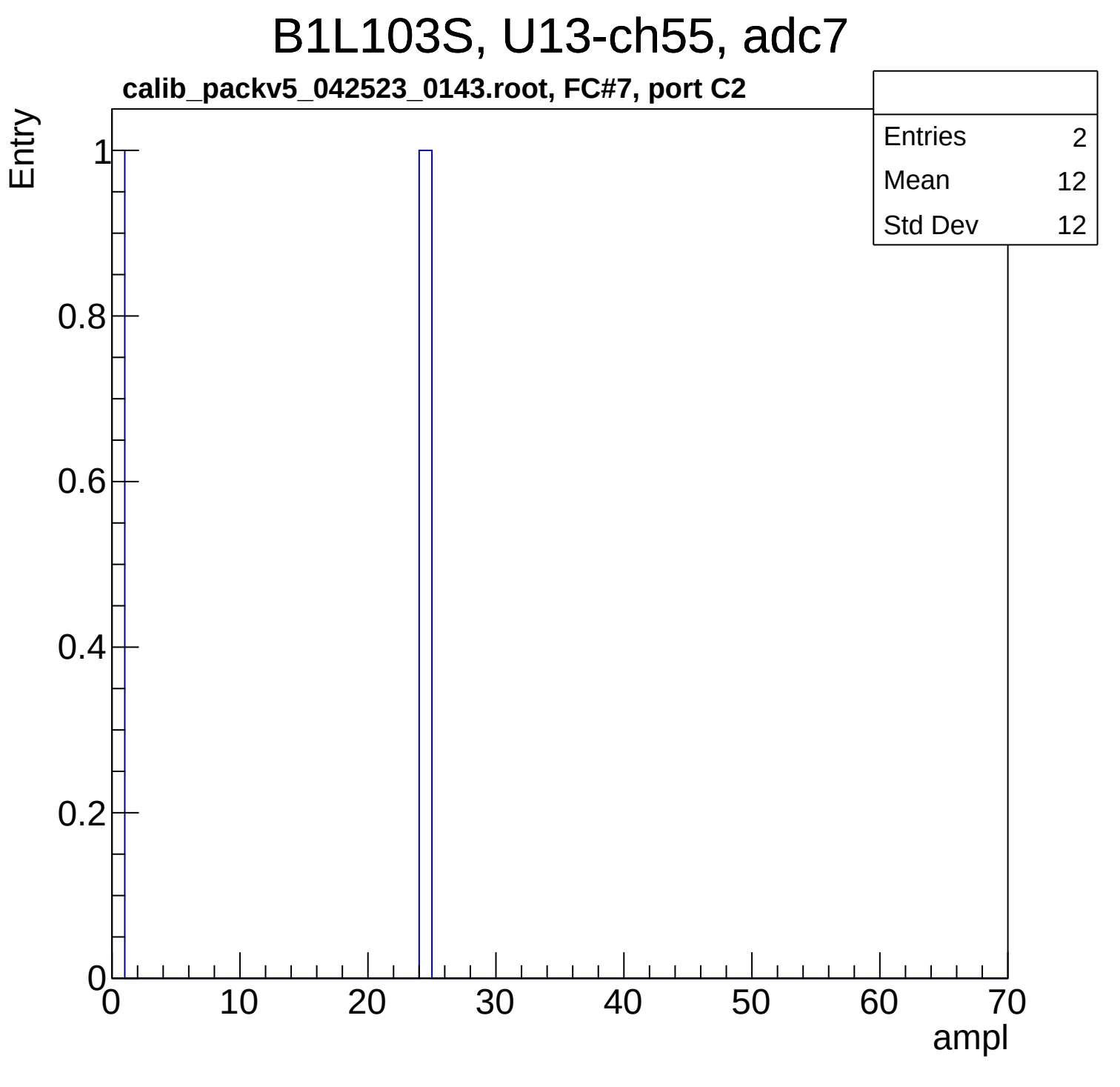

B1L103S, U13-ch55, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch55, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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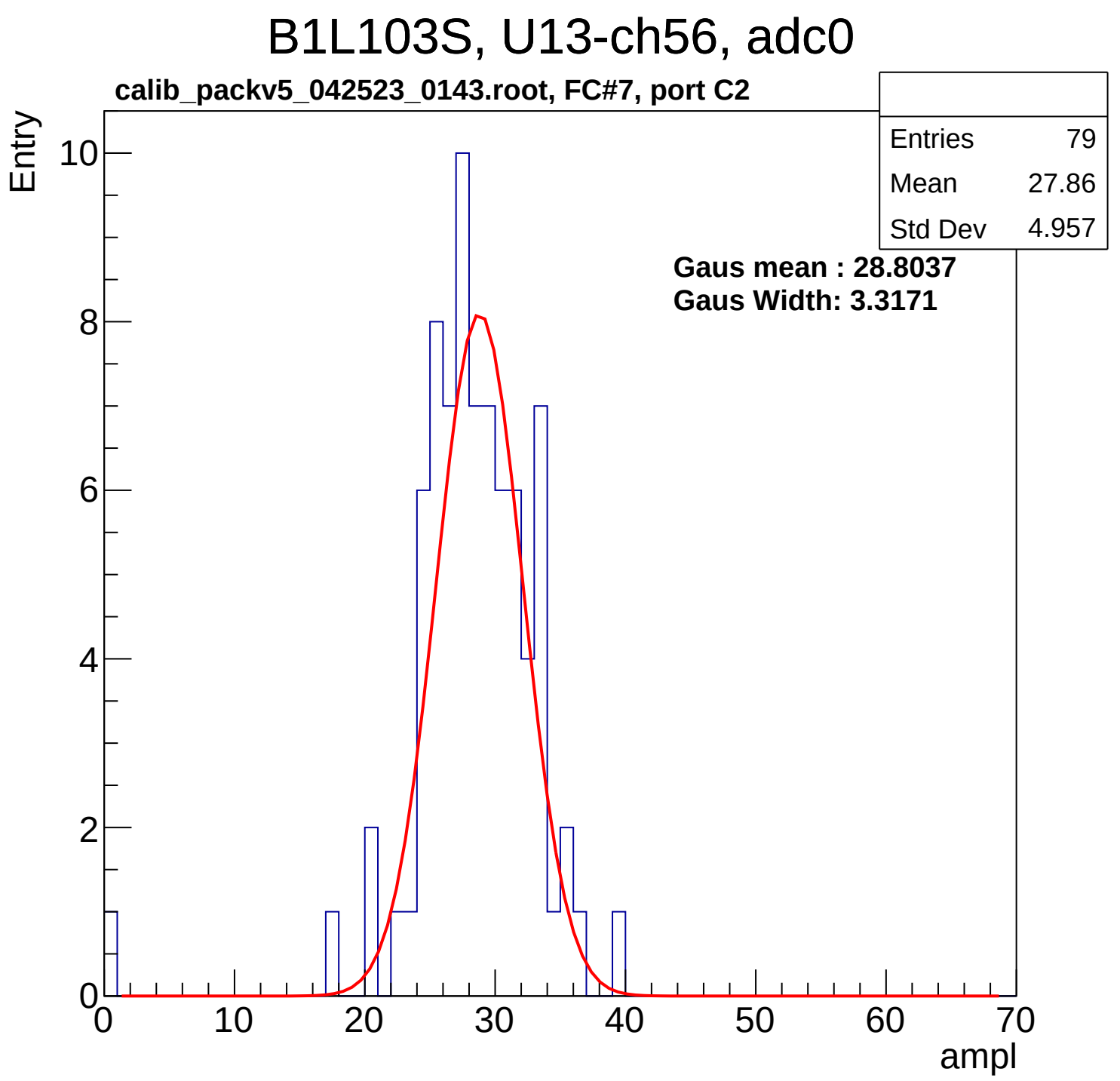
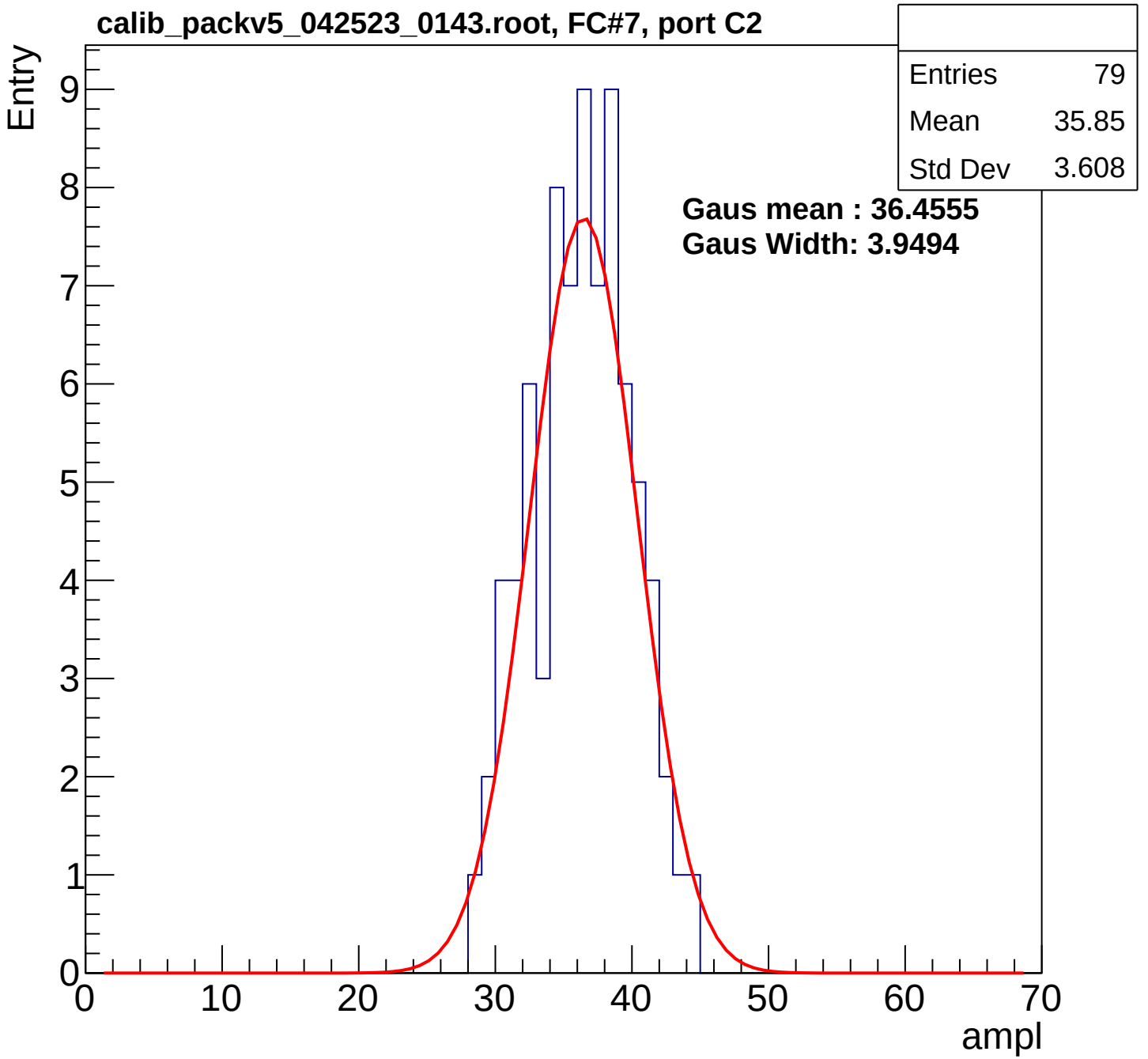
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch56, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 27.86
Std Dev 4.957
Gaus mean : 28.8037
Gaus Width: 3.3171
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch56, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 35.85
Std Dev 3.608
Gaus mean : 36.4555
Gaus Width: 3.9494
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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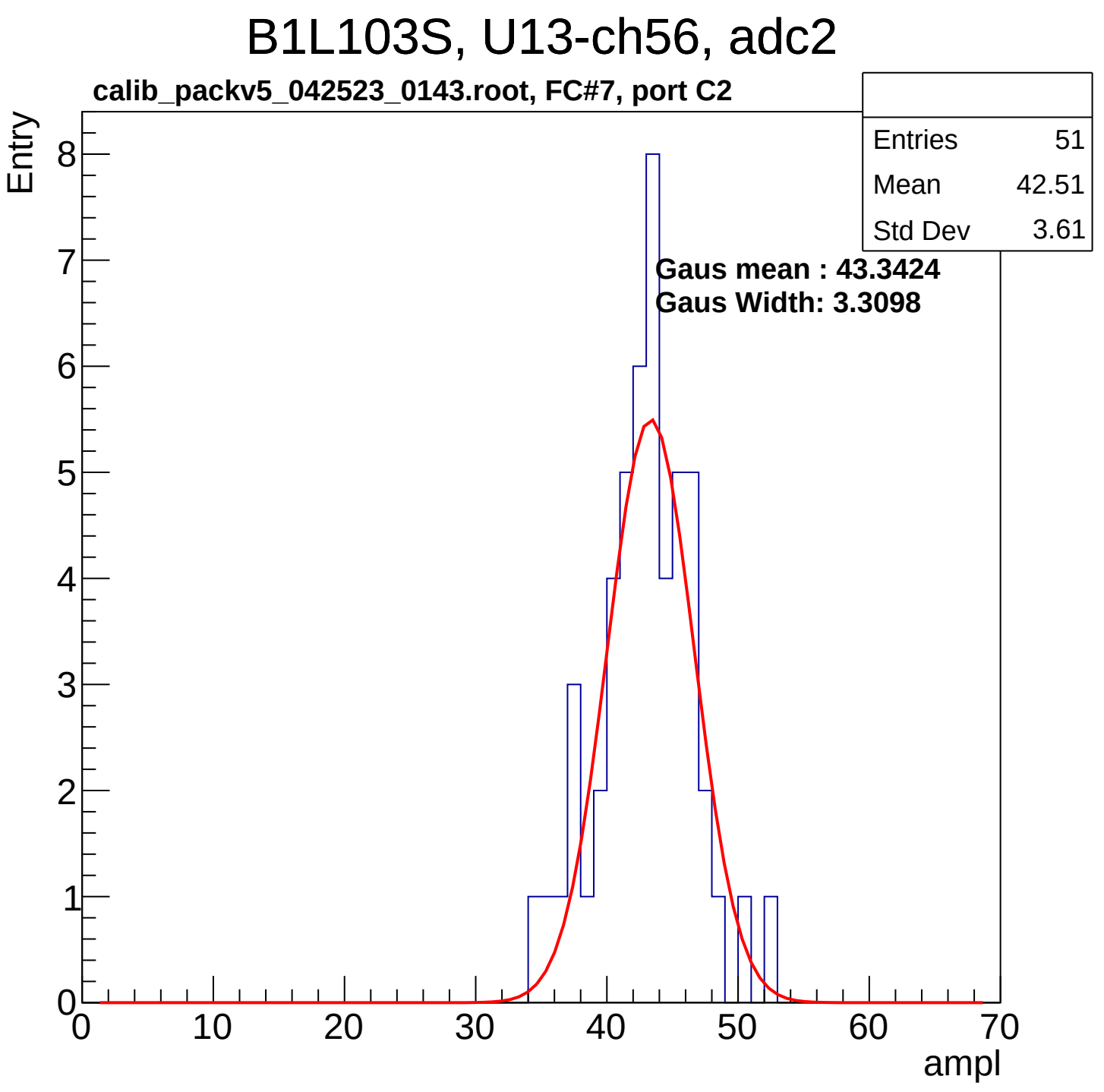
B1L103S, U13-ch56, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 42.51
Std Dev 3.61
Gaus mean : 43.3424
Gaus Width: 3.3098
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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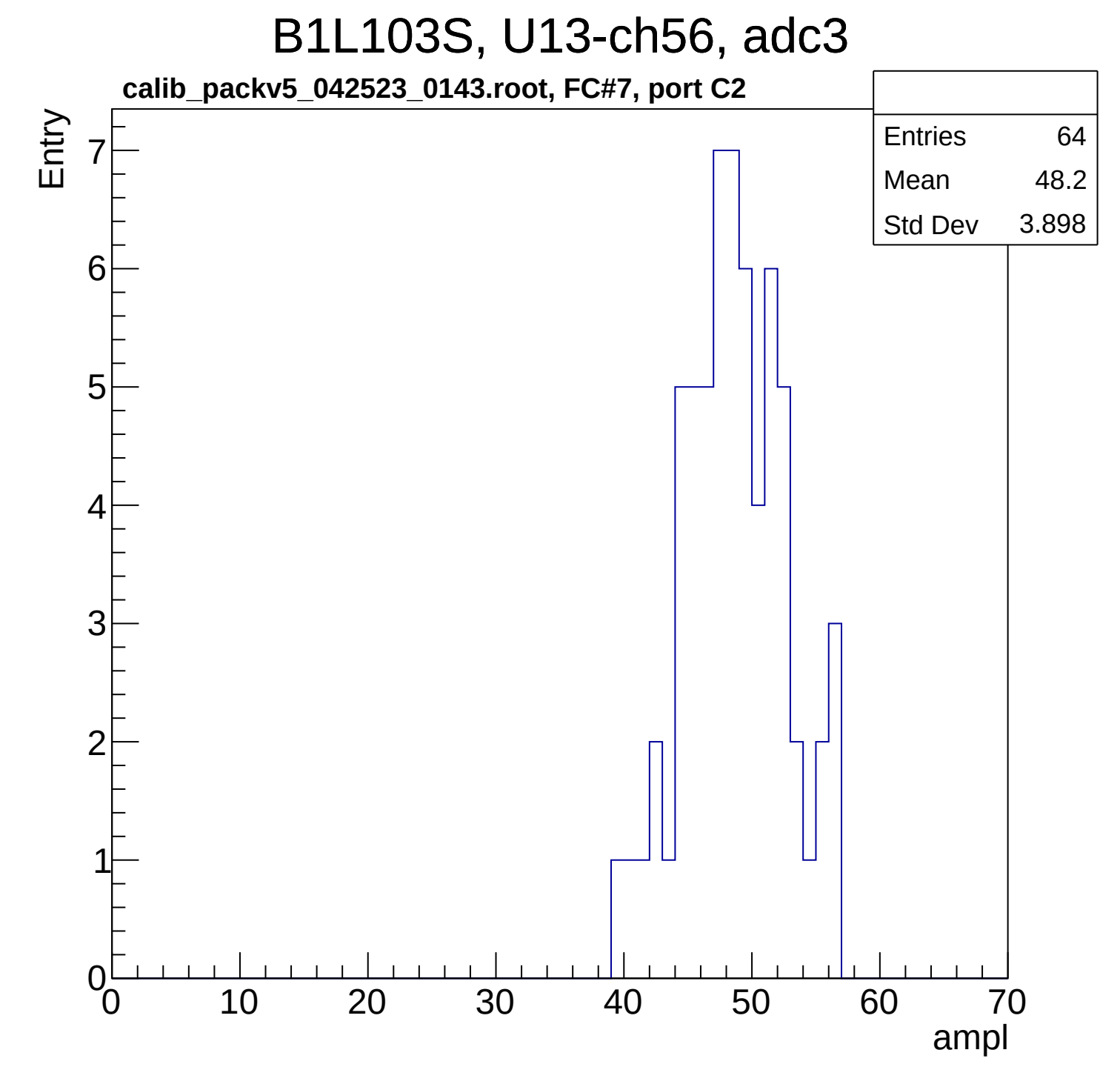
B1L103S, U13-ch56, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.2
Std Dev 3.898
Entry
ampl
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1
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4
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6
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0
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20
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40
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60
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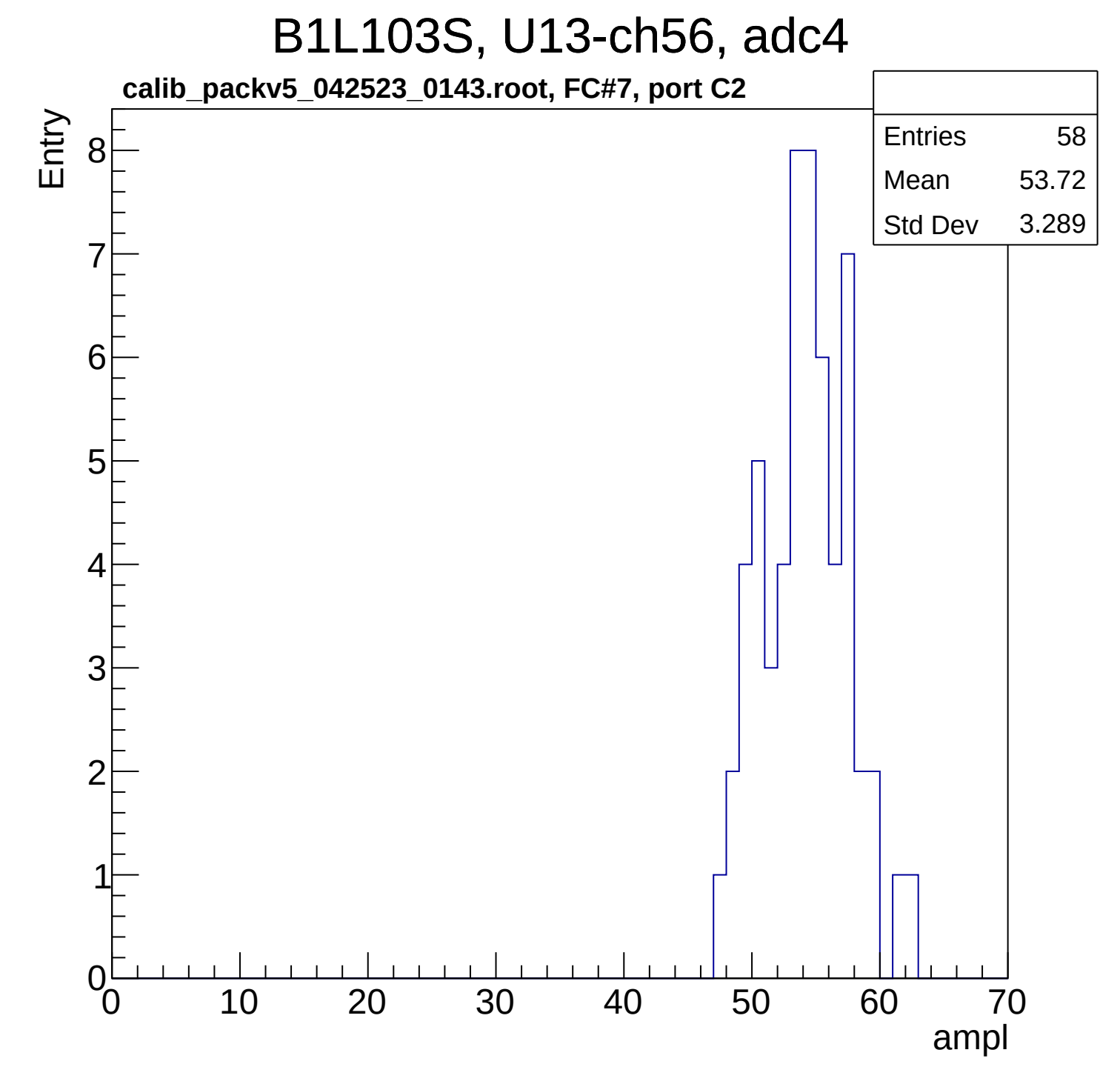
B1L103S, U13-ch56, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53.72
Std Dev 3.289
Entry
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3
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5
6
7
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10
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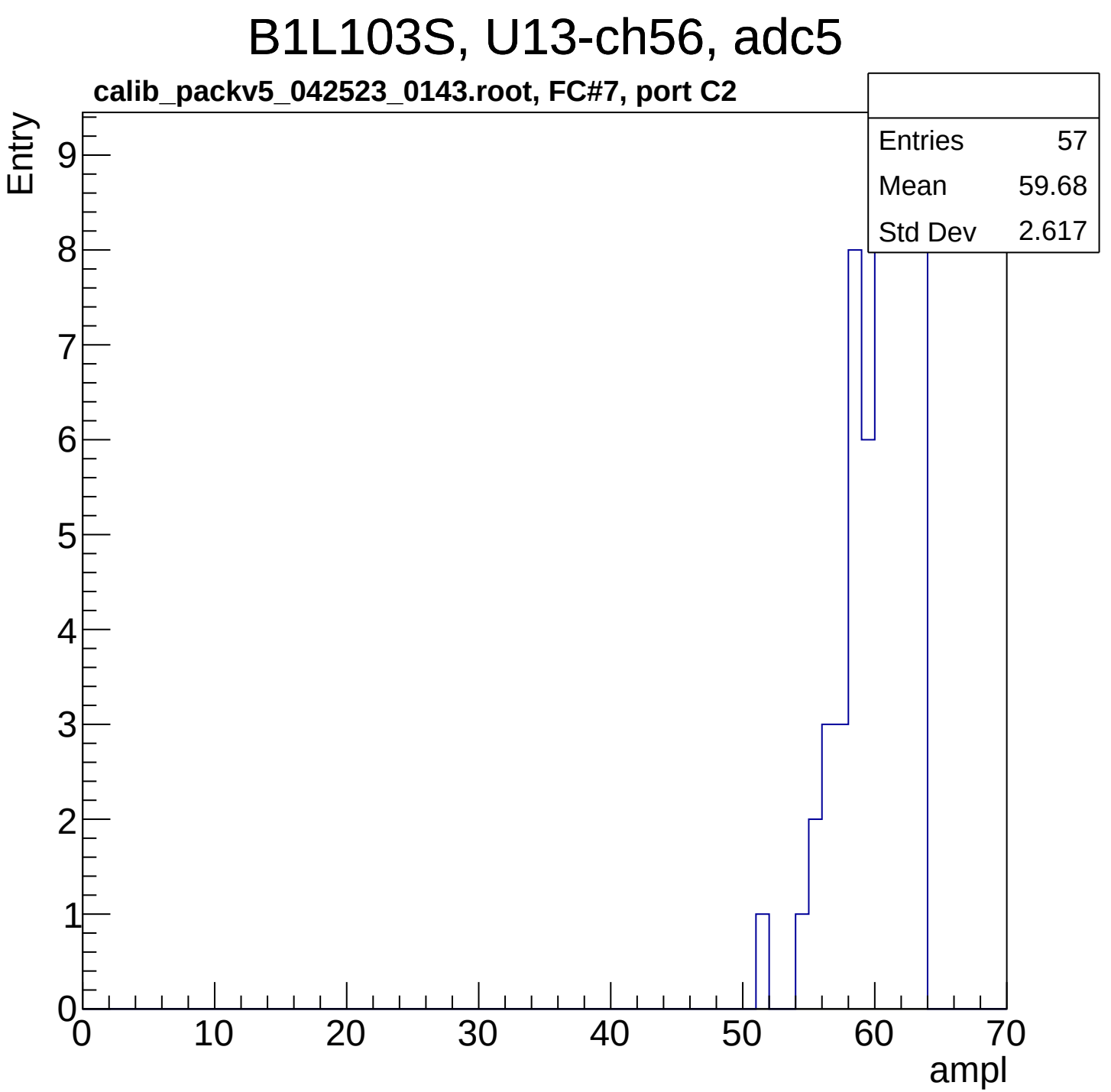
50
60
70
ampl


B1L103S, U13-ch56, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 59.68
Std Dev 2.617
Entry
ampl
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3
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6
7
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0
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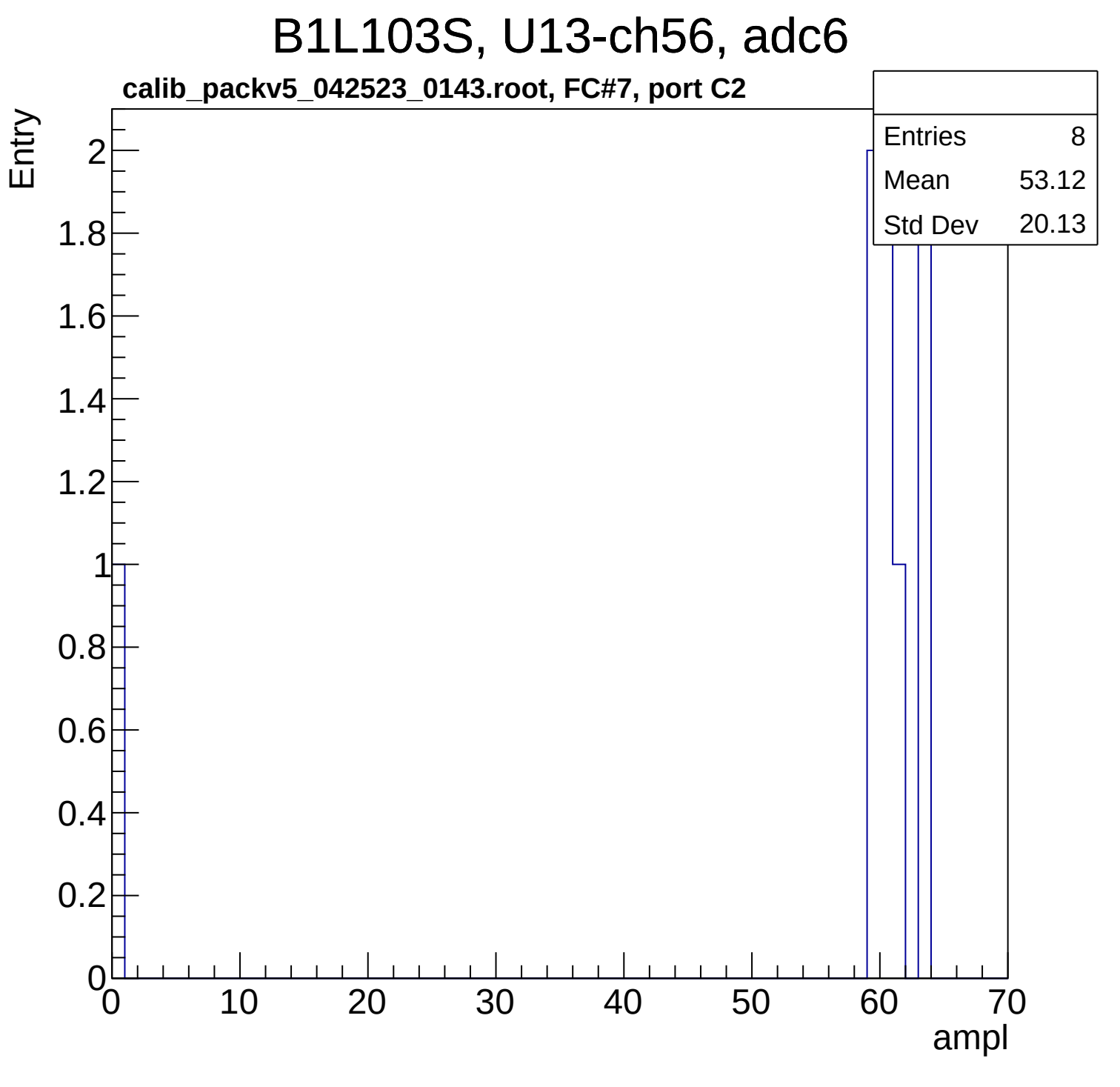
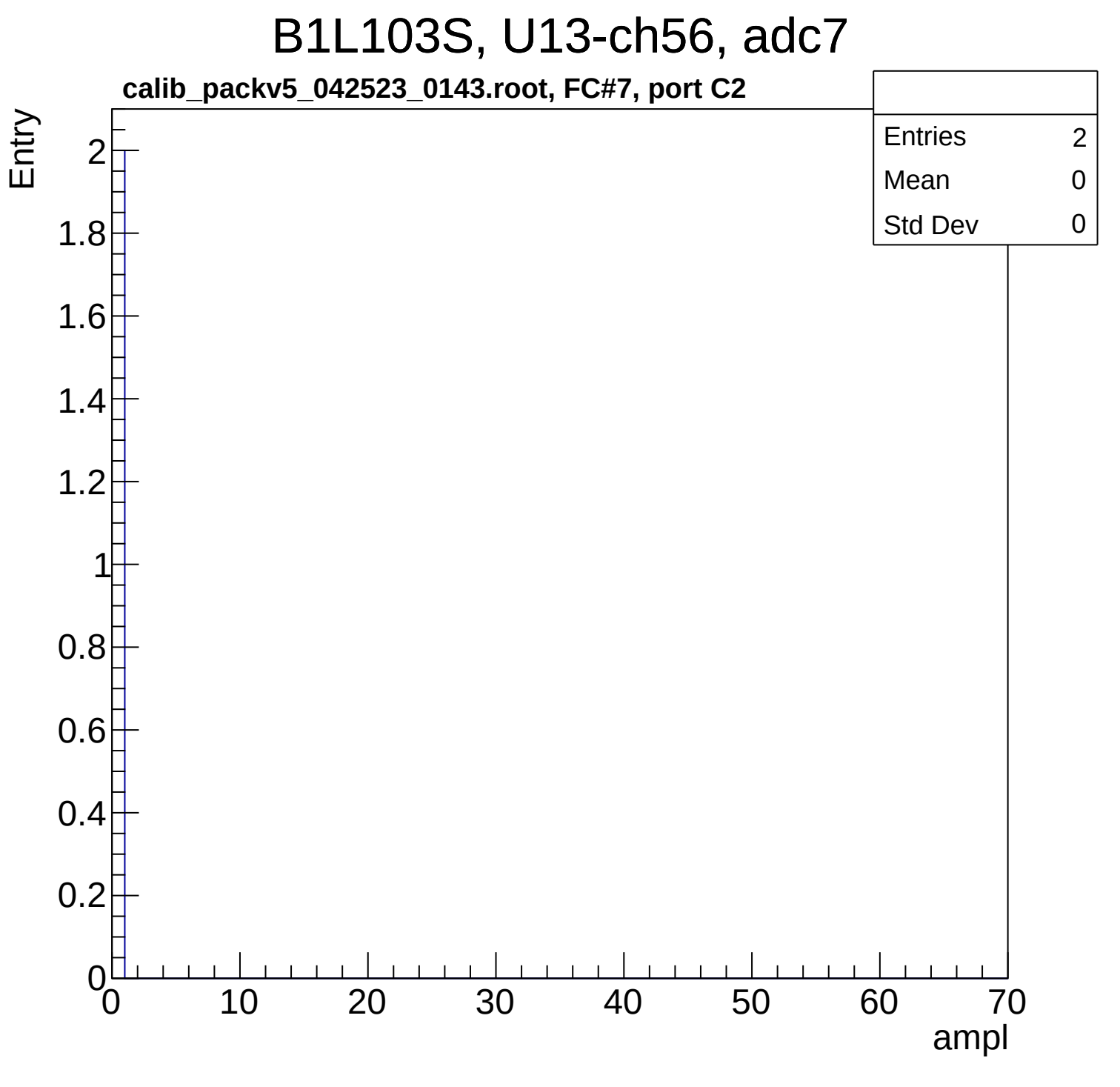

B1L103S, U13-ch56, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 53.12
Std Dev 20.13
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch56, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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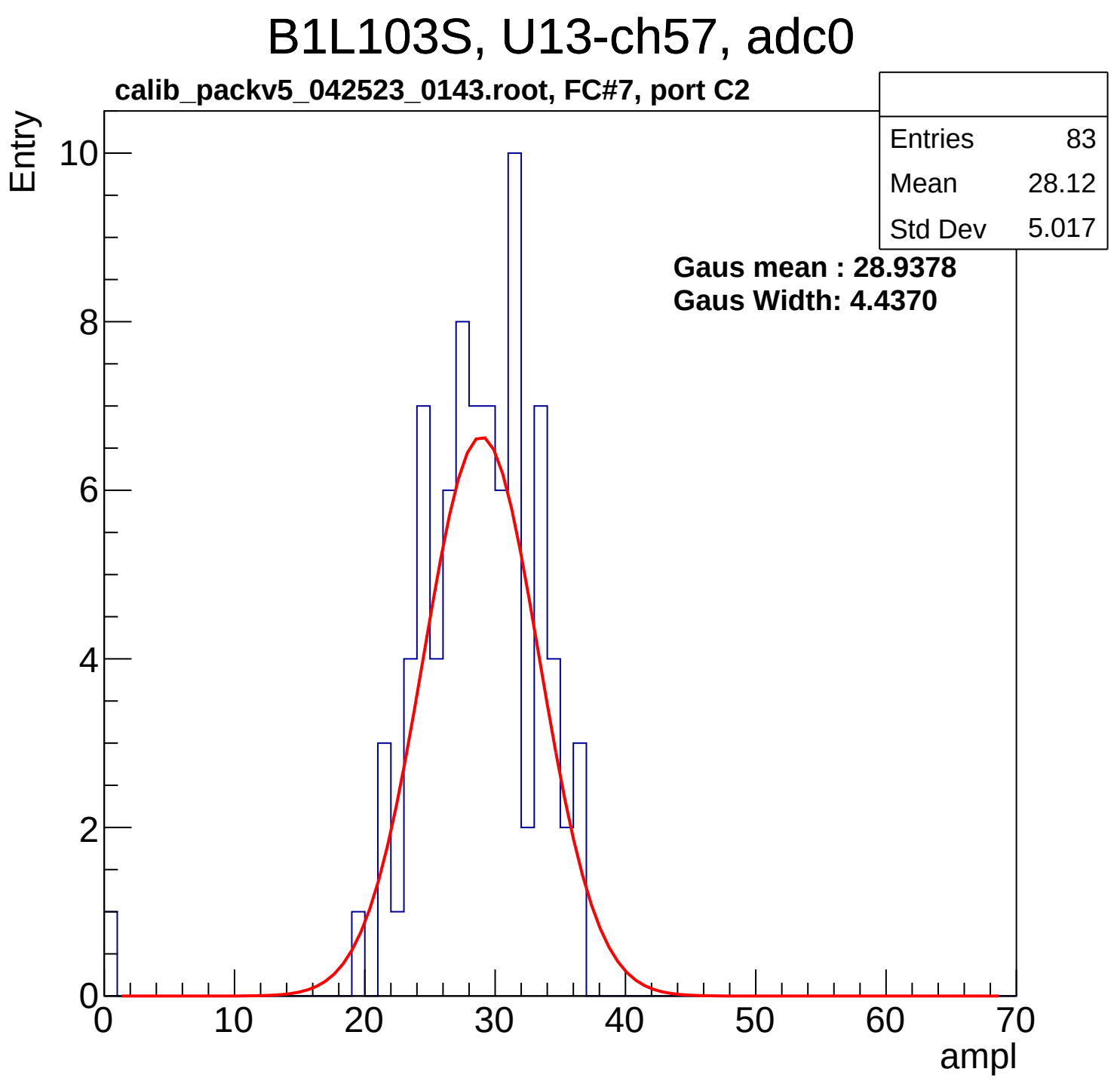
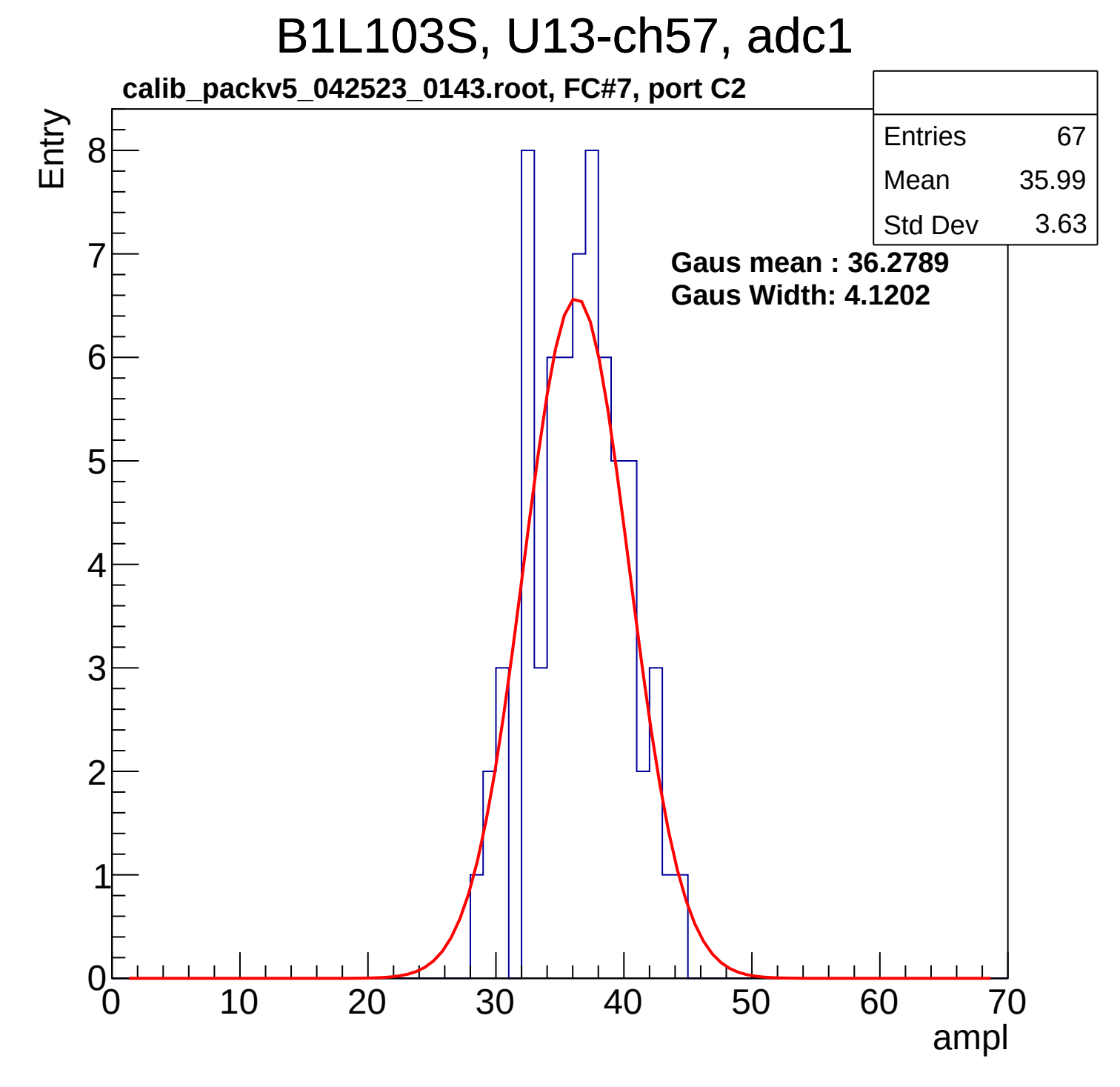
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch57, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 28.12
Std Dev 5.017
Gaus mean : 28.9378
Gaus Width: 4.4370
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch57, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.99
Std Dev 3.63
Gaus mean : 36.2789
Gaus Width: 4.1202
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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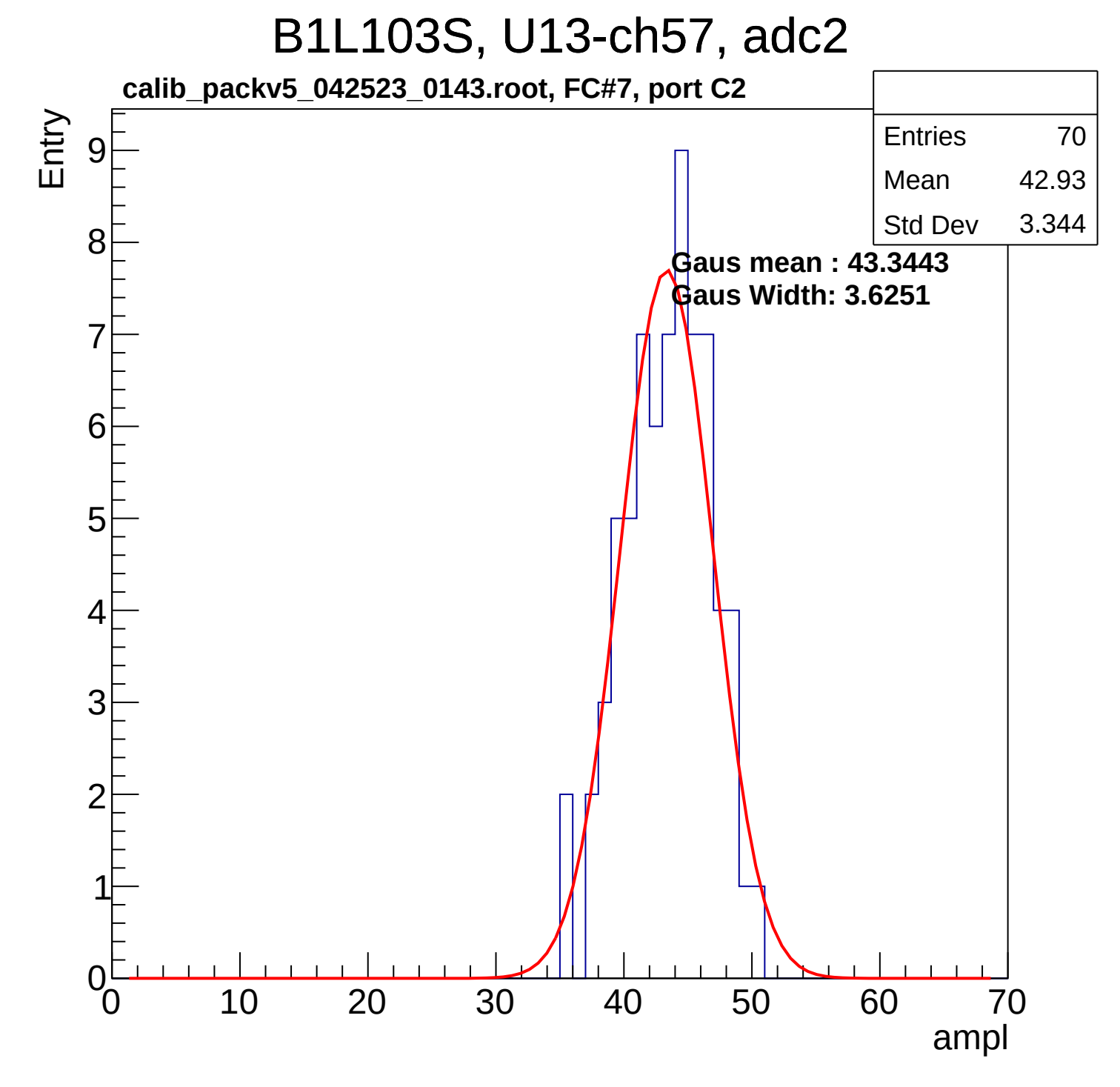
B1L103S, U13-ch57, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.93
Std Dev 3.344
Gaus mean : 43.3443
Gaus Width: 3.6251
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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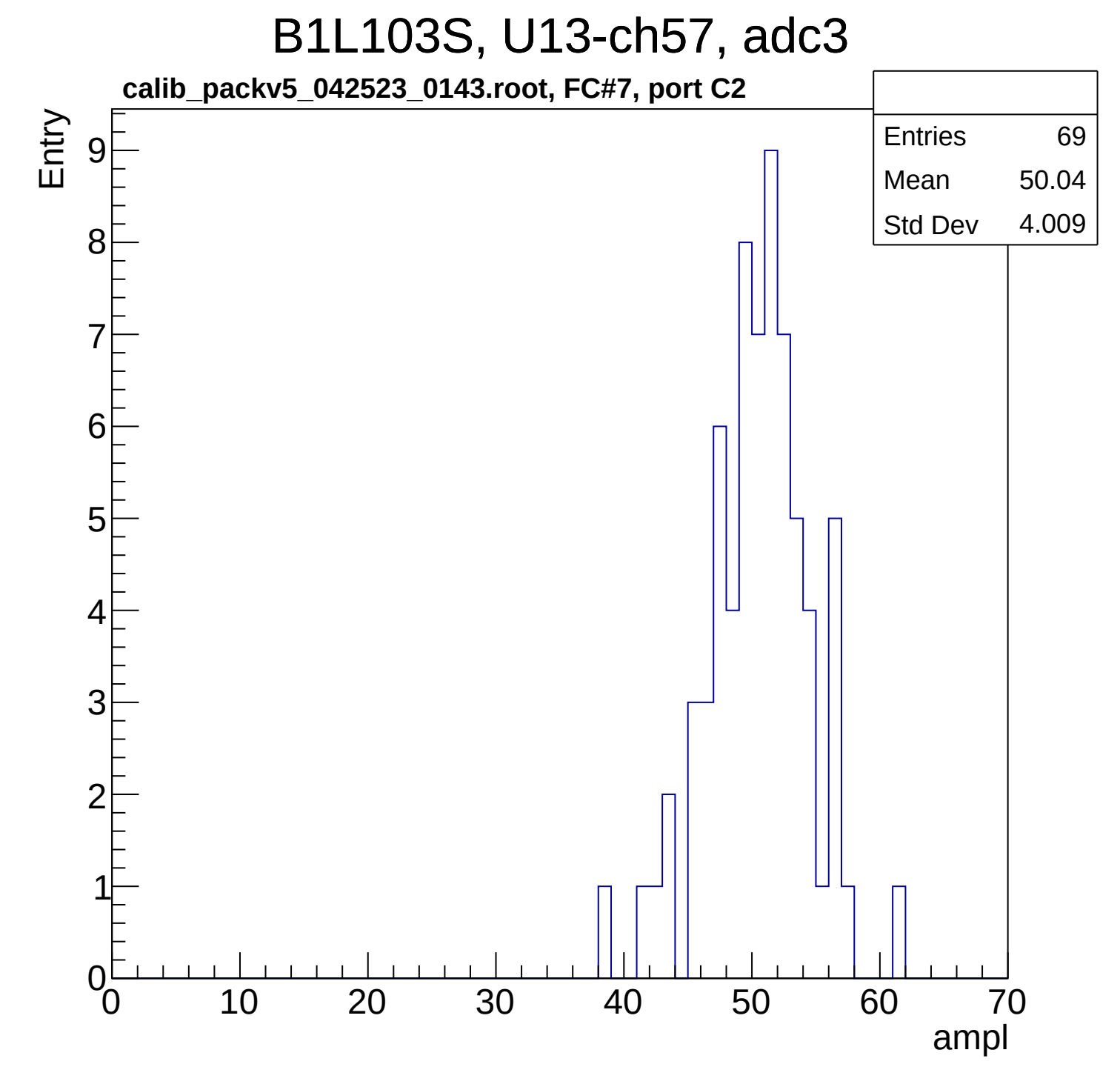
B1L103S, U13-ch57, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.04
Std Dev 4.009
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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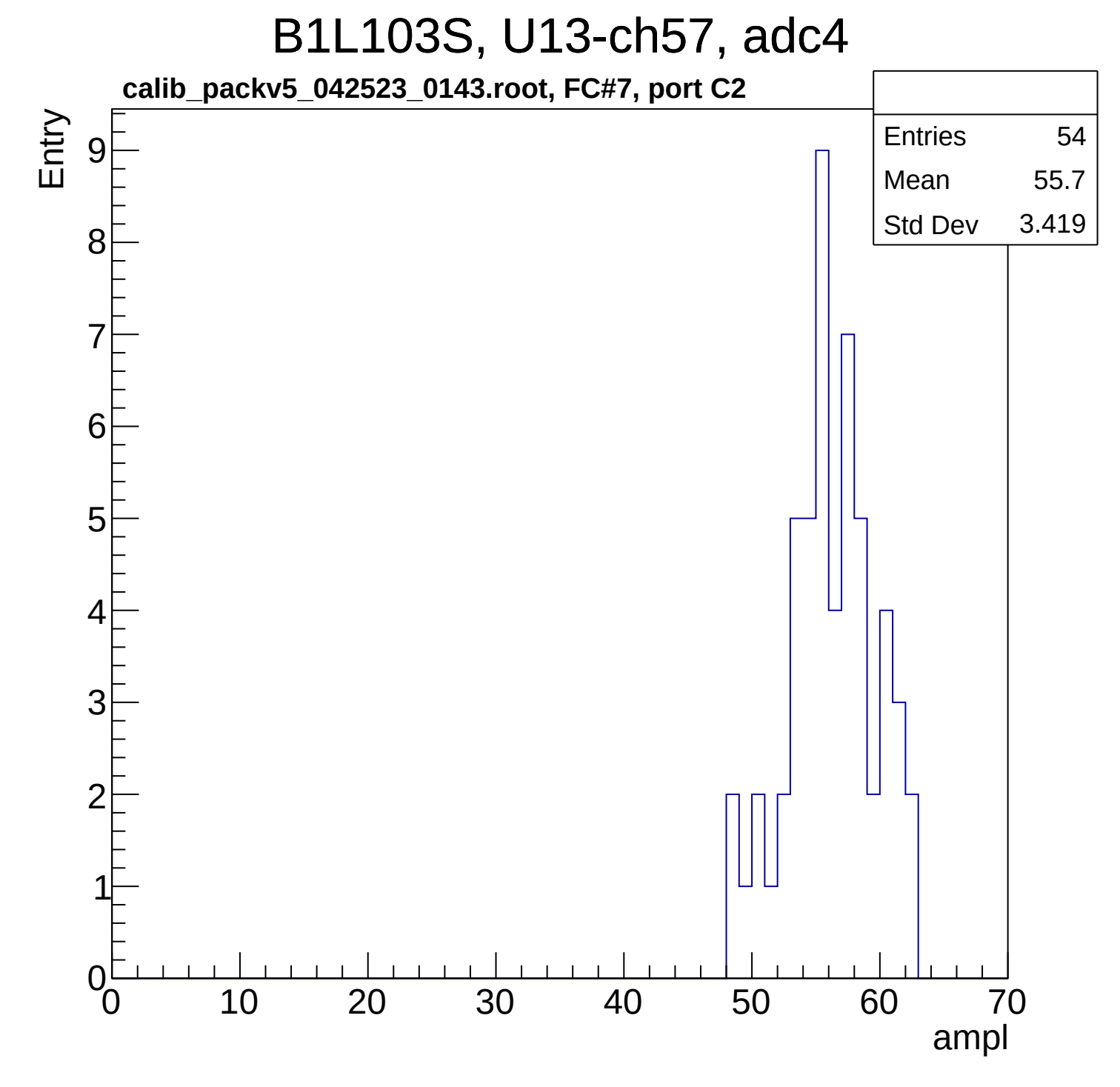
B1L103S, U13-ch57, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.7
Std Dev 3.419
Entry
ampl
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3
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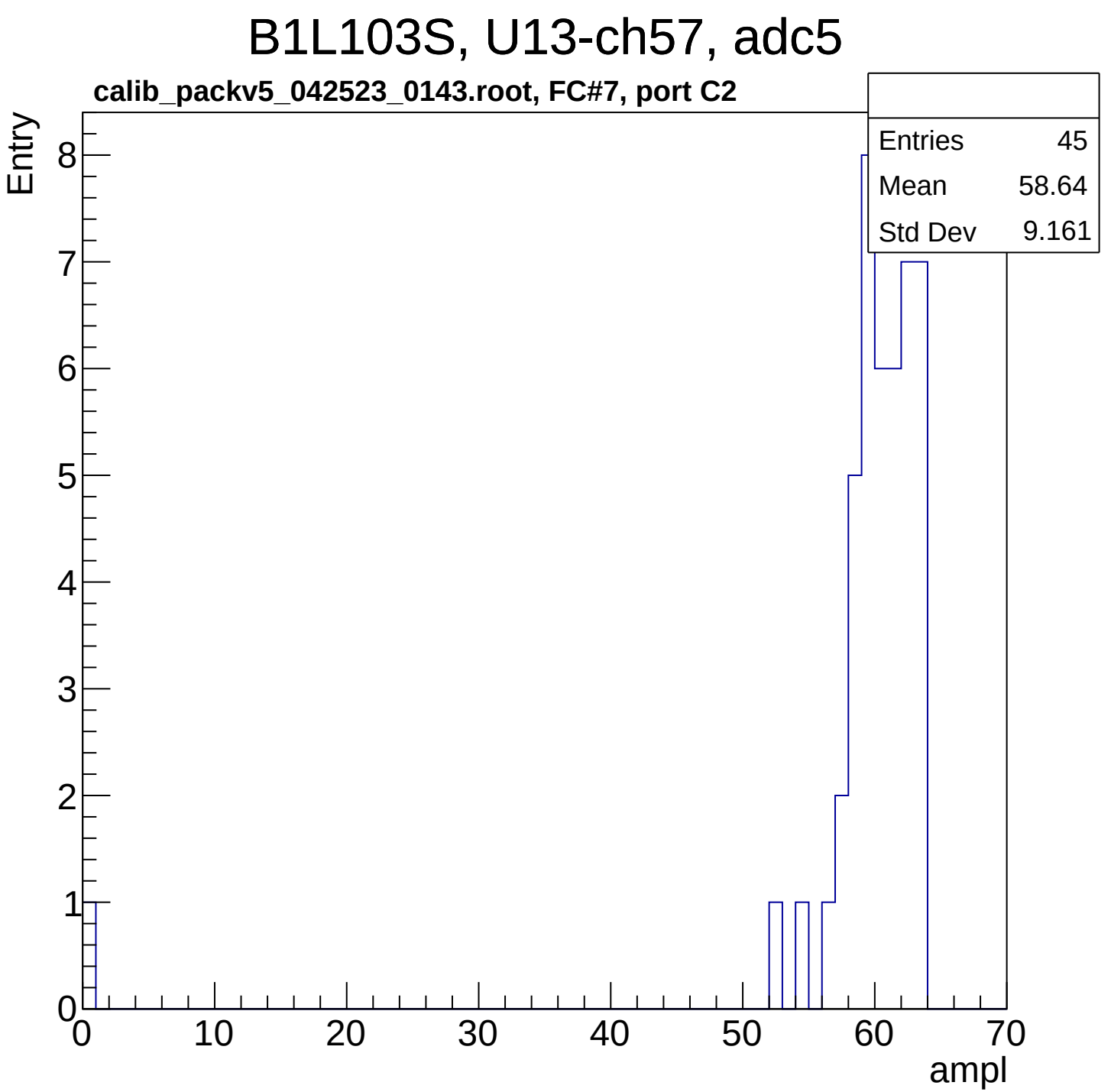
B1L103S, U13-ch57, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.64
Std Dev 9.161
Entry
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2
3
4
5
6
7
8
0
10
20
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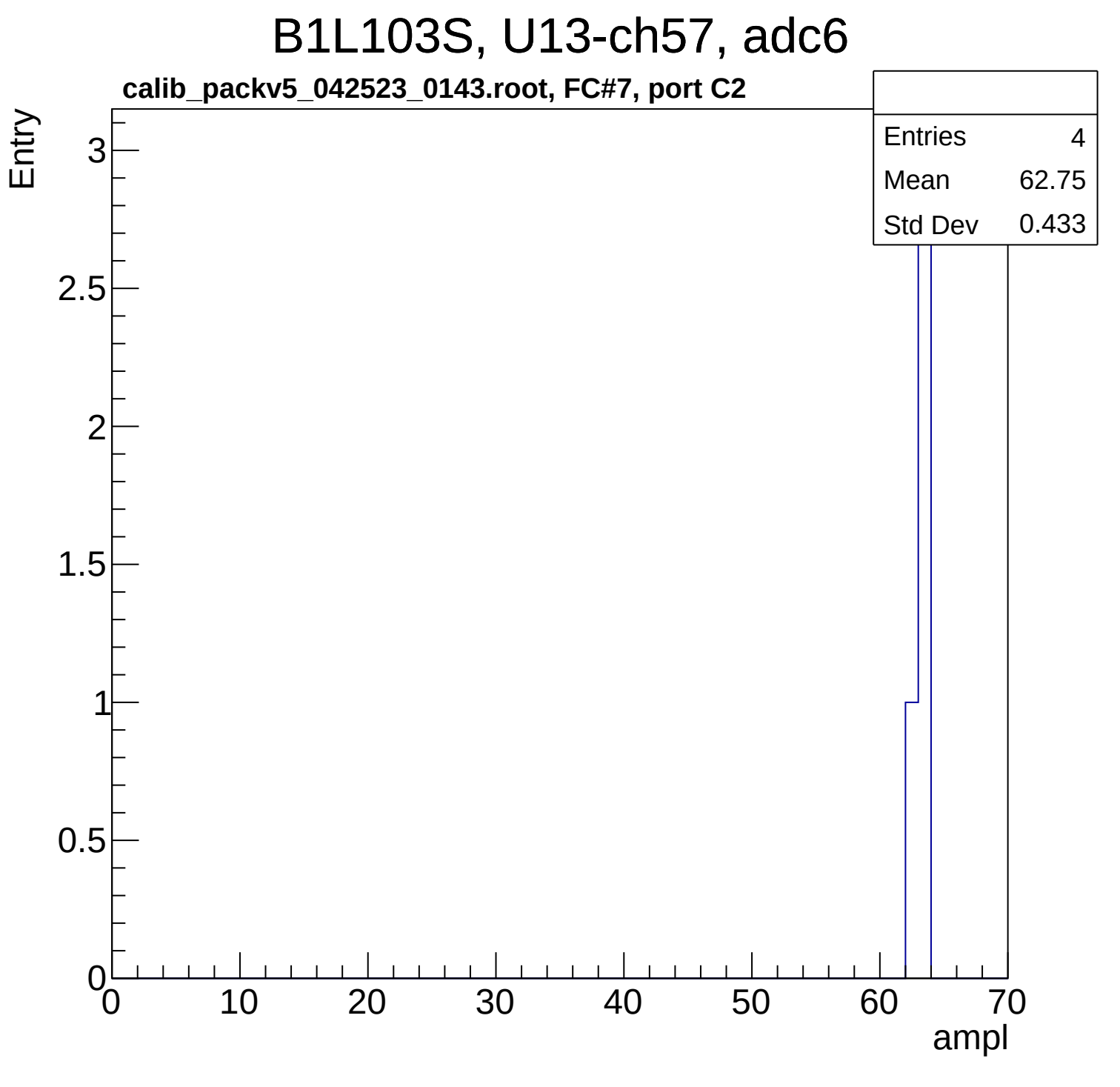
50
60
70
ampl


B1L103S, U13-ch57, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
30
40
50
60
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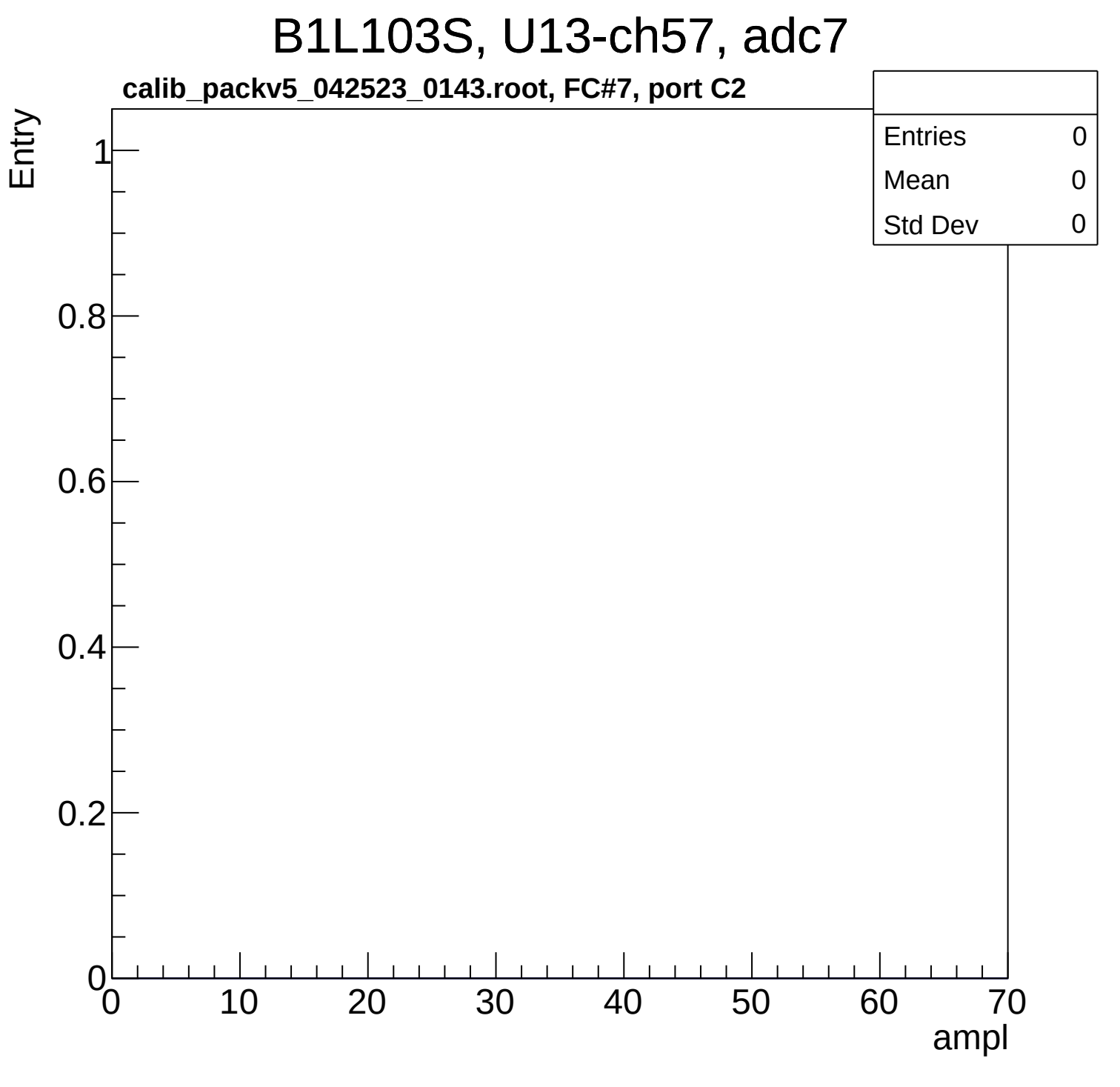
B1L103S, U13-ch57, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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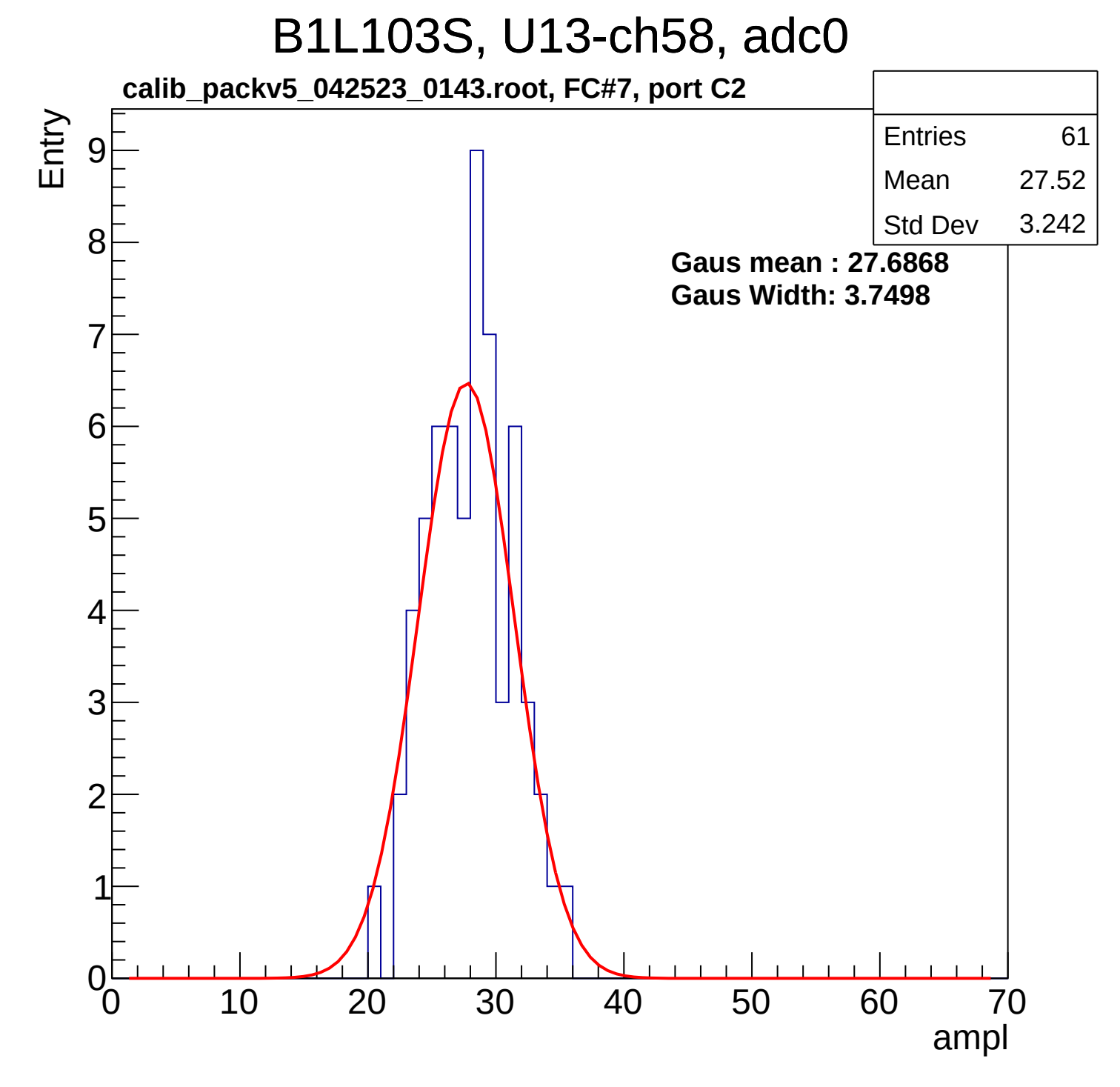
50
60
70
ampl


B1L103S, U13-ch58, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 27.52
Std Dev 3.242
Gaus mean : 27.6868
Gaus Width: 3.7498
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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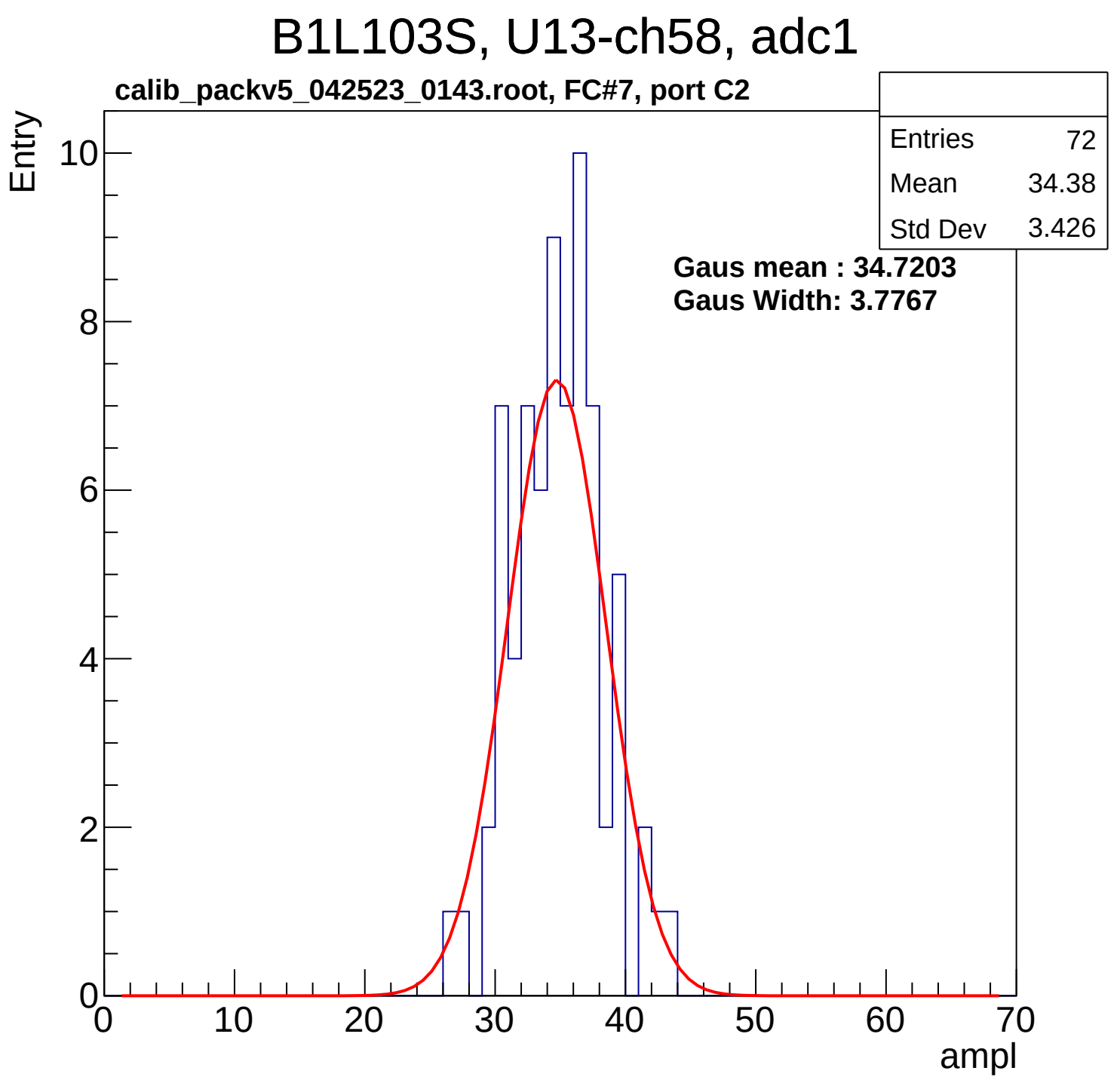
B1L103S, U13-ch58, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 34.38
Std Dev 3.426
Gaus mean : 34.7203
Gaus Width: 3.7767
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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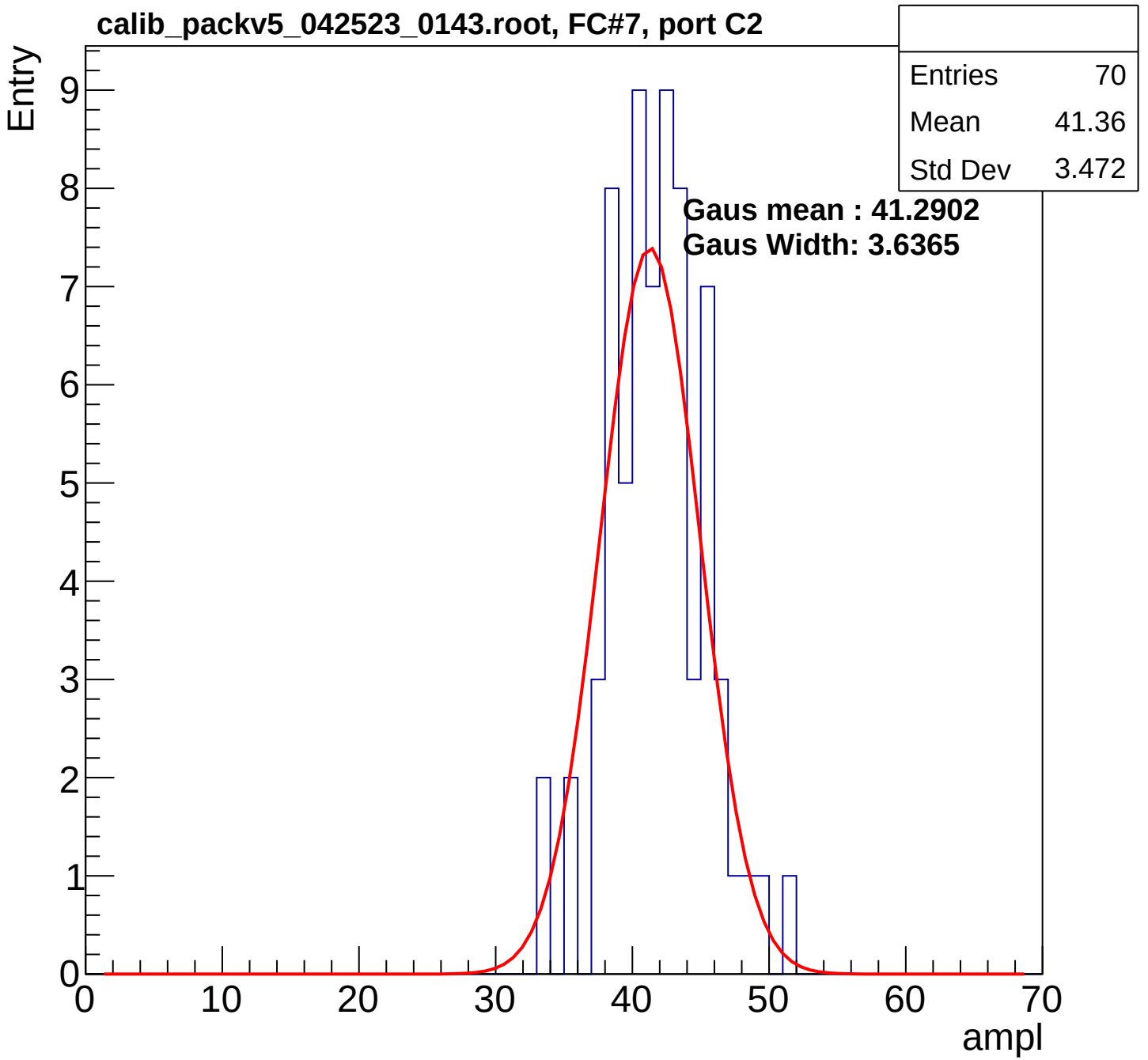
B1L103S, U13-ch58, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 41.36
Std Dev 3.472
Gaus mean : 41.2902
Gaus Width: 3.6365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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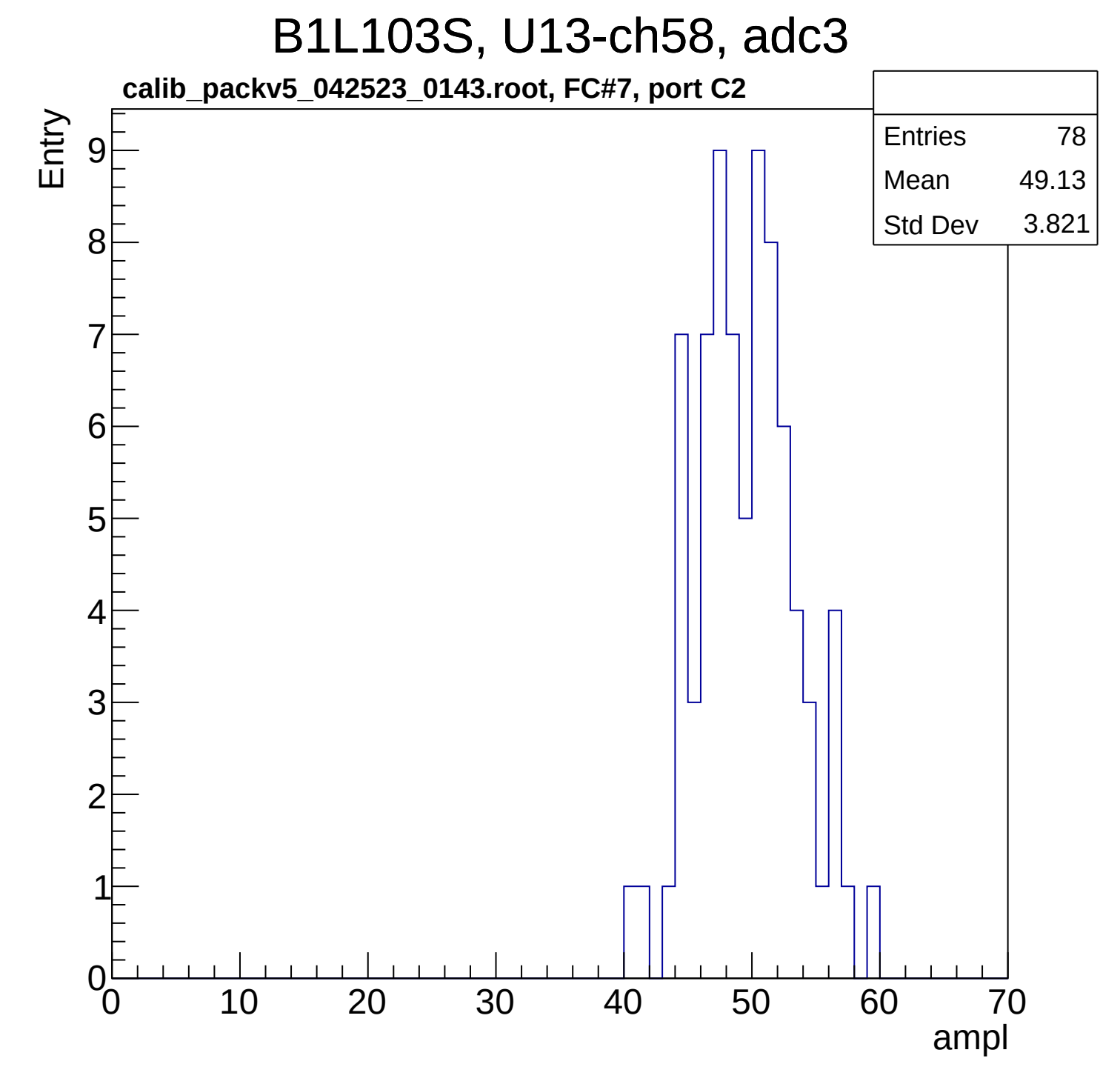
B1L103S, U13-ch58, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 49.13
Std Dev 3.821
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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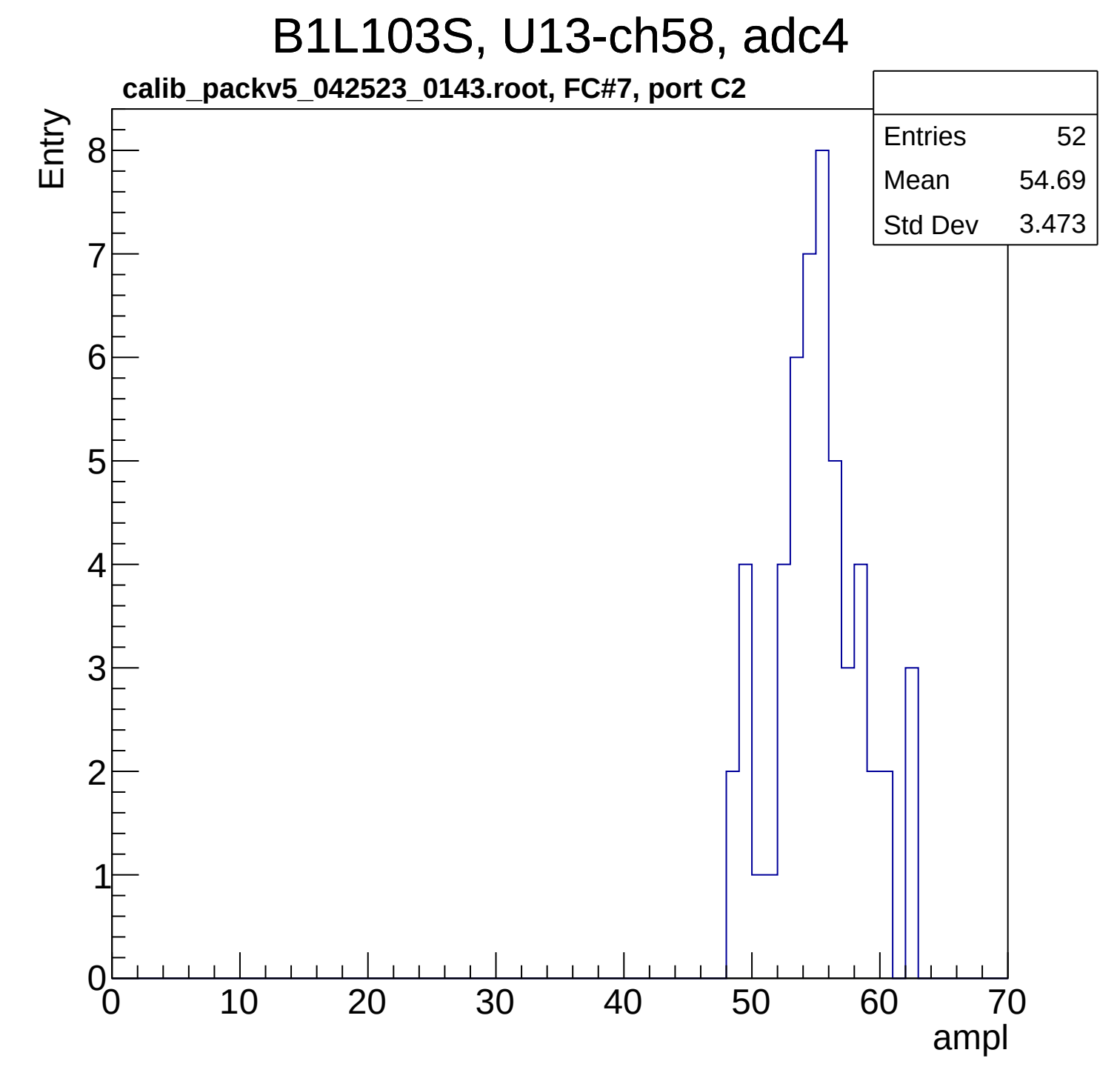
B1L103S, U13-ch58, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 54.69
Std Dev 3.473
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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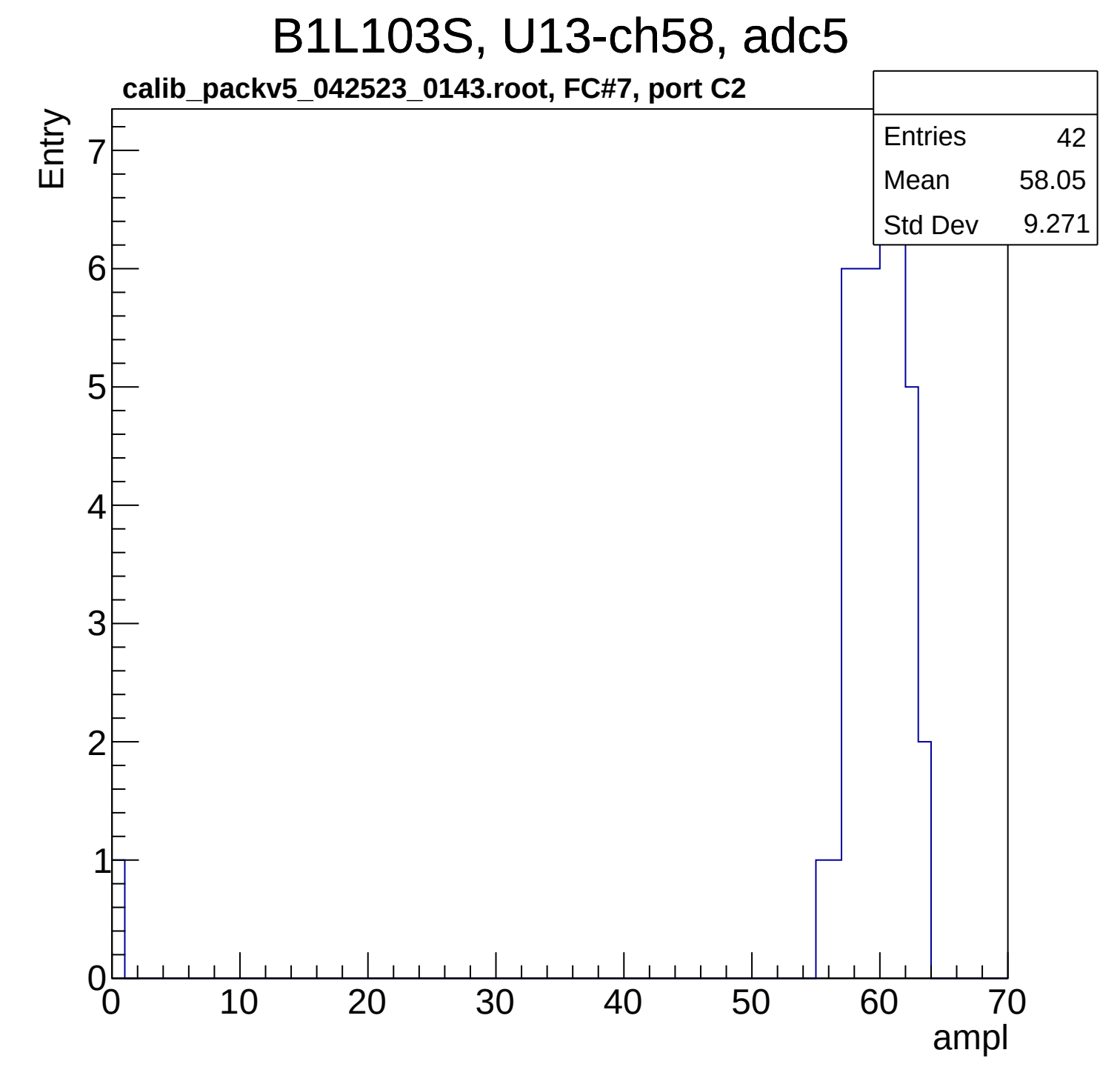
B1L103S, U13-ch58, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.05
Std Dev 9.271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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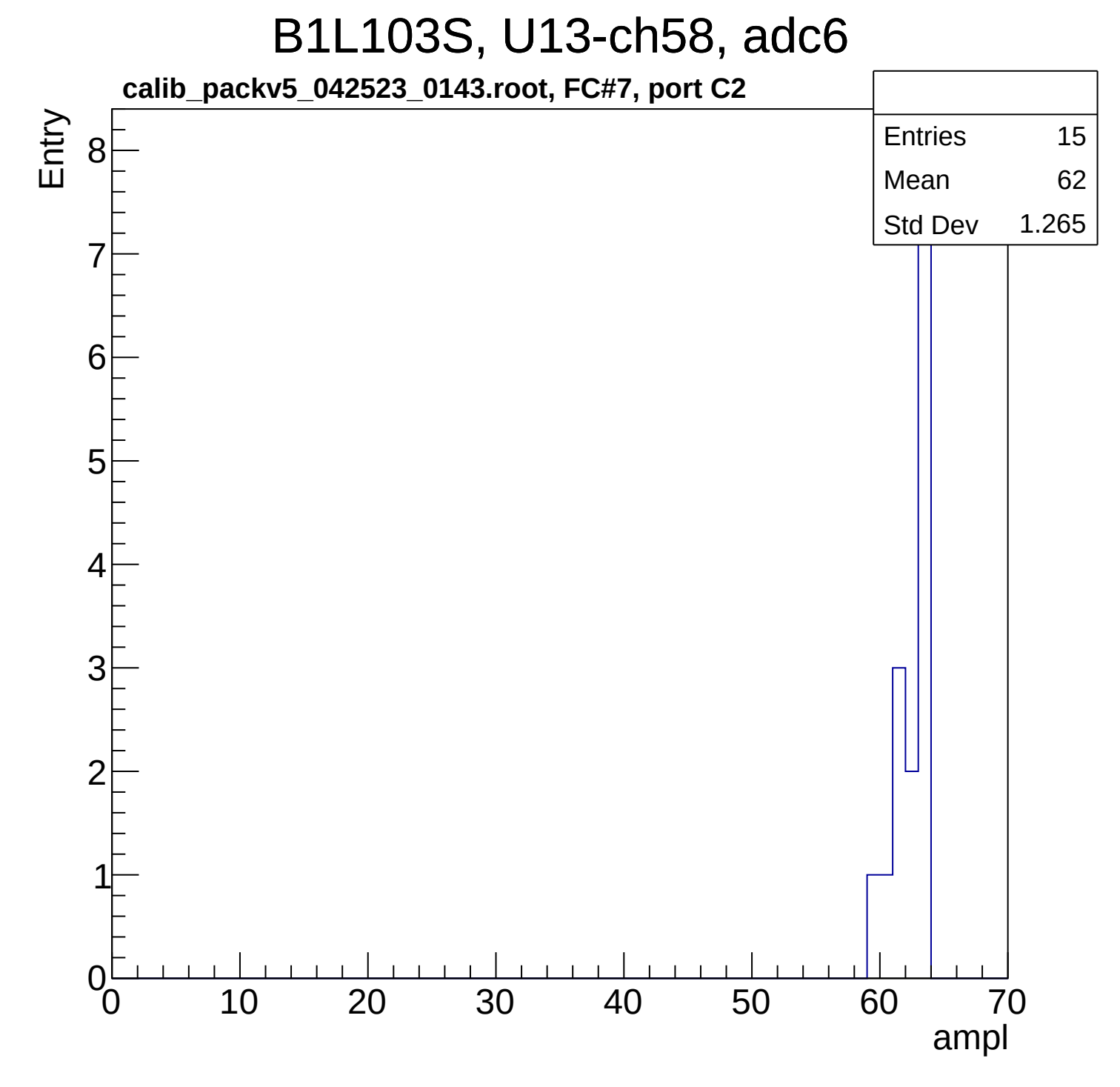
B1L103S, U13-ch58, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 62
Std Dev 1.265
Entry
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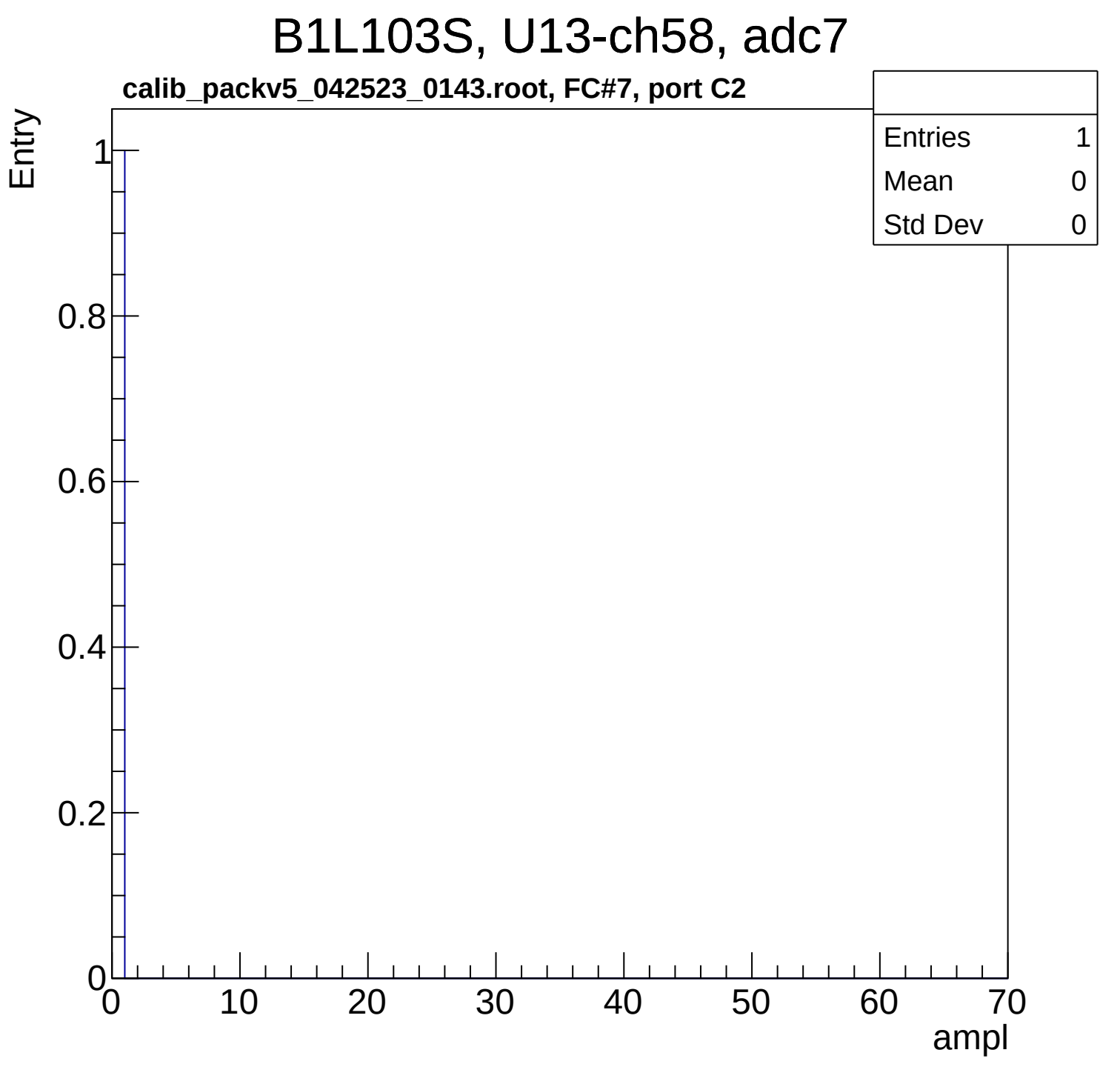
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch58, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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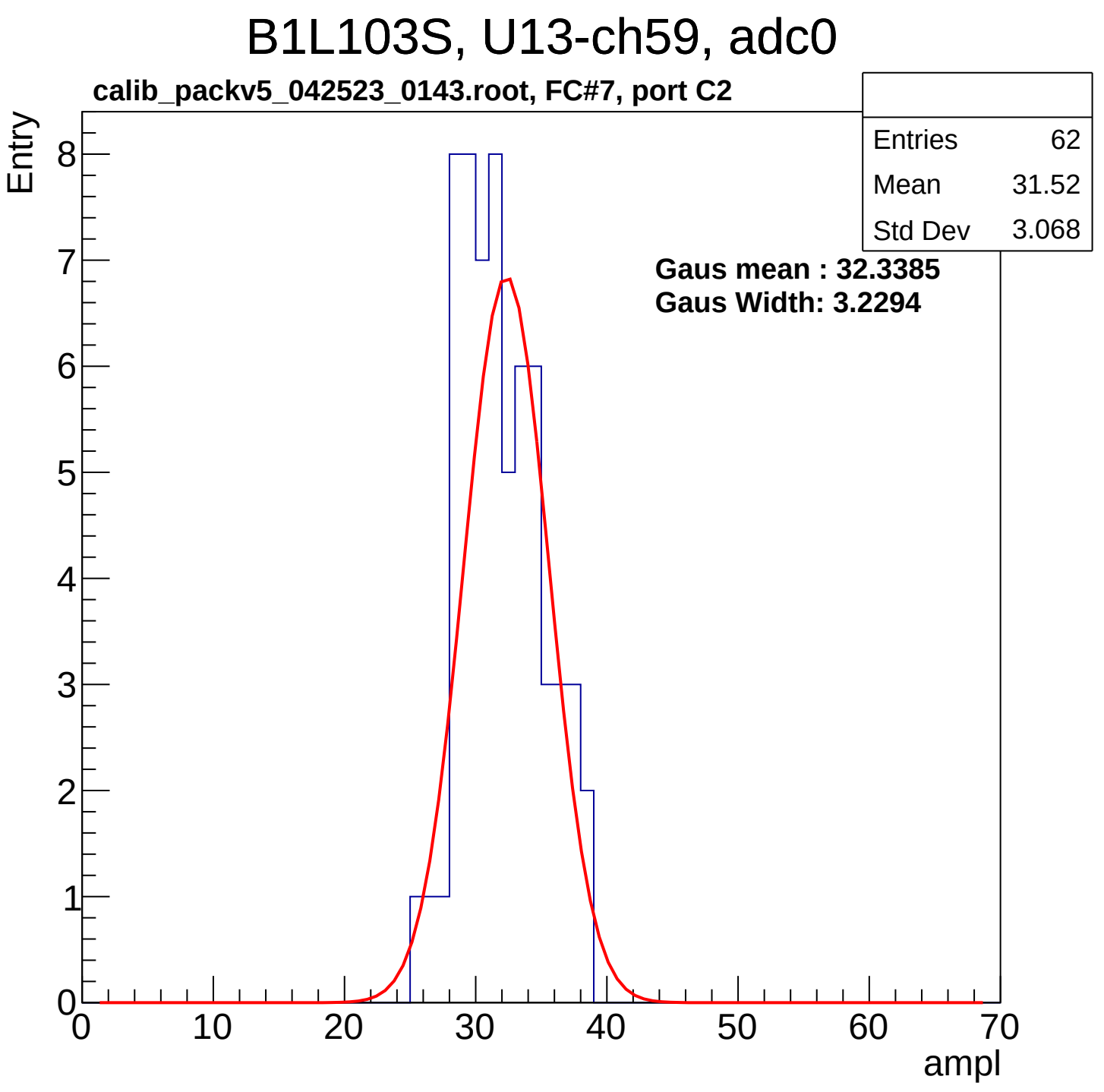
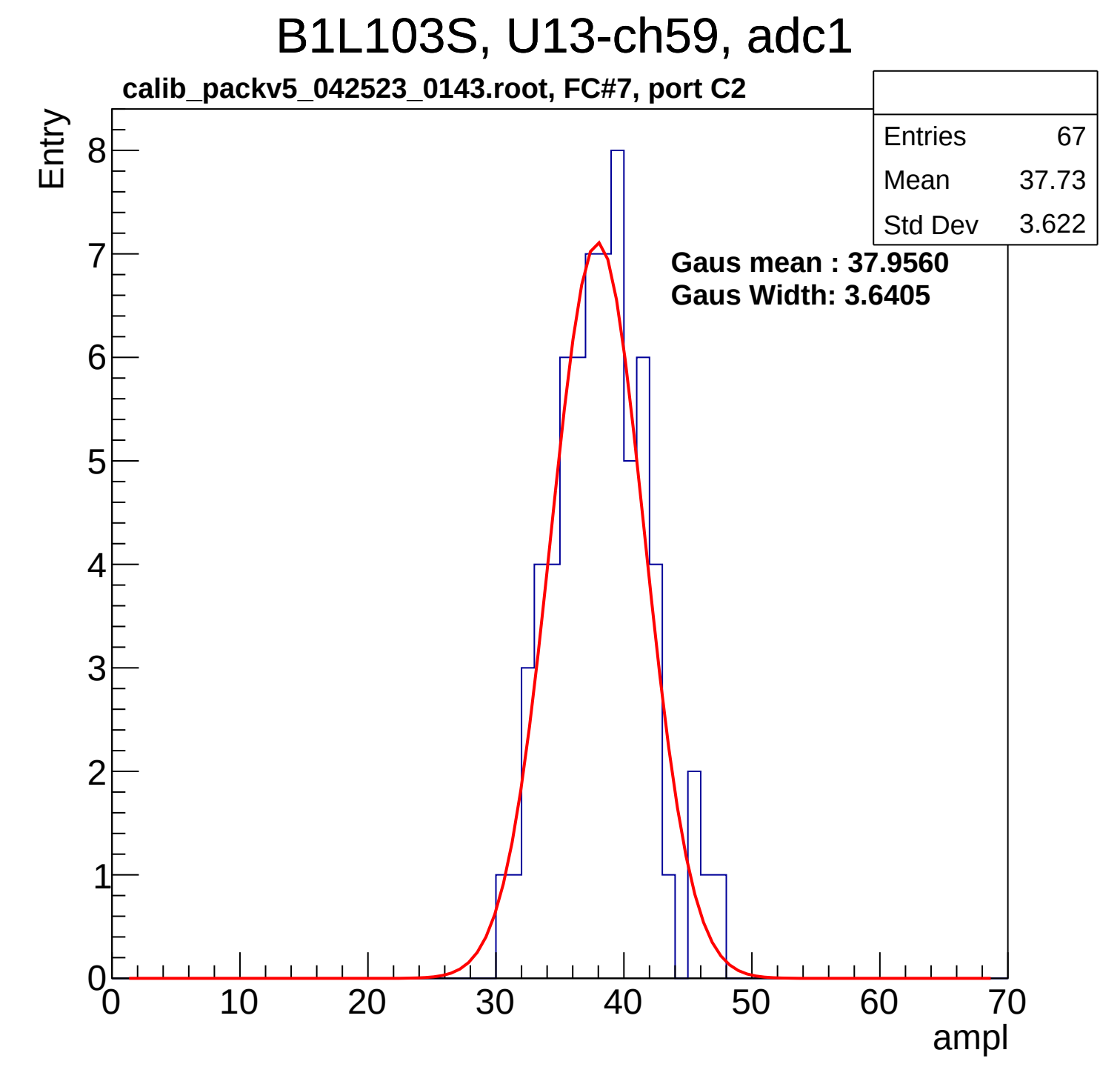
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch59, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 31.52
Std Dev 3.068
Gaus mean : 32.3385
Gaus Width: 3.2294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch59, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.73
Std Dev 3.622
Gaus mean : 37.9560
Gaus Width: 3.6405
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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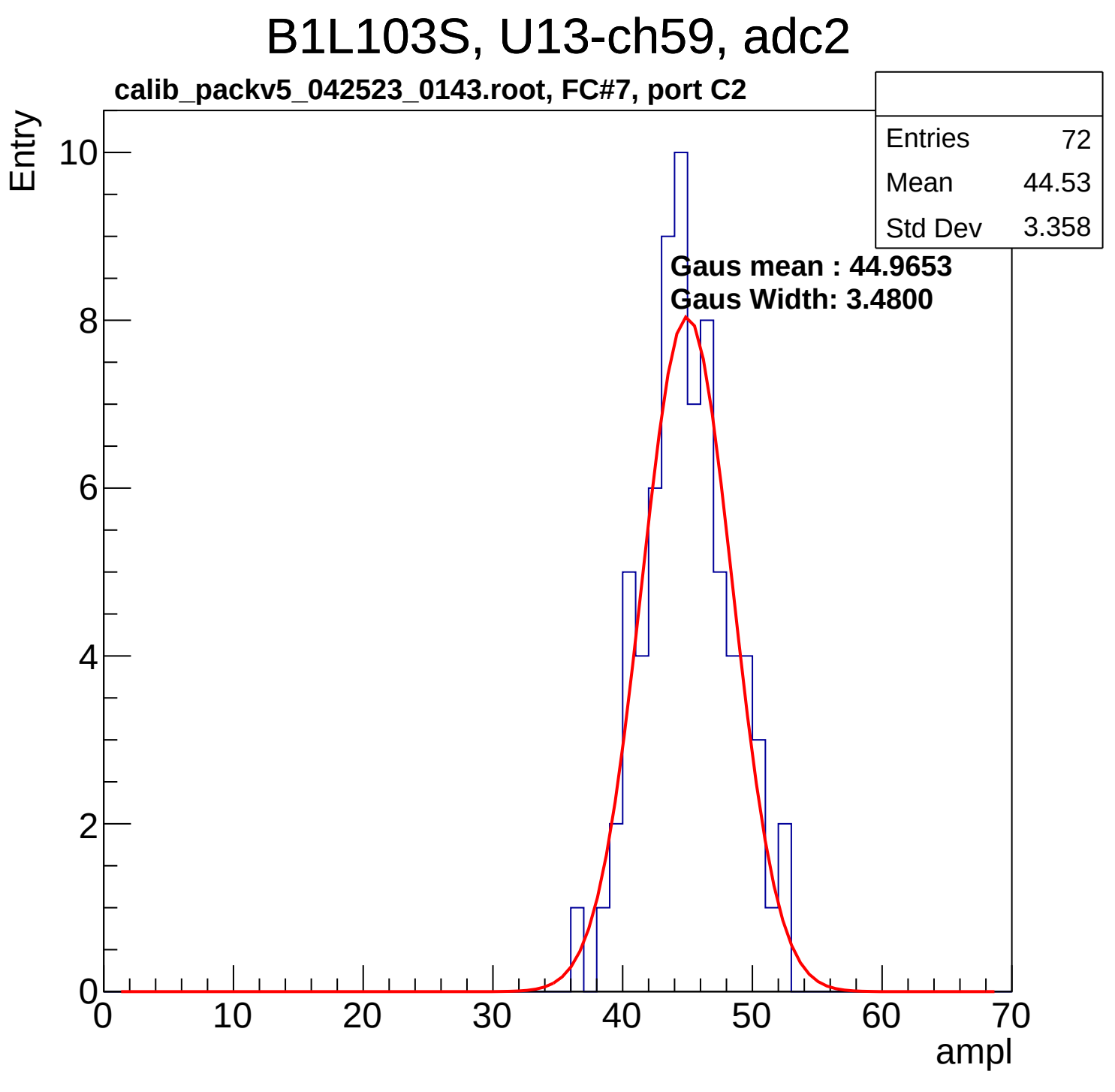
B1L103S, U13-ch59, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 44.53
Std Dev 3.358
Gaus mean : 44.9653
Gaus Width: 3.4800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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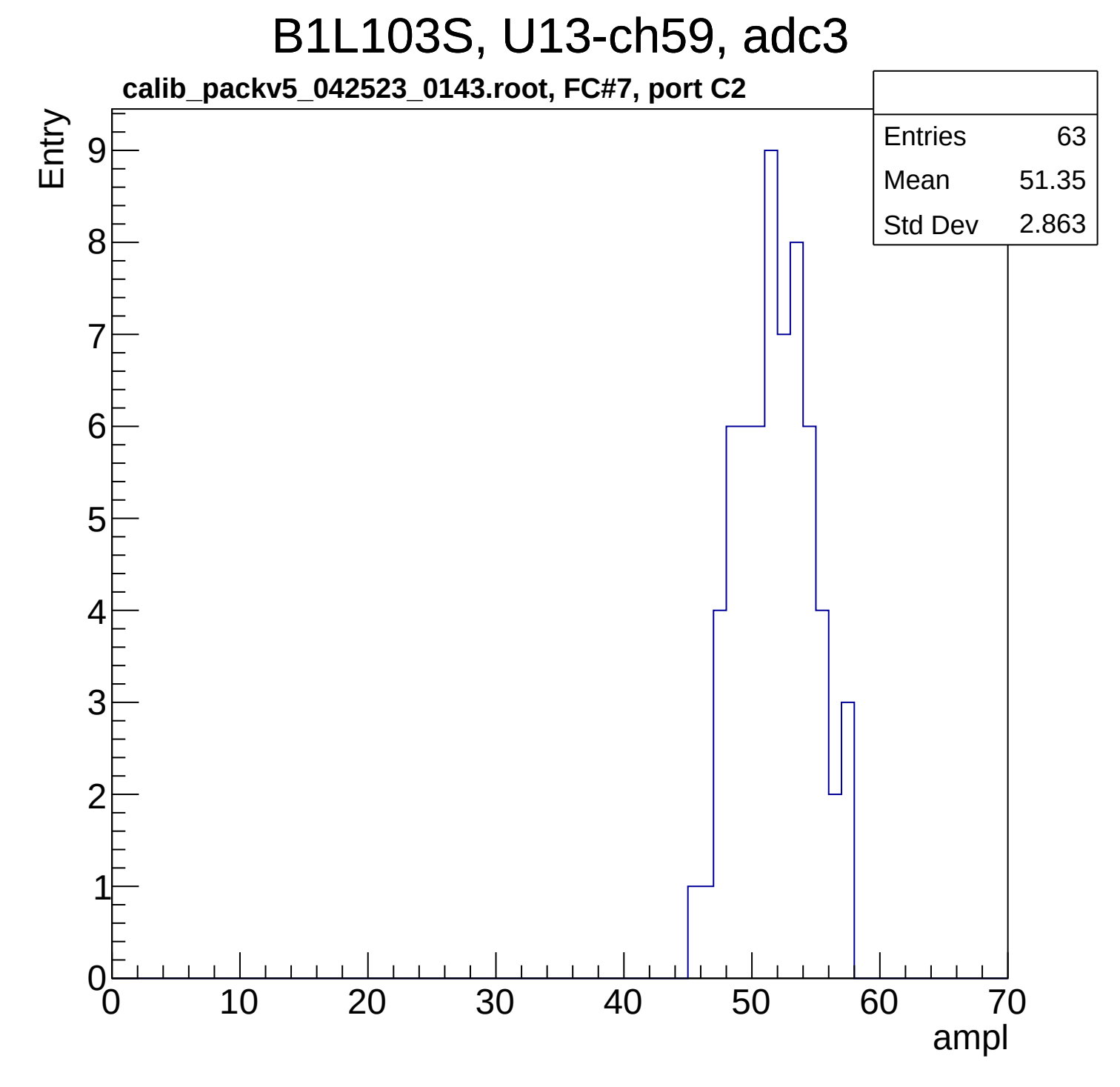
B1L103S, U13-ch59, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 51.35
Std Dev 2.863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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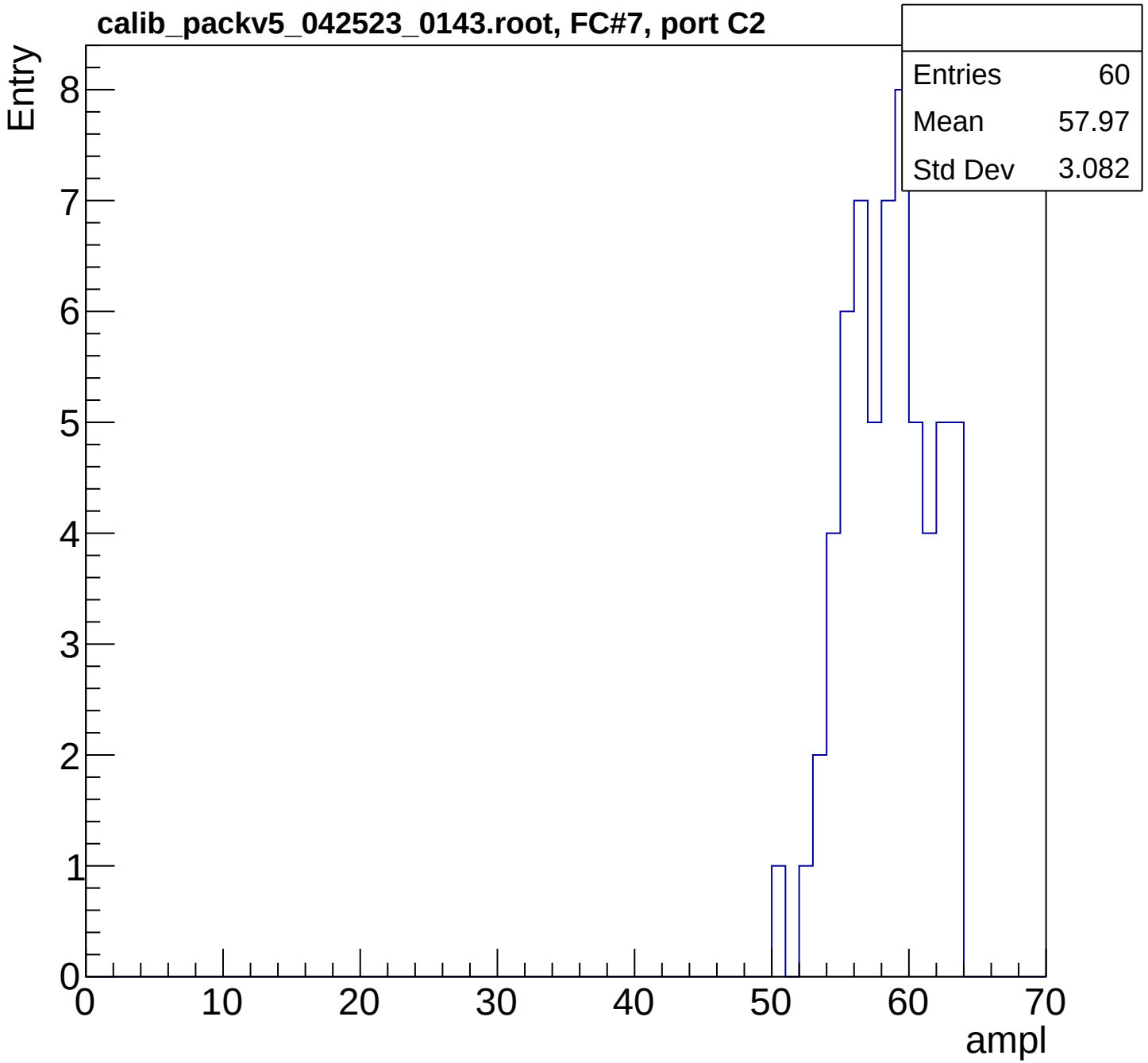
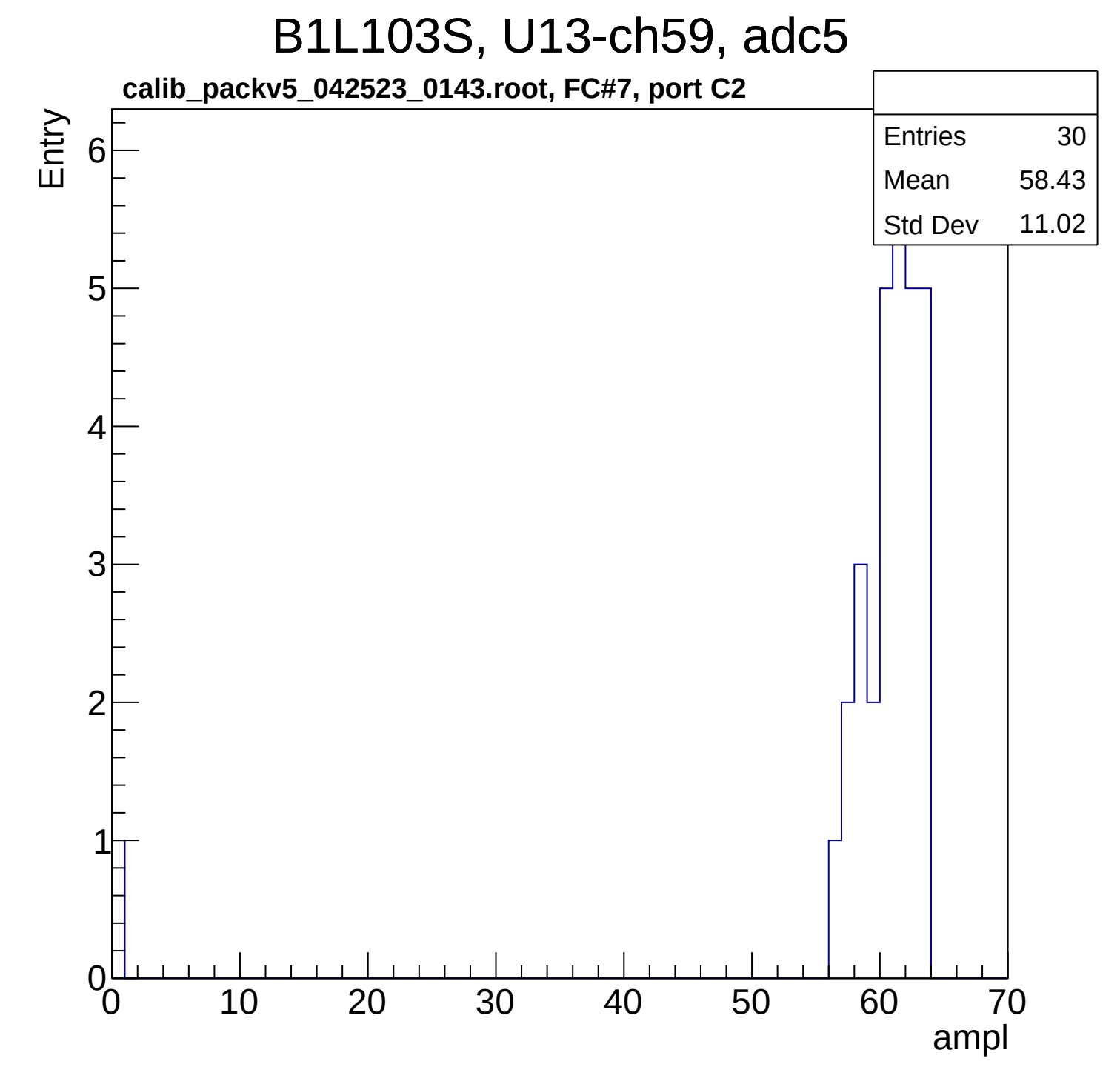
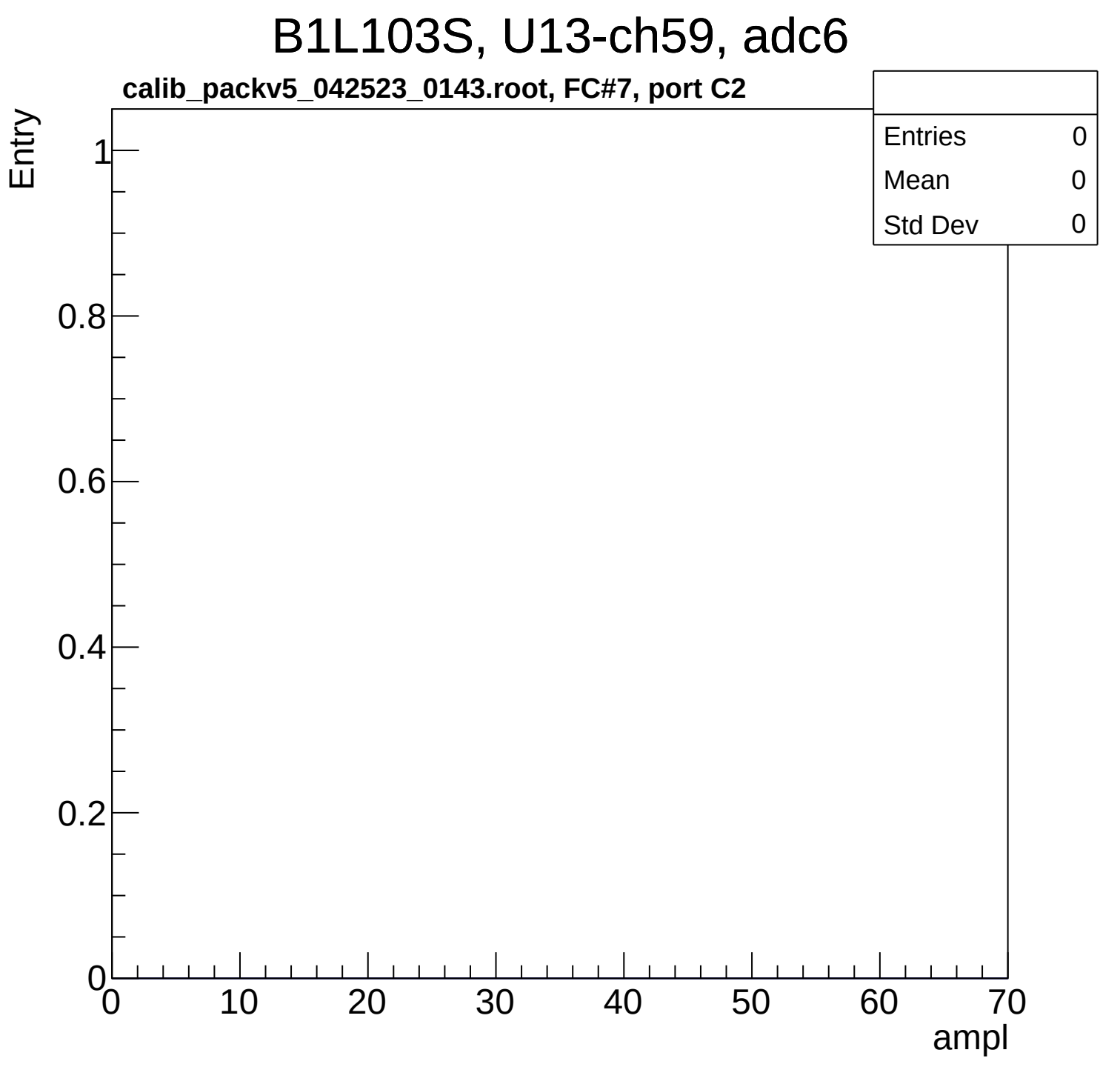

B1L103S, U13-ch59, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 57.97
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch59, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 58.43
Std Dev 11.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch59, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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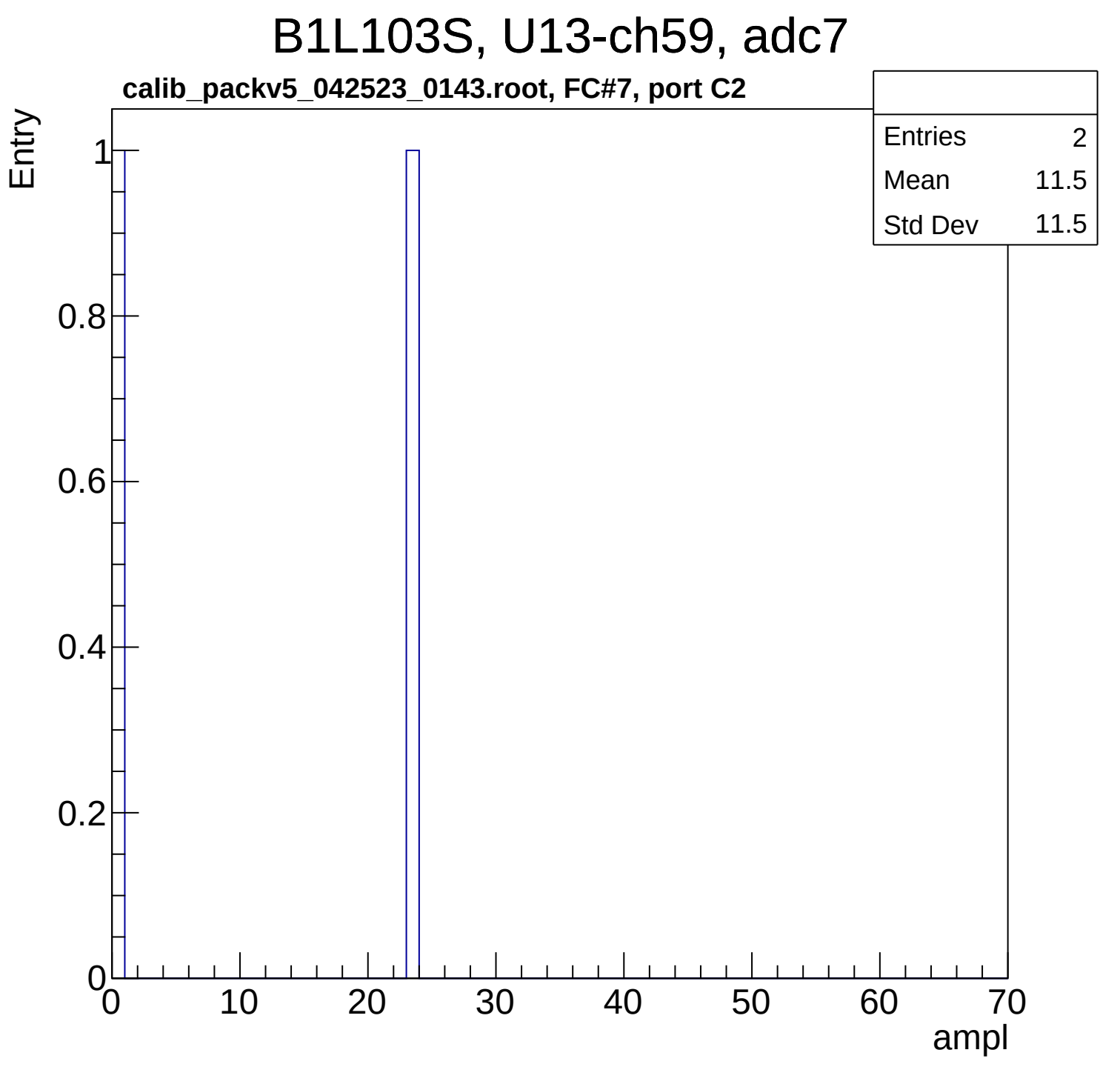
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch59, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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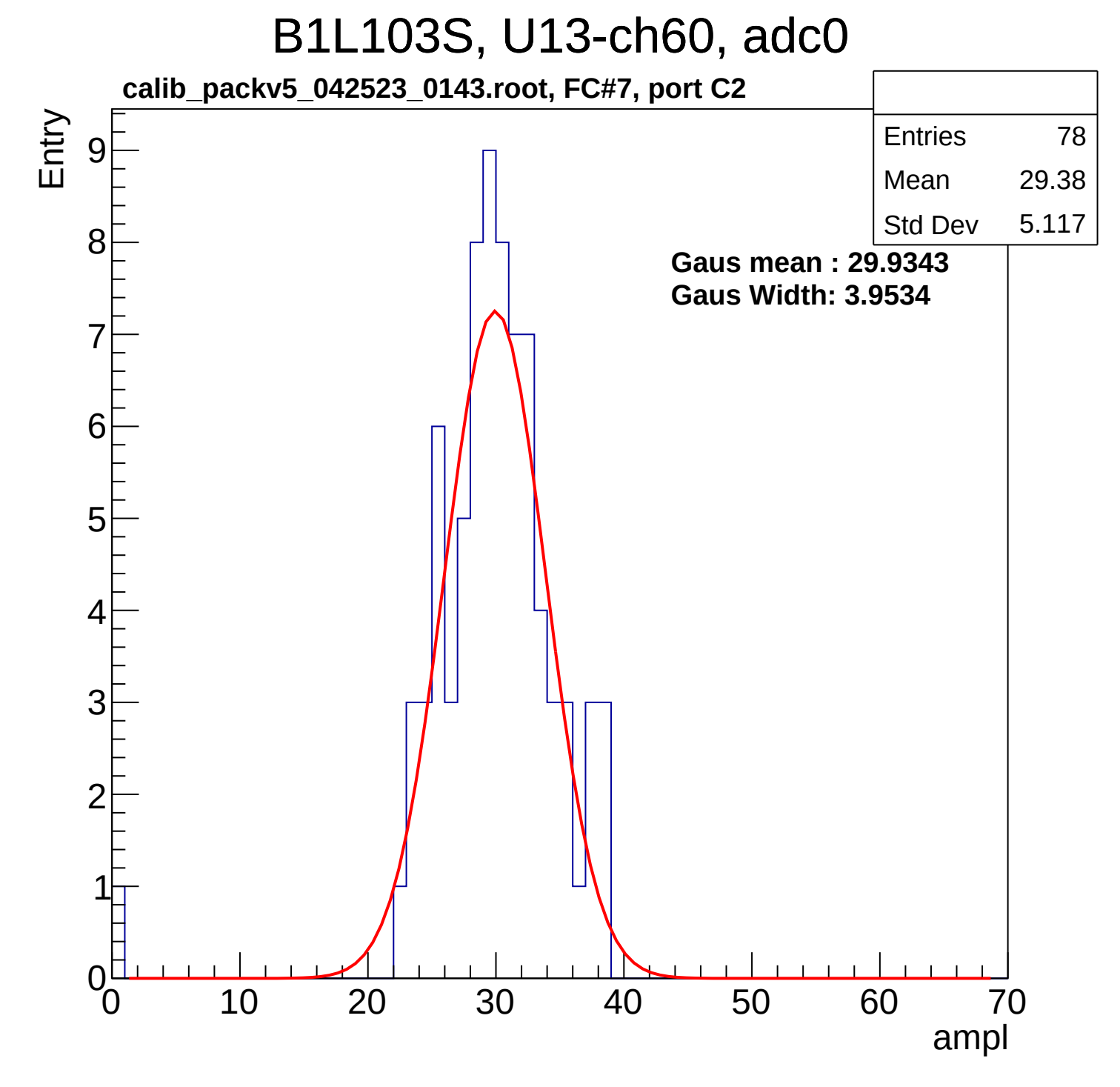
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch60, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 29.38
Std Dev 5.117
Gaus mean : 29.9343
Gaus Width: 3.9534
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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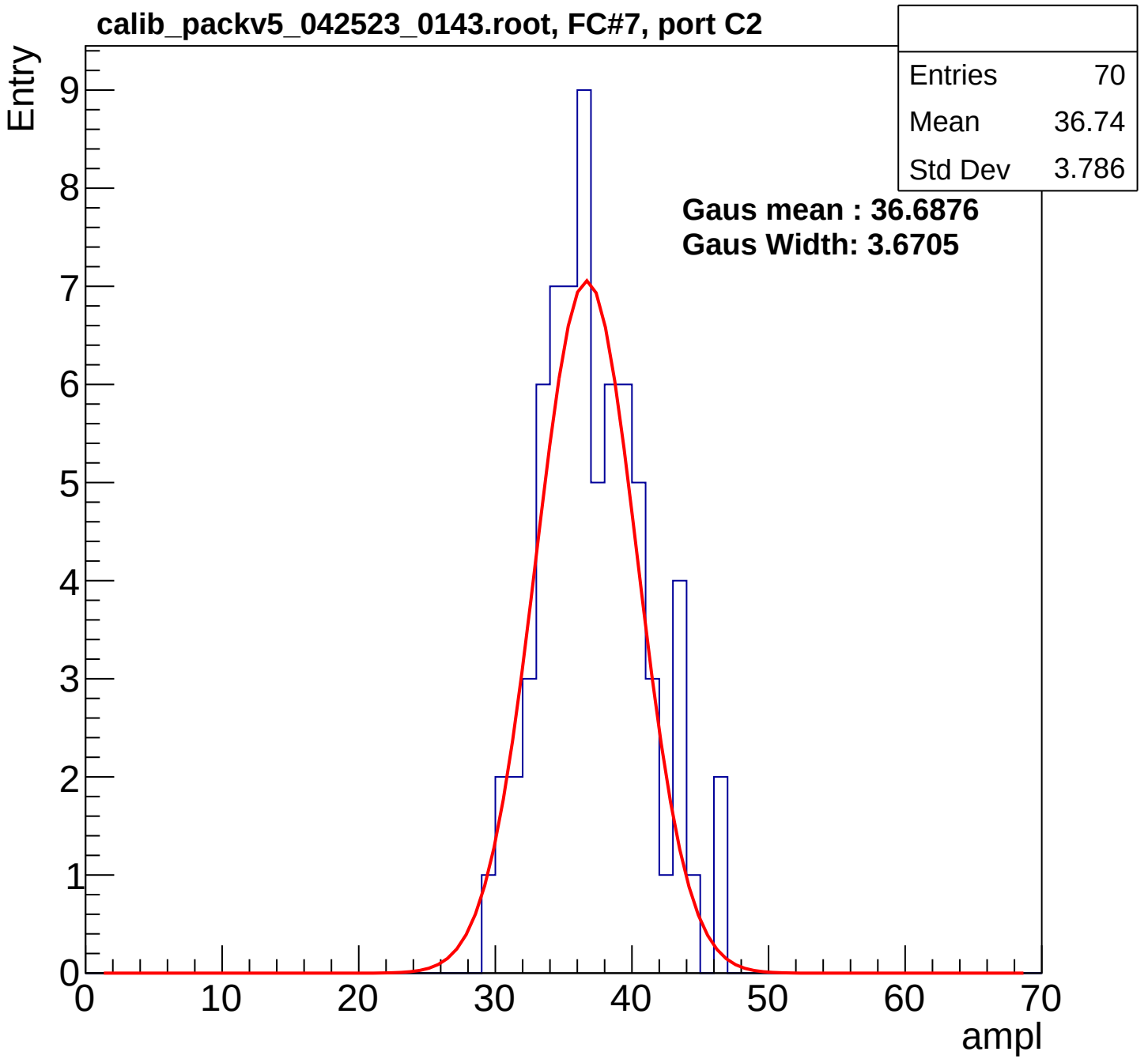
B1L103S, U13-ch60, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 36.74
Std Dev 3.786
Gaus mean : 36.6876
Gaus Width: 3.6705
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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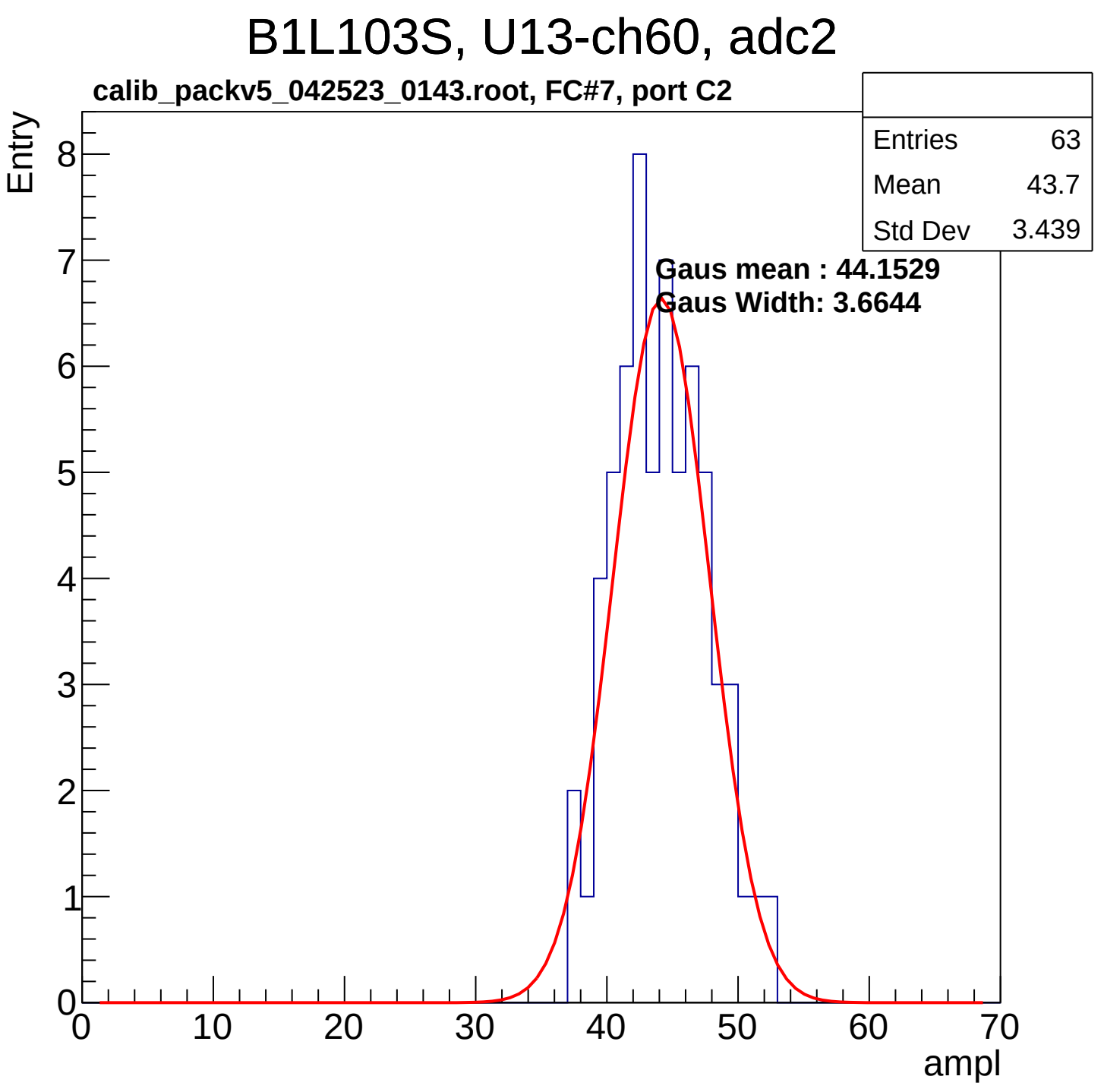
B1L103S, U13-ch60, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.7
Std Dev 3.439
Gaus mean : 44.1529
Gaus Width: 3.6644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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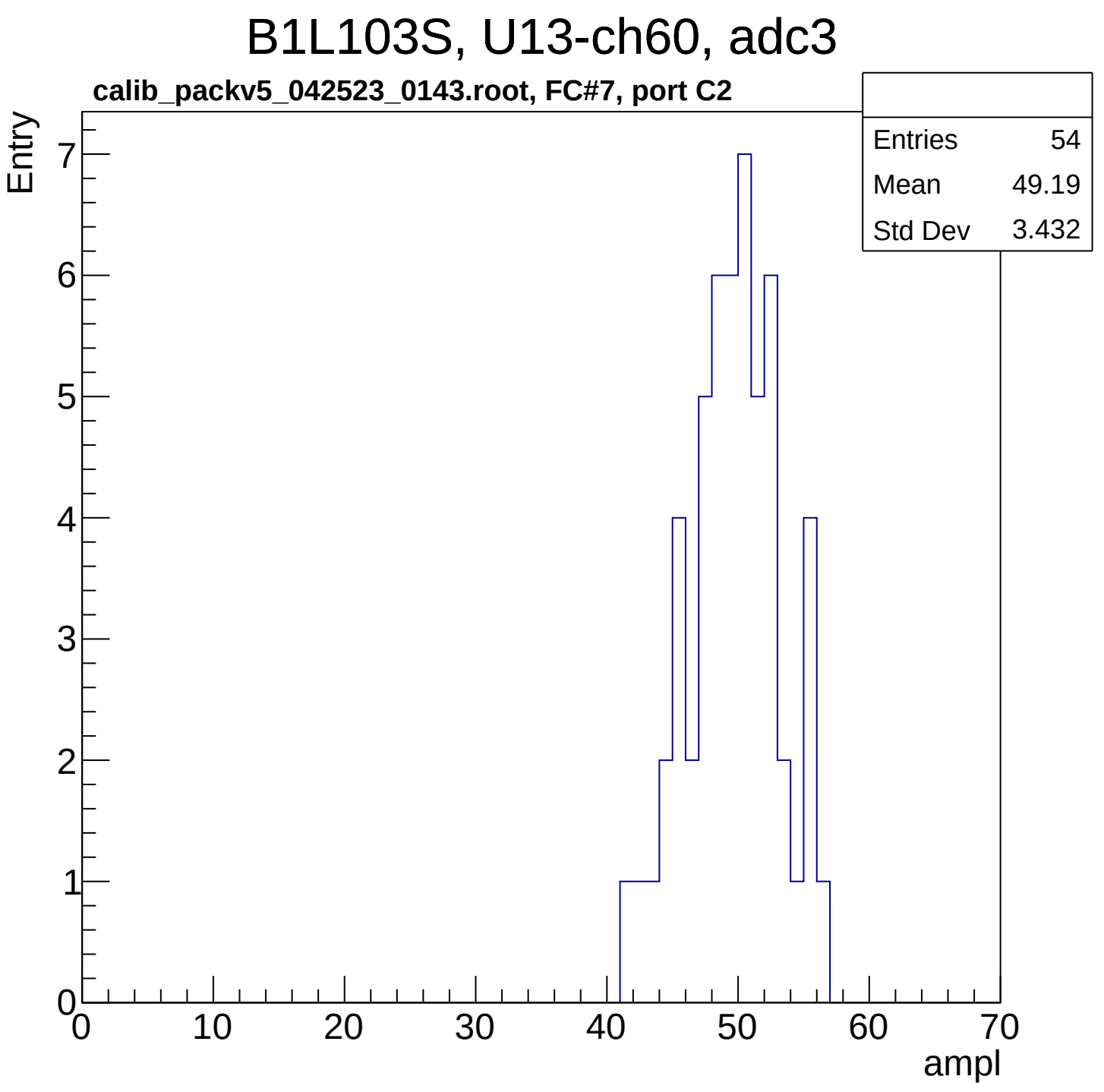
B1L103S, U13-ch60, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.19
Std Dev 3.432
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
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60
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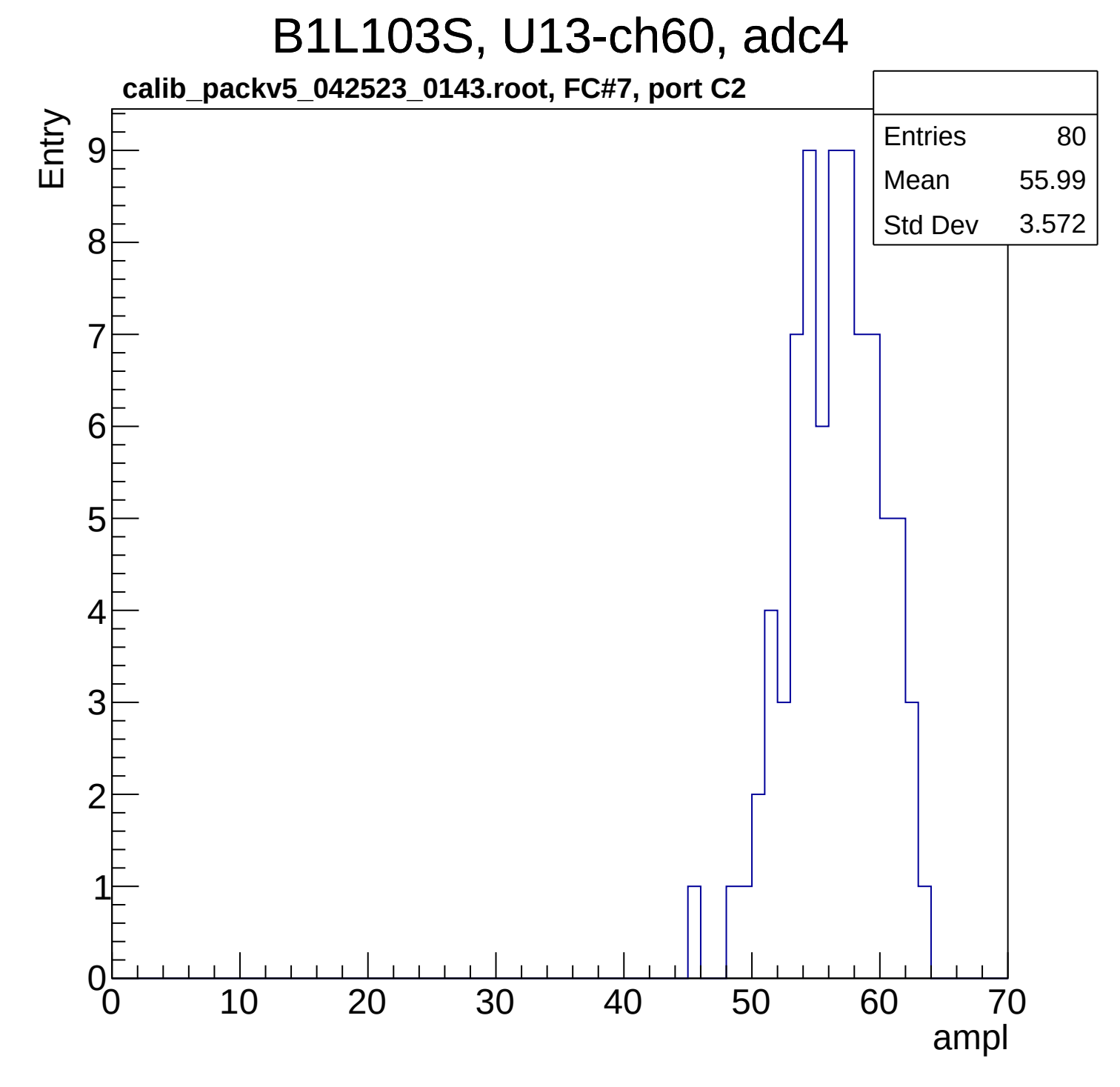
B1L103S, U13-ch60, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 55.99
Std Dev 3.572
Entry
ampl
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5
6
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0
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60
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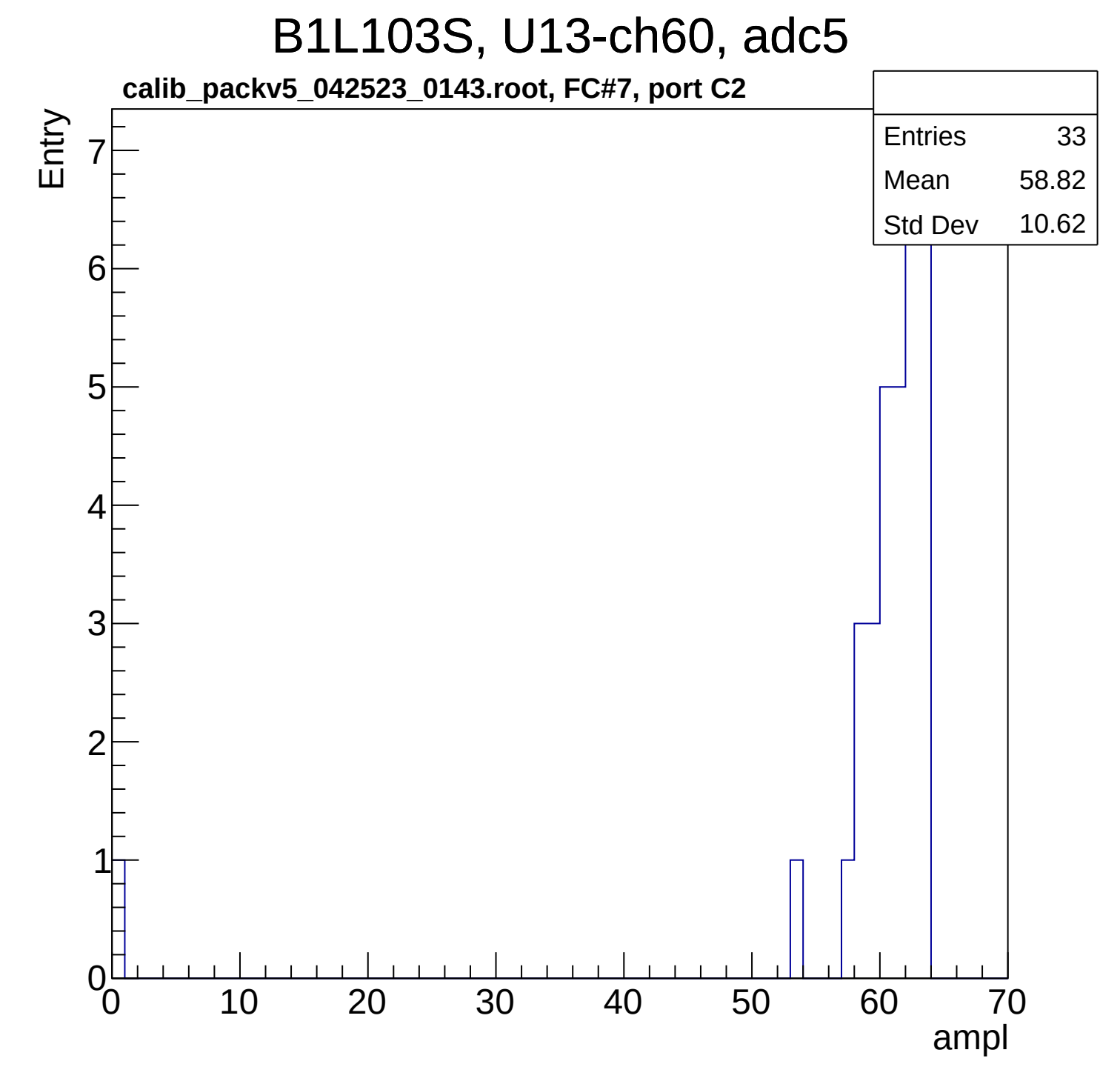
B1L103S, U13-ch60, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.82
Std Dev 10.62
Entry
ampl
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4
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6
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0
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20
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40
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60
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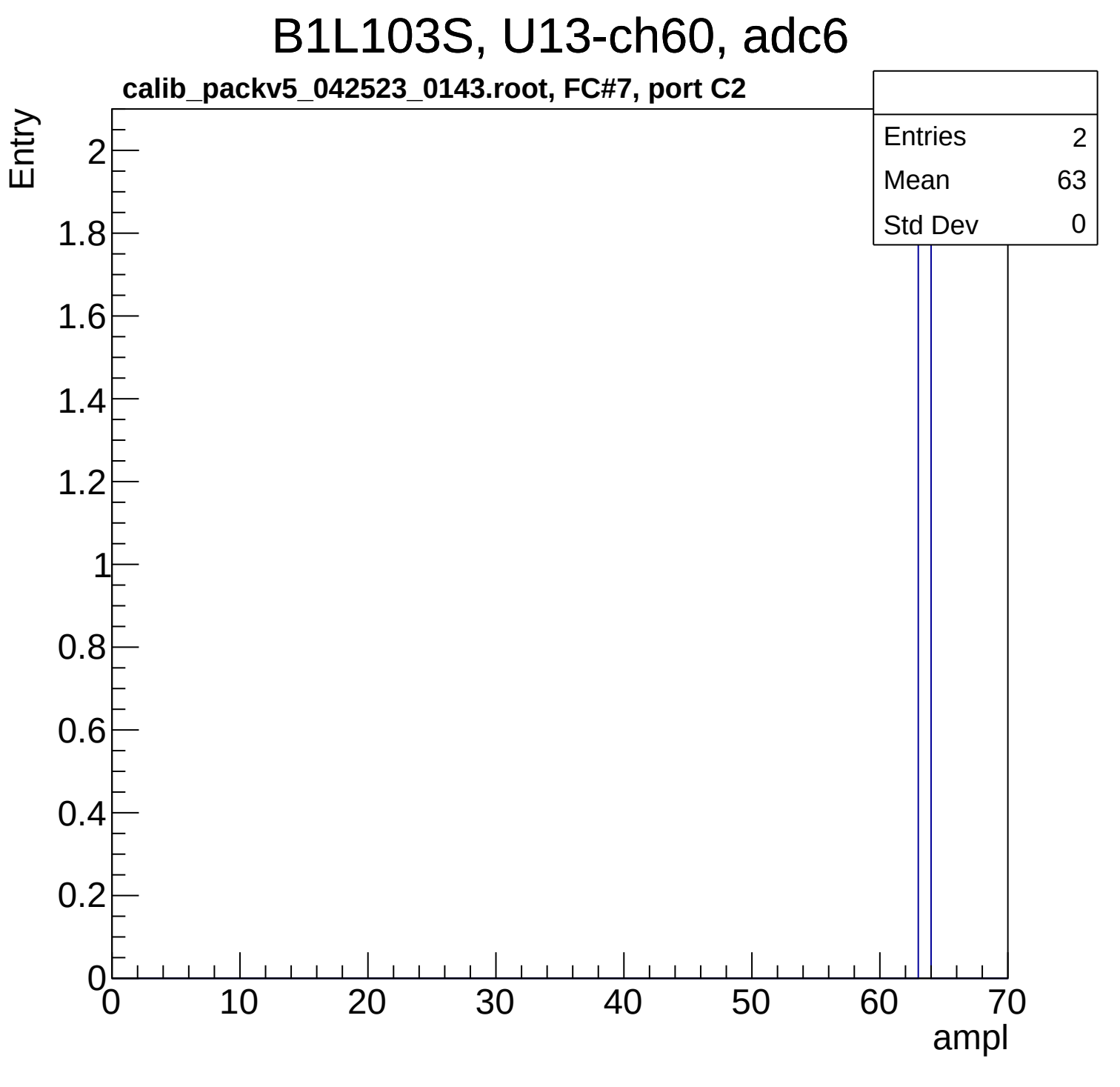
B1L103S, U13-ch60, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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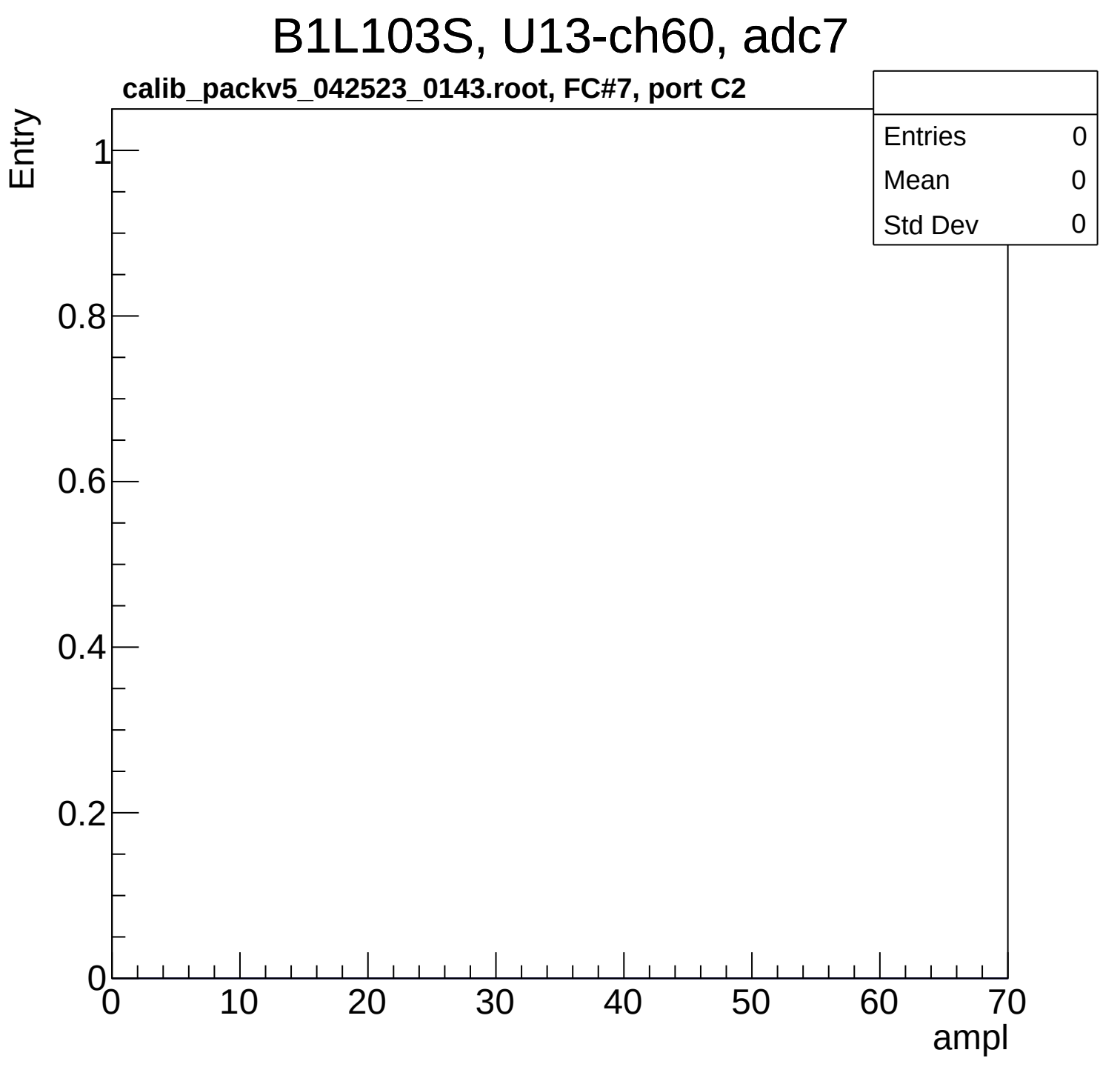
B1L103S, U13-ch60, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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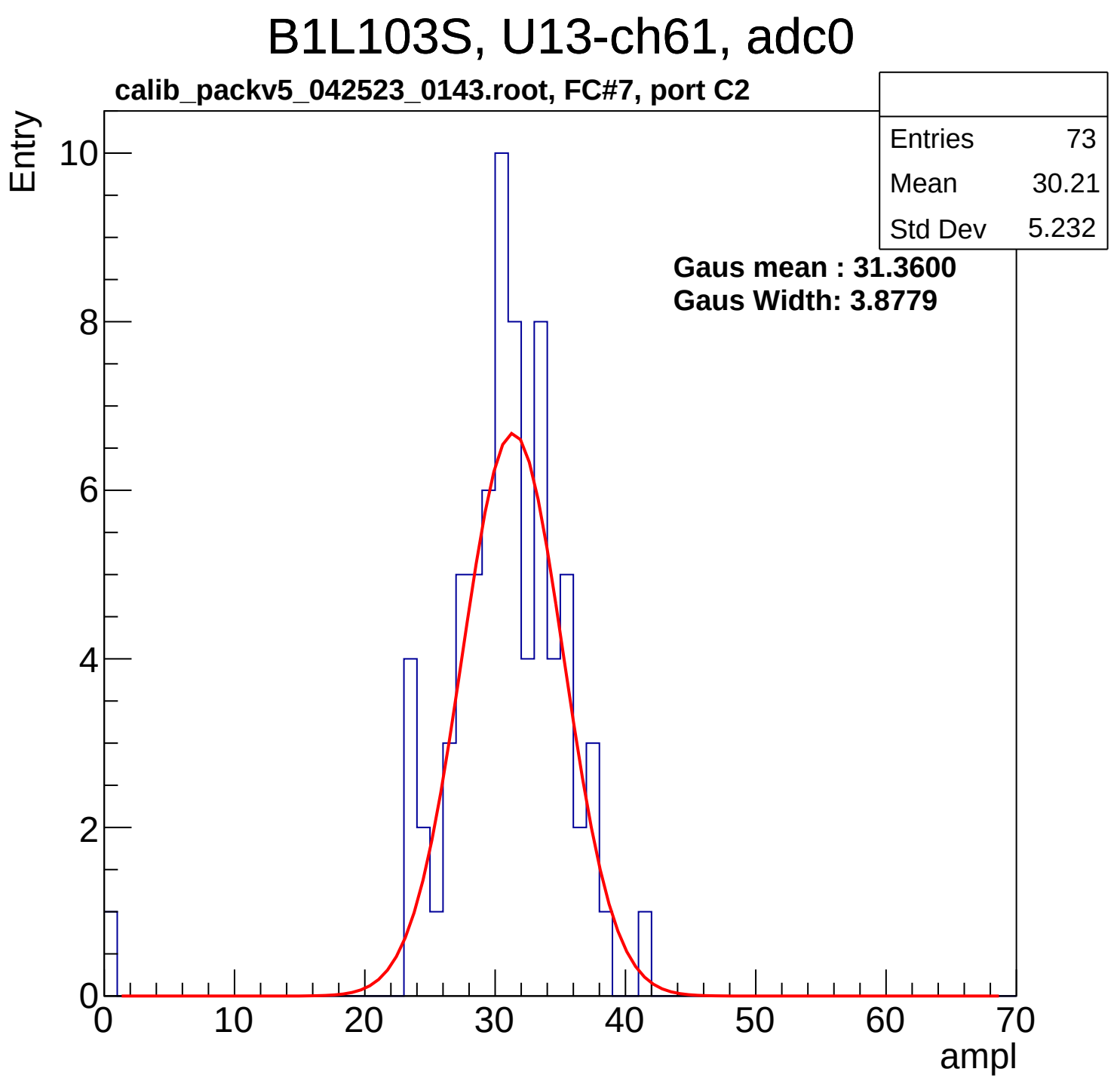
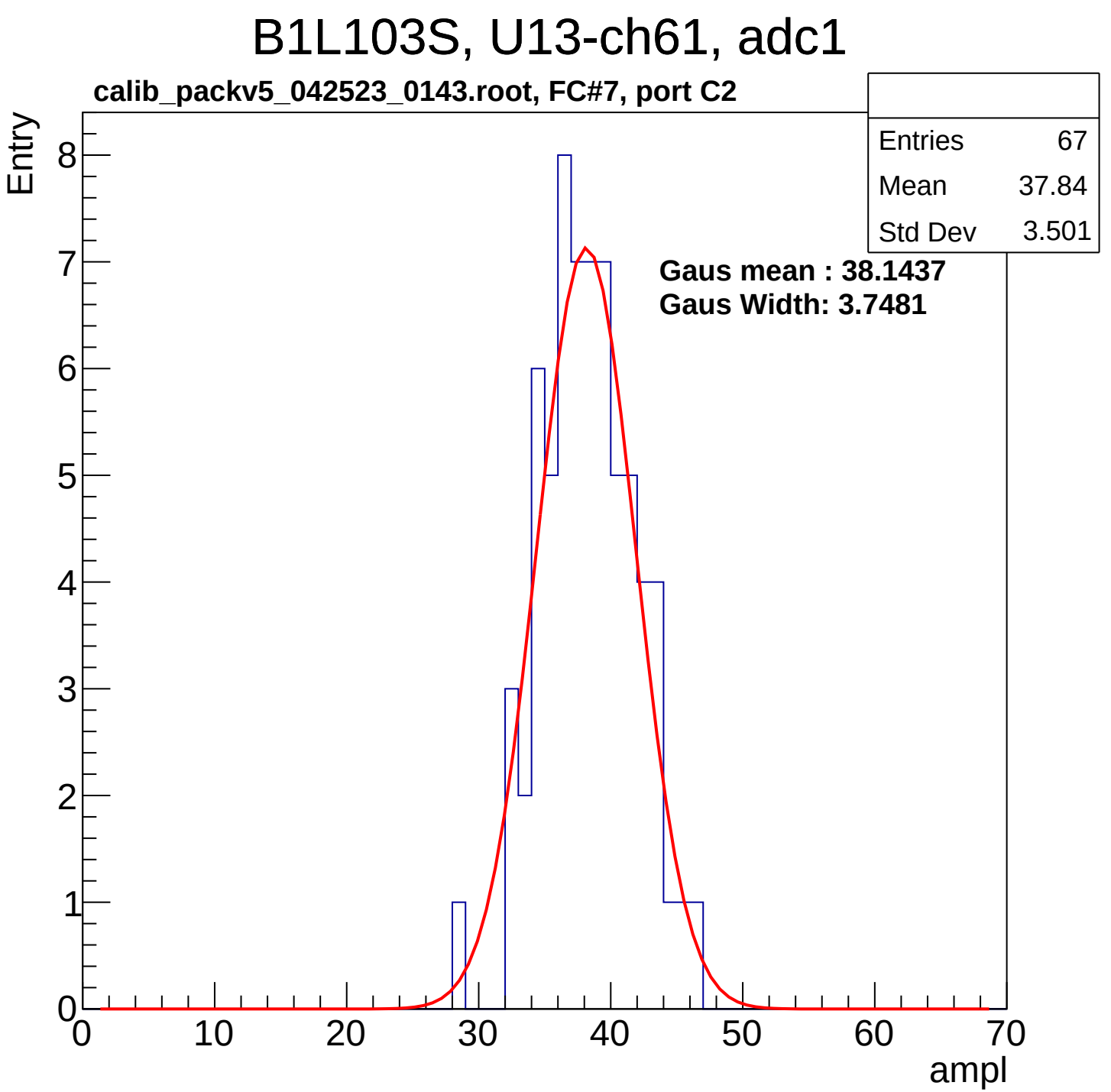
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch61, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 30.21
Std Dev 5.232
Gaus mean : 31.3600
Gaus Width: 3.8779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch61, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.84
Std Dev 3.501
Gaus mean : 38.1437
Gaus Width: 3.7481
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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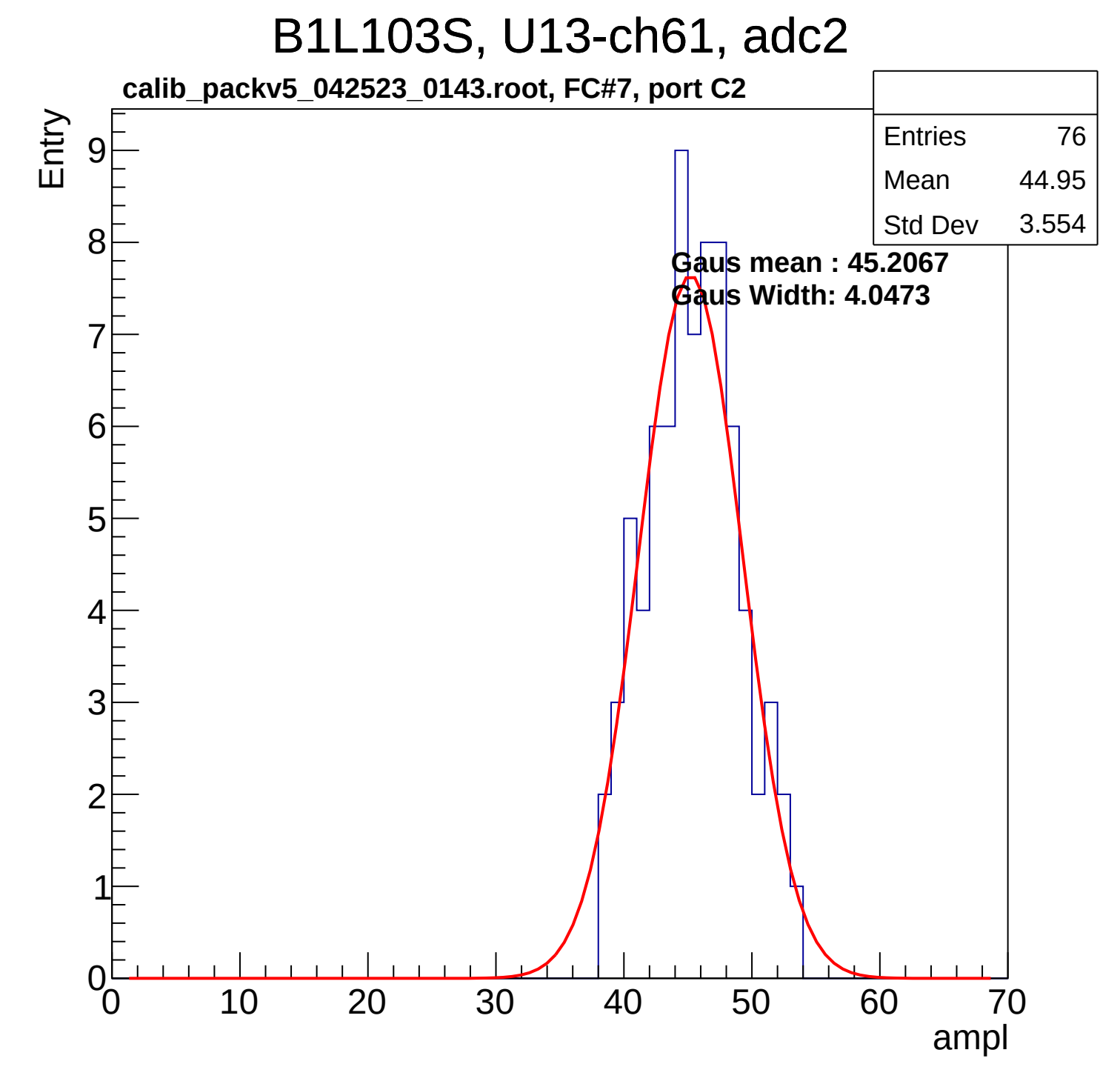
B1L103S, U13-ch61, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 44.95
Std Dev 3.554
Gaus mean : 45.2067
Gaus Width: 4.0473
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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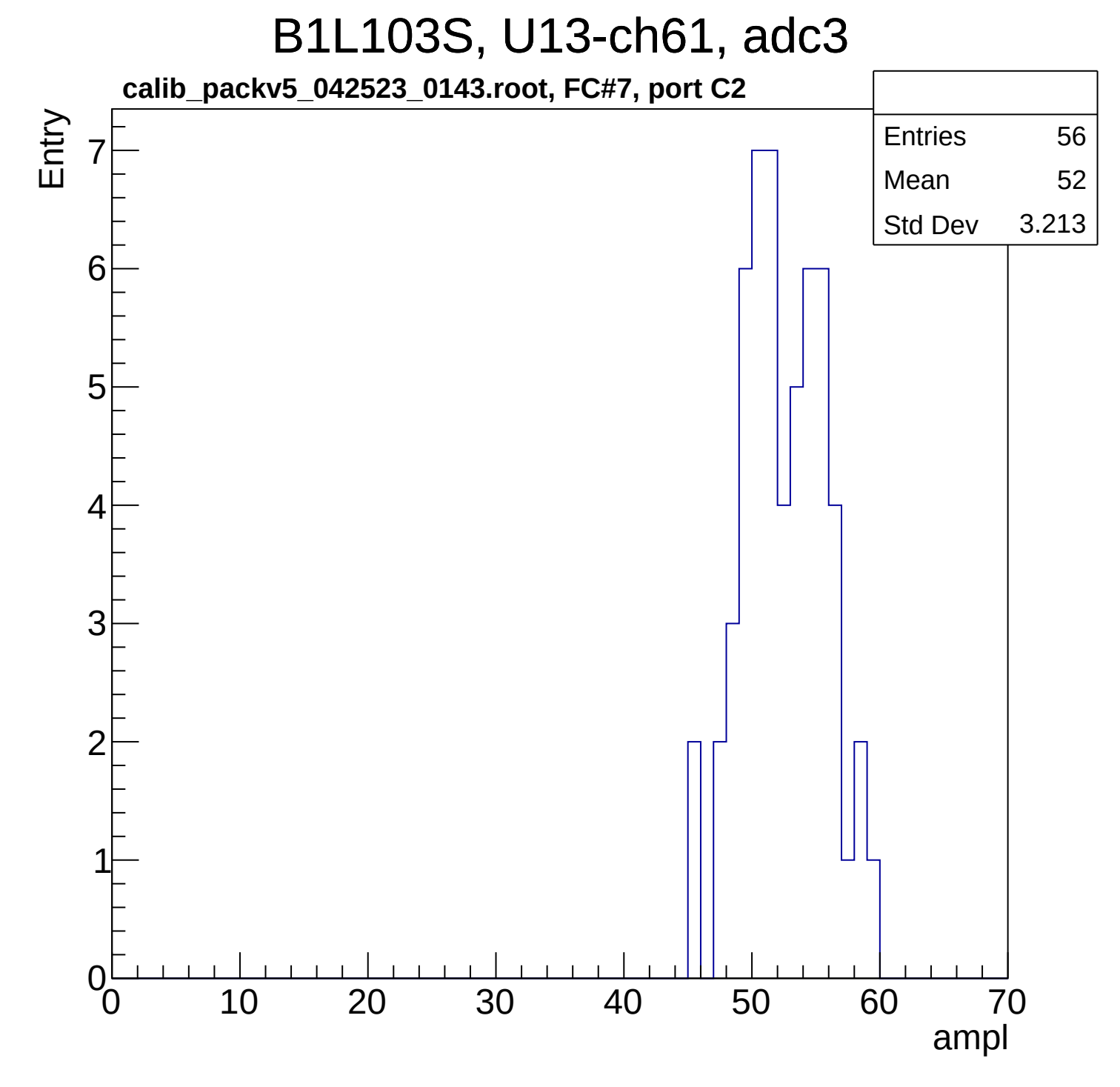
B1L103S, U13-ch61, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 52
Std Dev 3.213
Entry
ampl
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4
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7
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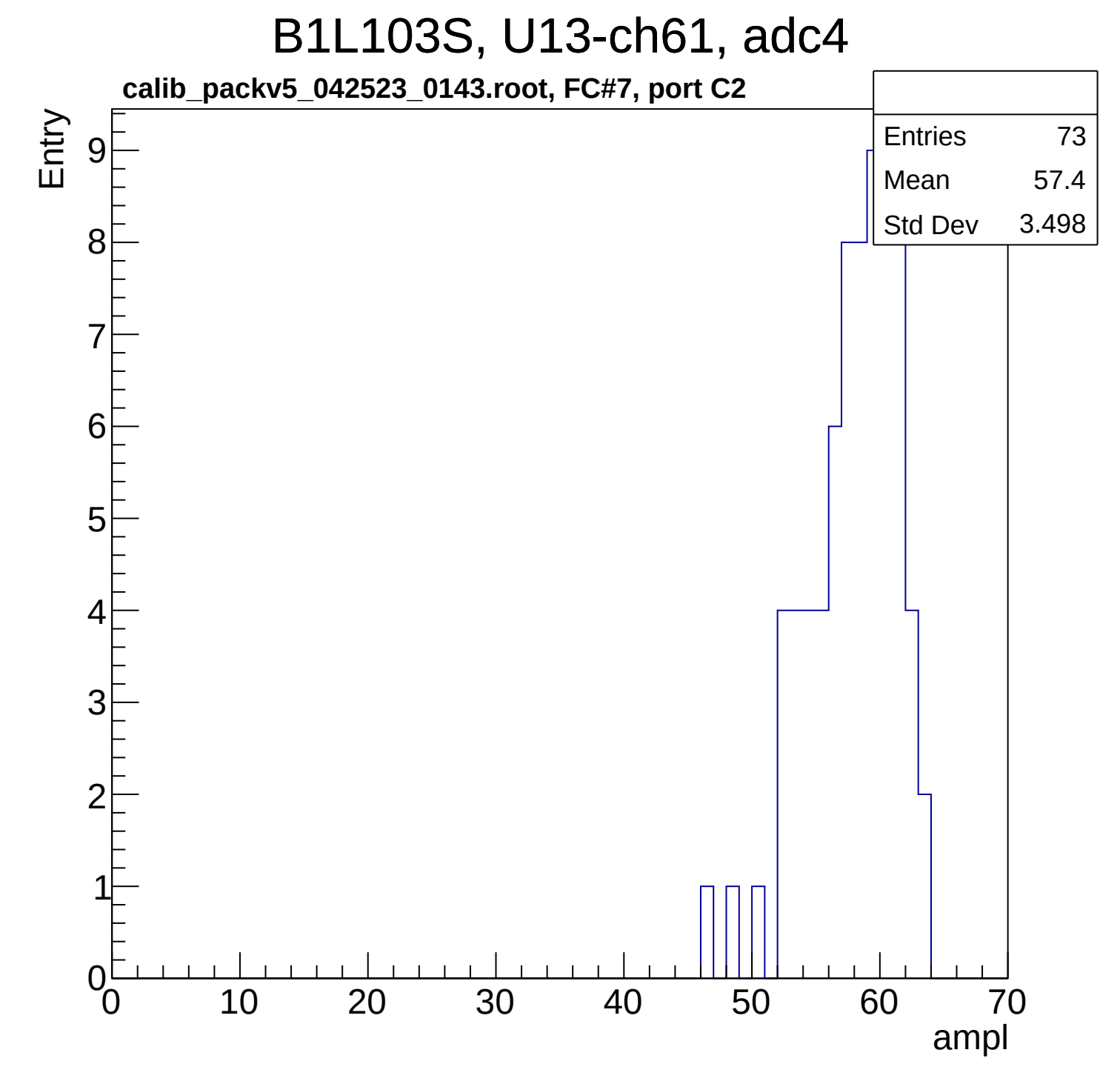
B1L103S, U13-ch61, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 57.4
Std Dev 3.498
Entry
ampl
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3
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0
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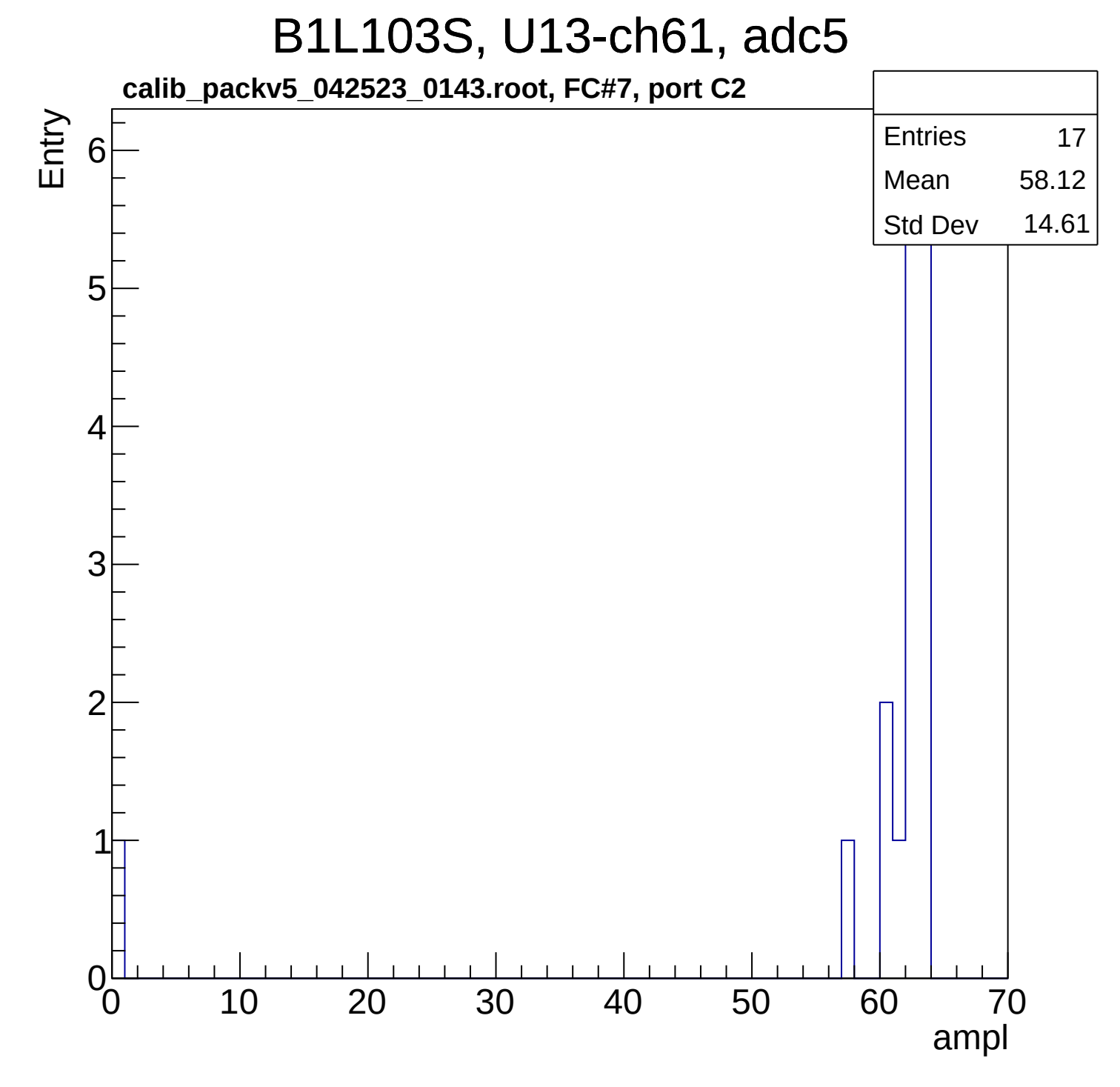
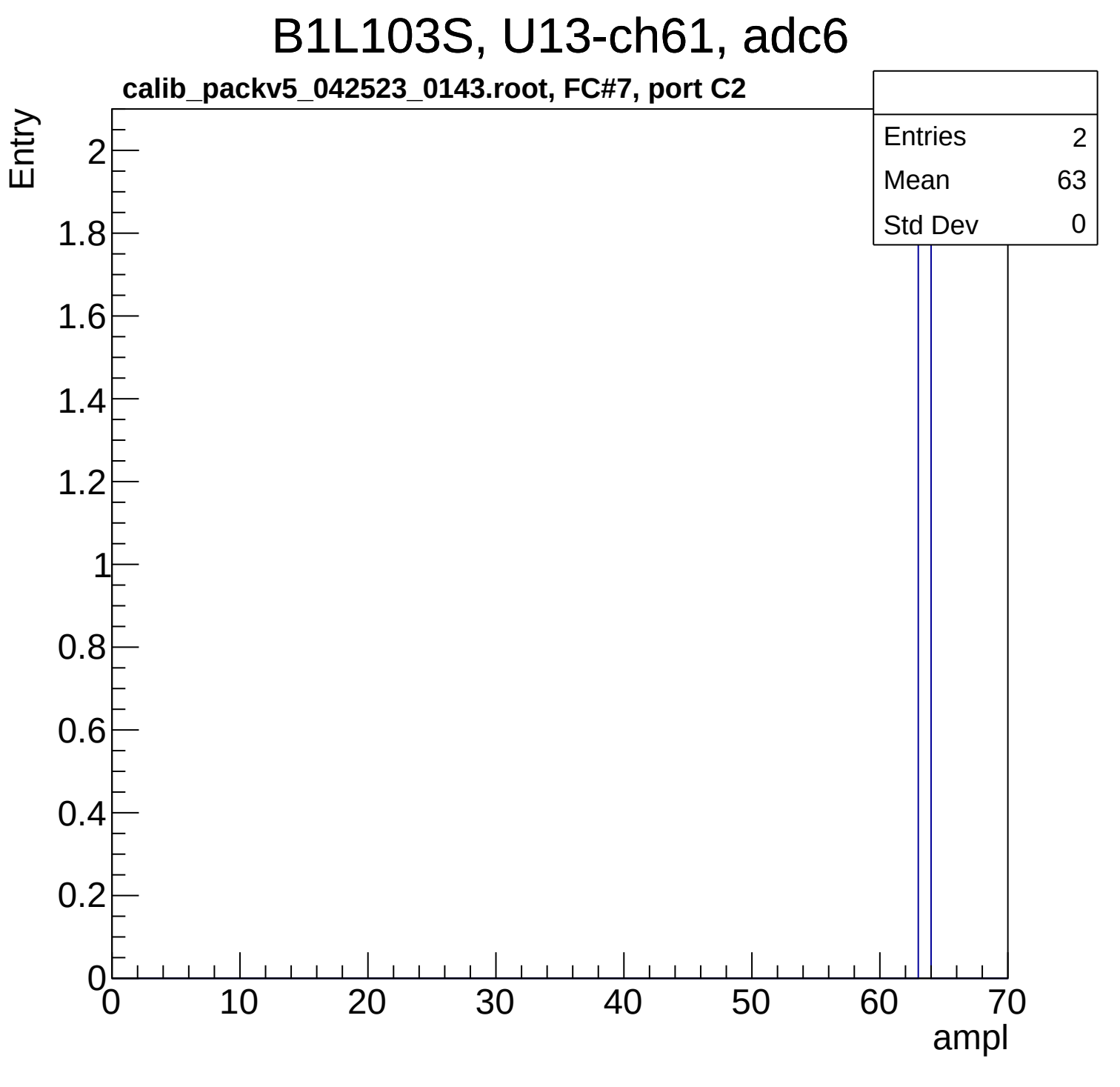

B1L103S, U13-ch61, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 58.12
Std Dev 14.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch61, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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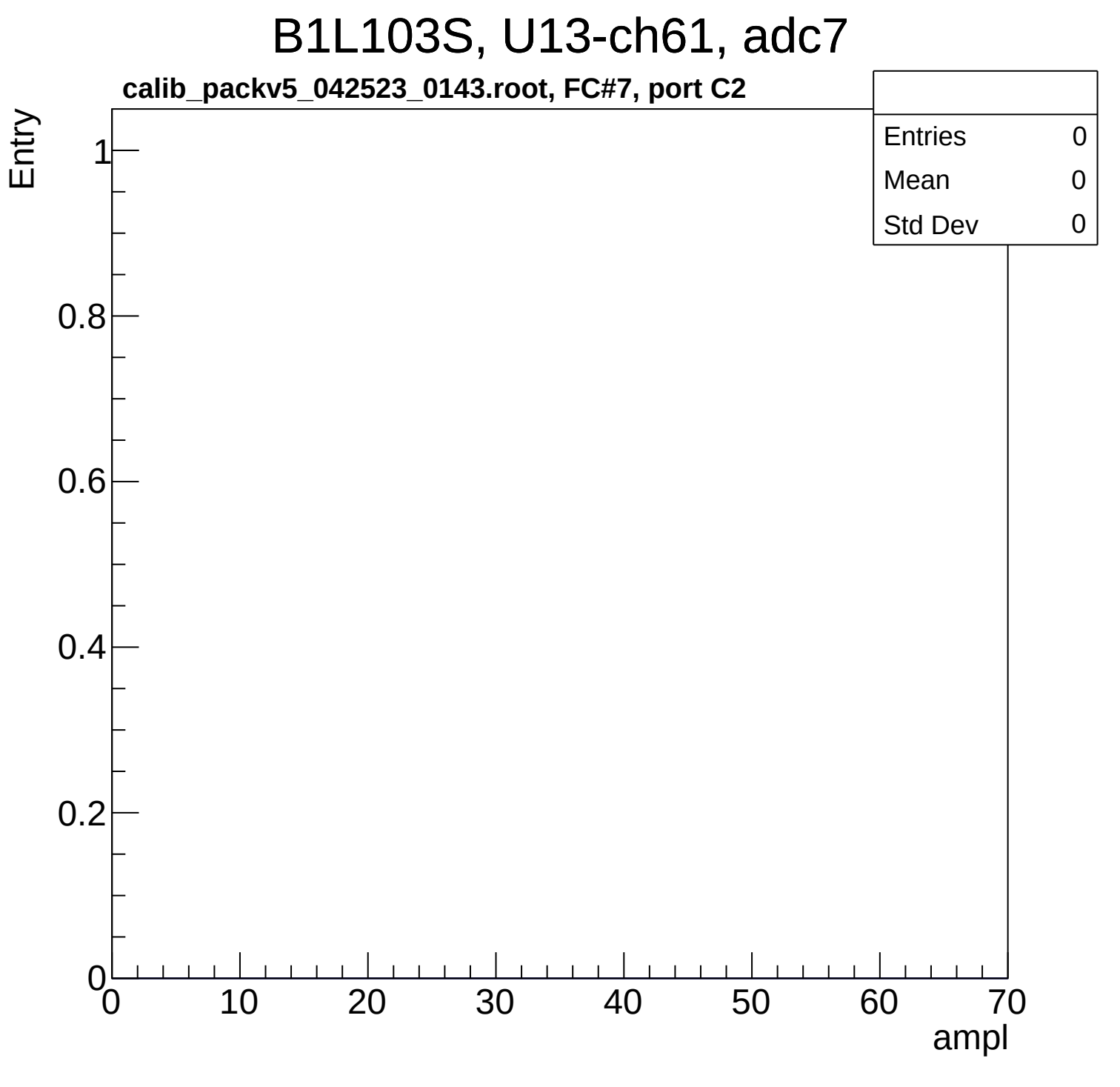
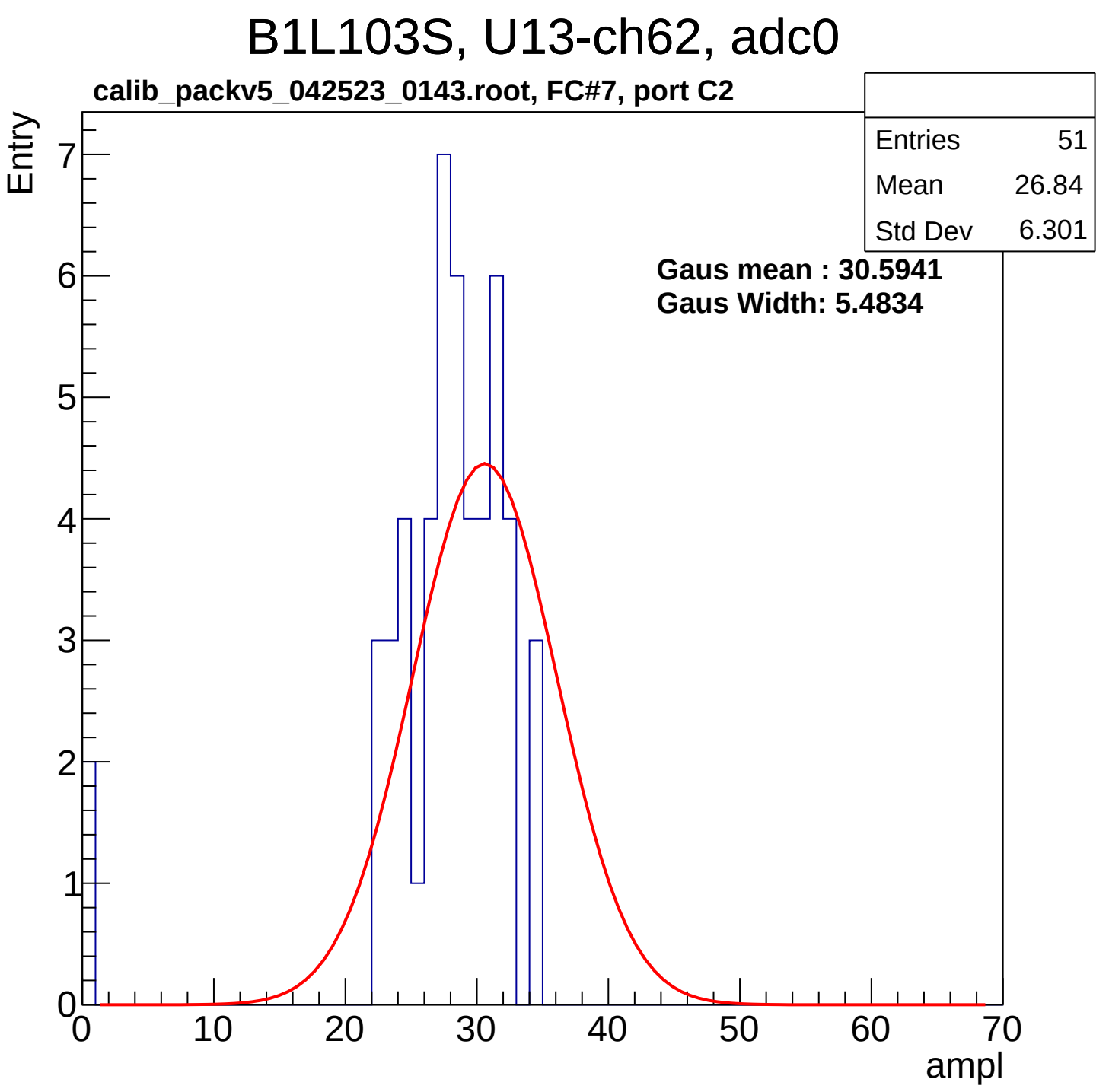

B1L103S, U13-ch61, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch62, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 26.84
Std Dev 6.301
Gaus mean : 30.5941
Gaus Width: 5.4834
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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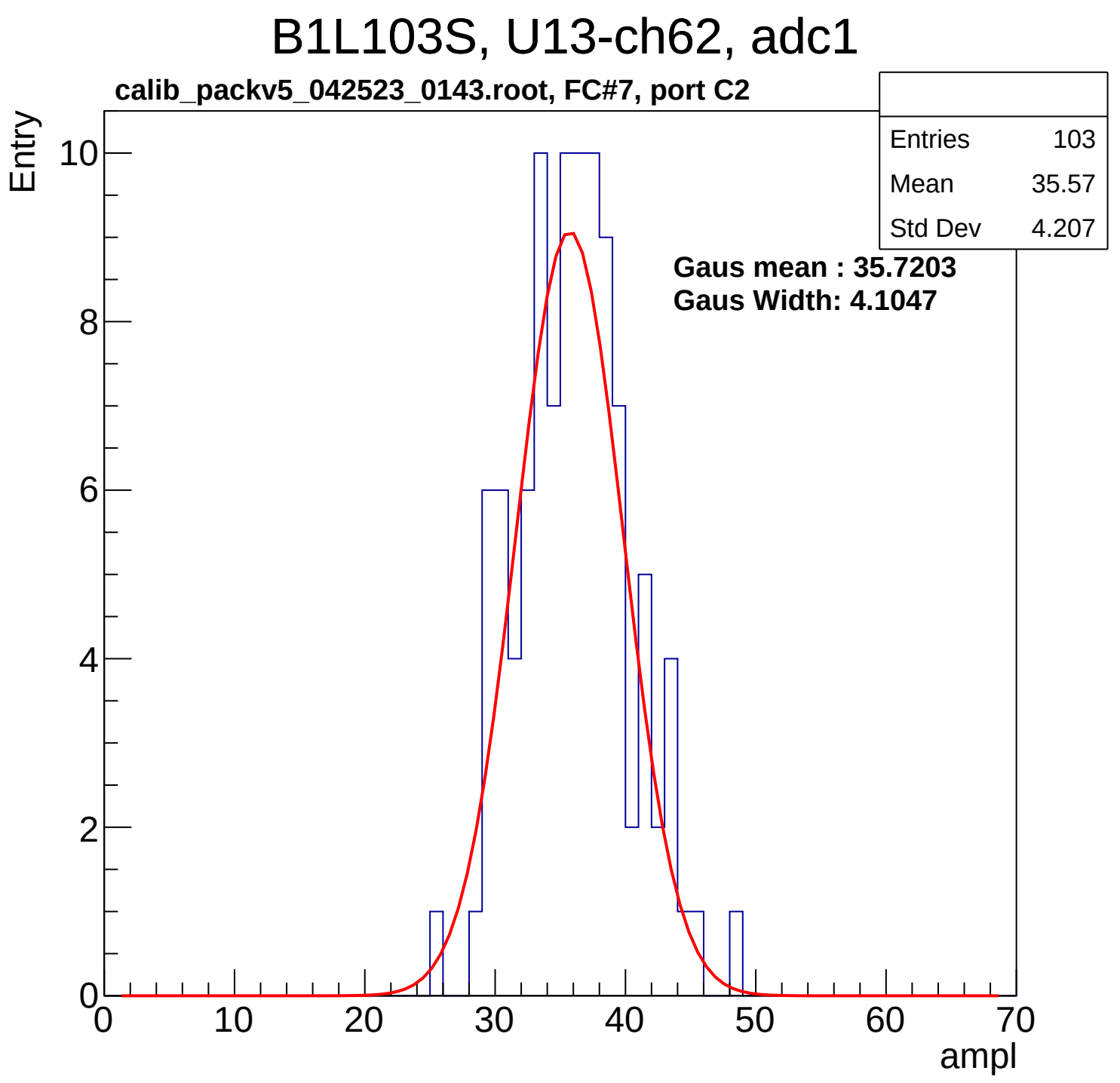
B1L103S, U13-ch62, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 103
Mean 35.57
Std Dev 4.207
Gaus mean : 35.7203
Gaus Width: 4.1047
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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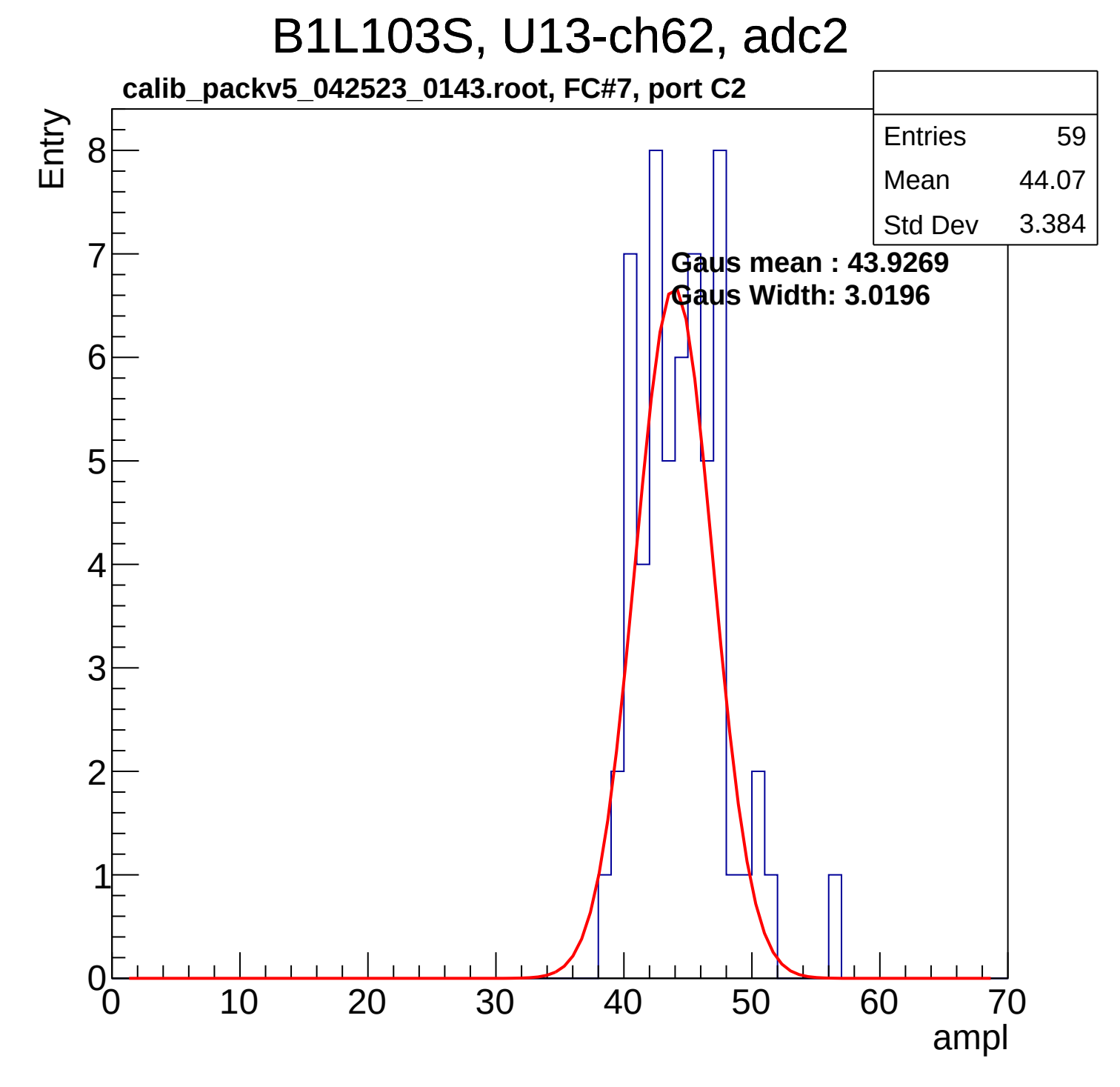
B1L103S, U13-ch62, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 44.07
Std Dev 3.384
Gaus mean : 43.9269
Gaus Width: 3.0196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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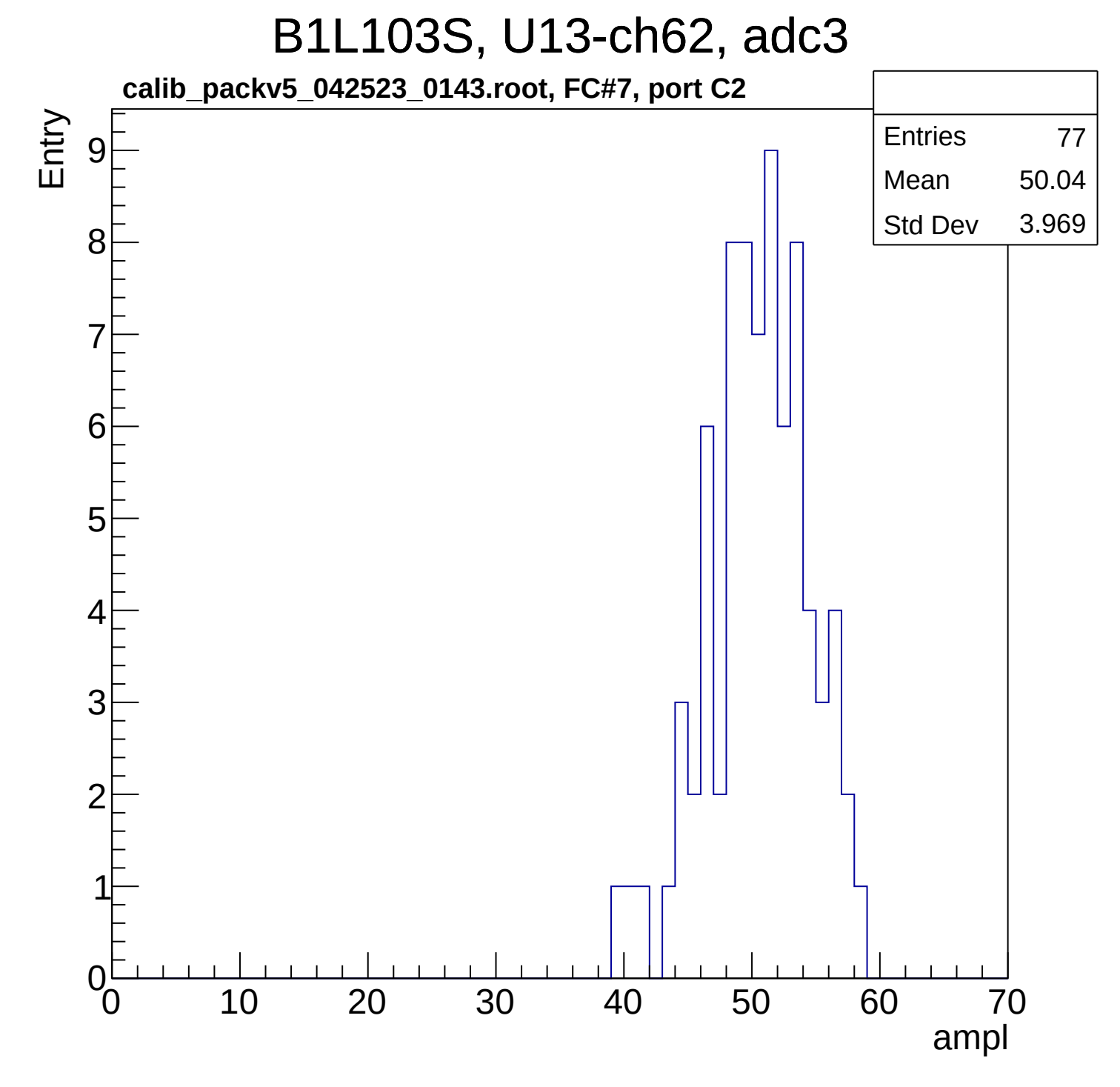
B1L103S, U13-ch62, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 50.04
Std Dev 3.969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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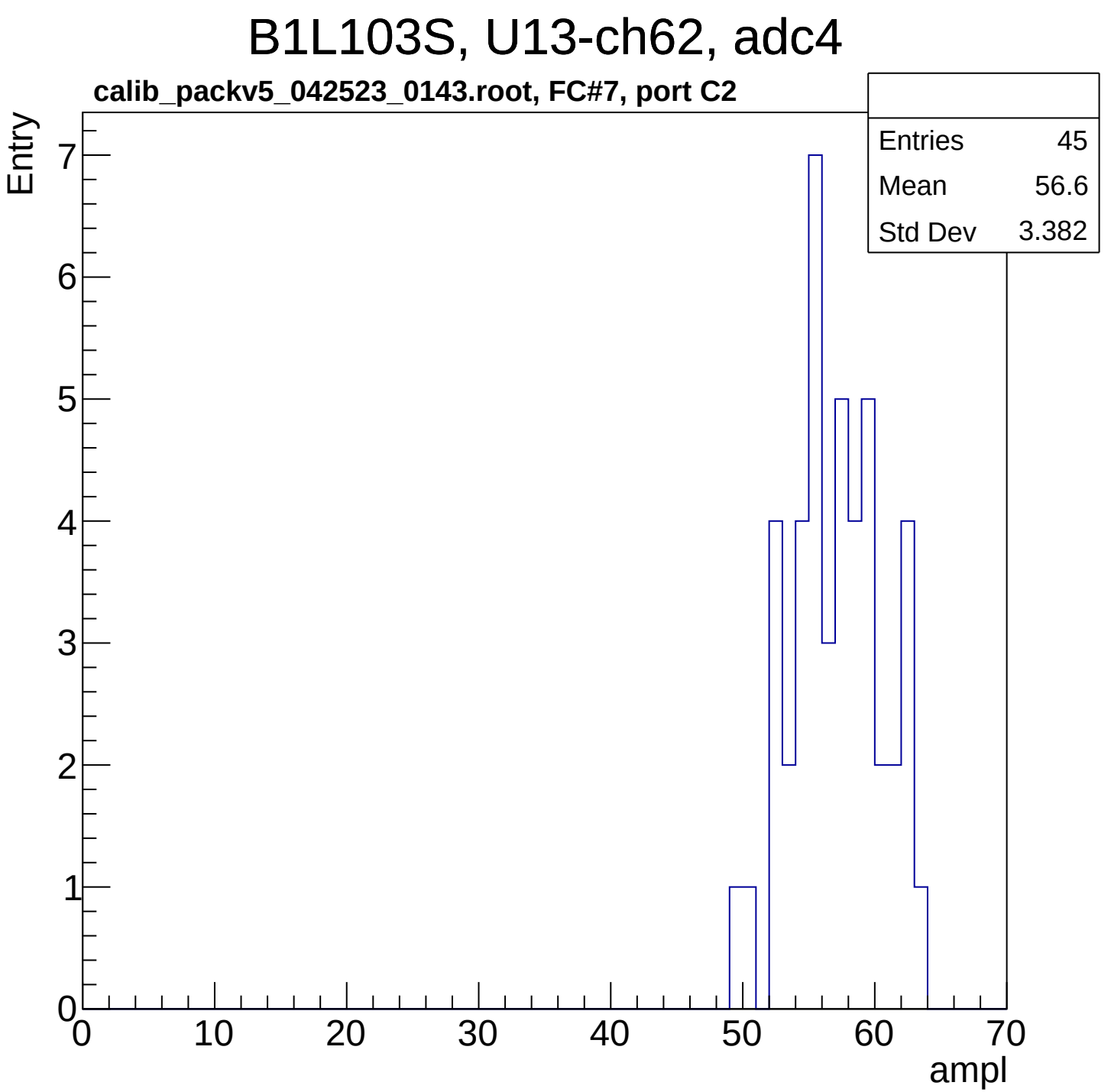
B1L103S, U13-ch62, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 56.6
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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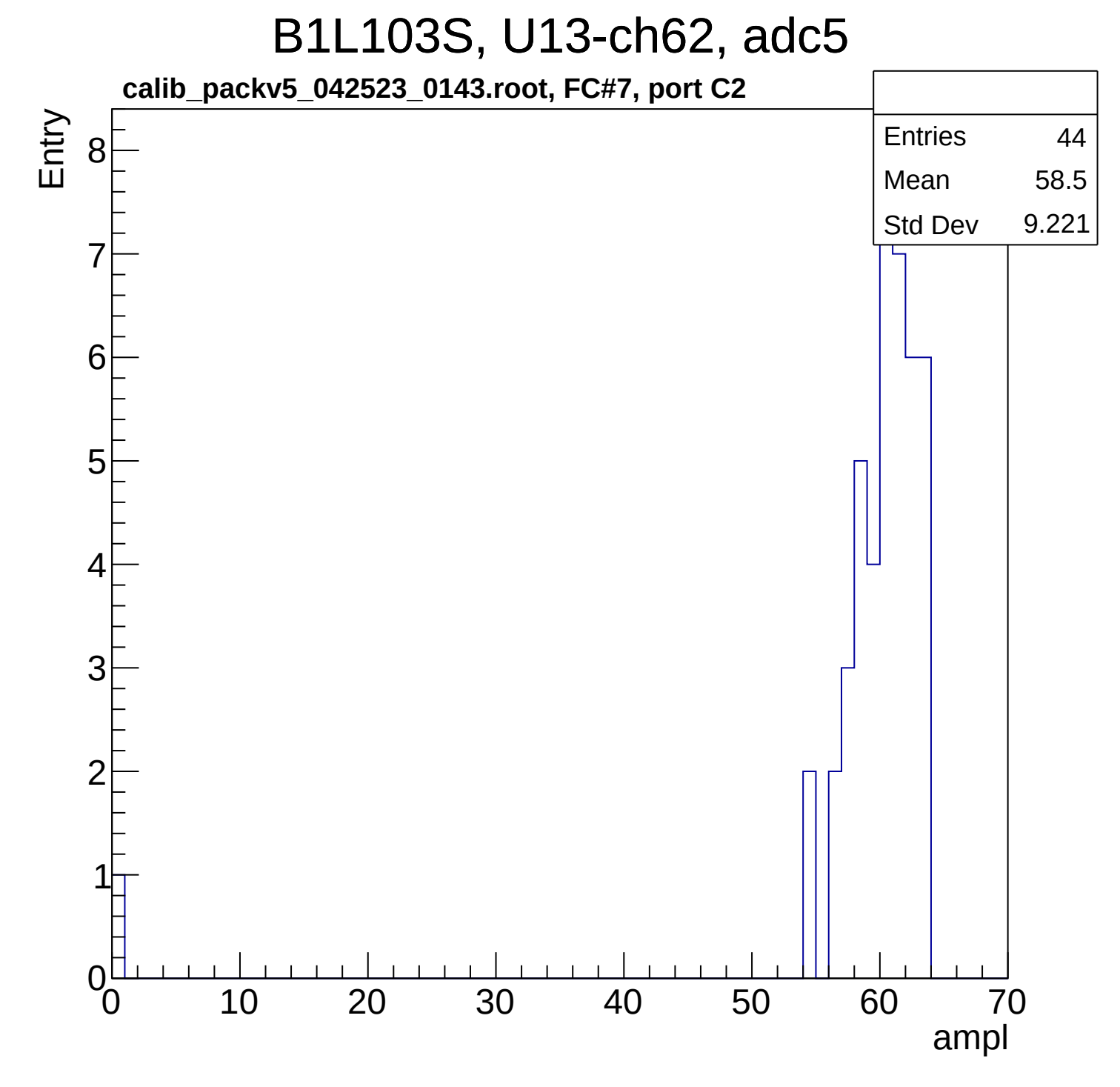
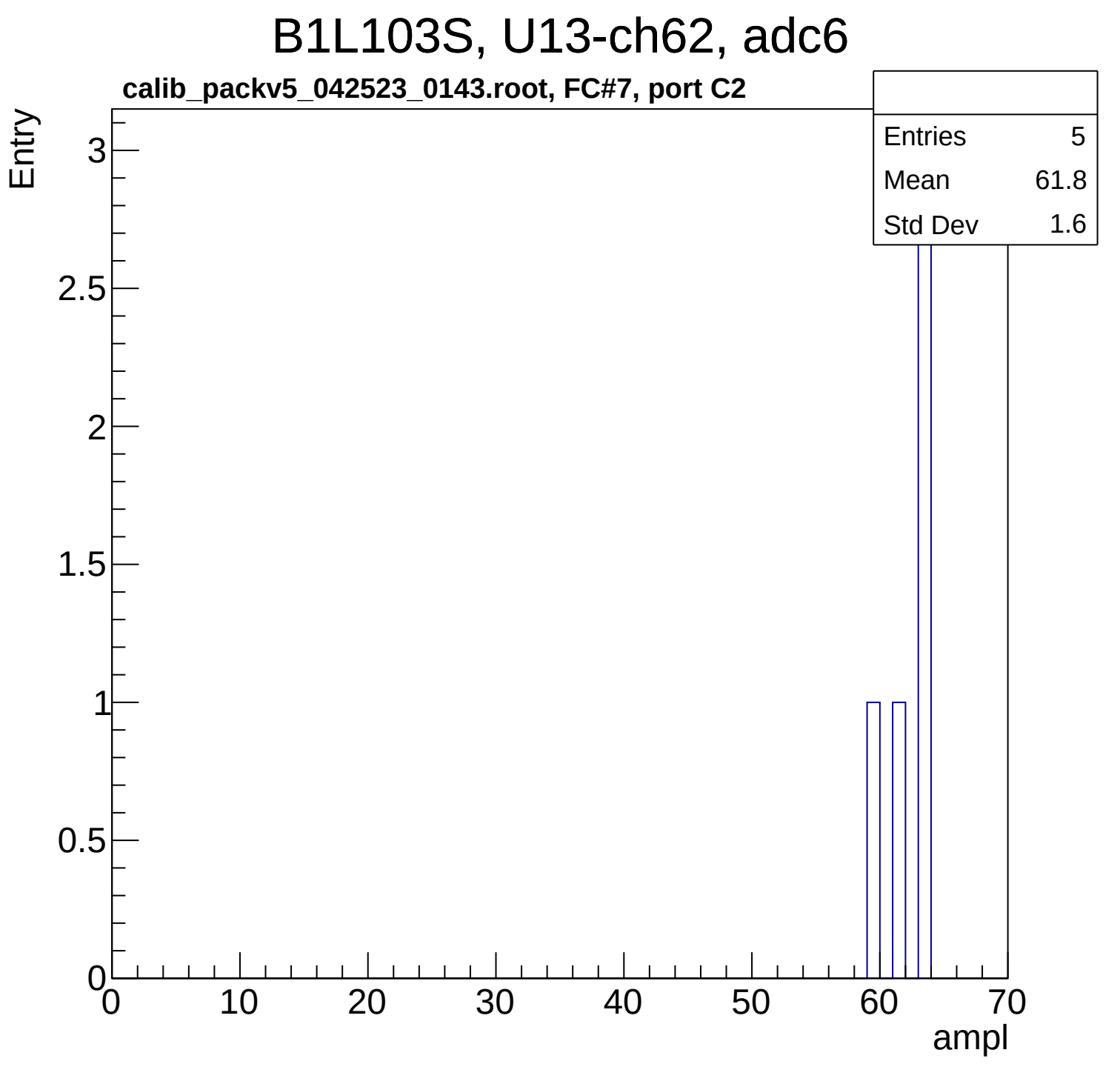
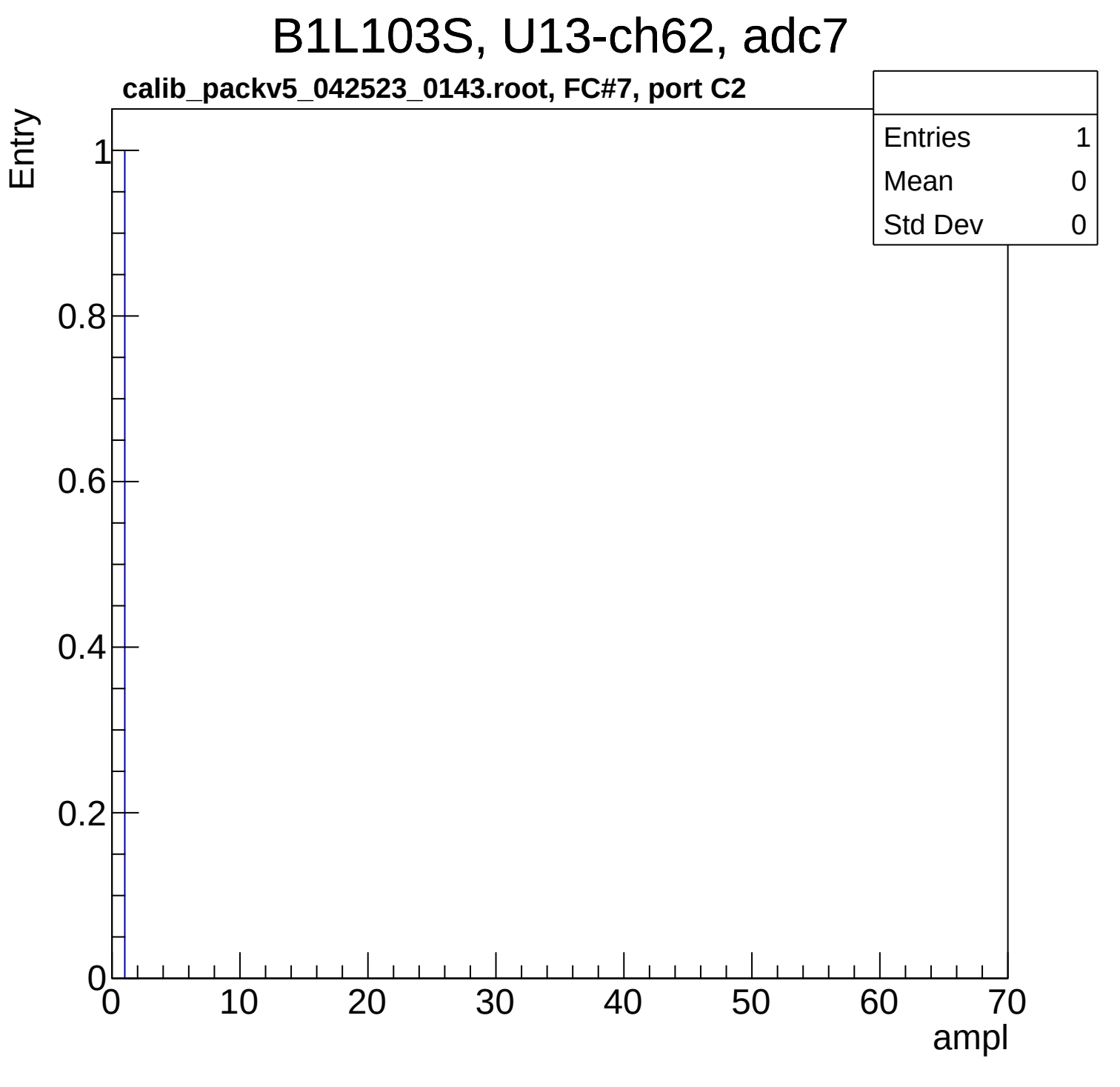
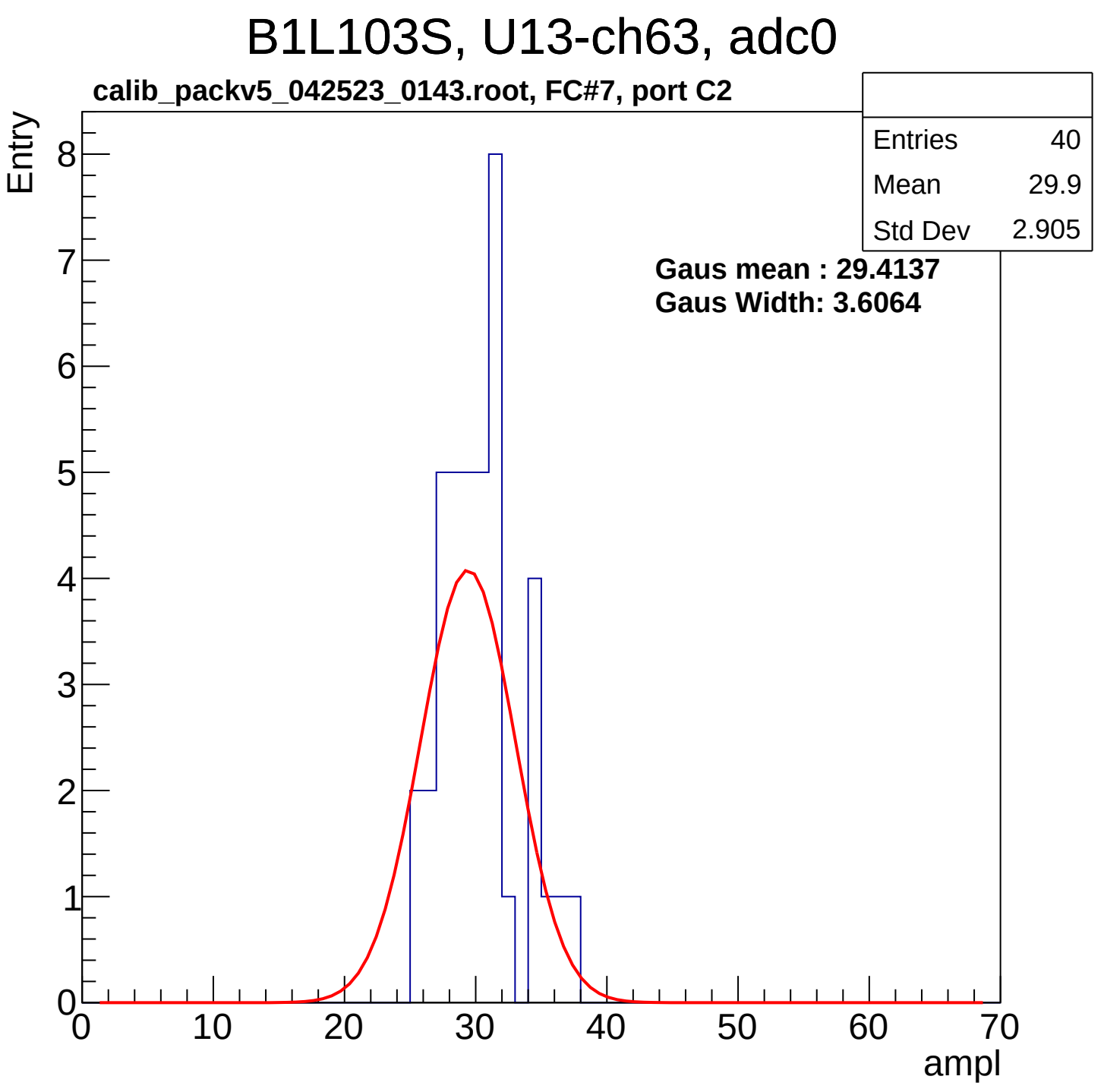
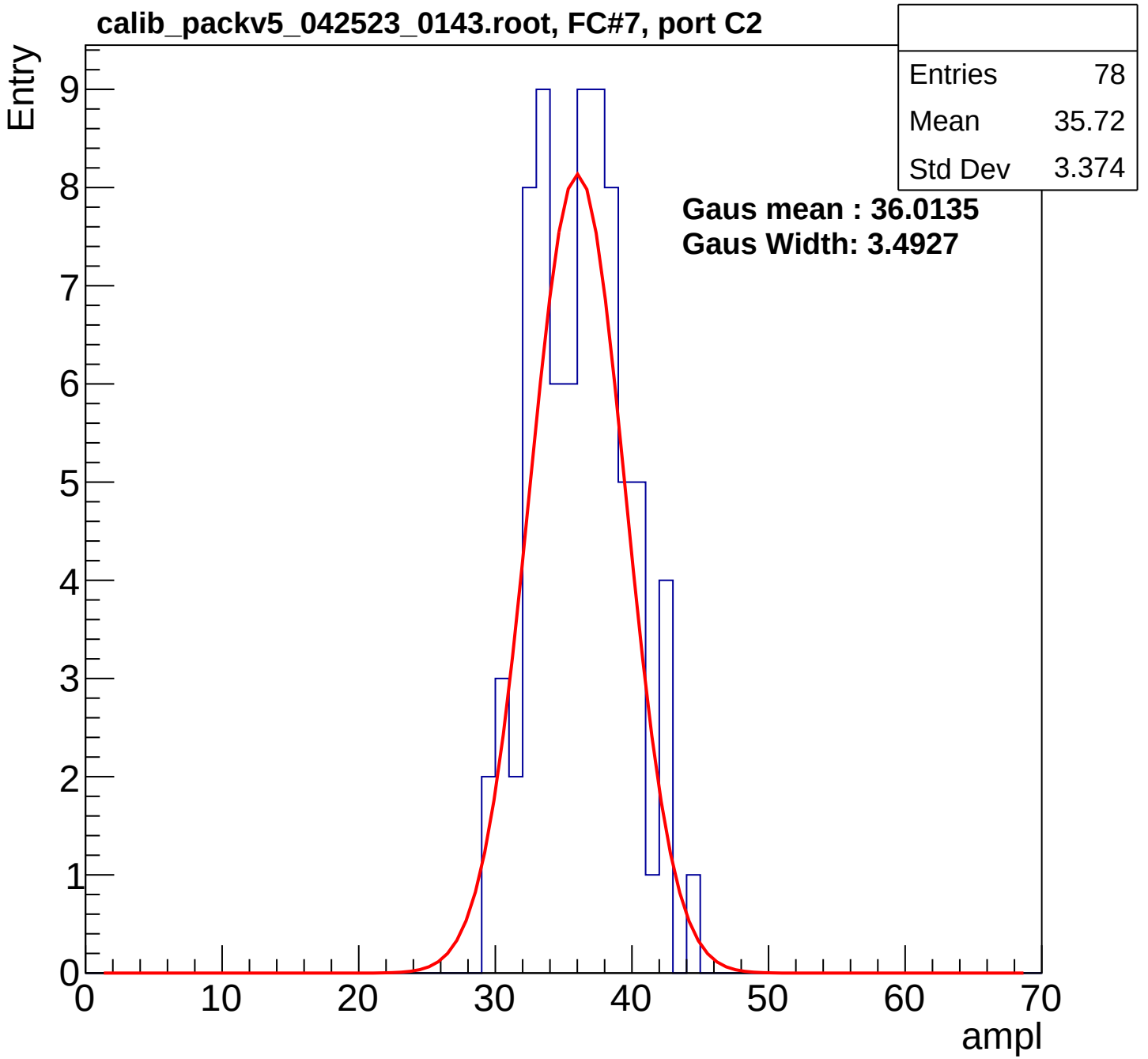
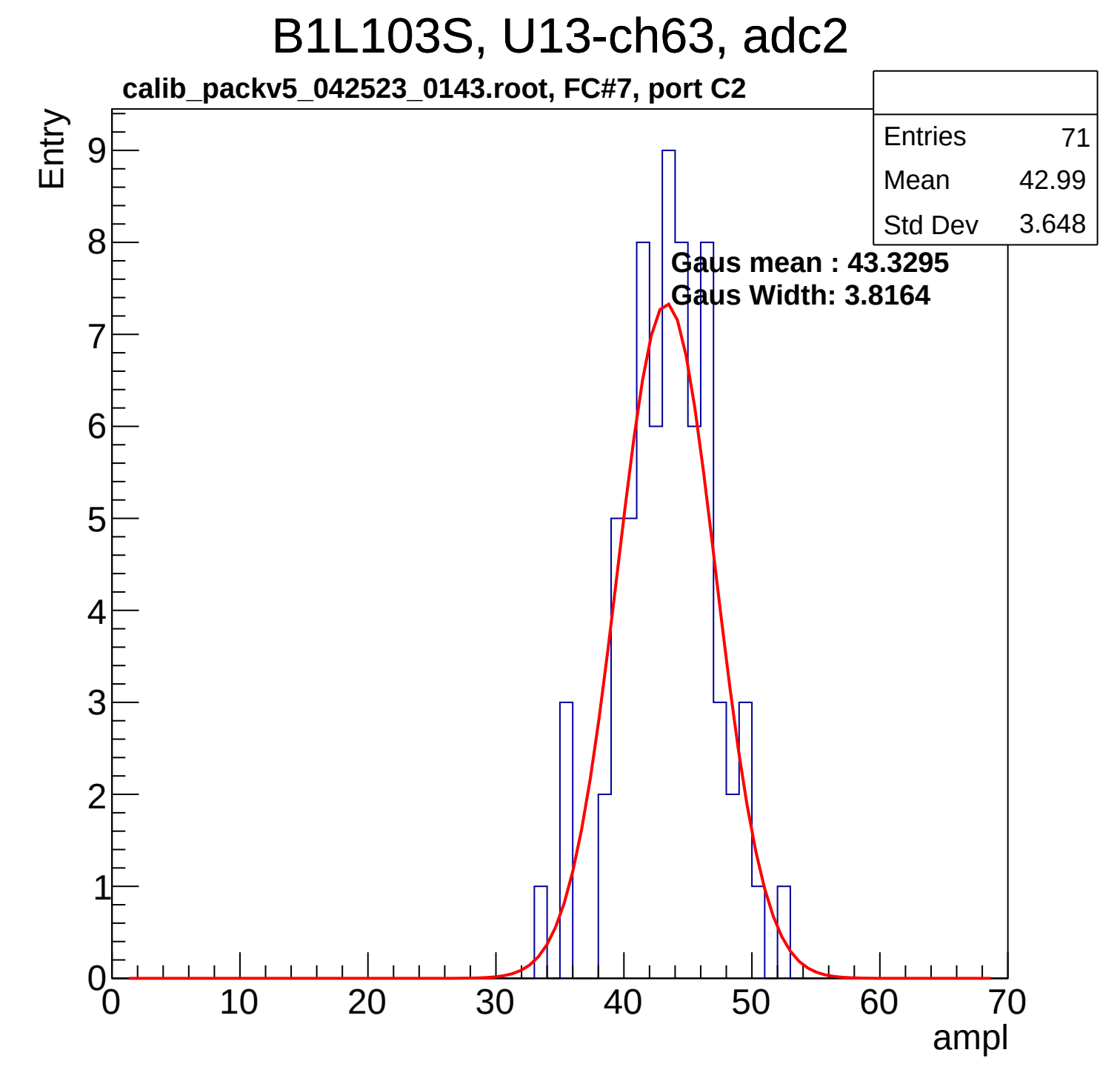

B1L103S, U13-ch62, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.5
Std Dev 9.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

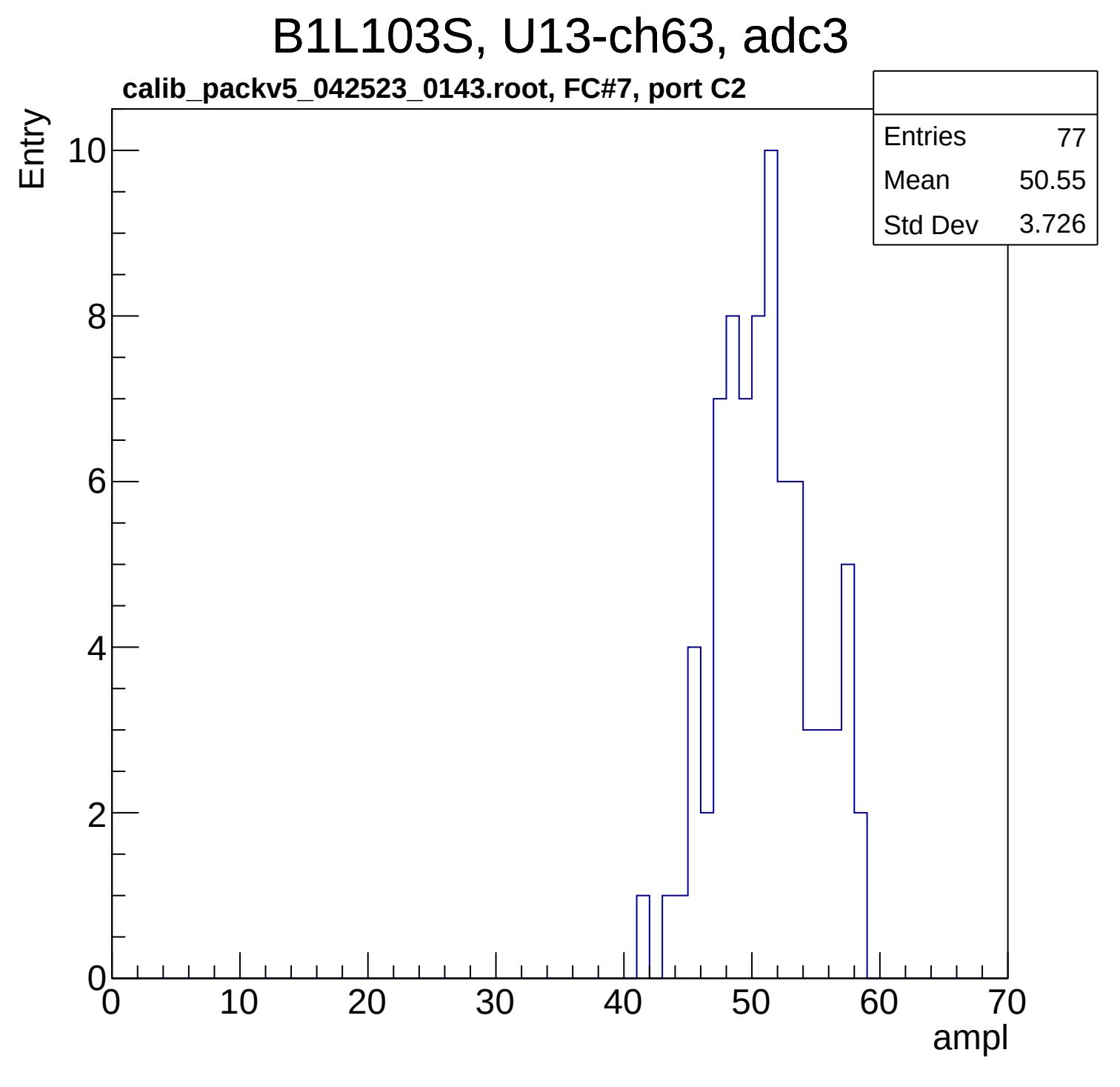

B1L103S, U13-ch62, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.8
Std Dev 1.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

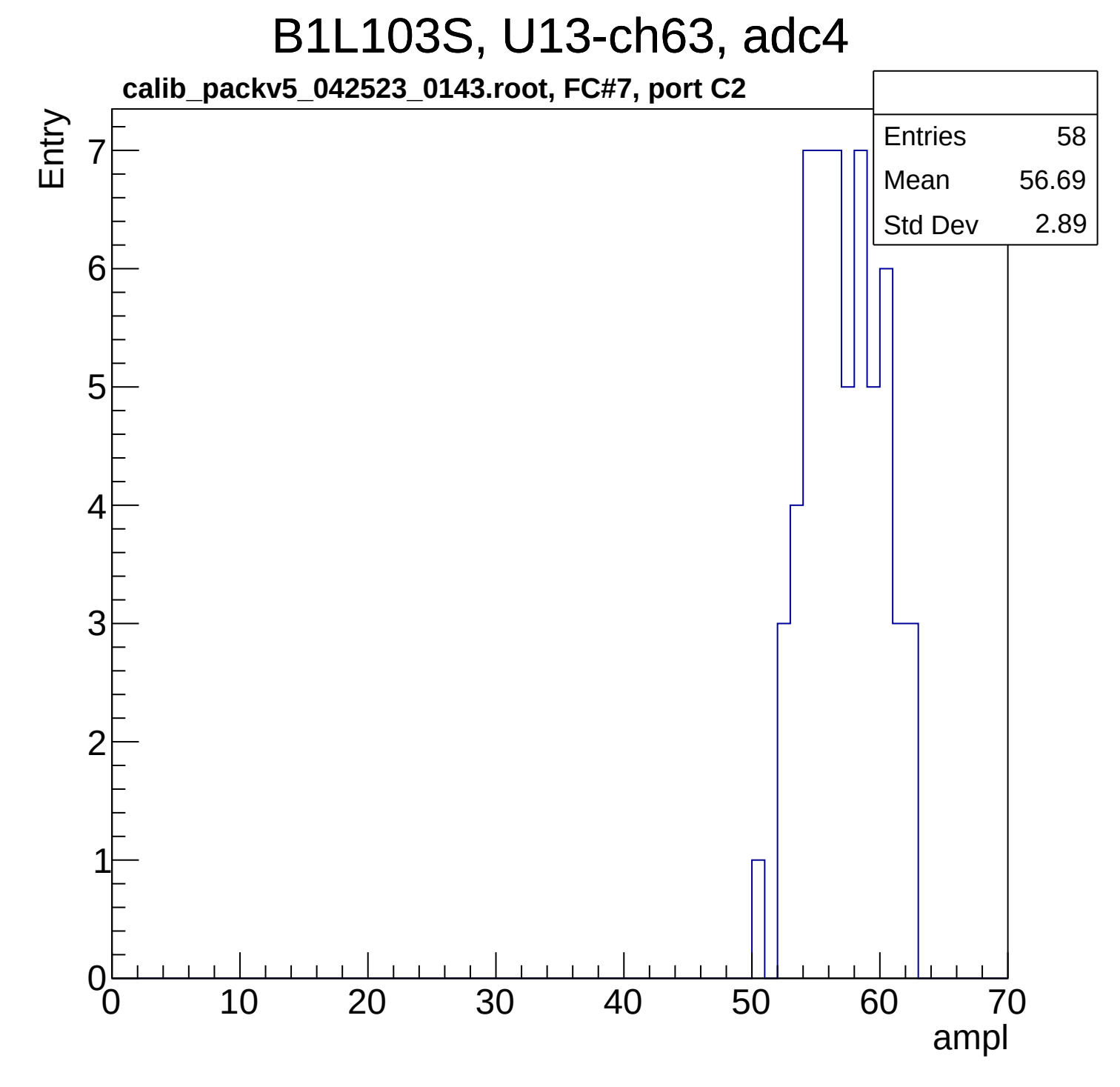

B1L103S, U13-ch62, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

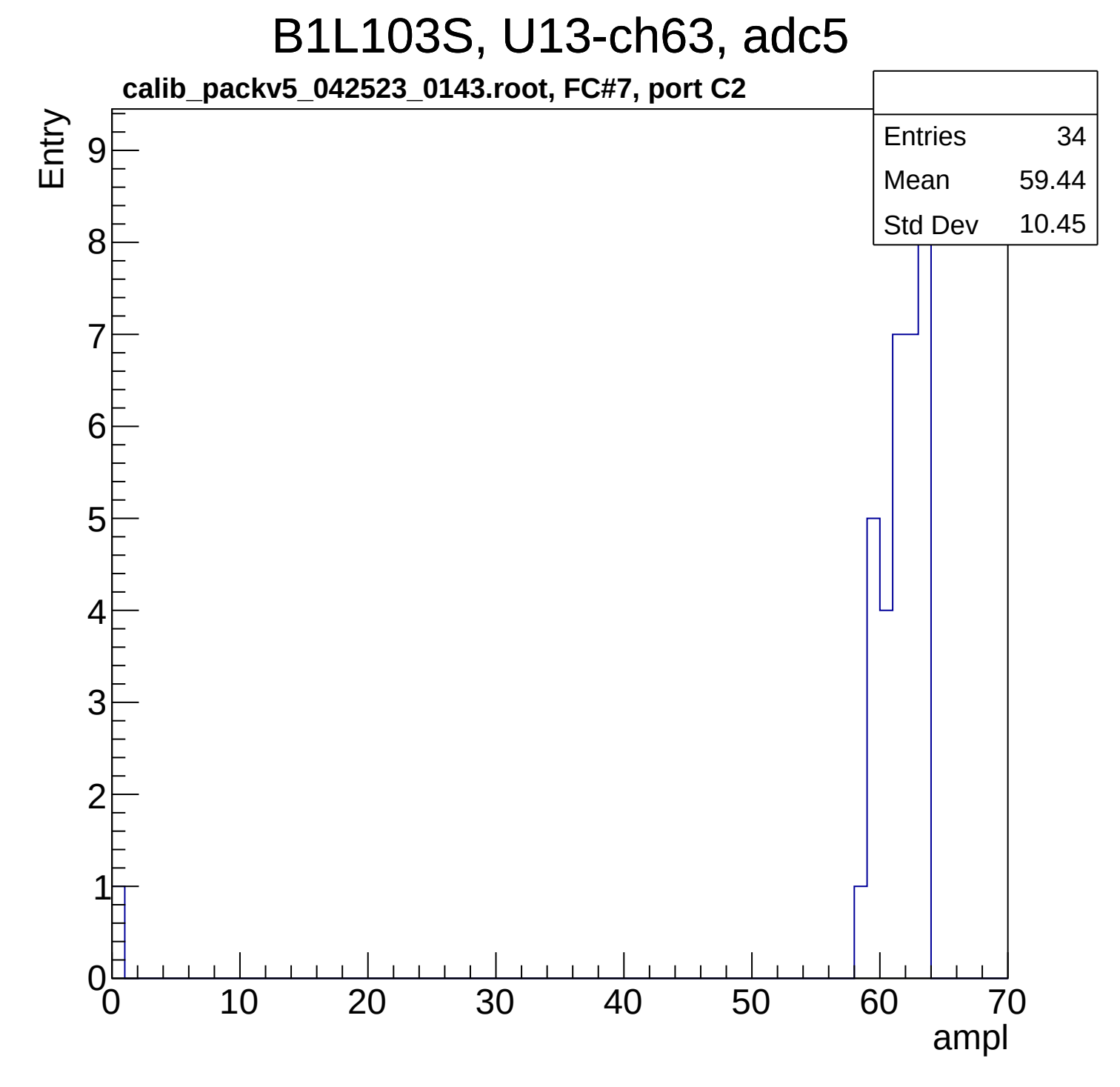

B1L103S, U13-ch63, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 29.9
Std Dev 2.905
Gaus mean : 29.4137
Gaus Width: 3.6064
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch63, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 35.72
Std Dev 3.374
Gaus mean : 36.0135
Gaus Width: 3.4927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch63, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.99
Std Dev 3.648
Gaus mean : 43.3295
Gaus Width: 3.8164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch63, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 50.55
Std Dev 3.726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch63, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.69
Std Dev 2.89
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch63, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59.44
Std Dev 10.45
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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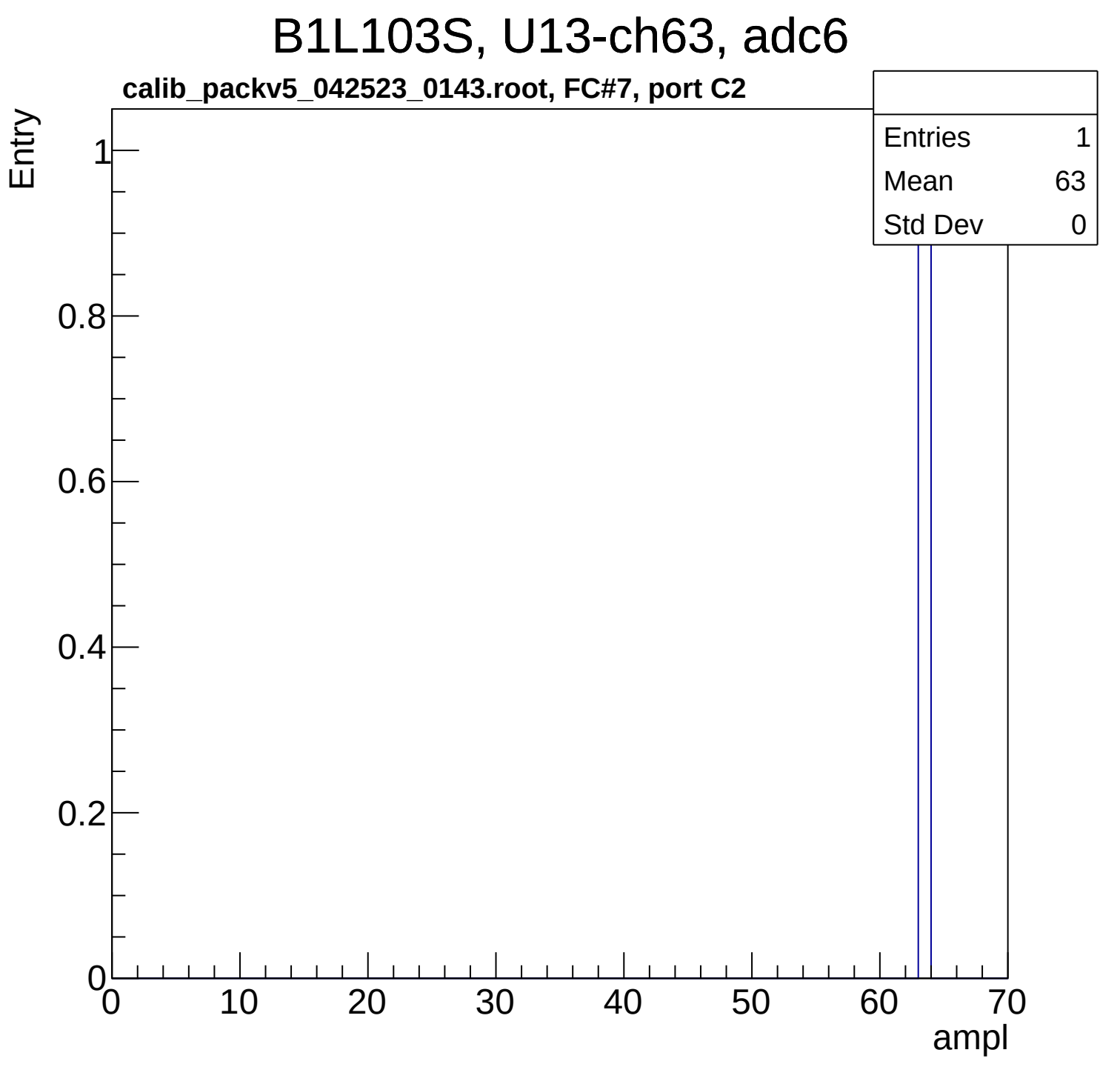
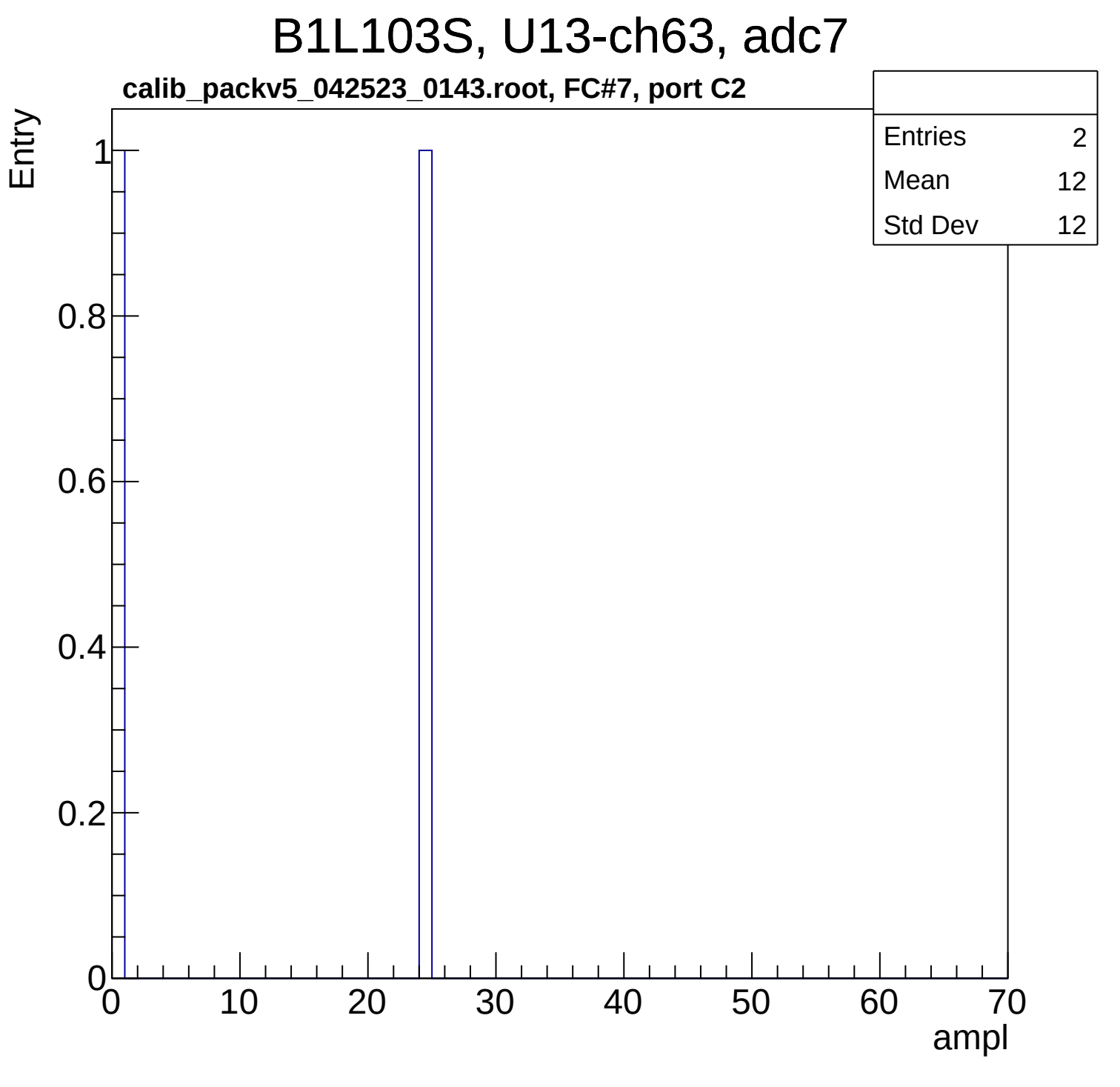

B1L103S, U13-ch63, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch63, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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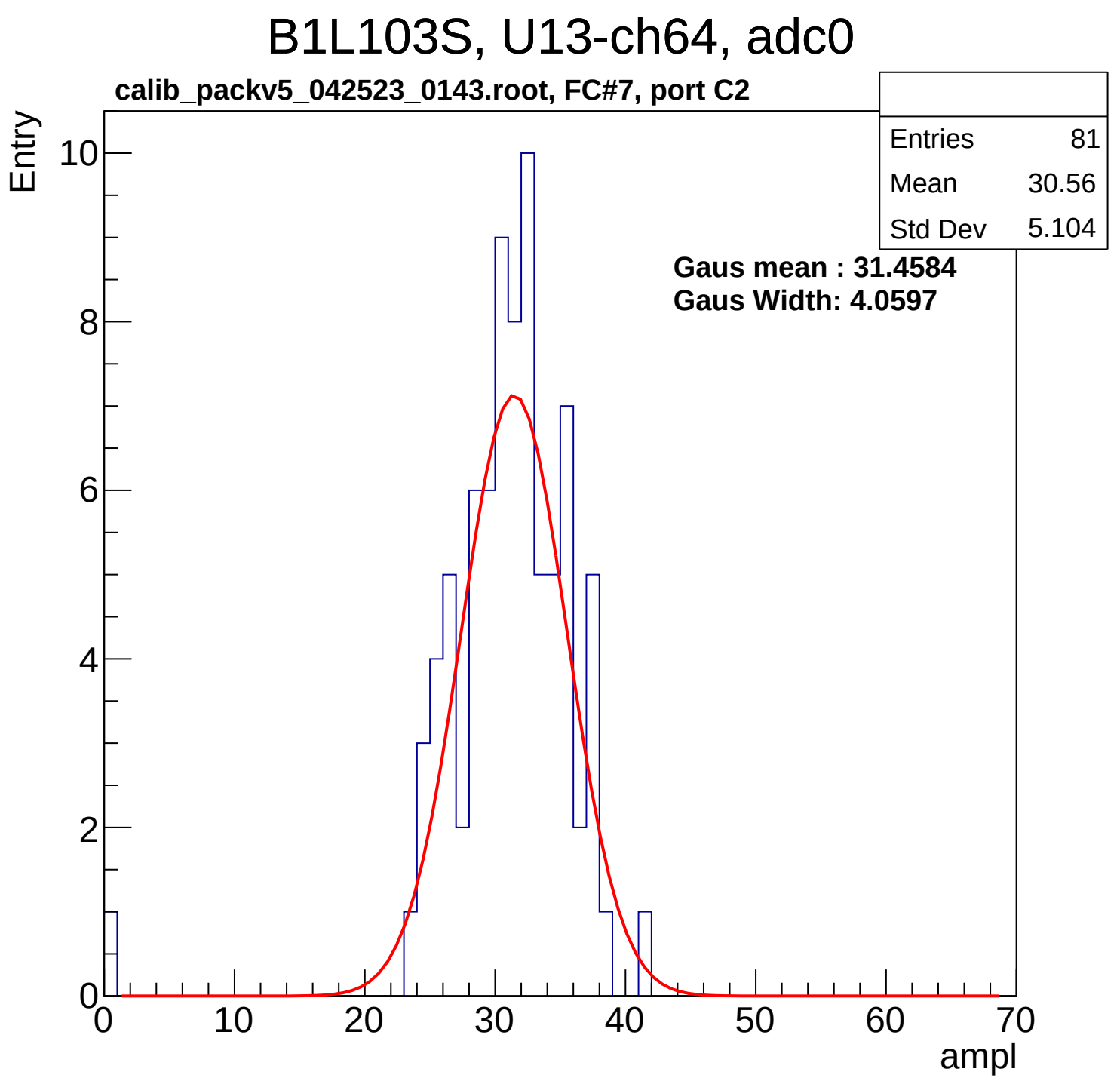
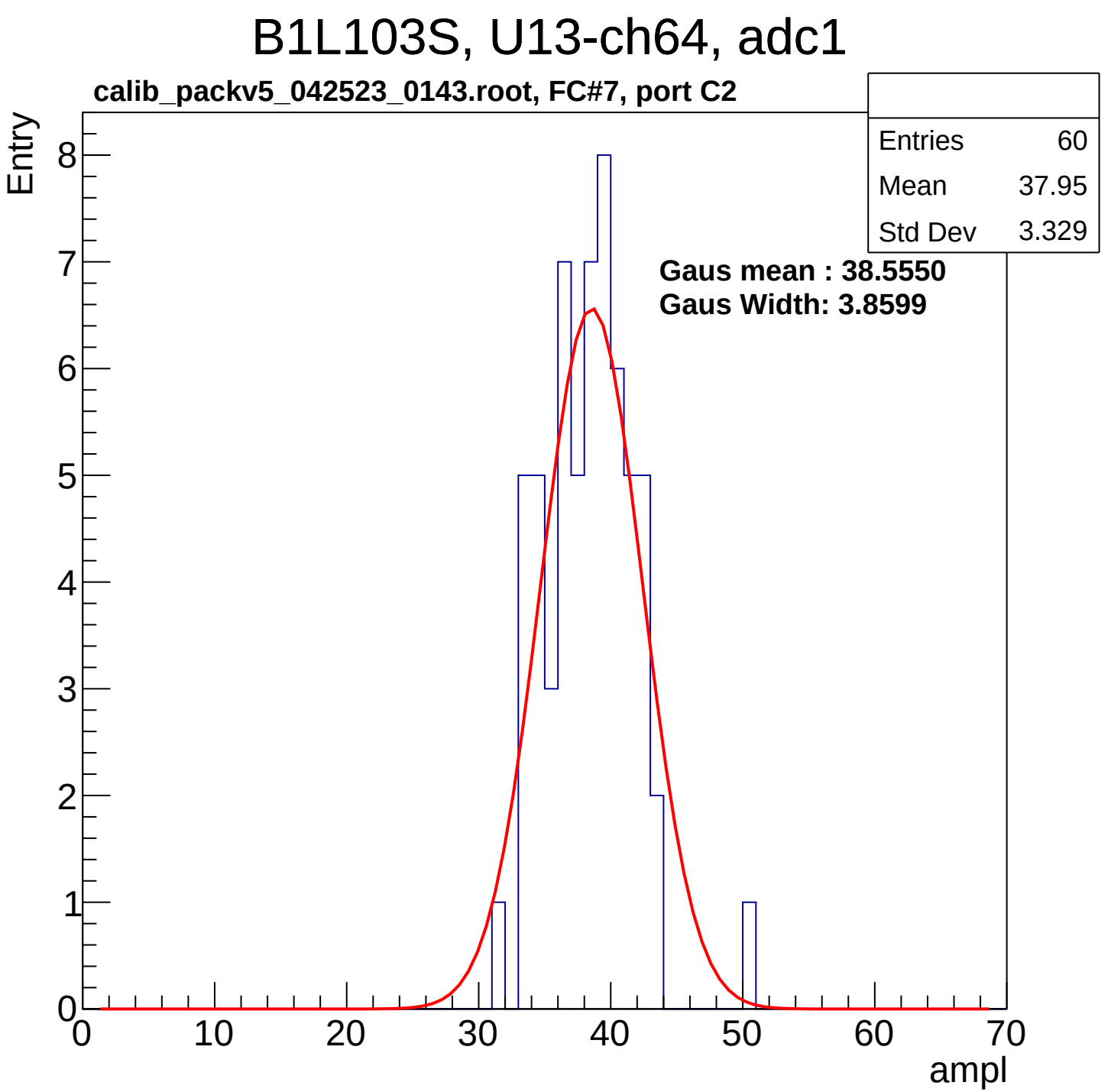
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch64, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 30.56
Std Dev 5.104
Gaus mean : 31.4584
Gaus Width: 4.0597
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch64, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 37.95
Std Dev 3.329
Gaus mean : 38.5550
Gaus Width: 3.8599
Entry
ampl
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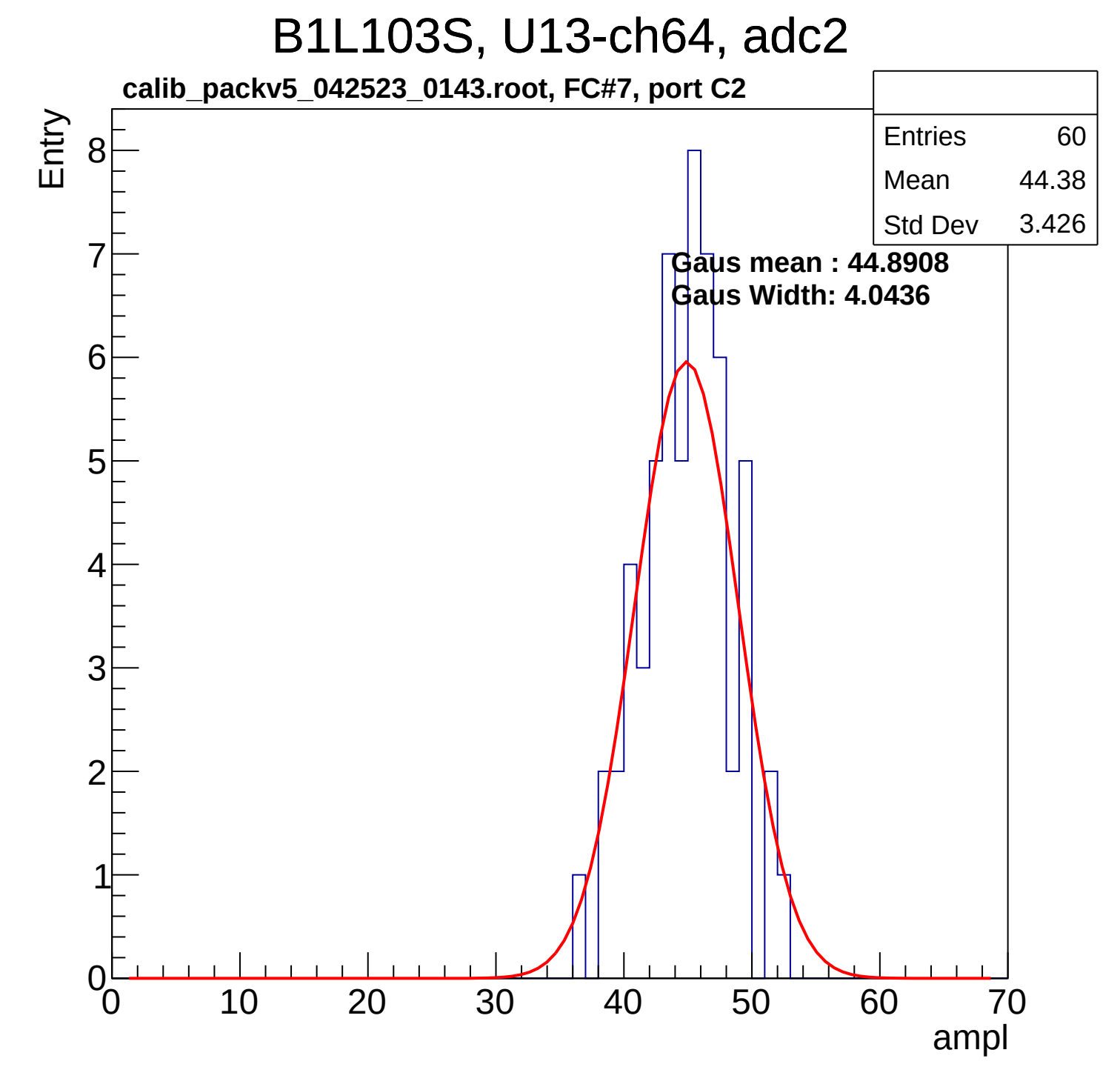
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch64, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 44.38
Std Dev 3.426
Gaus mean : 44.8908
Gaus Width: 4.0436
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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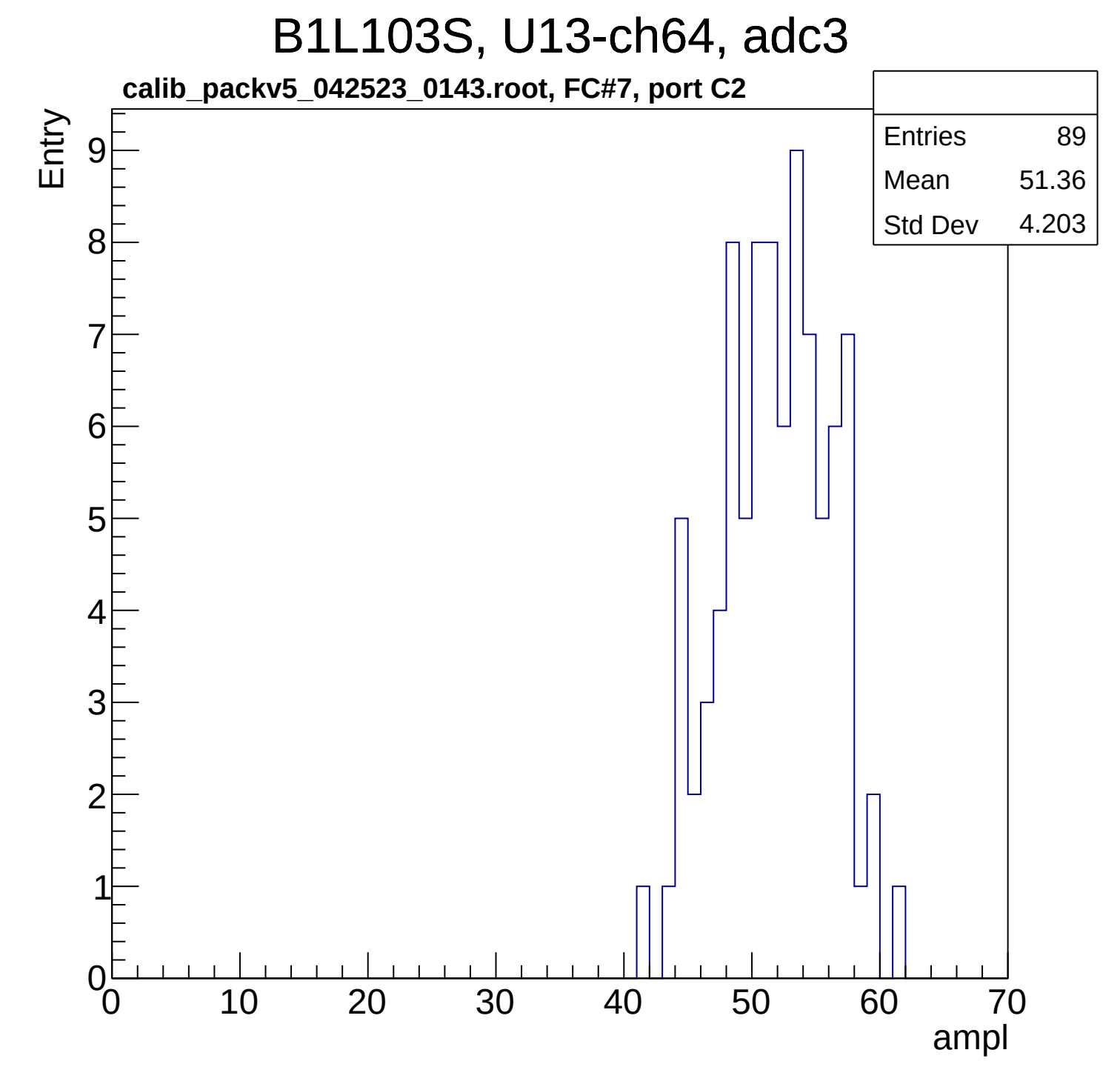
B1L103S, U13-ch64, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 51.36
Std Dev 4.203
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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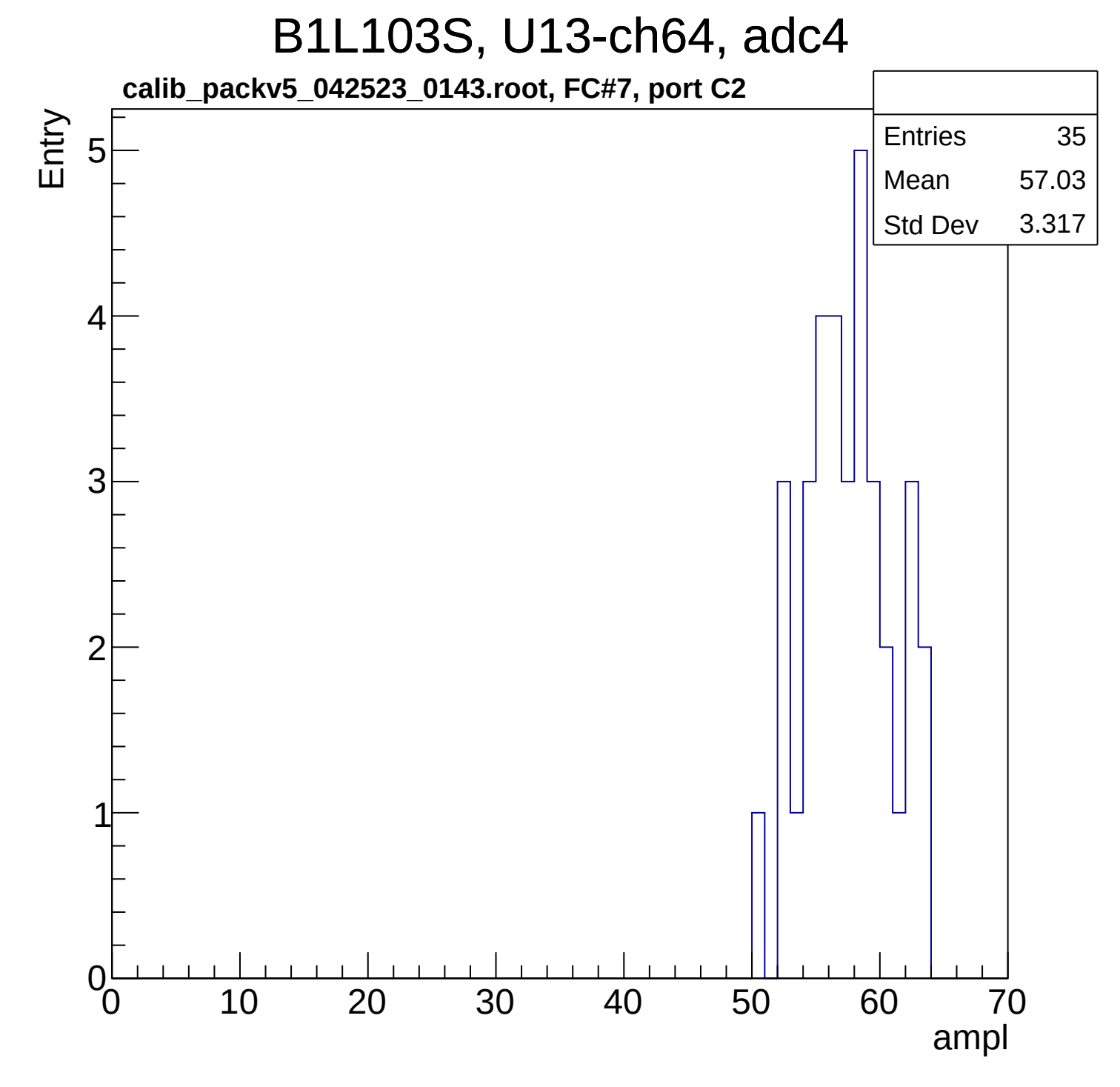
B1L103S, U13-ch64, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 57.03
Std Dev 3.317
Entry
ampl
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2
3
4
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0
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20
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40
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60
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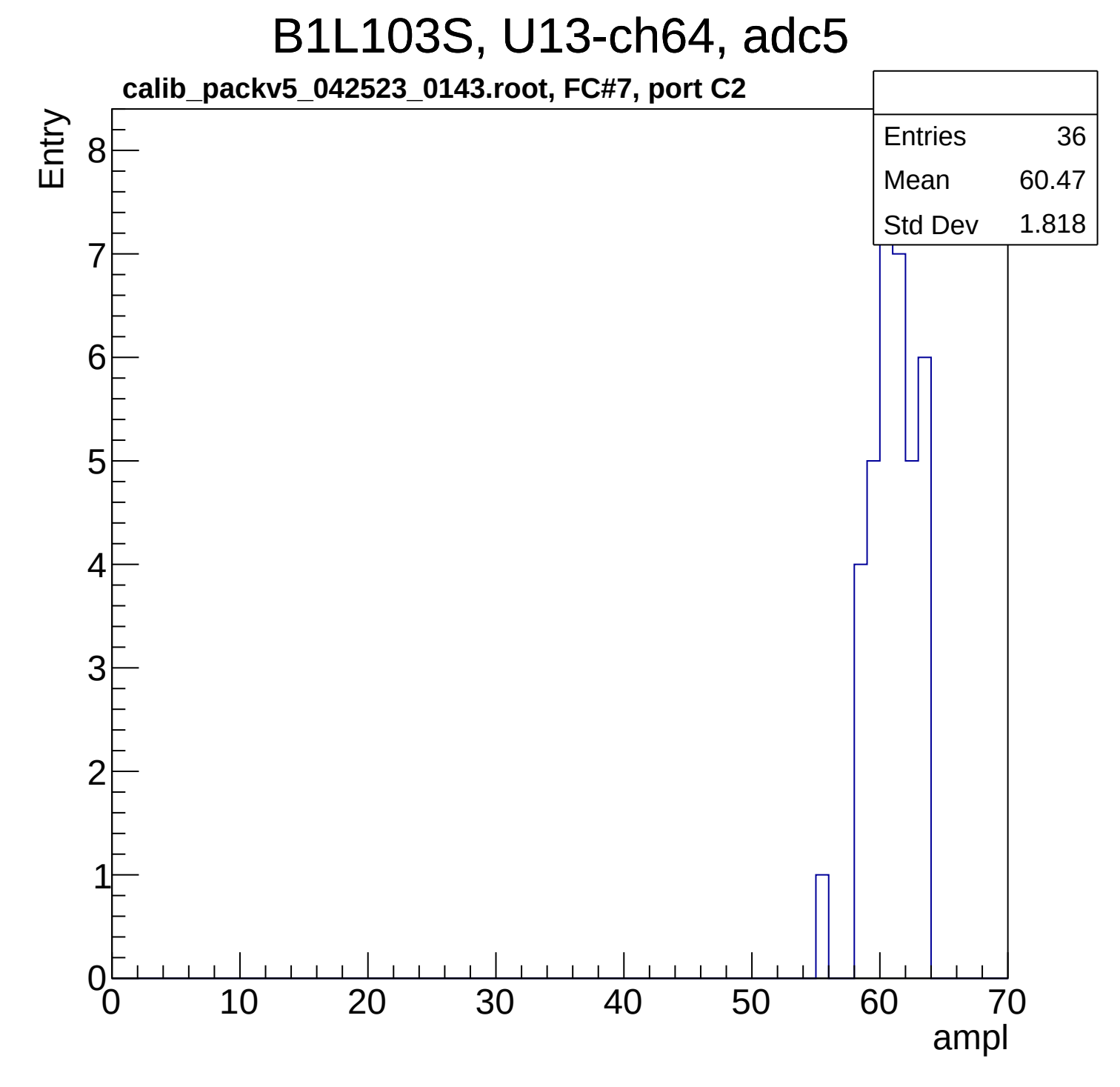
B1L103S, U13-ch64, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 60.47
Std Dev 1.818
Entry
ampl
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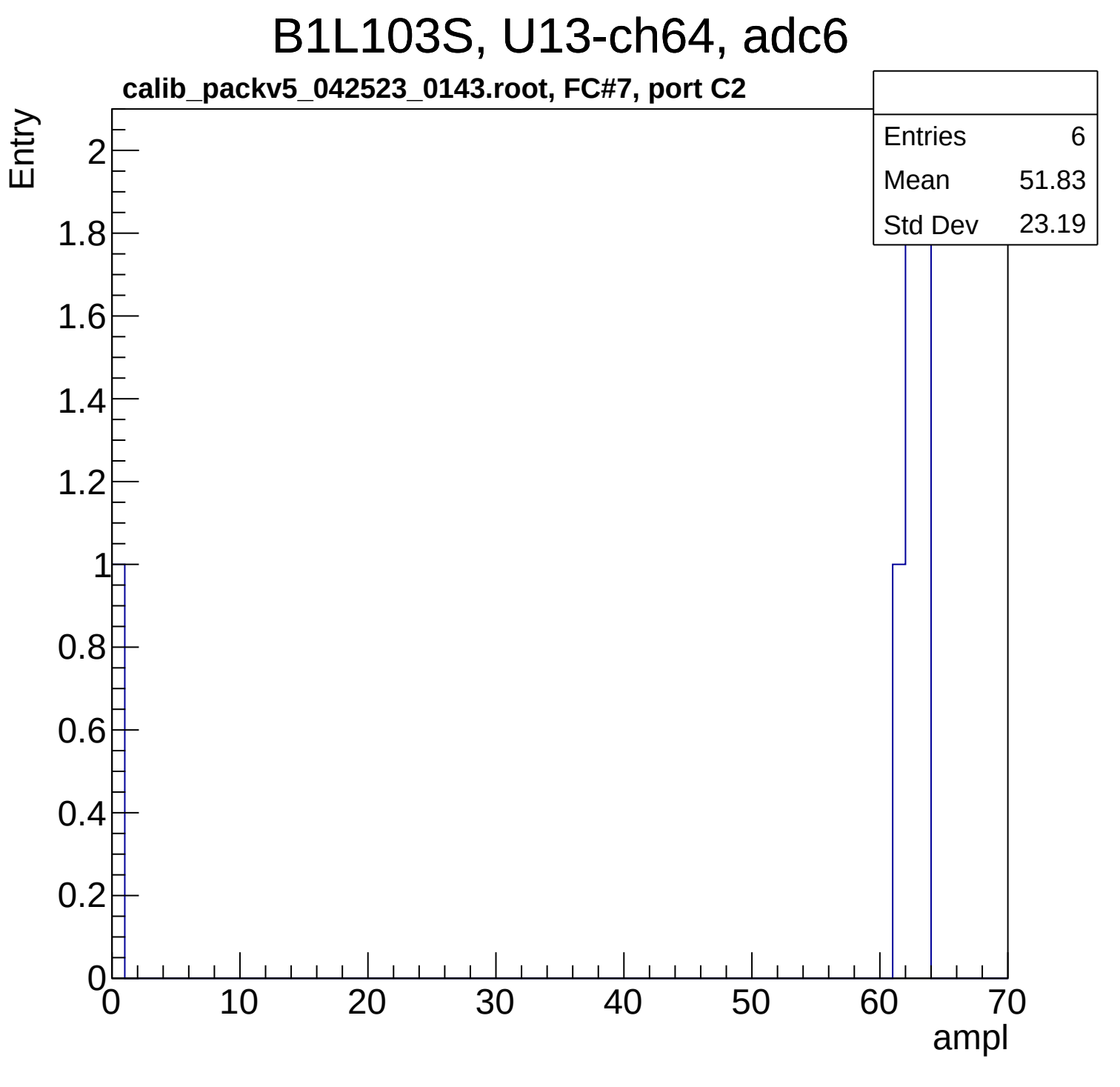
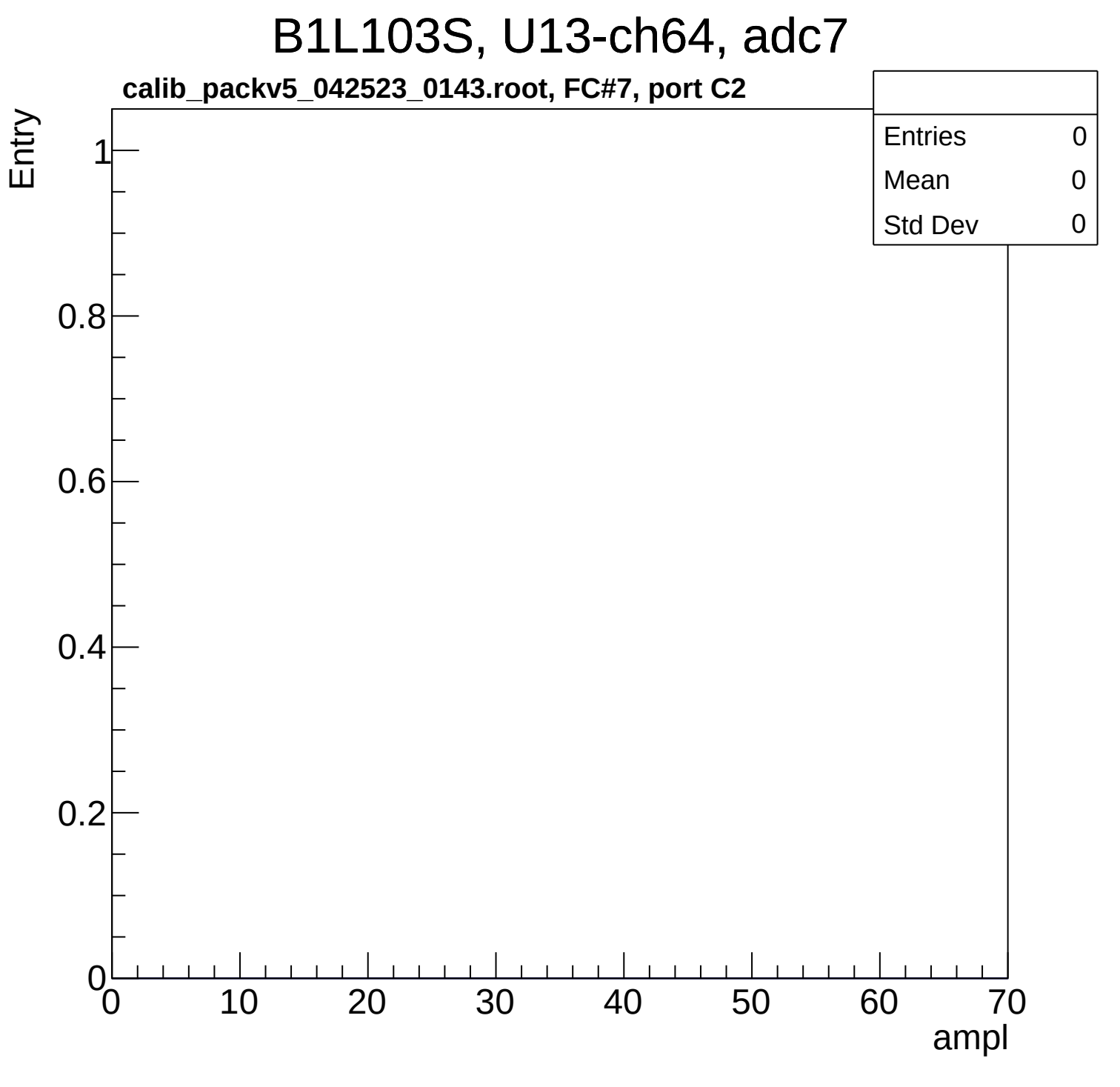
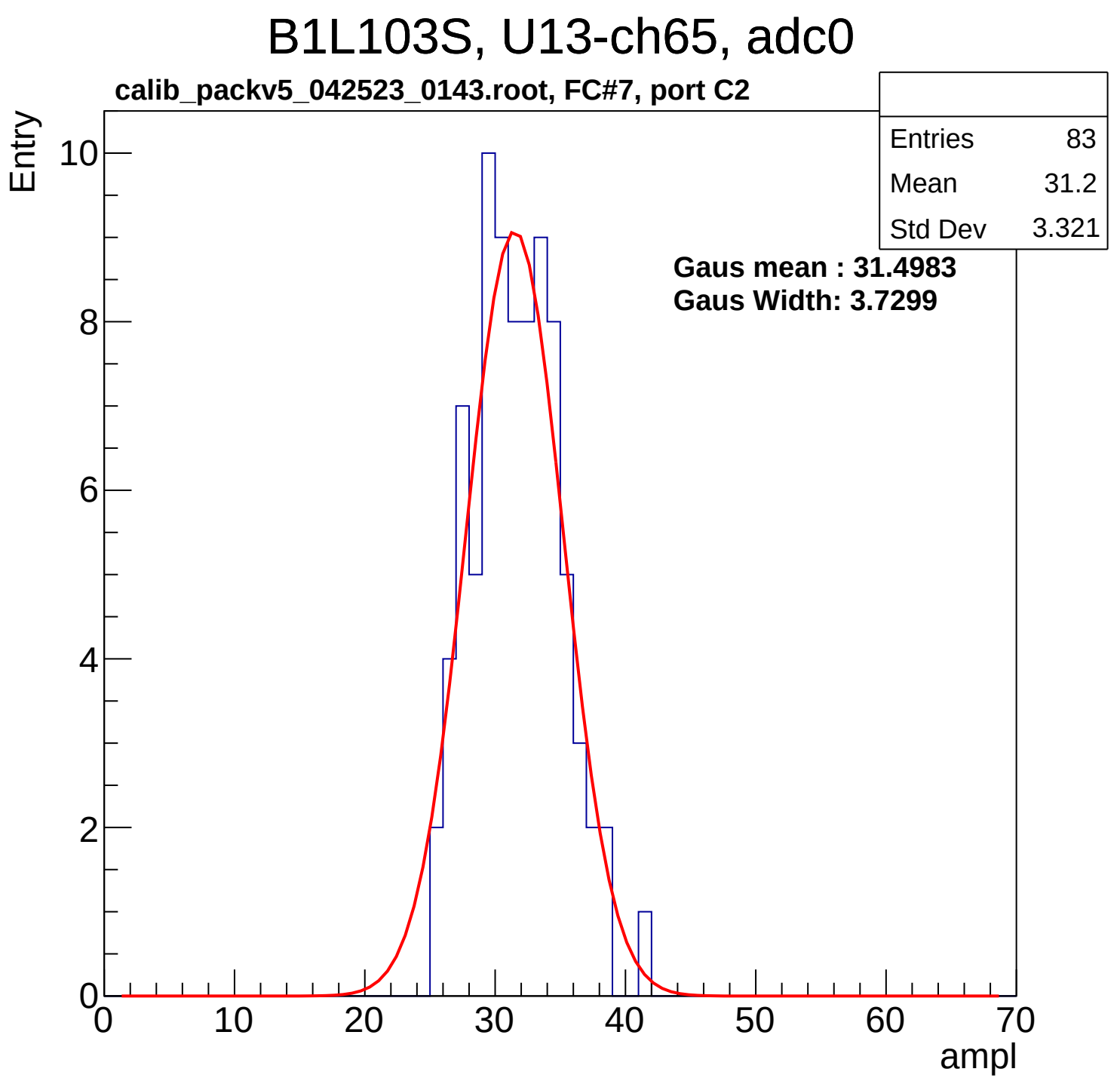
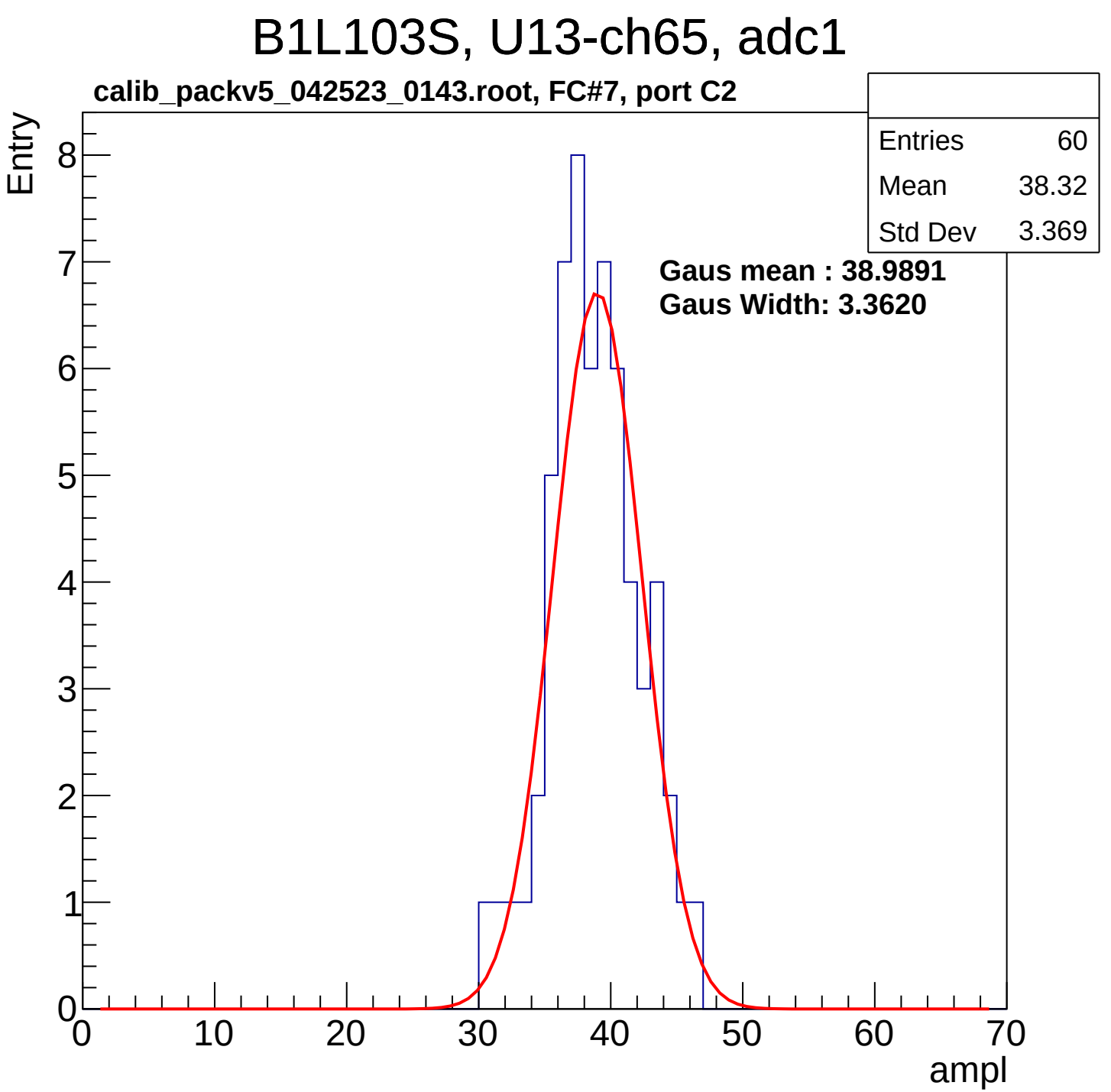
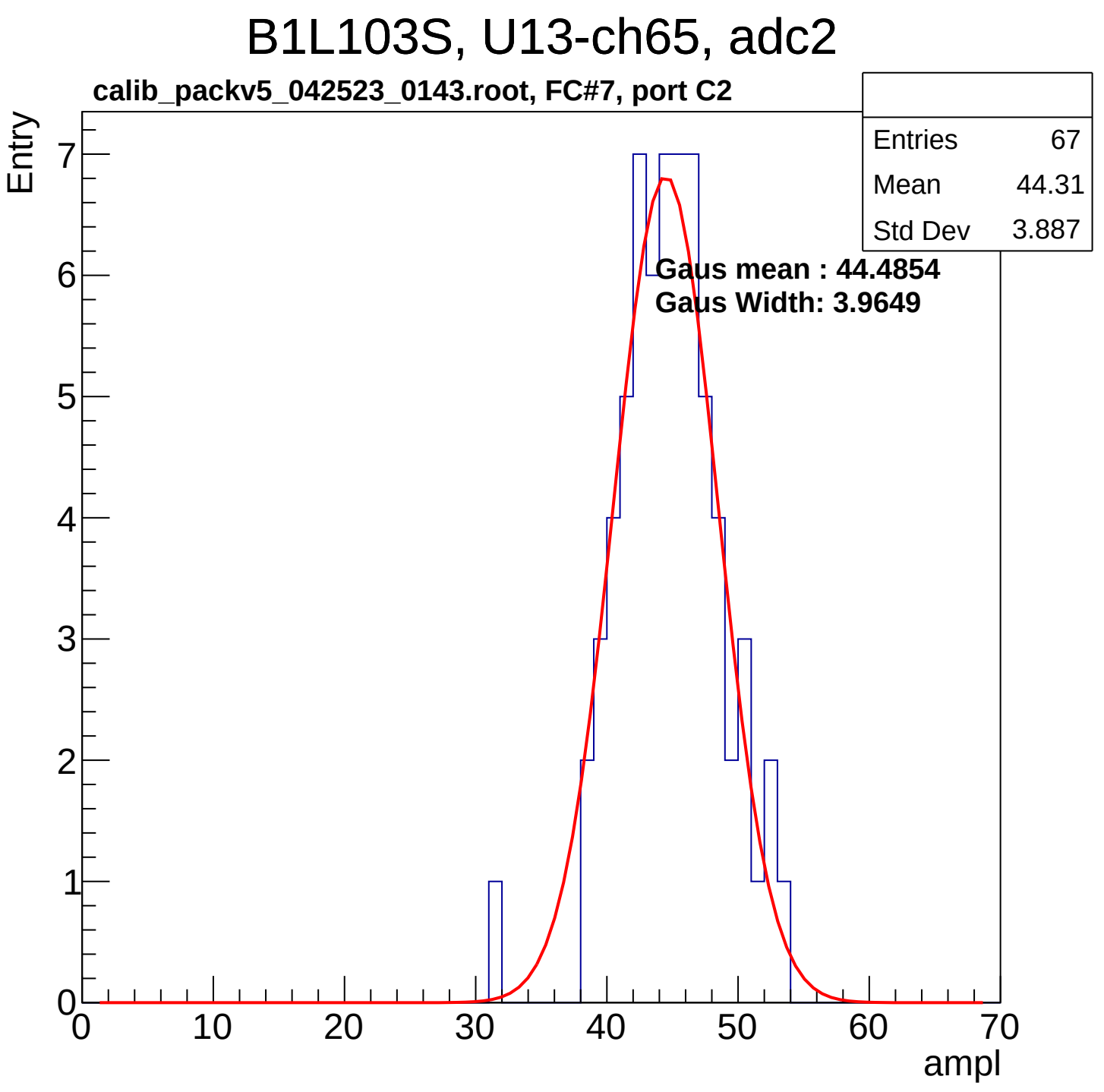
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch64, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch64, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch65, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 31.2
Std Dev 3.321
Gaus mean : 31.4983
Gaus Width: 3.7299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch65, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 38.32
Std Dev 3.369
Gaus mean : 38.9891
Gaus Width: 3.3620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch65, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 44.31
Std Dev 3.887
Gaus mean : 44.4854
Gaus Width: 3.9649
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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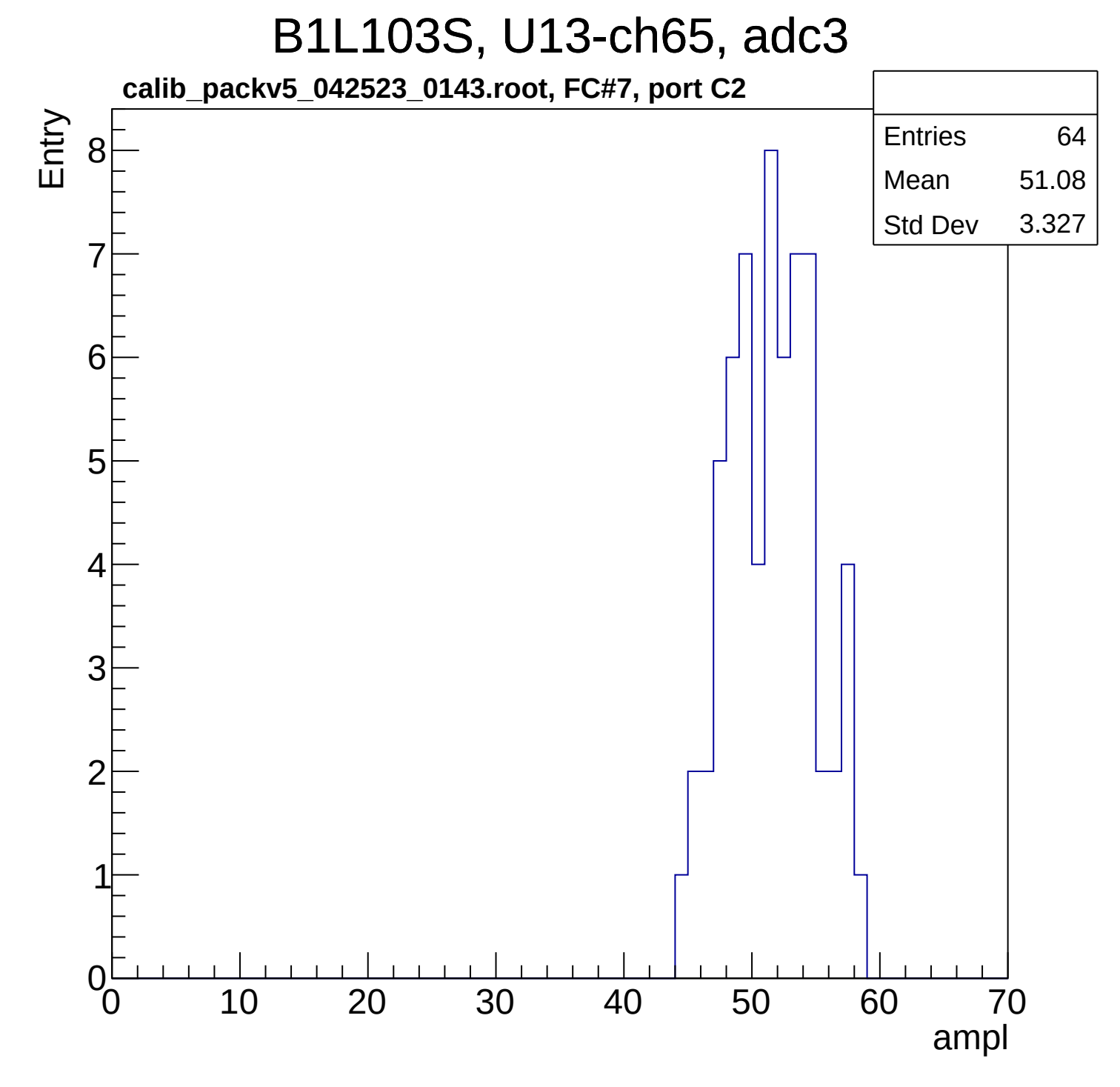
B1L103S, U13-ch65, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 51.08
Std Dev 3.327
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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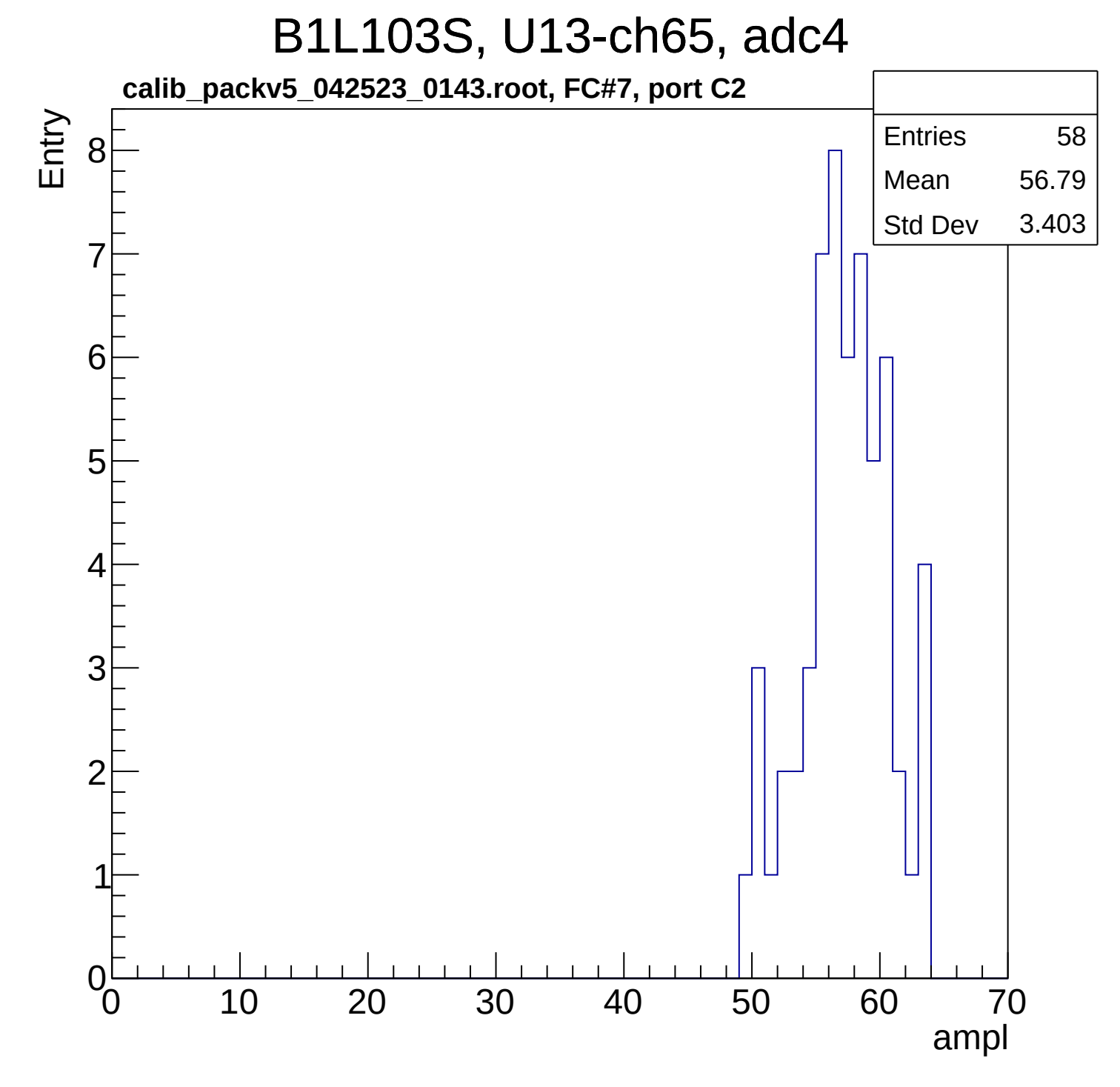
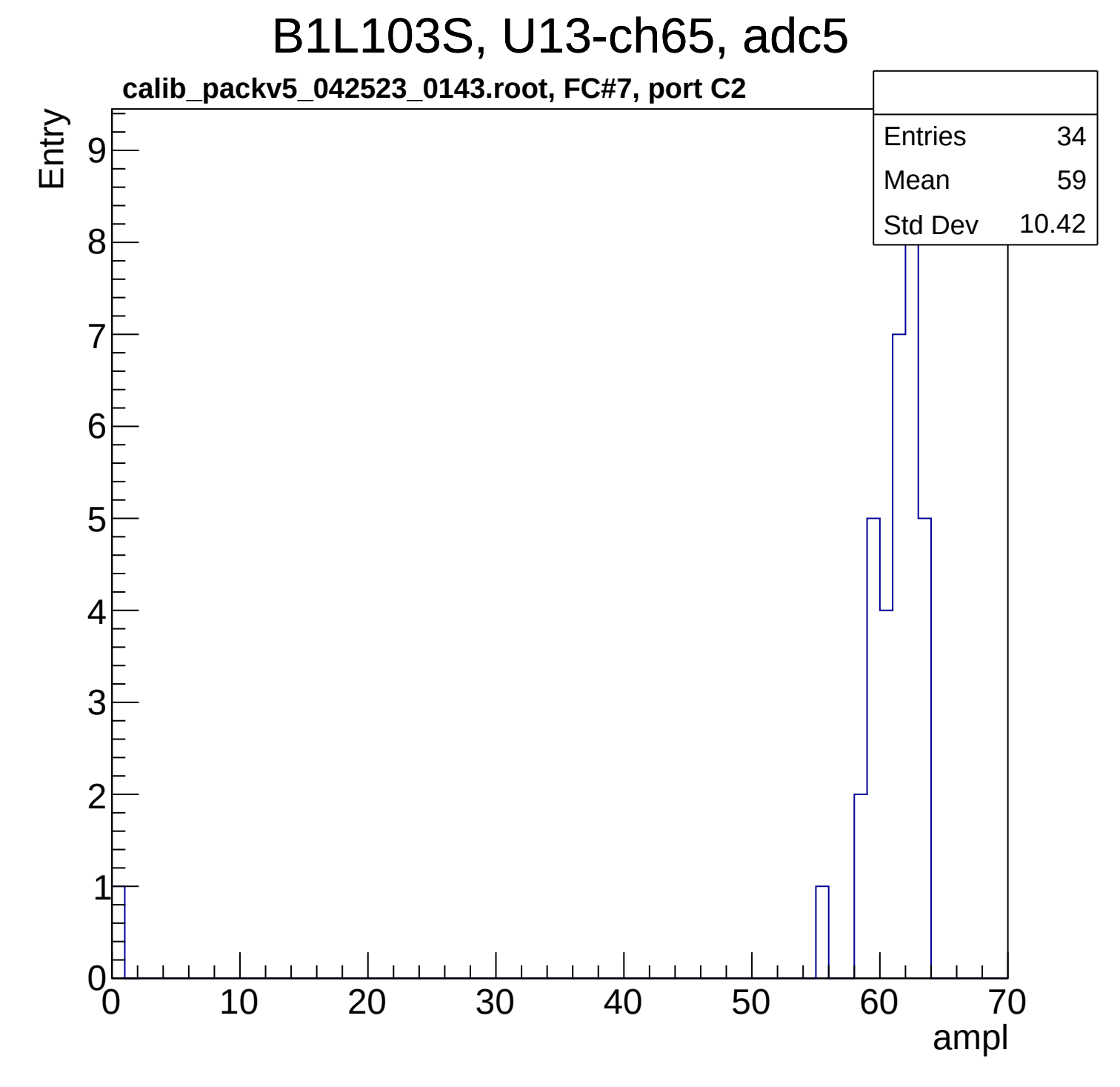

B1L103S, U13-ch65, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.79
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch65, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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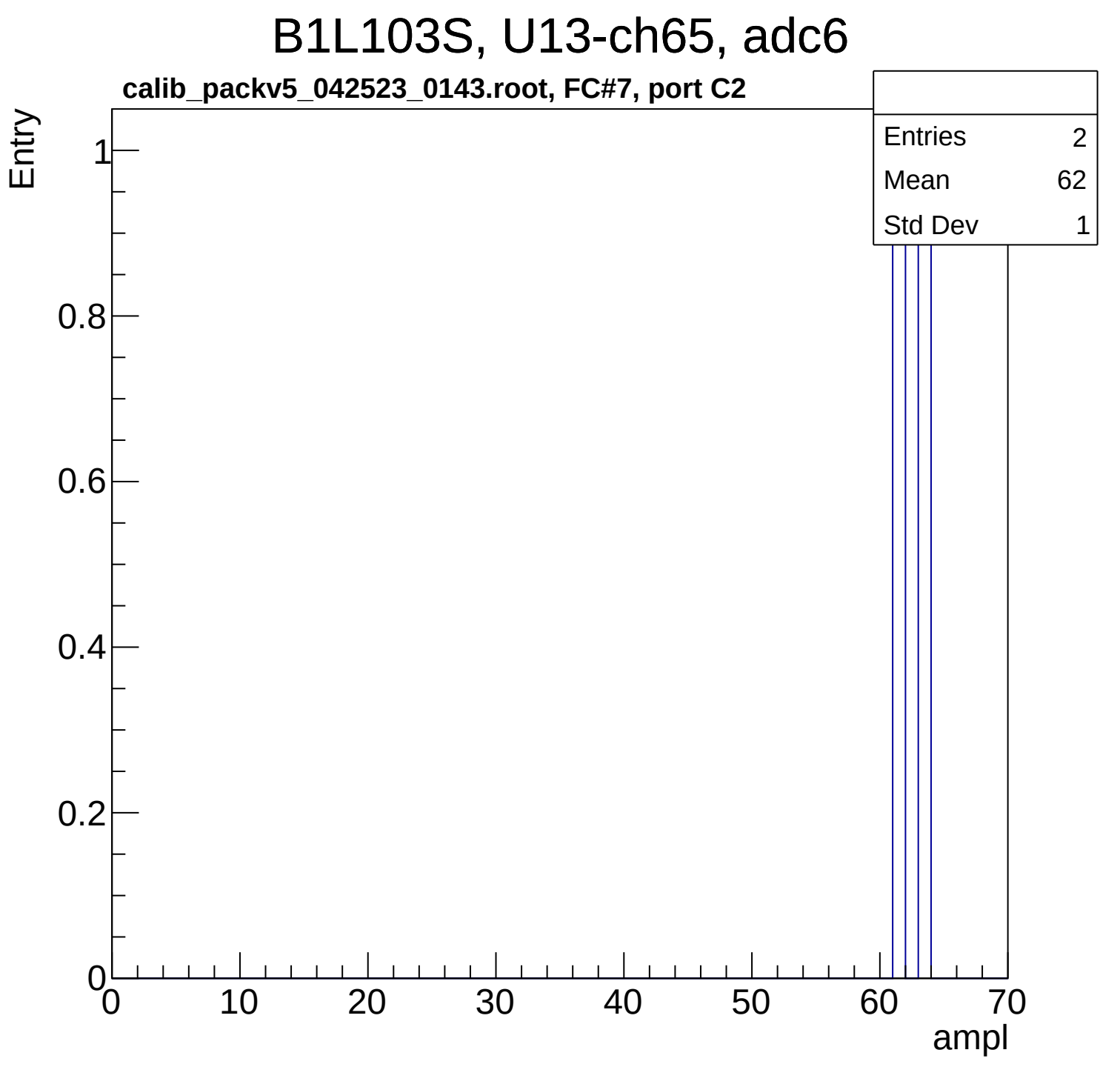
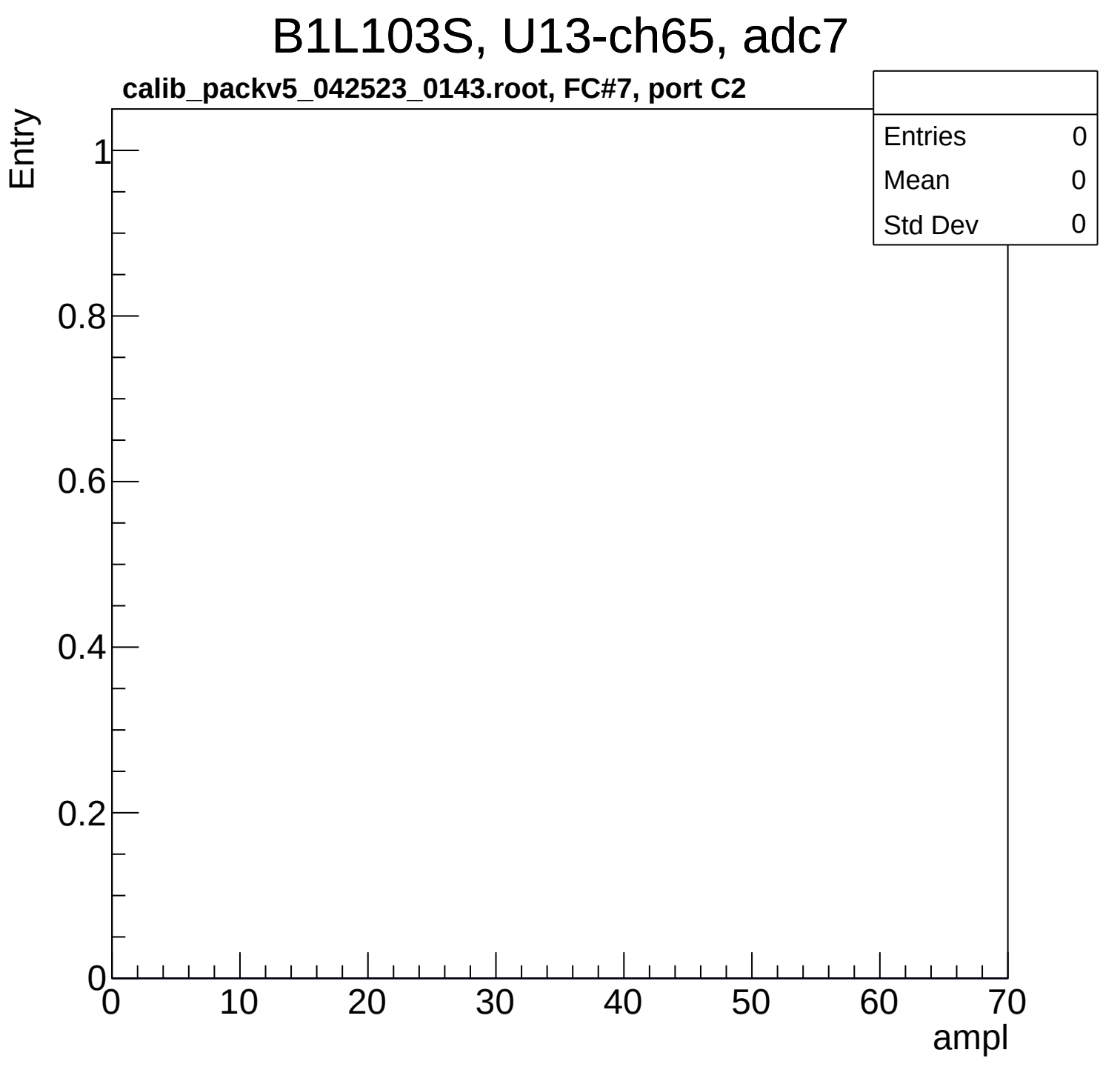
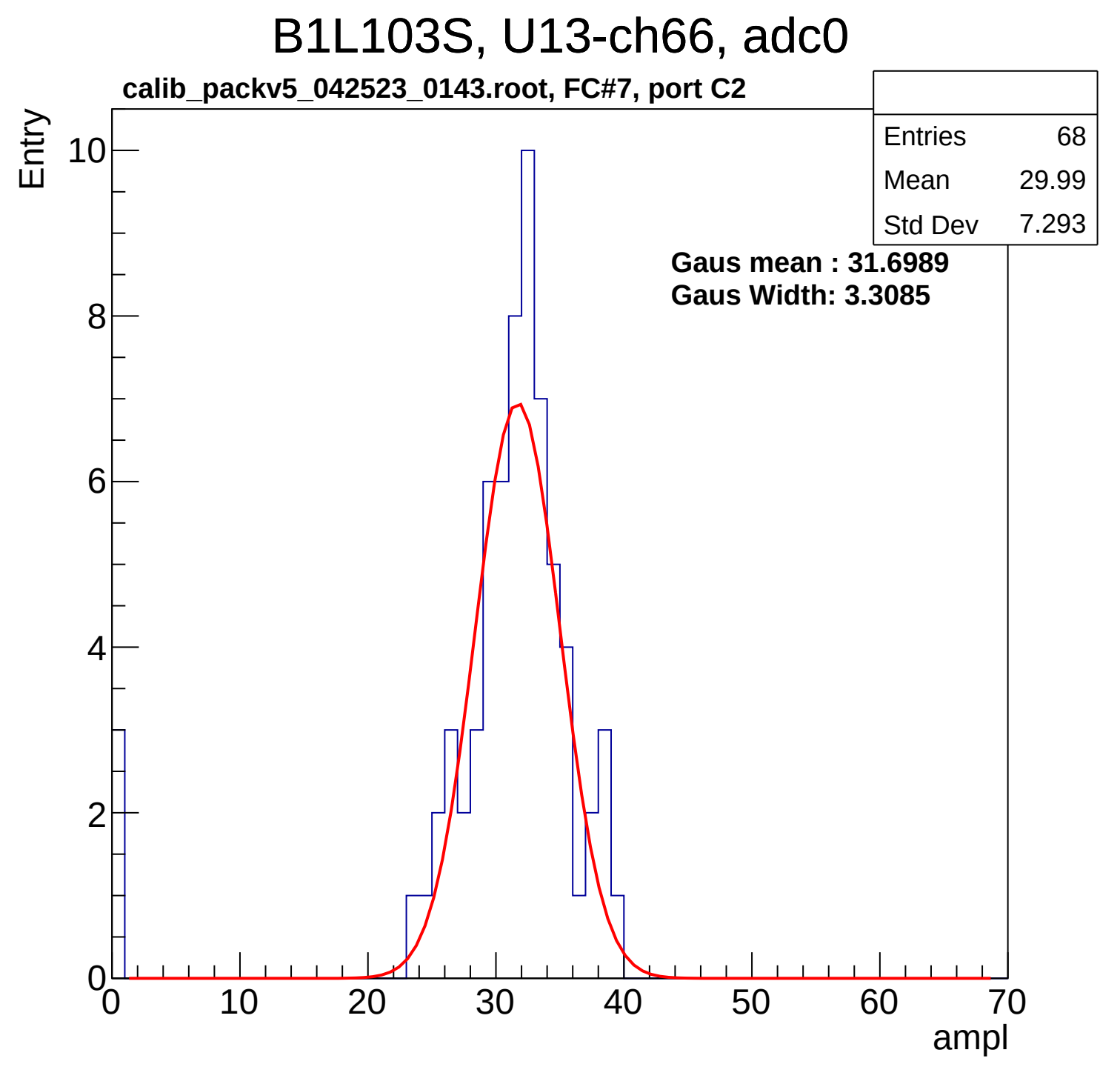

B1L103S, U13-ch65, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch65, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch66, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.99
Std Dev 7.293
Gaus mean : 31.6989
Gaus Width: 3.3085
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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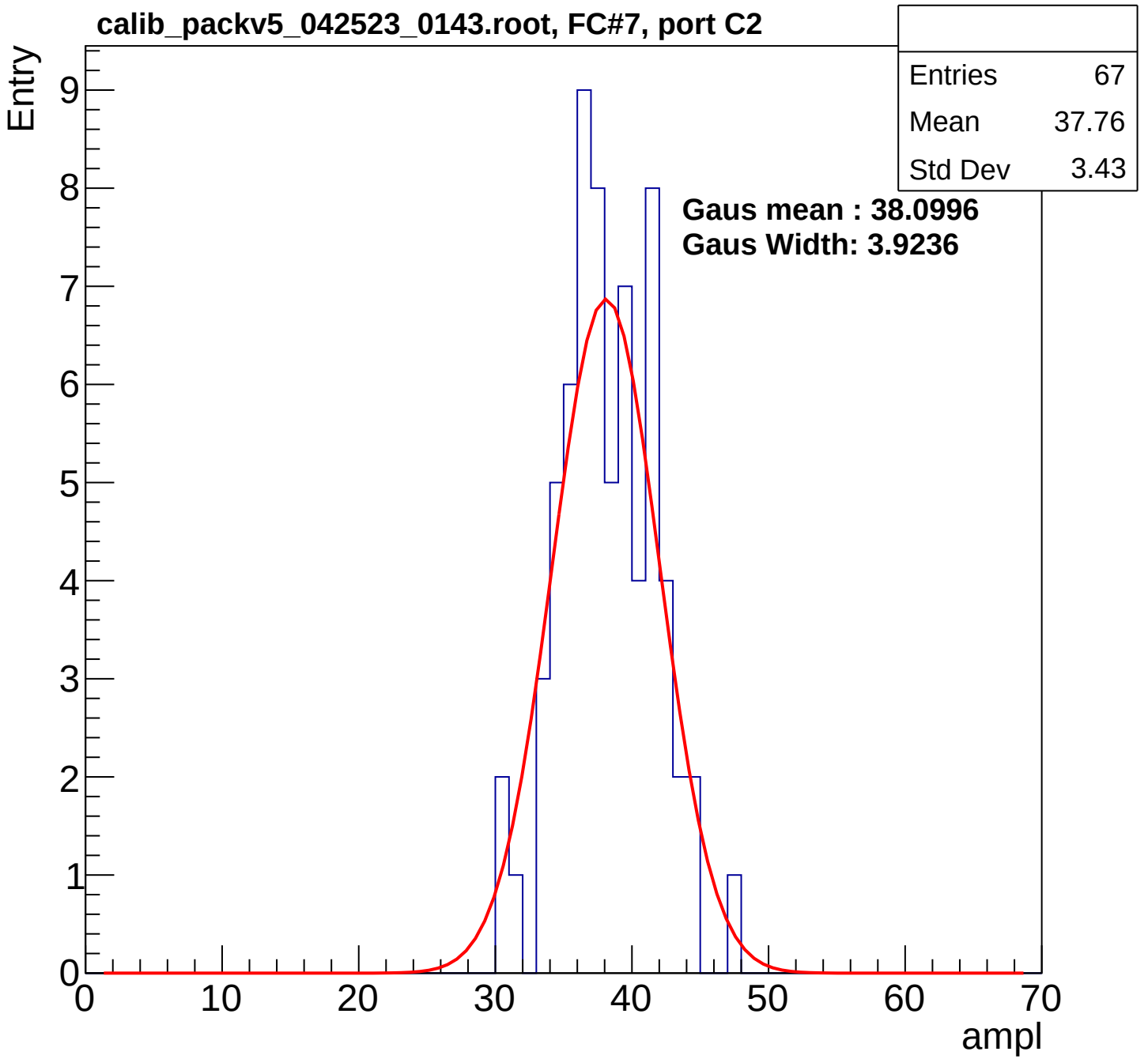
B1L103S, U13-ch66, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.76
Std Dev 3.43
Gaus mean : 38.0996
Gaus Width: 3.9236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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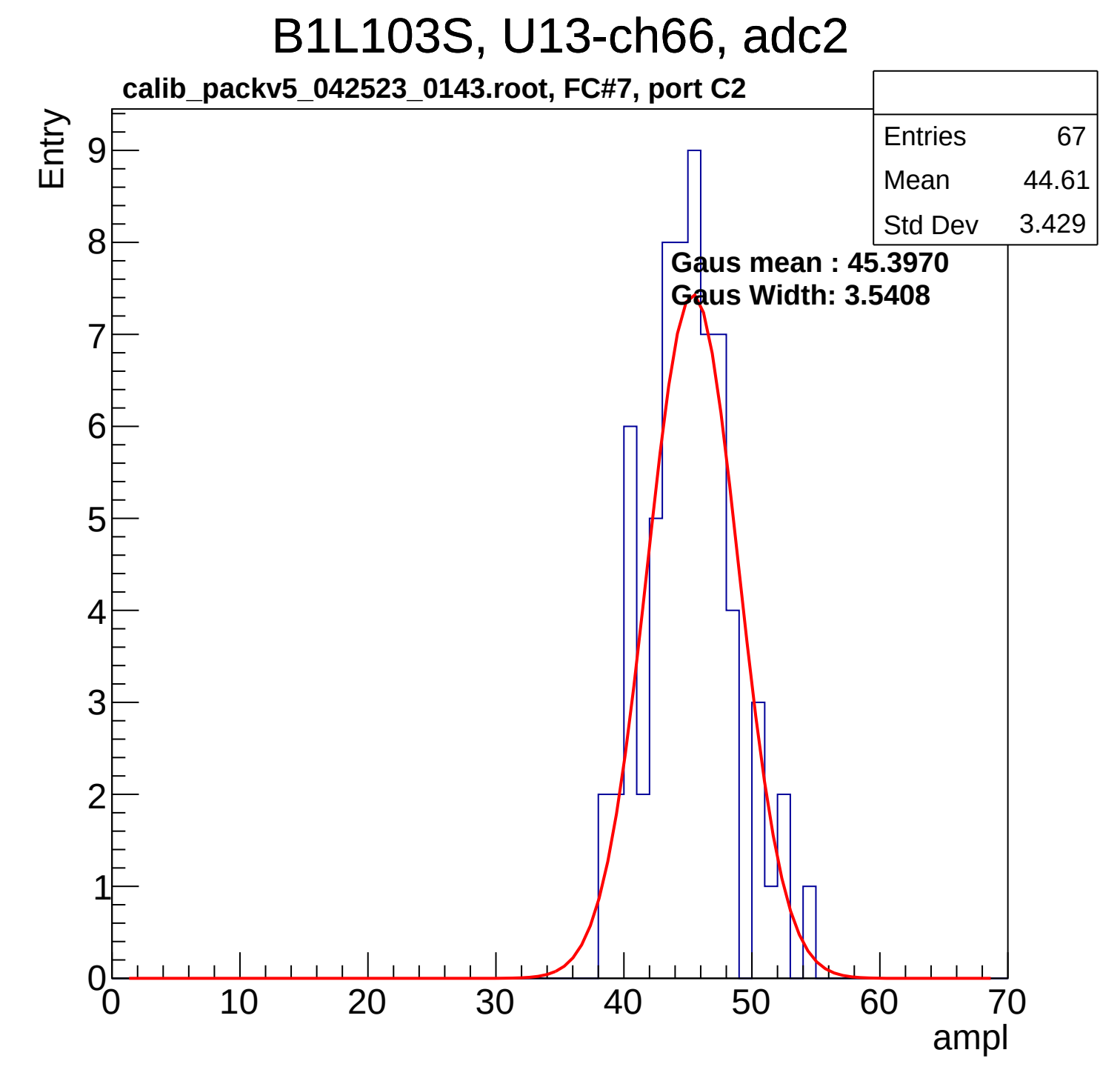
B1L103S, U13-ch66, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 44.61
Std Dev 3.429
Gaus mean : 45.3970
Gaus Width: 3.5408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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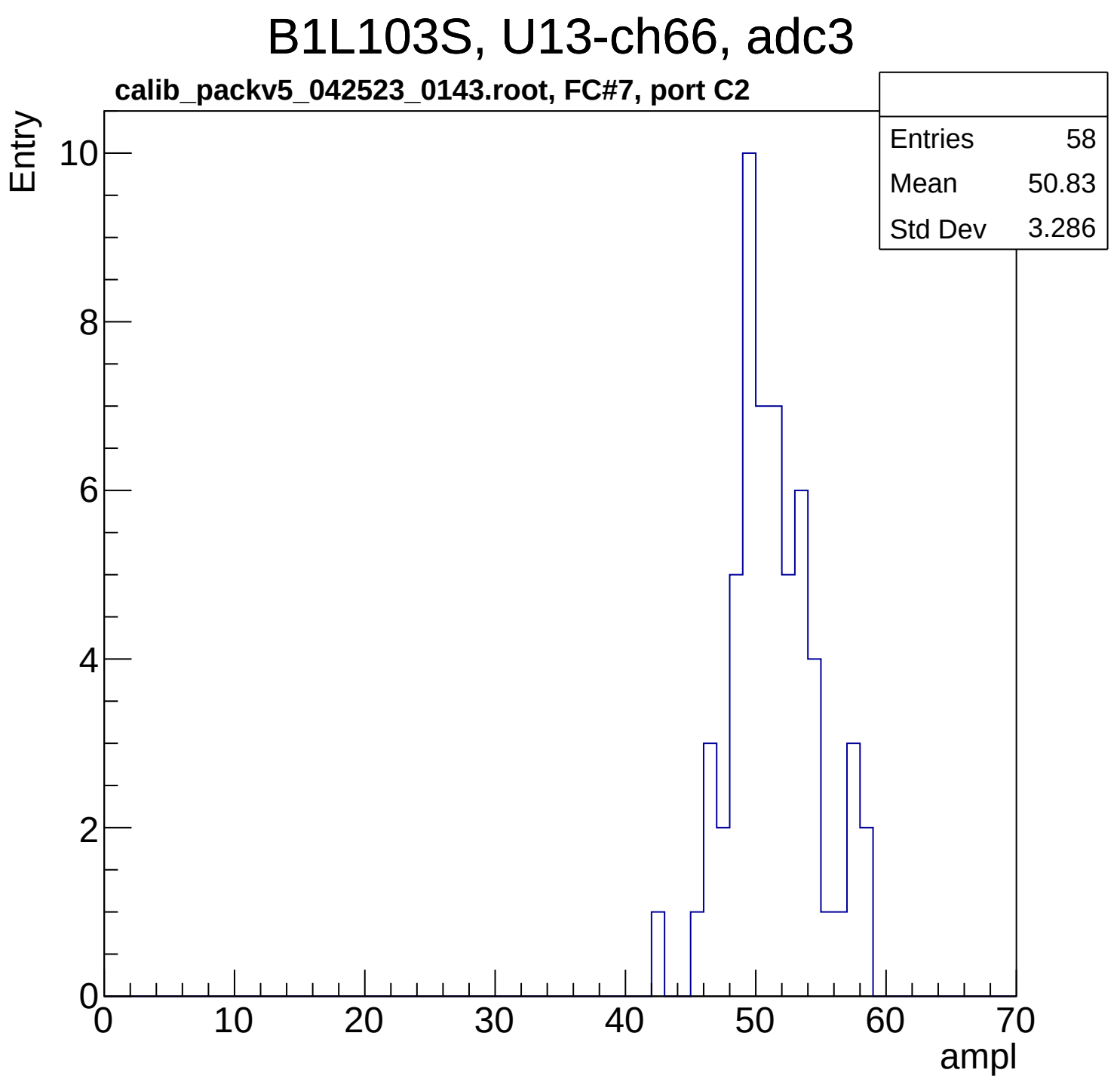
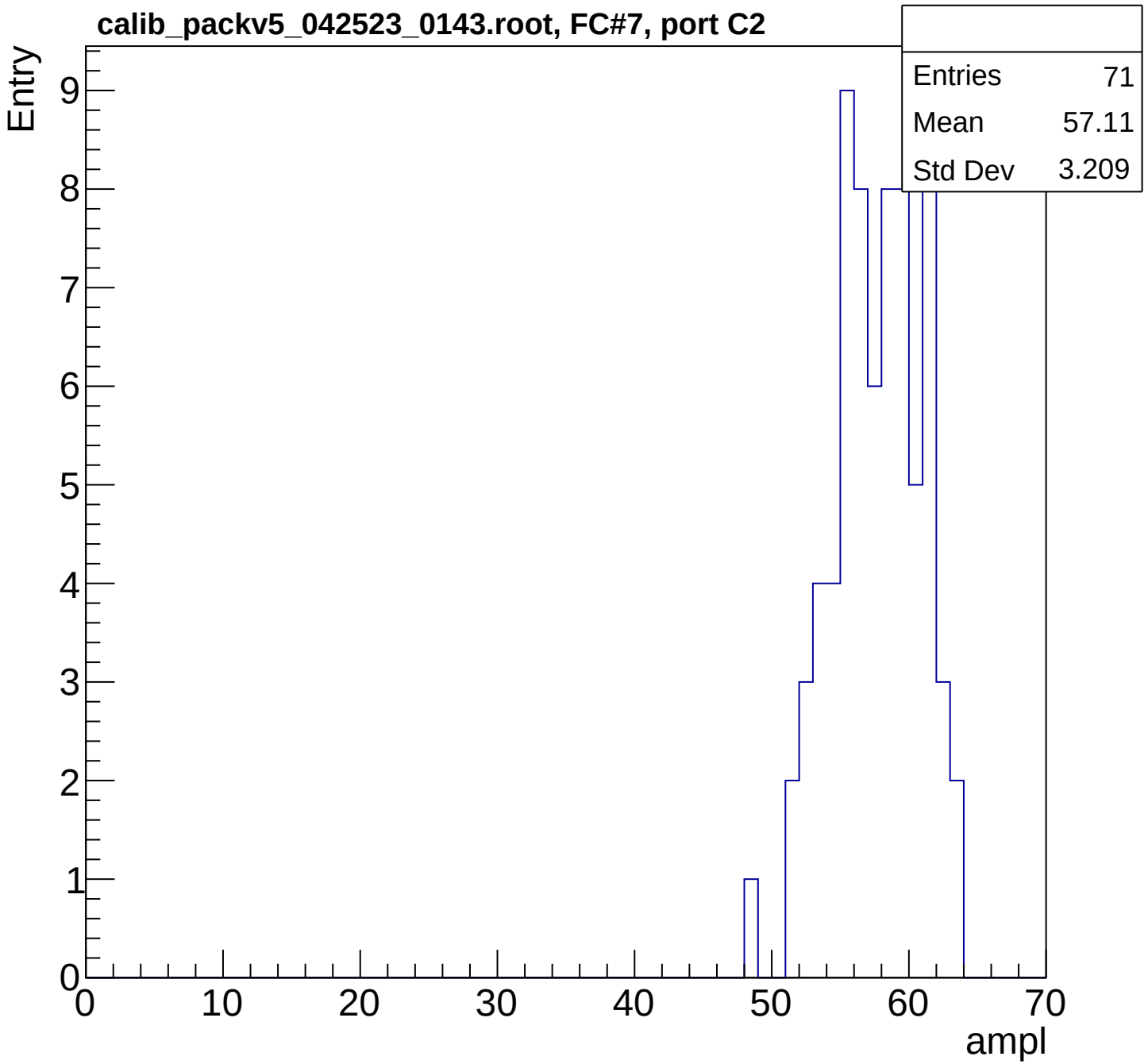
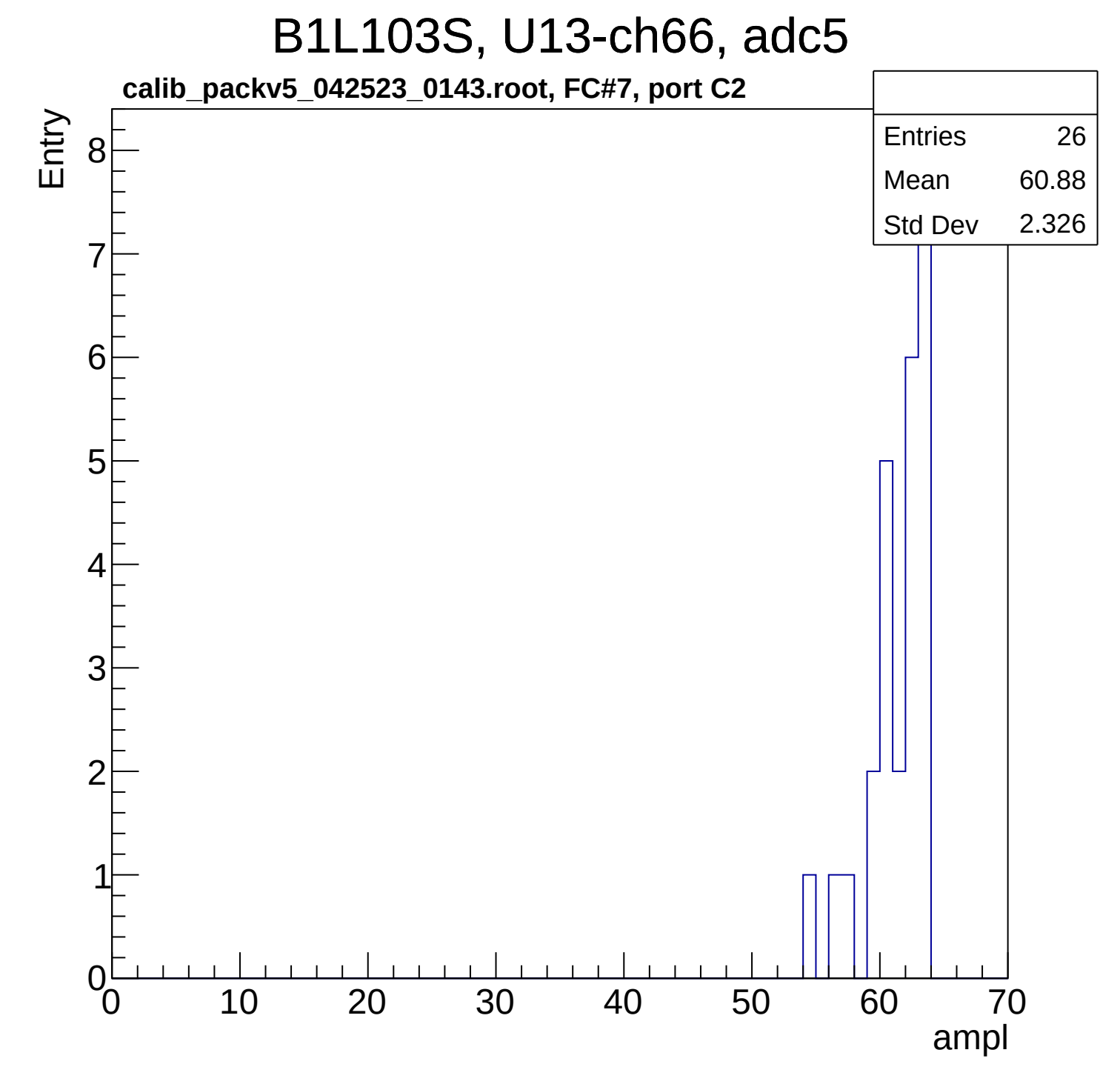

B1L103S, U13-ch66, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 50.83
Std Dev 3.286
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch66, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 57.11
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch66, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.88
Std Dev 2.326
Entry
ampl
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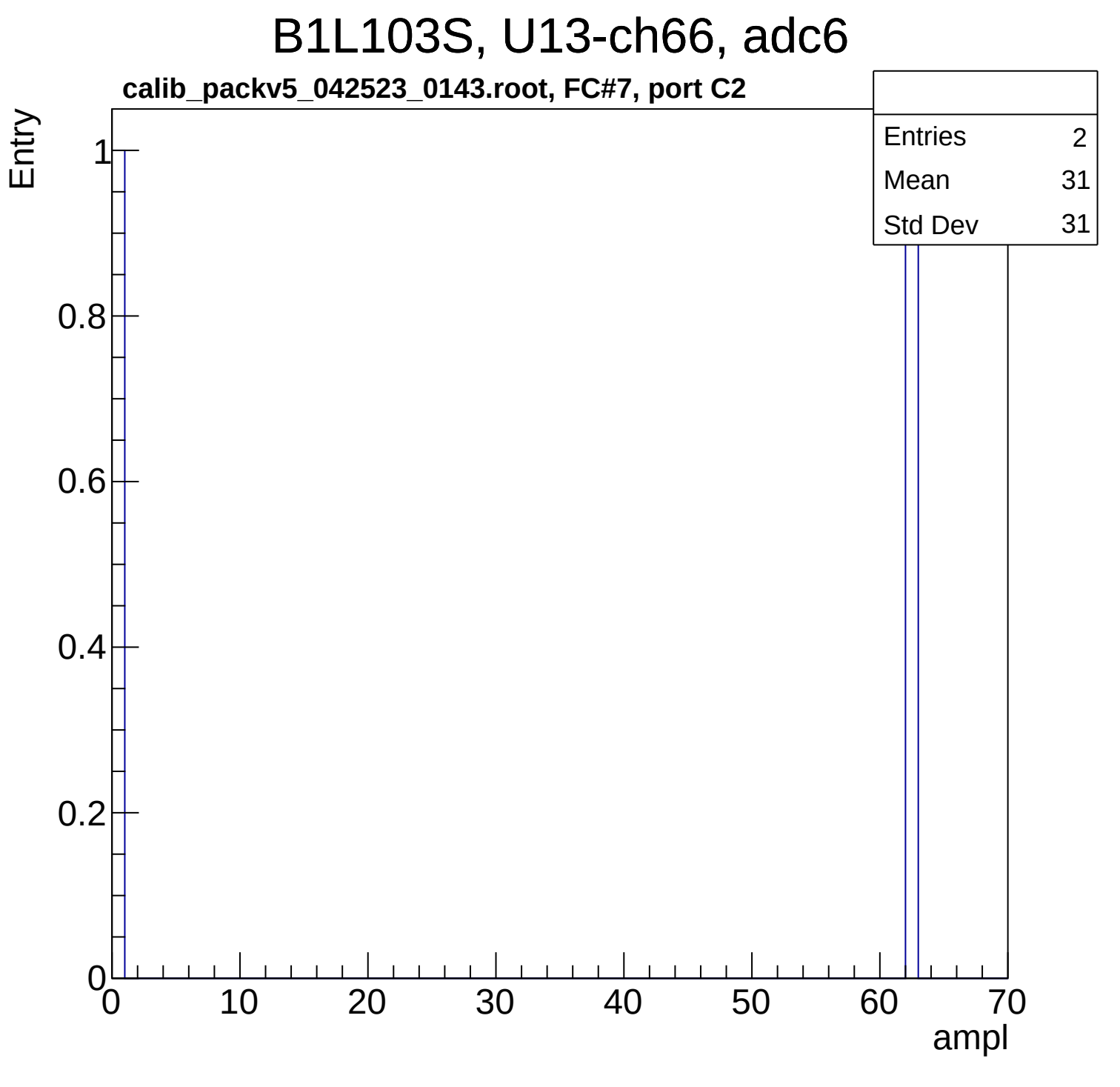
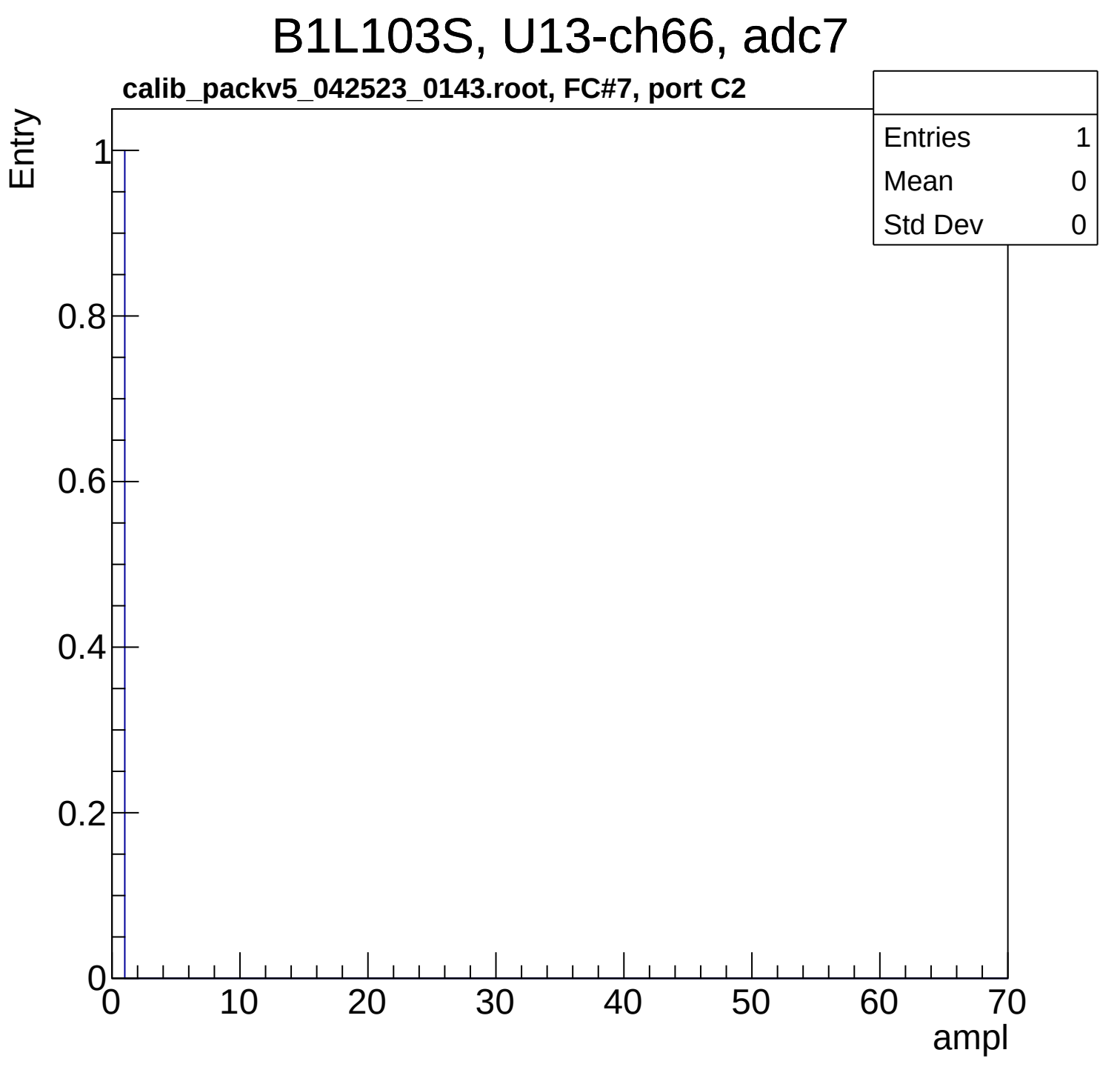
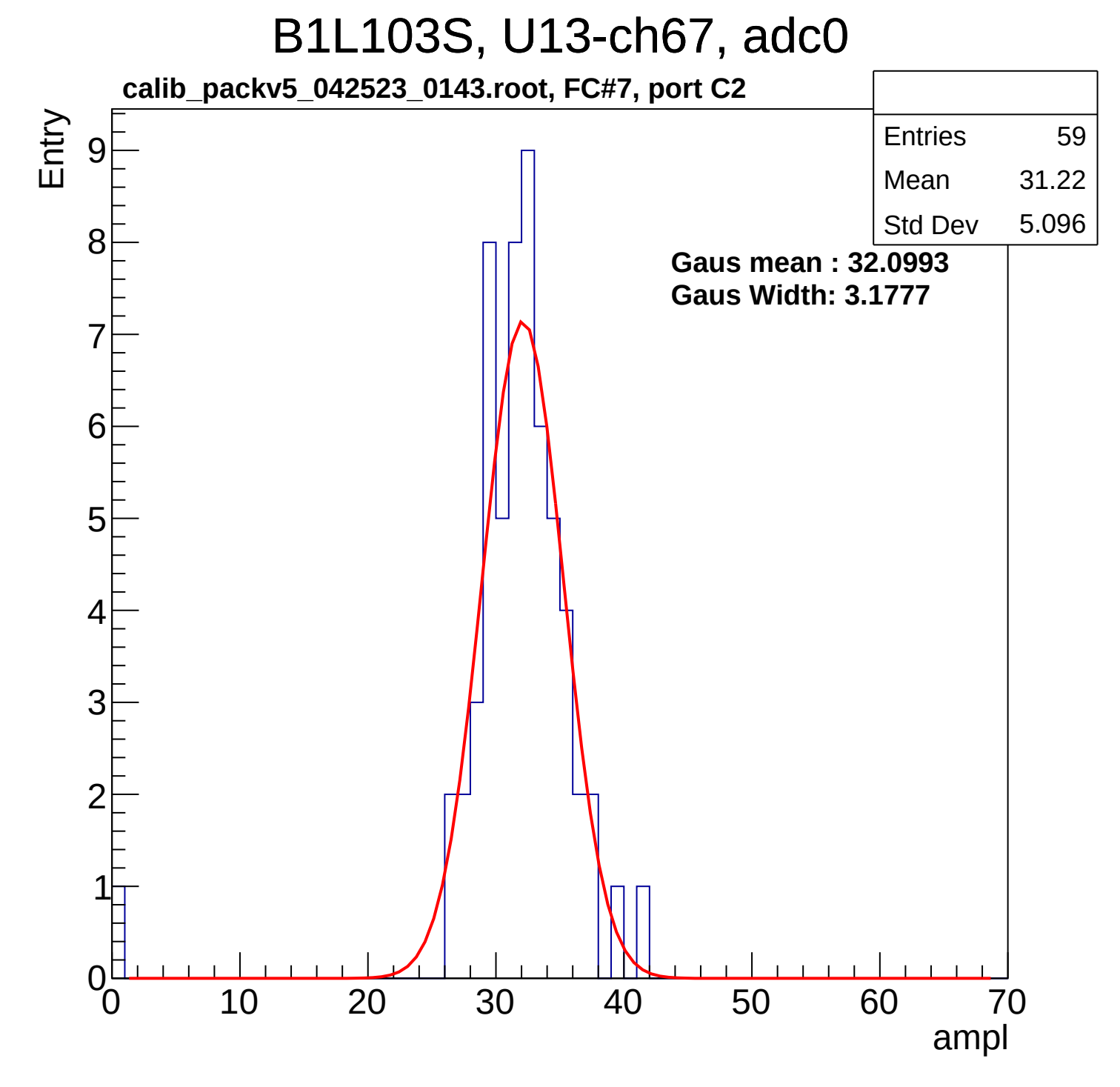
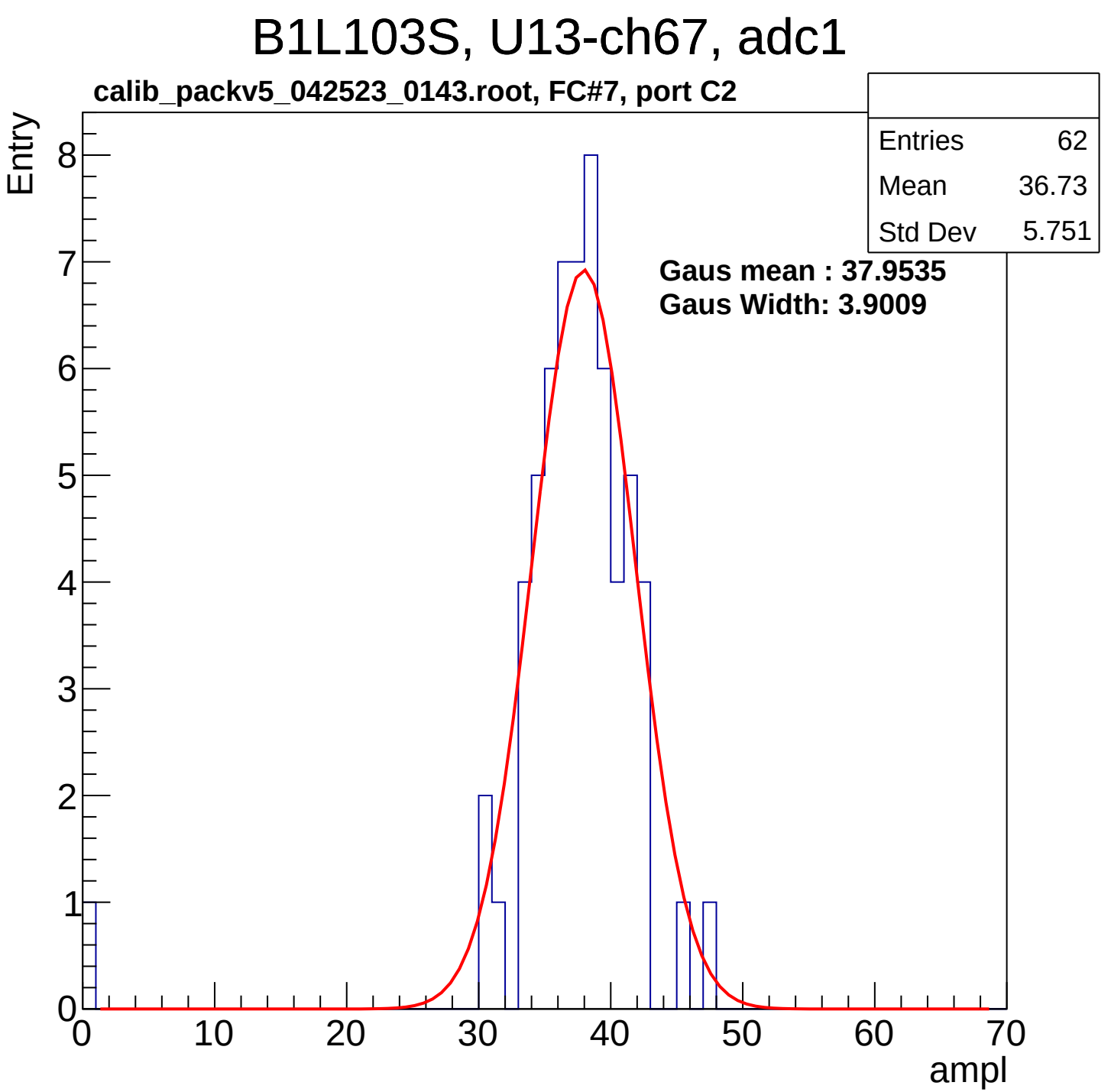
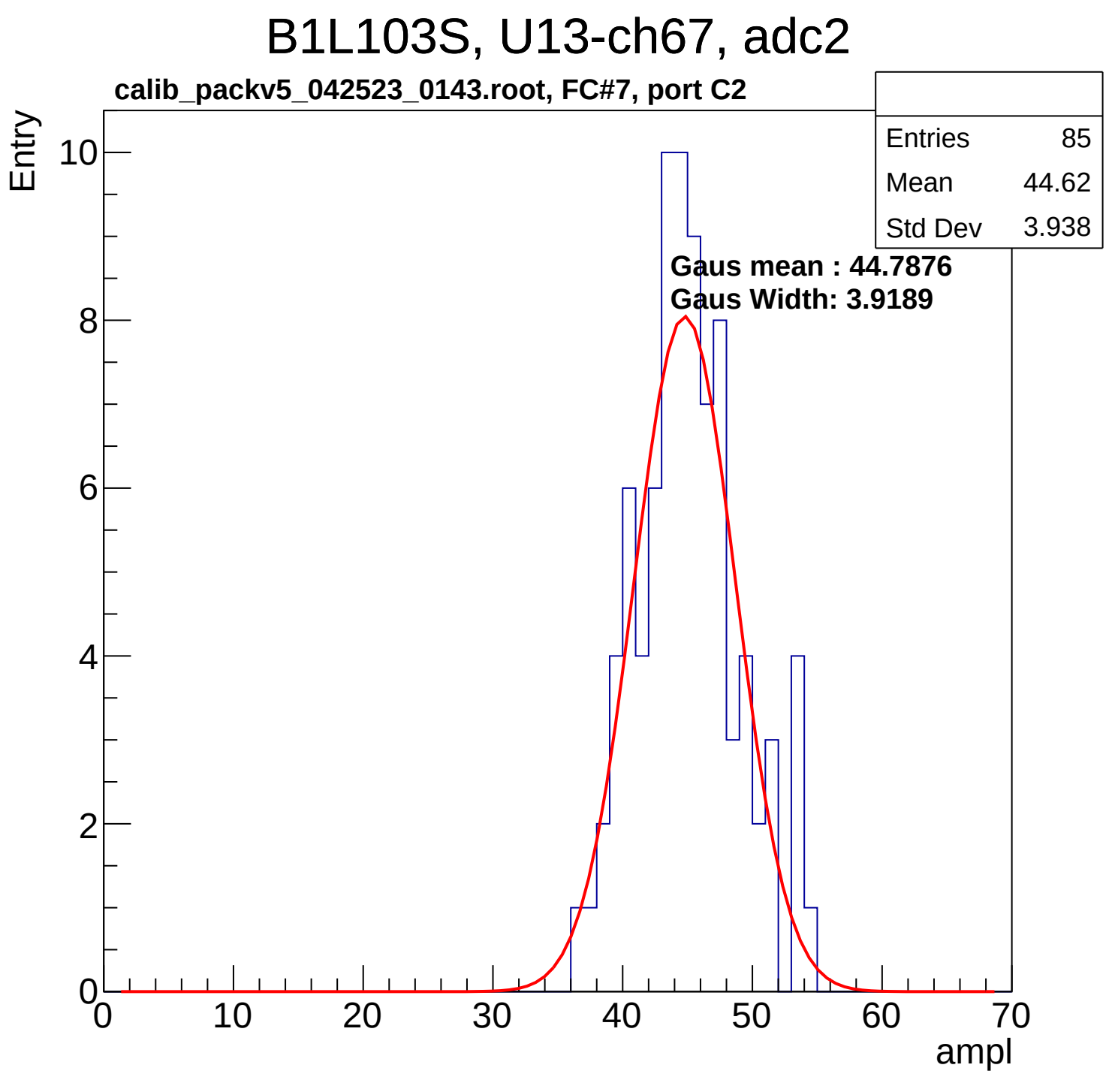
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

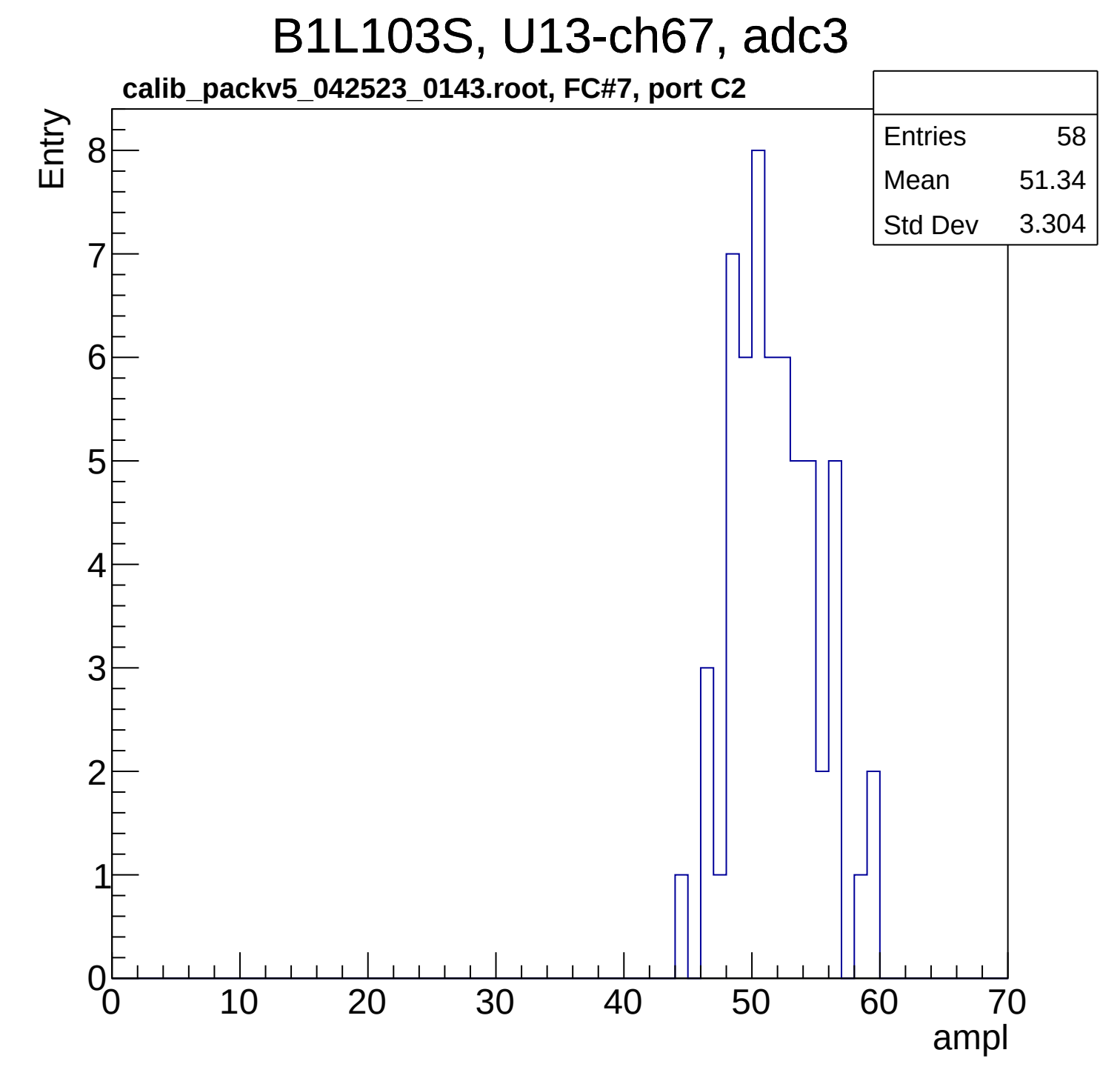

B1L103S, U13-ch66, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch66, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch67, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 31.22
Std Dev 5.096
Gaus mean : 32.0993
Gaus Width: 3.1777
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch67, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 36.73
Std Dev 5.751
Gaus mean : 37.9535
Gaus Width: 3.9009
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch67, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 44.62
Std Dev 3.938
Gaus mean : 44.7876
Gaus Width: 3.9189
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch67, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 51.34
Std Dev 3.304
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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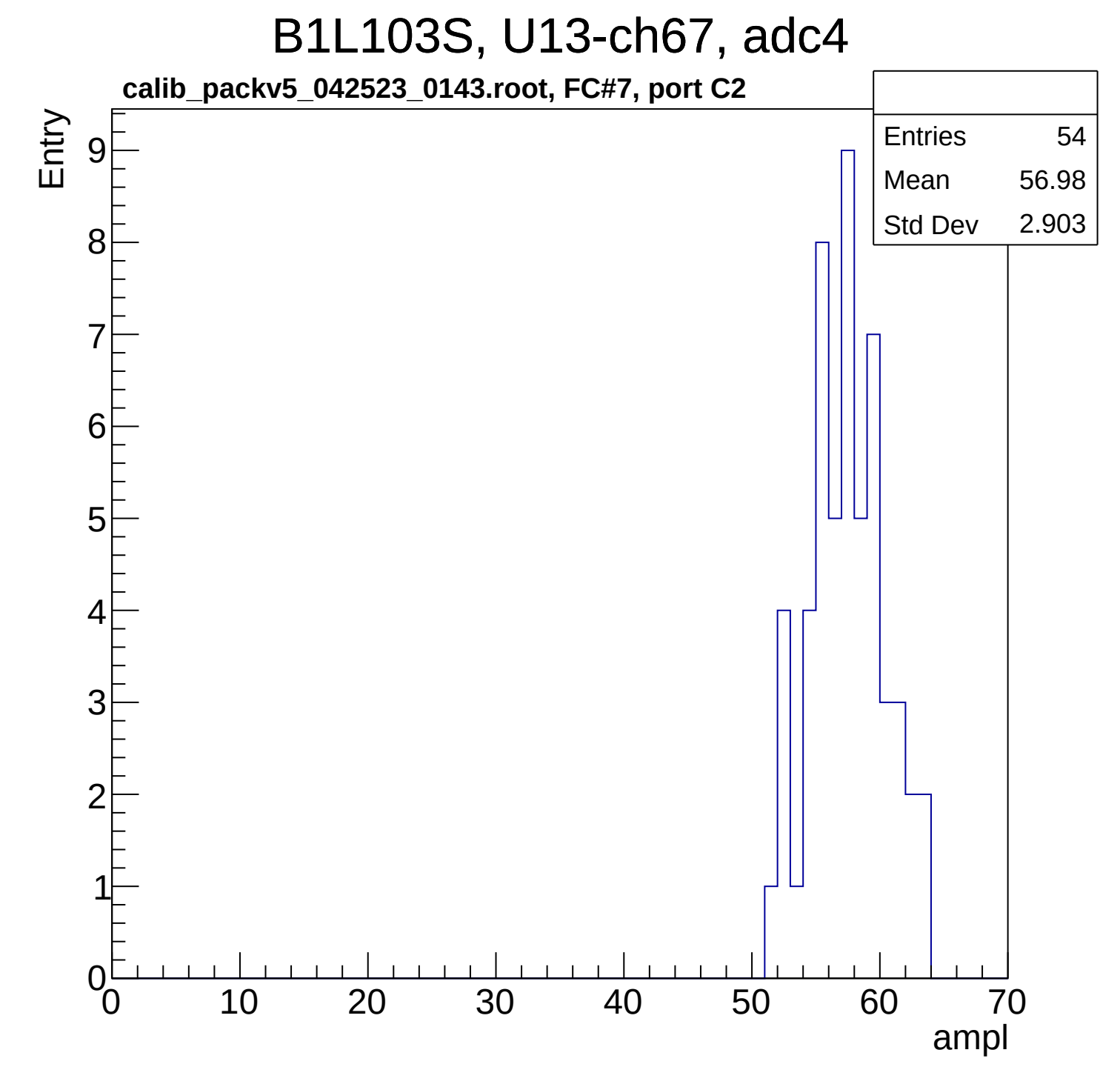
B1L103S, U13-ch67, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.98
Std Dev 2.903
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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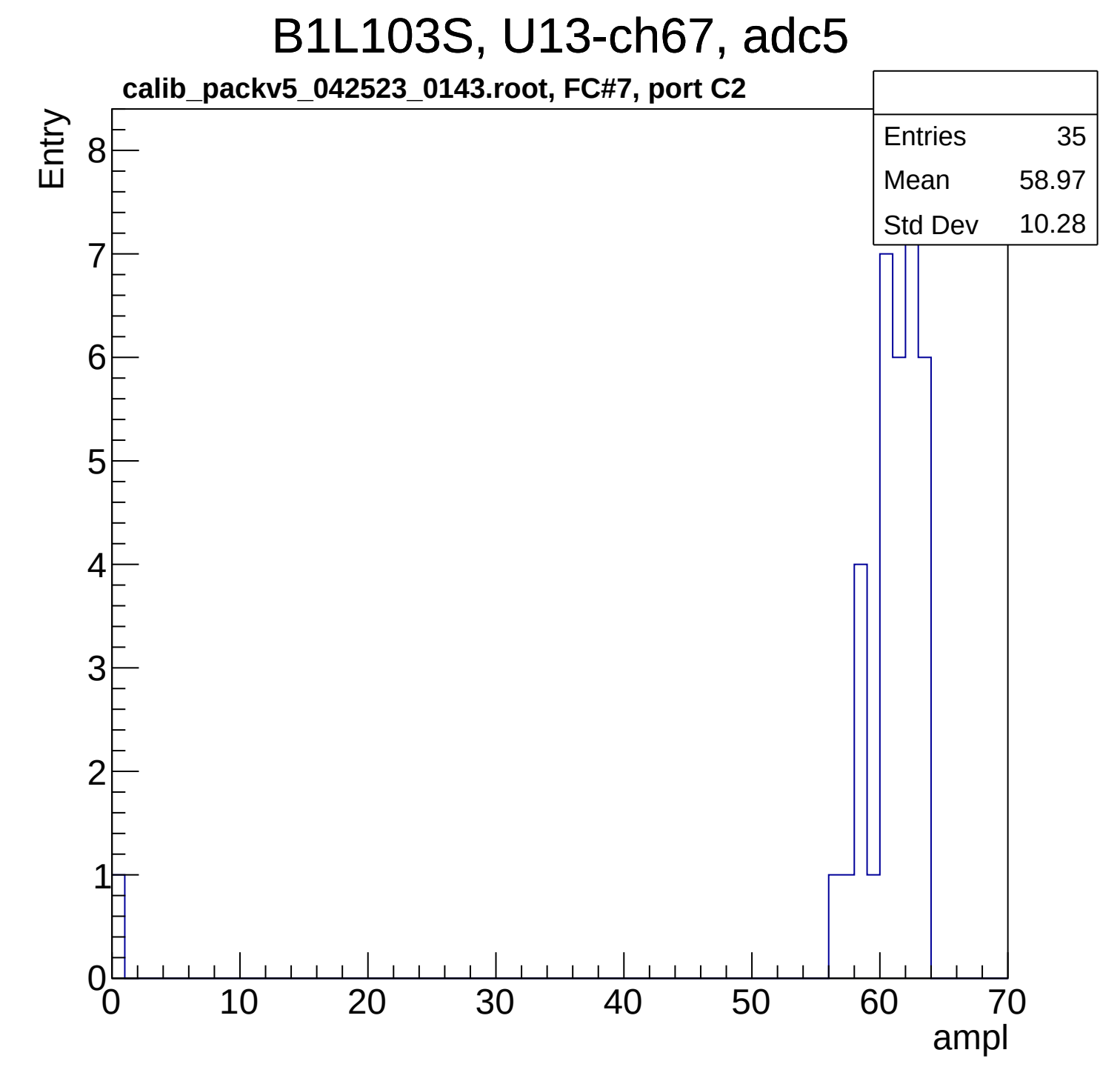
60
70


B1L103S, U13-ch67, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 58.97
Std Dev 10.28
Entry
ampl
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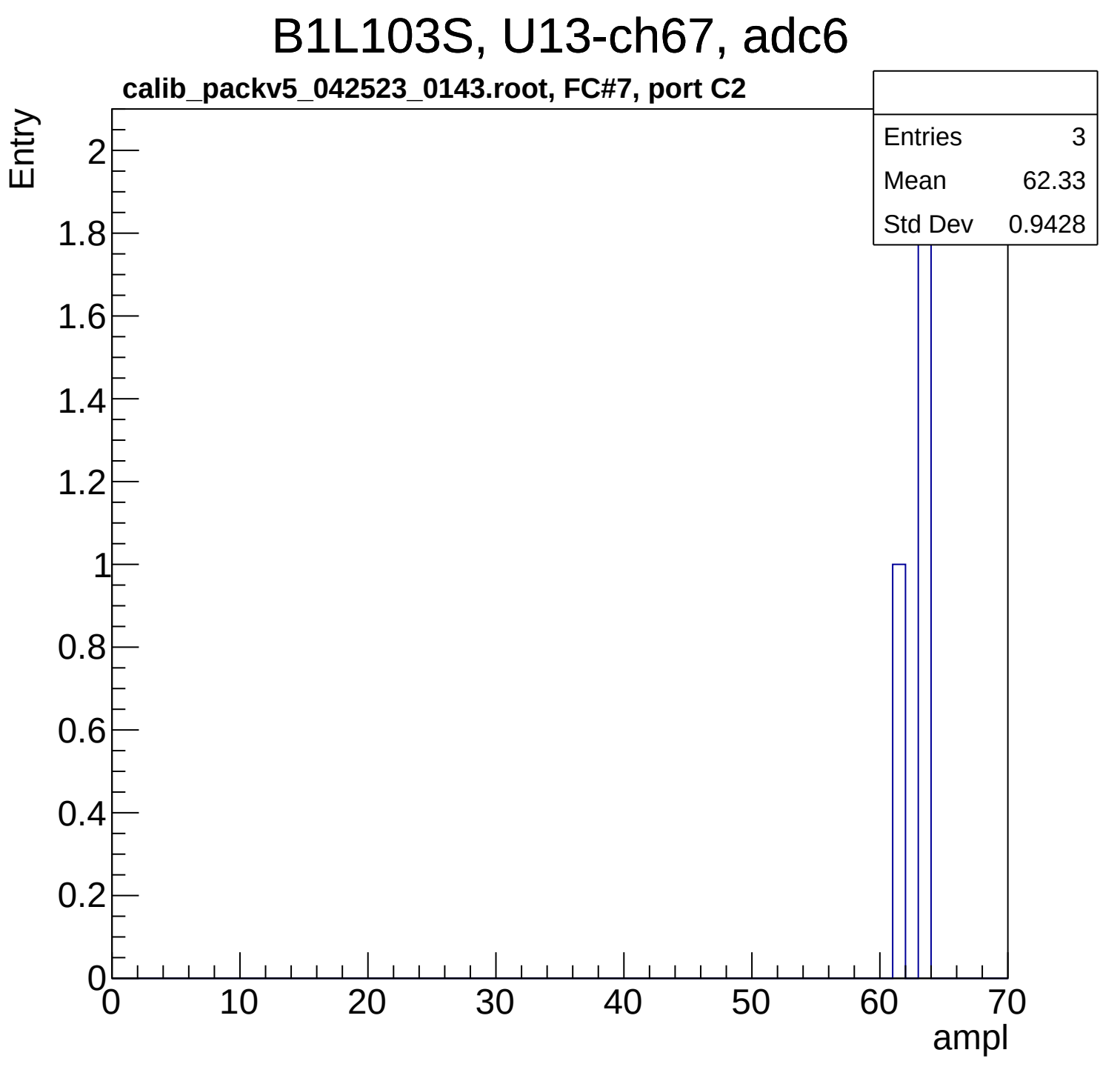
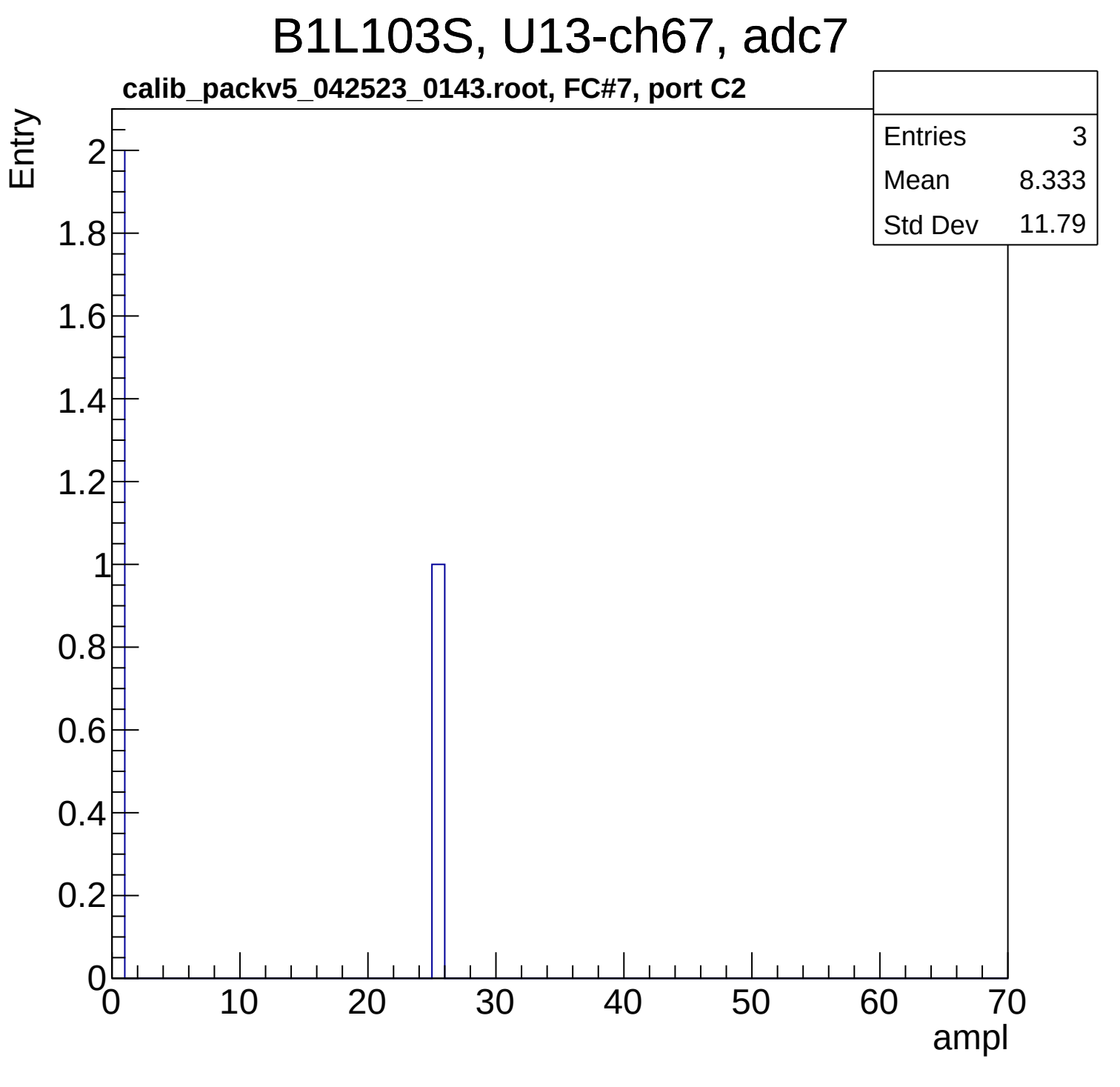
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch67, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch67, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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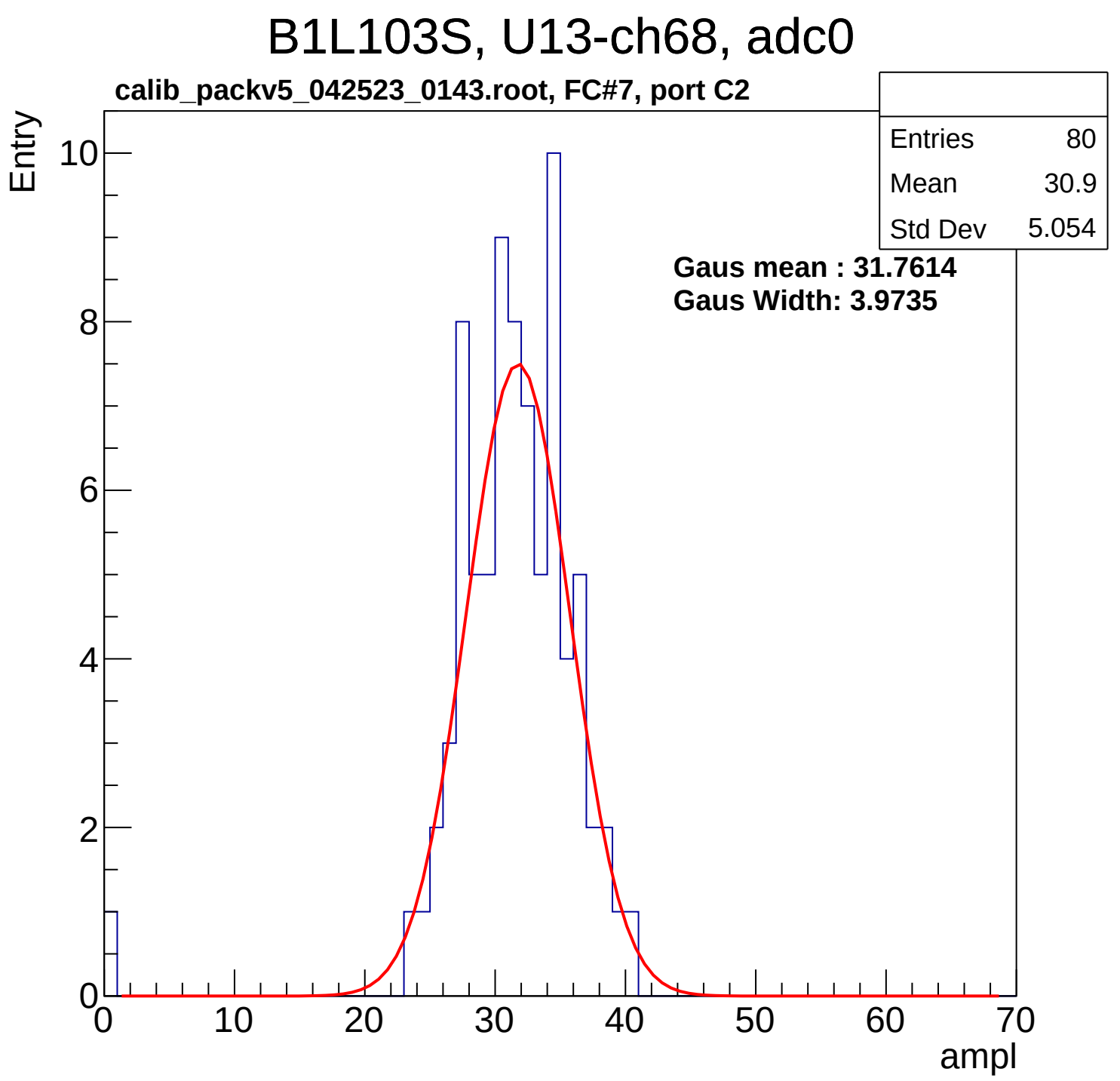
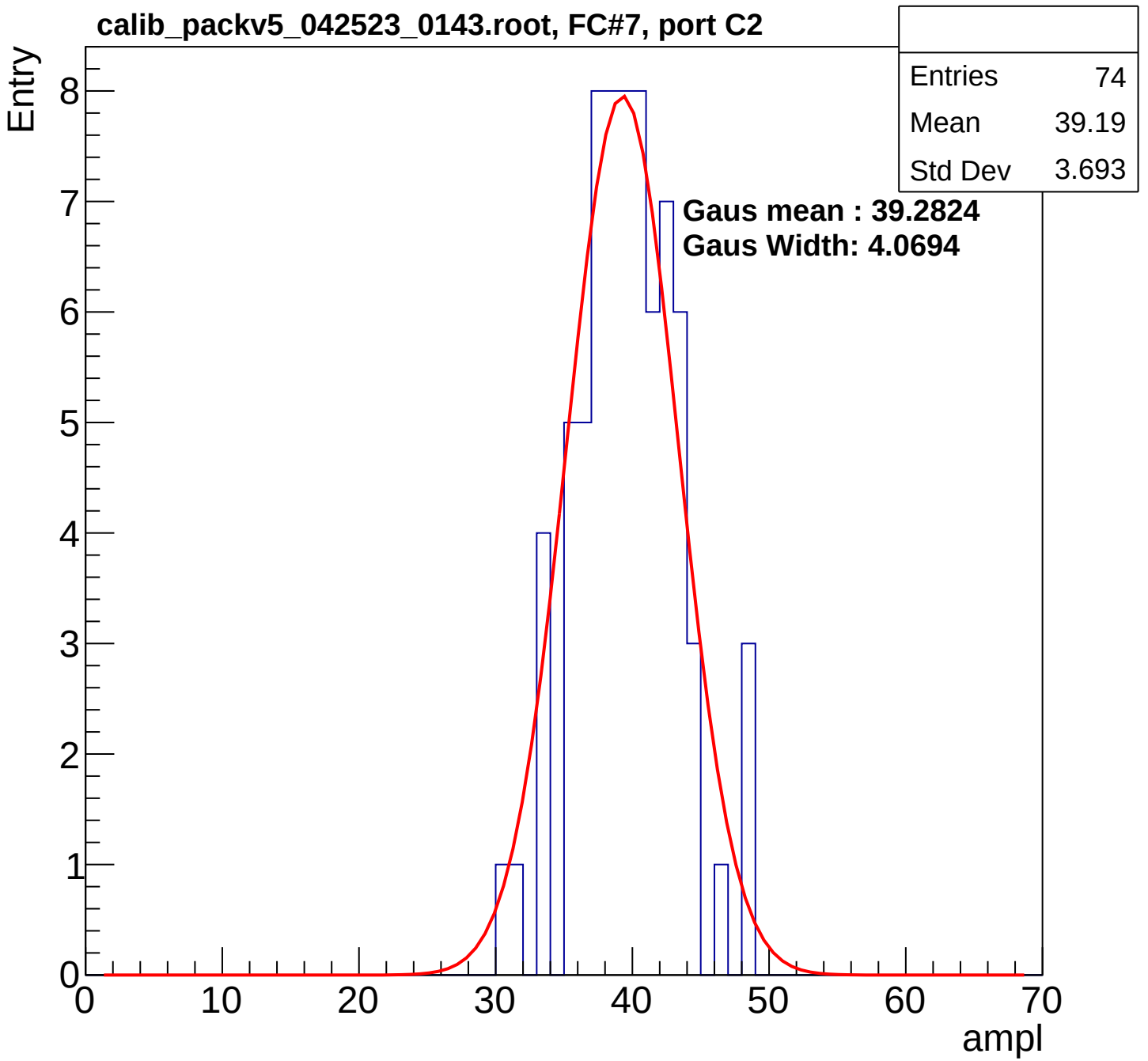
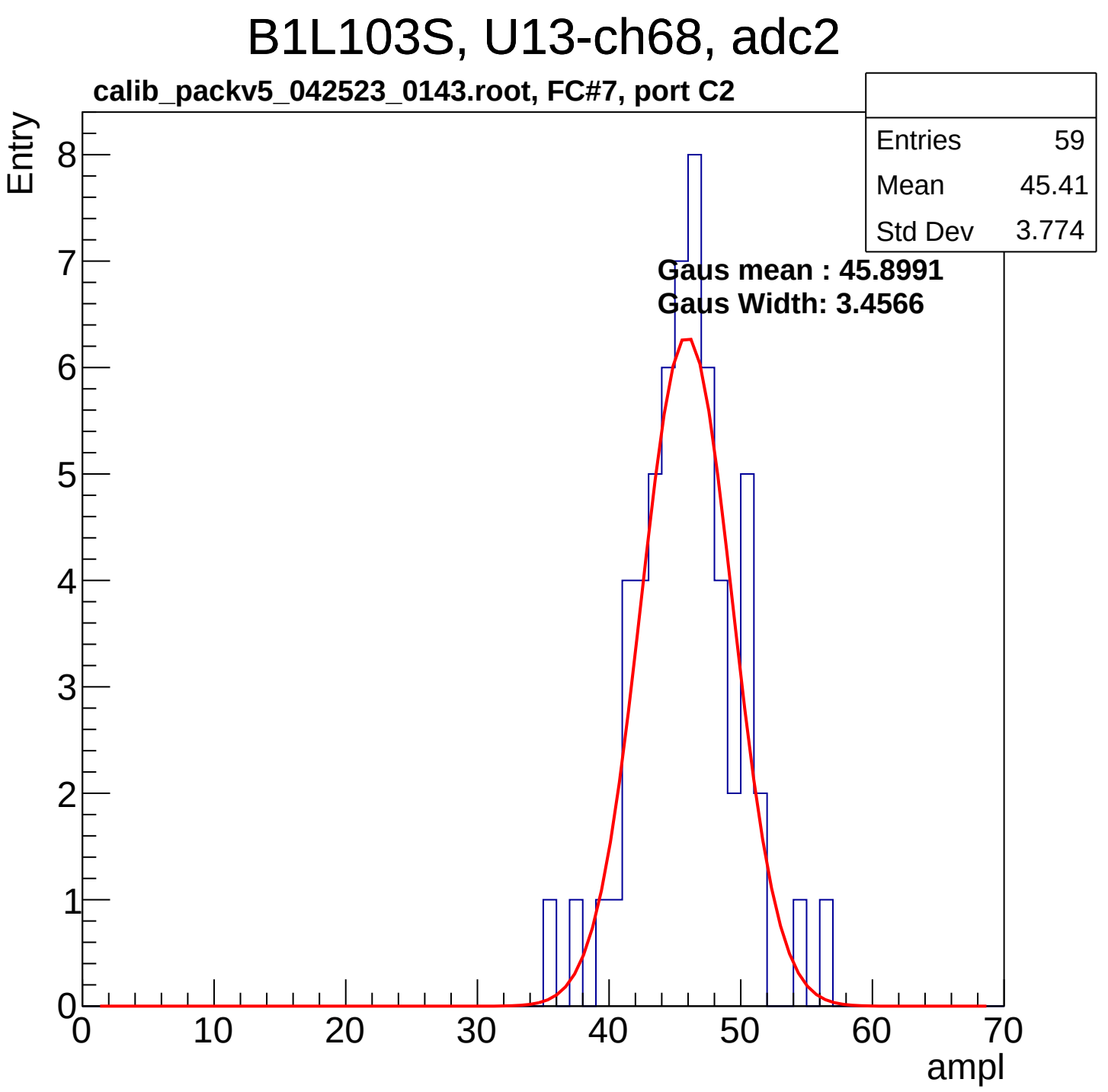

B1L103S, U13-ch68, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 30.9
Std Dev 5.054
Gaus mean : 31.7614
Gaus Width: 3.9735
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch68, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 39.19
Std Dev 3.693
Gaus mean : 39.2824
Gaus Width: 4.0694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch68, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 45.41
Std Dev 3.774
Gaus mean : 45.8991
Gaus Width: 3.4566
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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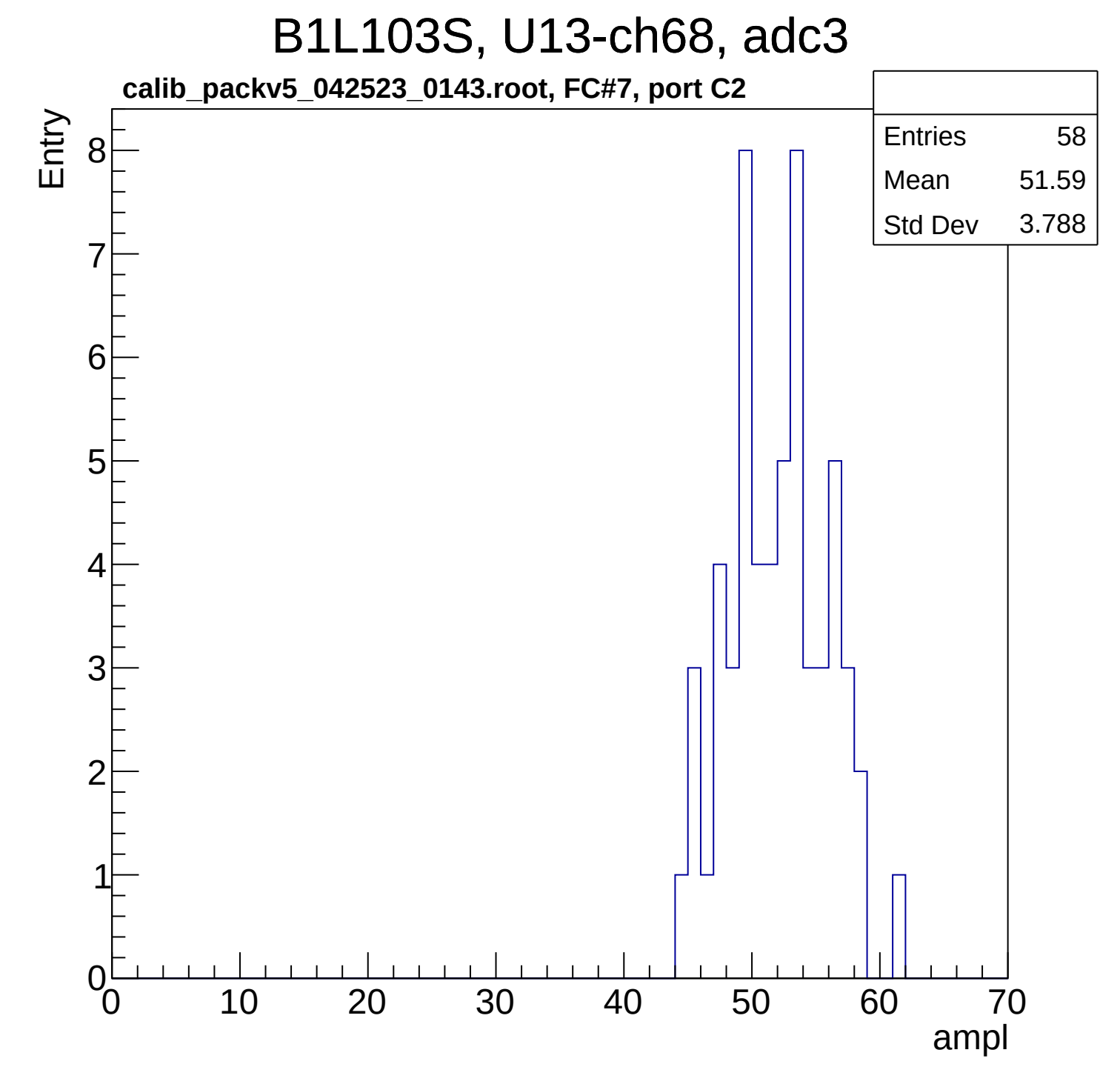
B1L103S, U13-ch68, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 51.59
Std Dev 3.788
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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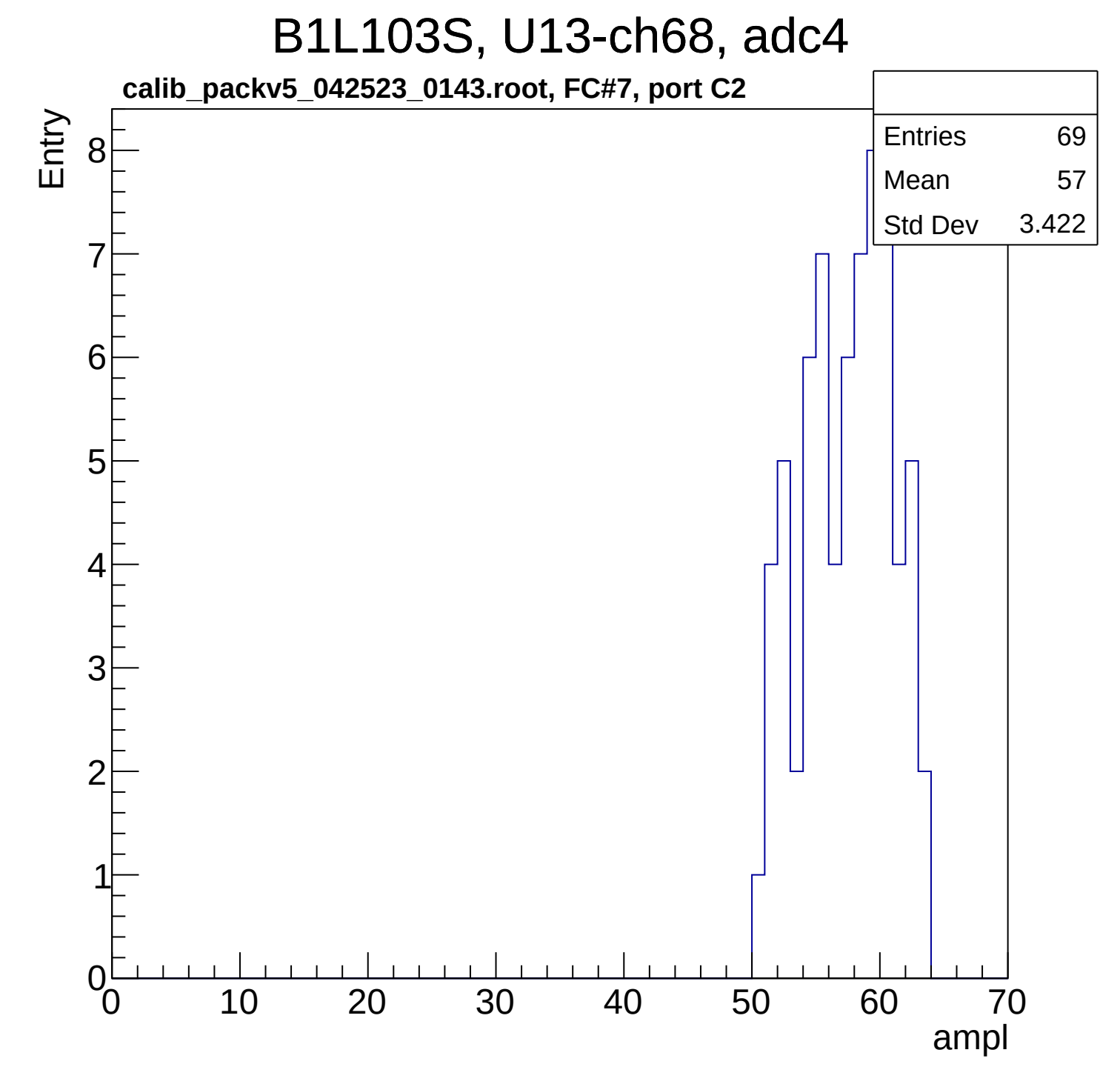
B1L103S, U13-ch68, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 57
Std Dev 3.422
Entry
ampl
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4
5
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7
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0
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20
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40
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60
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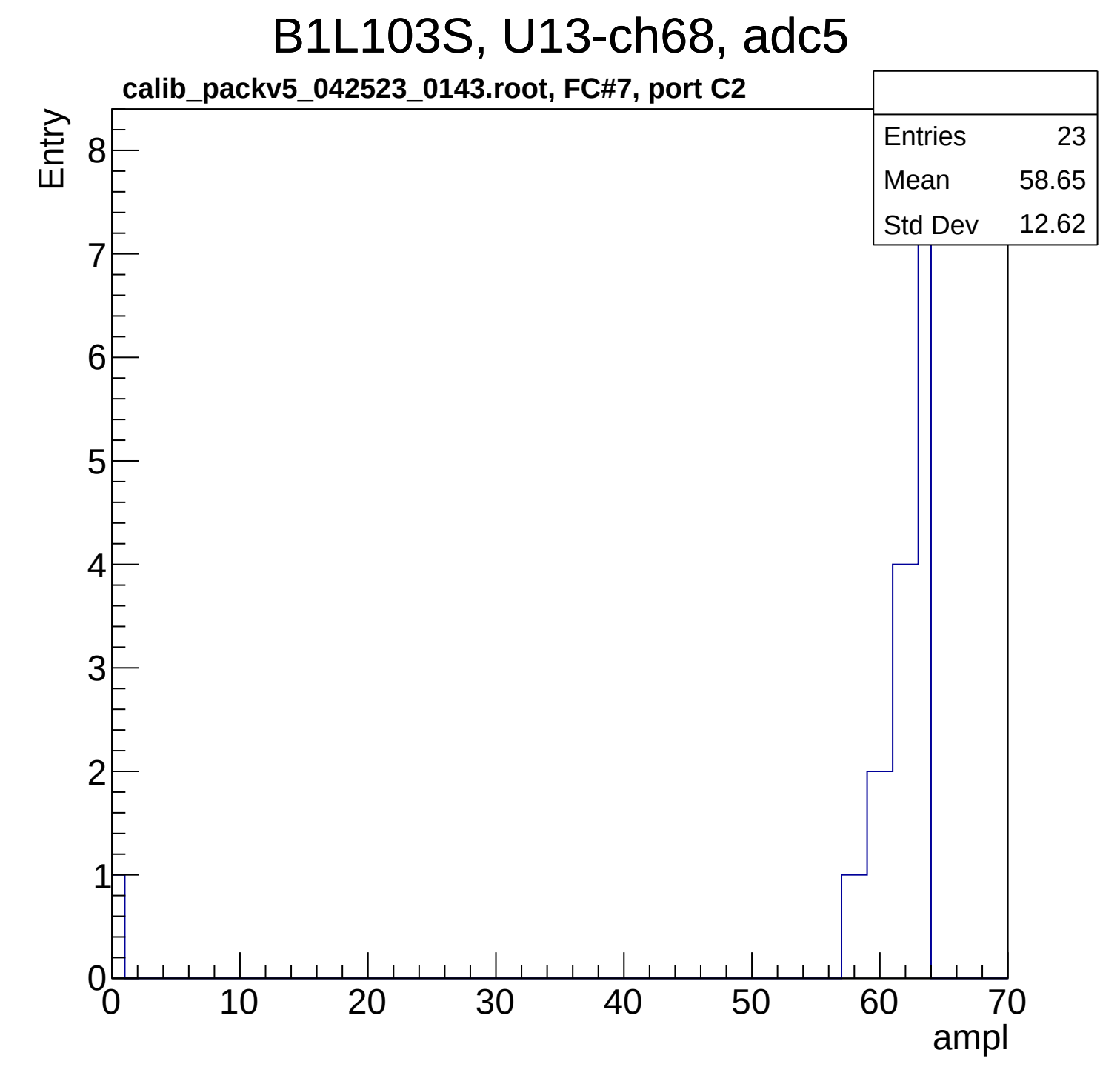
B1L103S, U13-ch68, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 23
Mean 58.65
Std Dev 12.62
Entry
ampl
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1
2
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4
5
6
7
8
0
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20
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40
50
60
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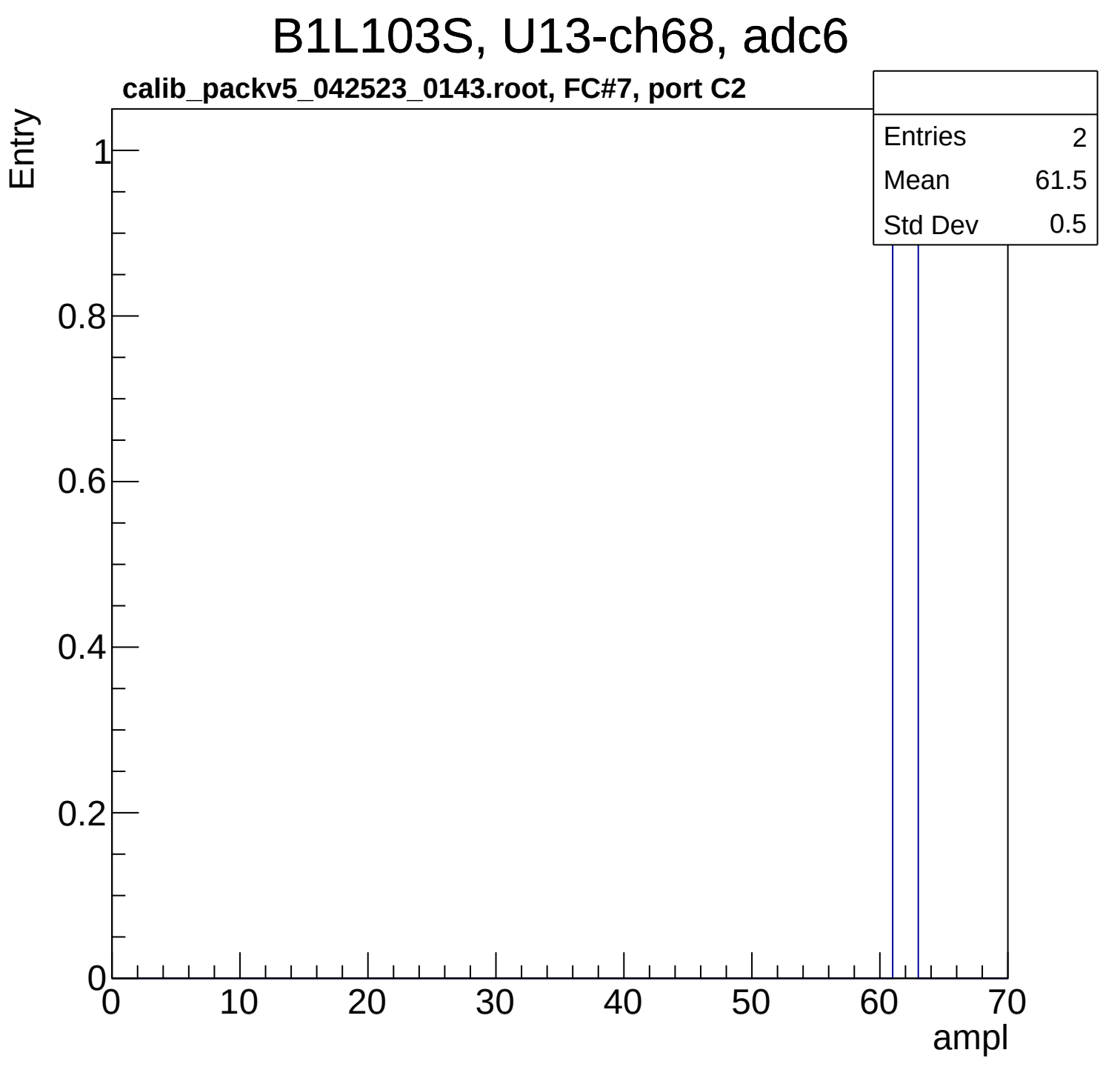
B1L103S, U13-ch68, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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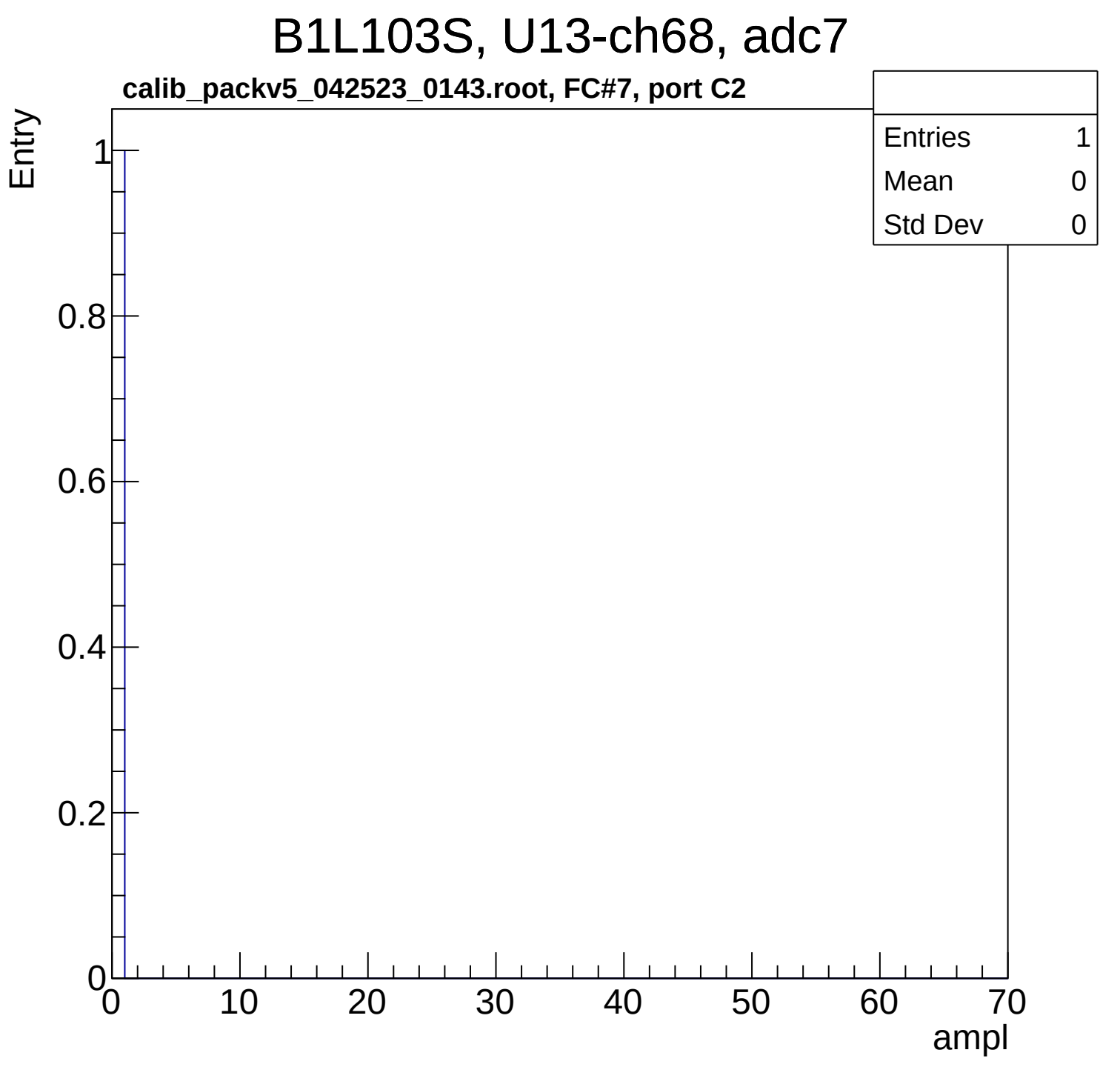
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch68, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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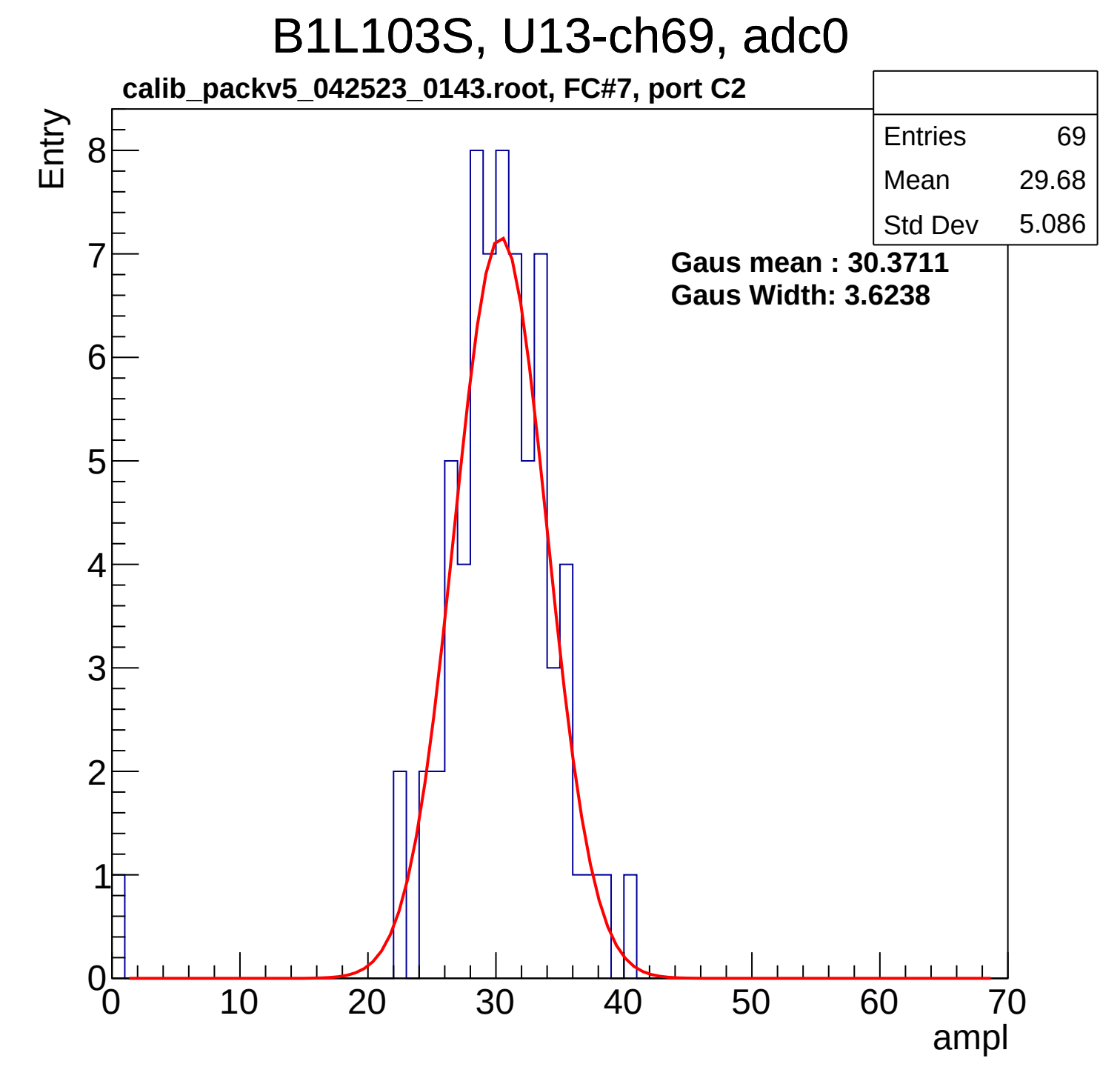
50
60
70
ampl


B1L103S, U13-ch69, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 29.68
Std Dev 5.086
Gaus mean : 30.3711
Gaus Width: 3.6238
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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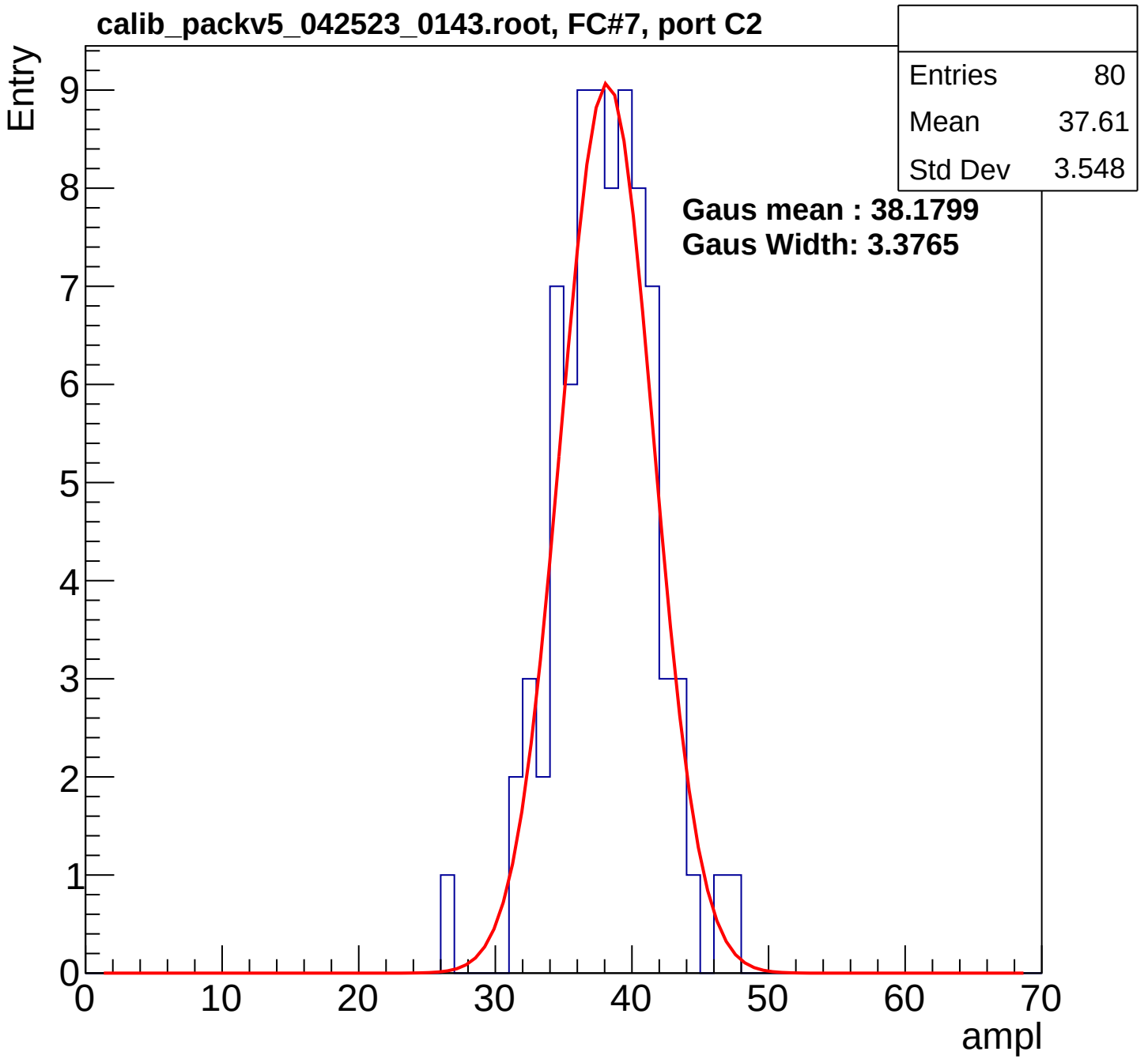
B1L103S, U13-ch69, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 37.61
Std Dev 3.548
Gaus mean : 38.1799
Gaus Width: 3.3765
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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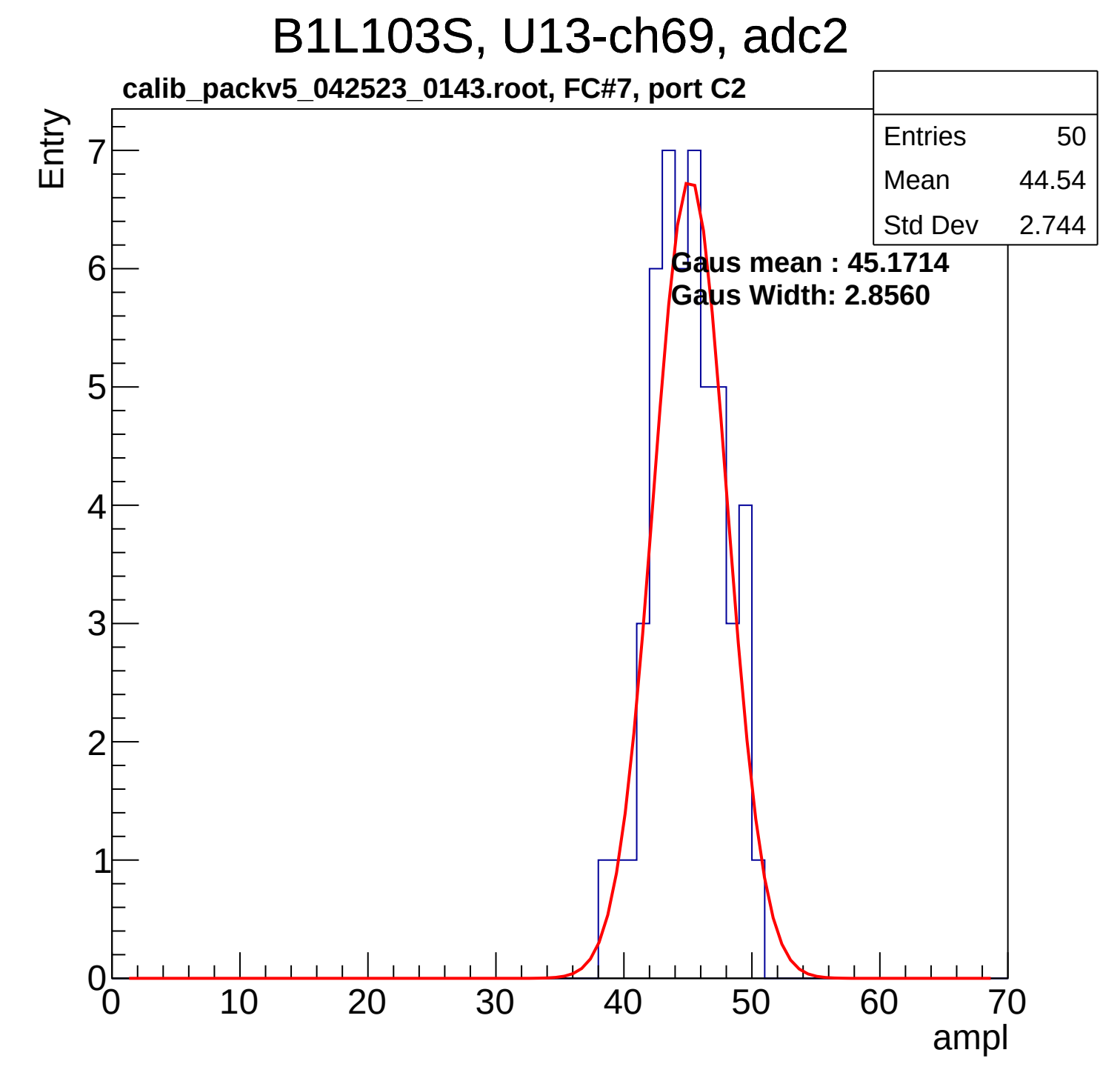
B1L103S, U13-ch69, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 44.54
Std Dev 2.744
Gaus mean : 45.1714
Gaus Width: 2.8560
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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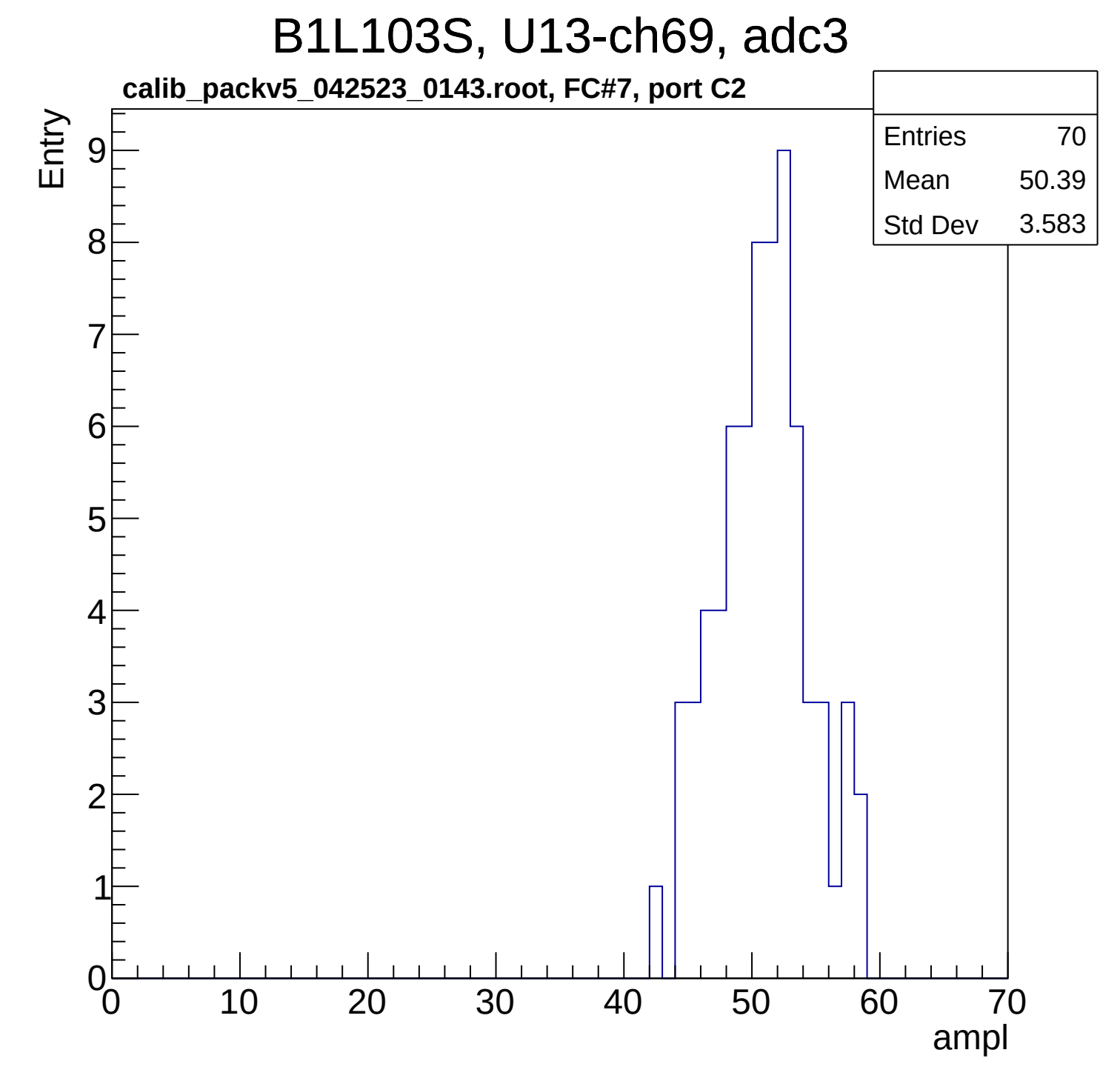
B1L103S, U13-ch69, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 50.39
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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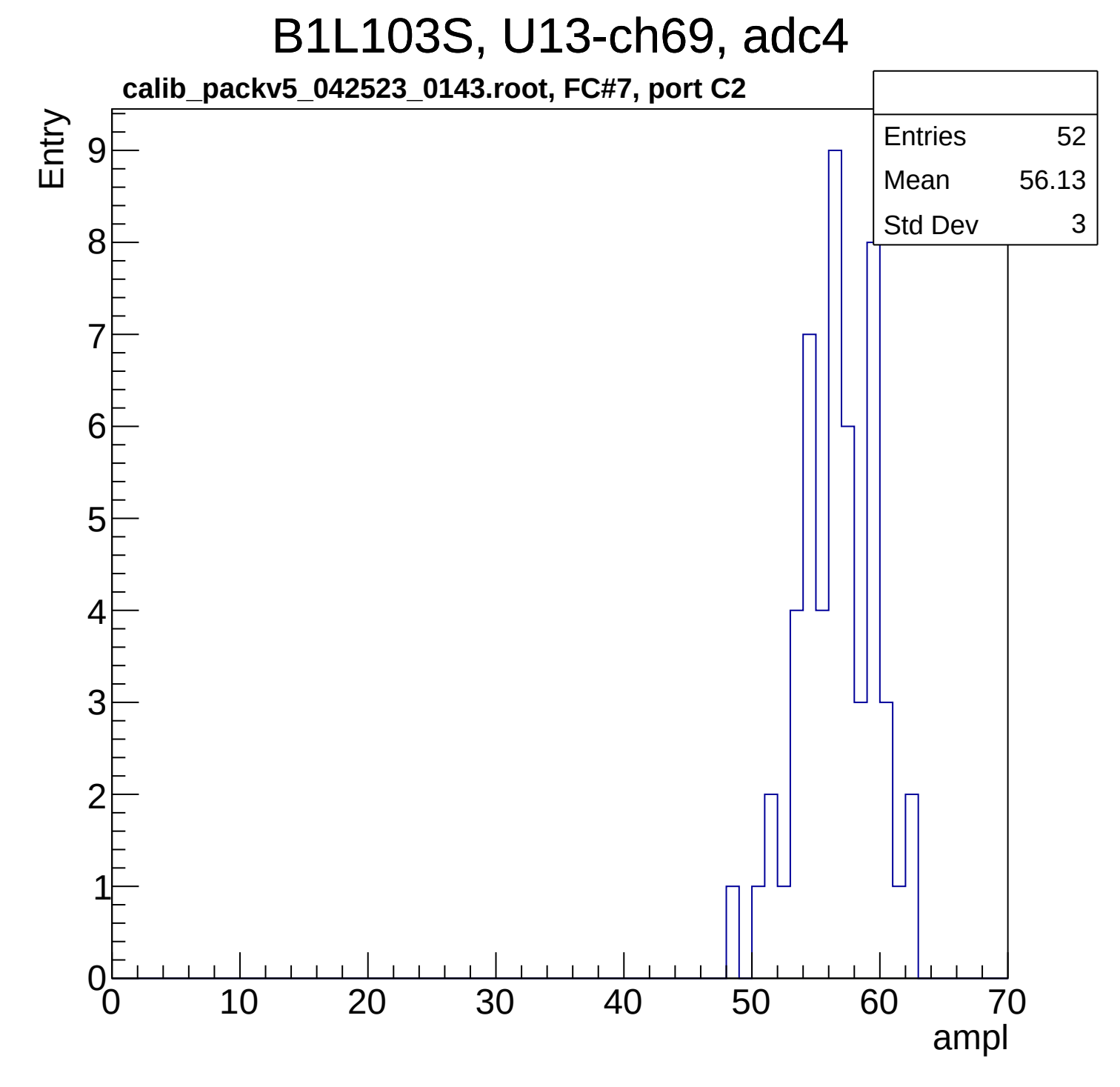
B1L103S, U13-ch69, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 56.13
Std Dev 3
Entry
ampl
0
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2
3
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6
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0
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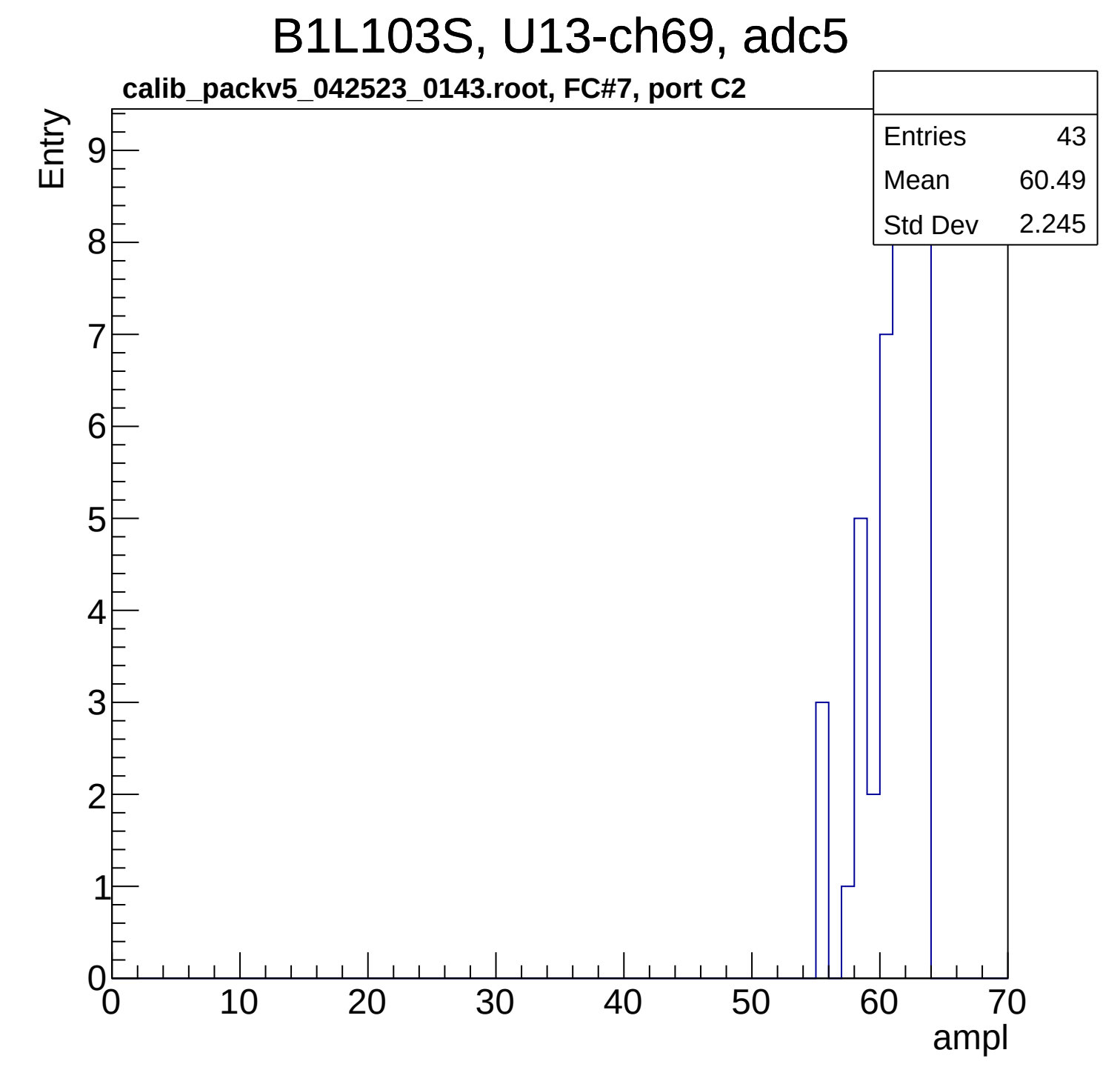
B1L103S, U13-ch69, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 60.49
Std Dev 2.245
Entry
ampl
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2
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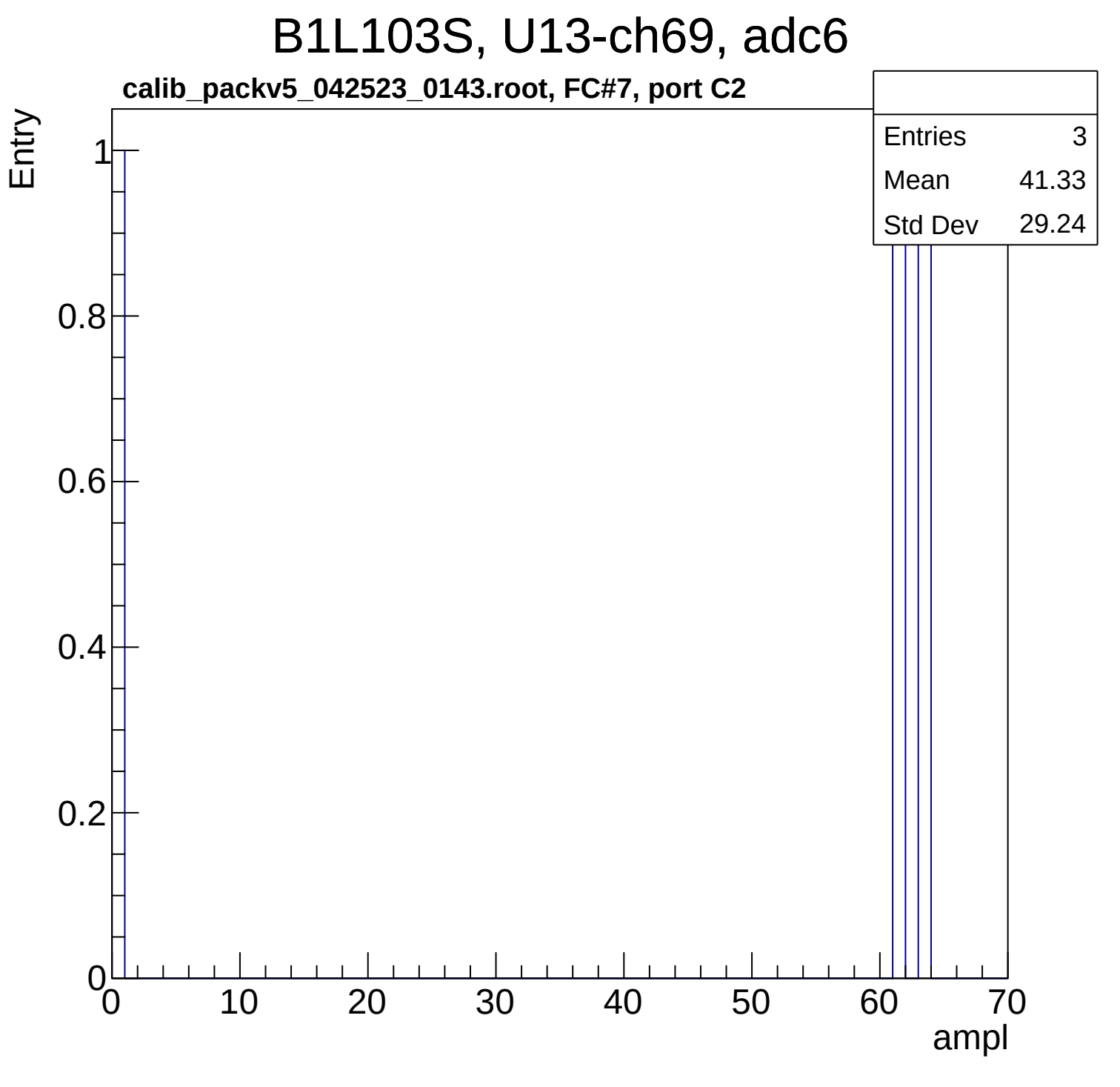
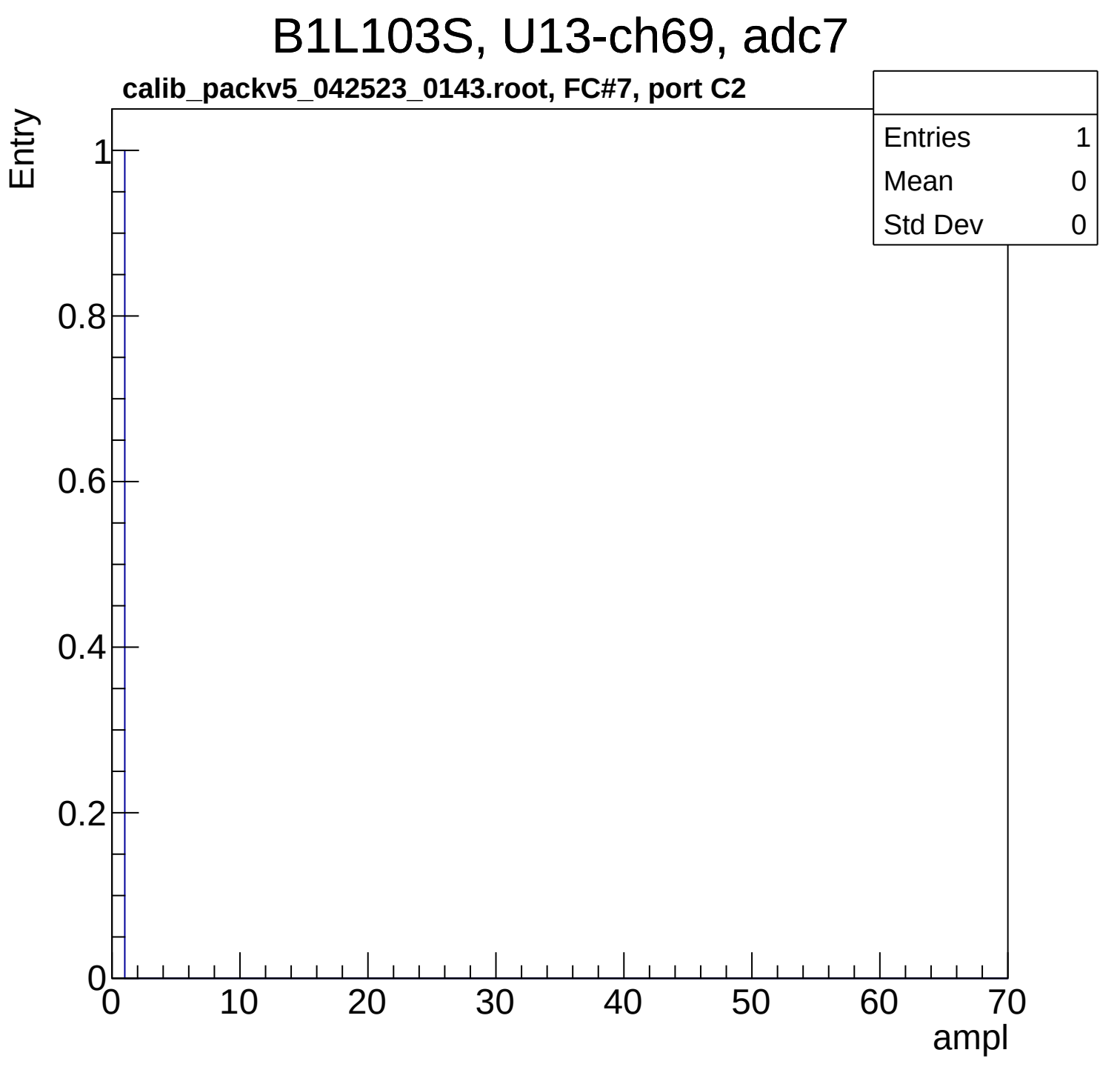
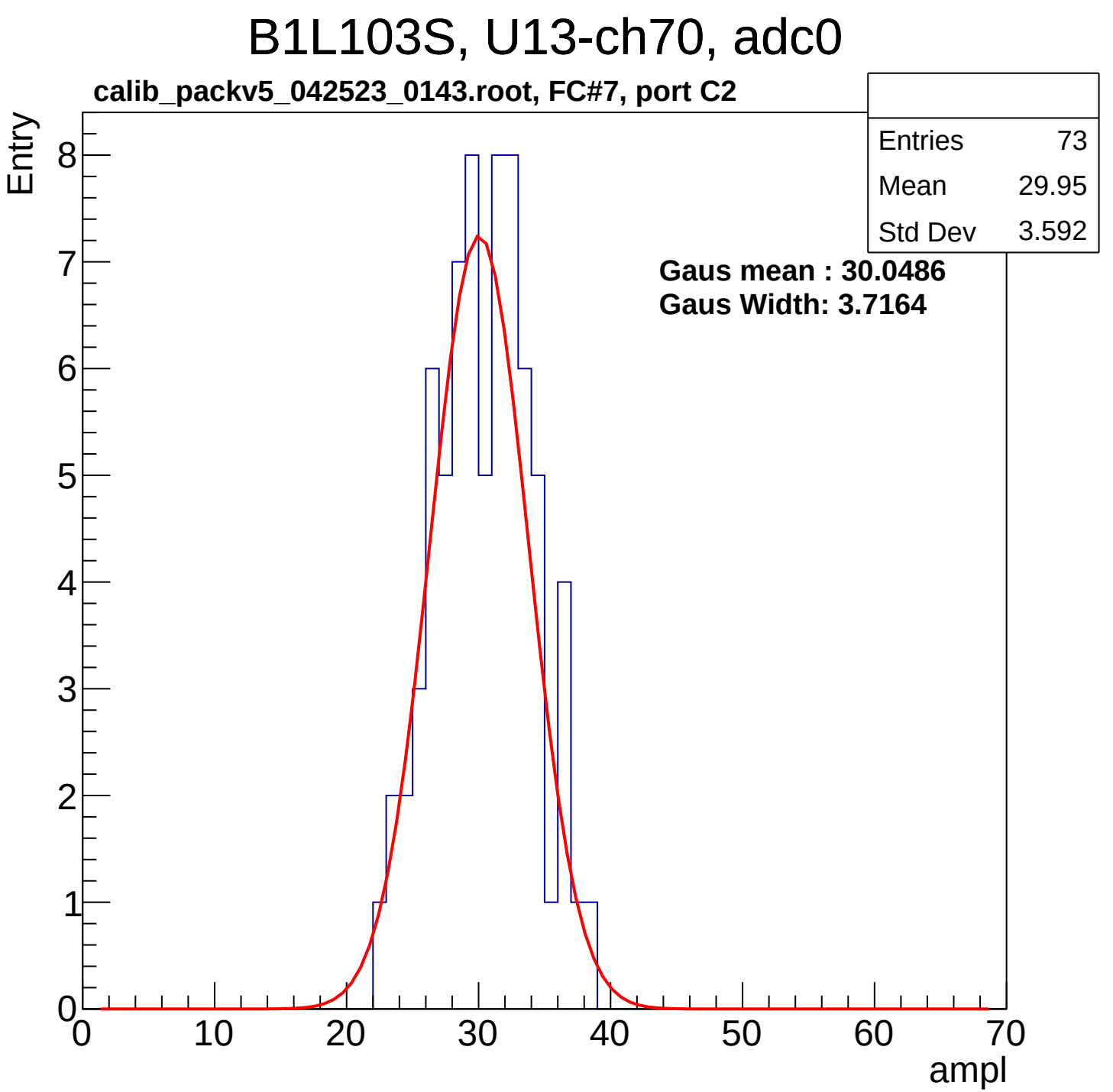

B1L103S, U13-ch69, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch69, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch70, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.95
Std Dev 3.592
Gaus mean : 30.0486
Gaus Width: 3.7164
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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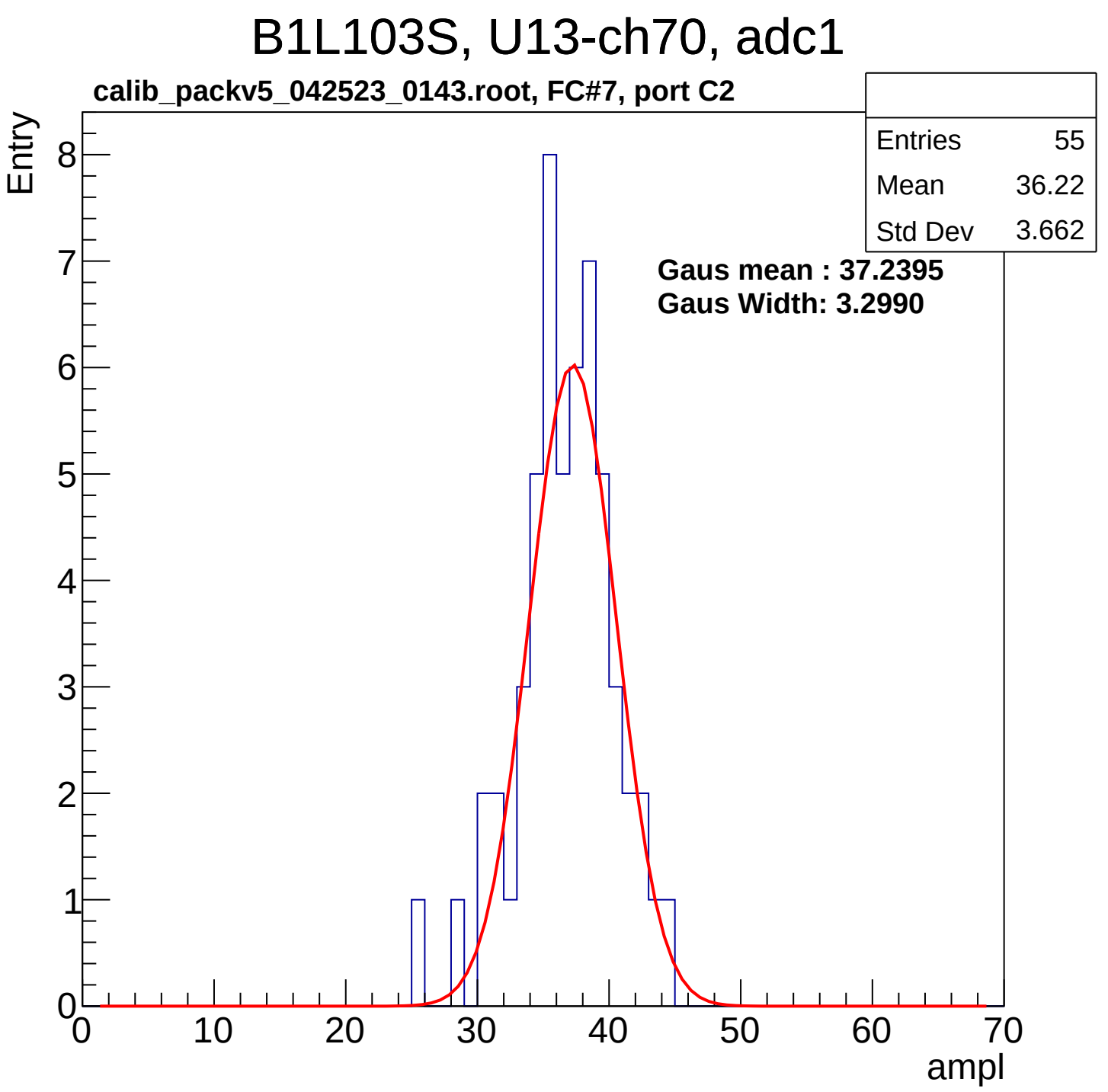
B1L103S, U13-ch70, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 36.22
Std Dev 3.662
Gaus mean : 37.2395
Gaus Width: 3.2990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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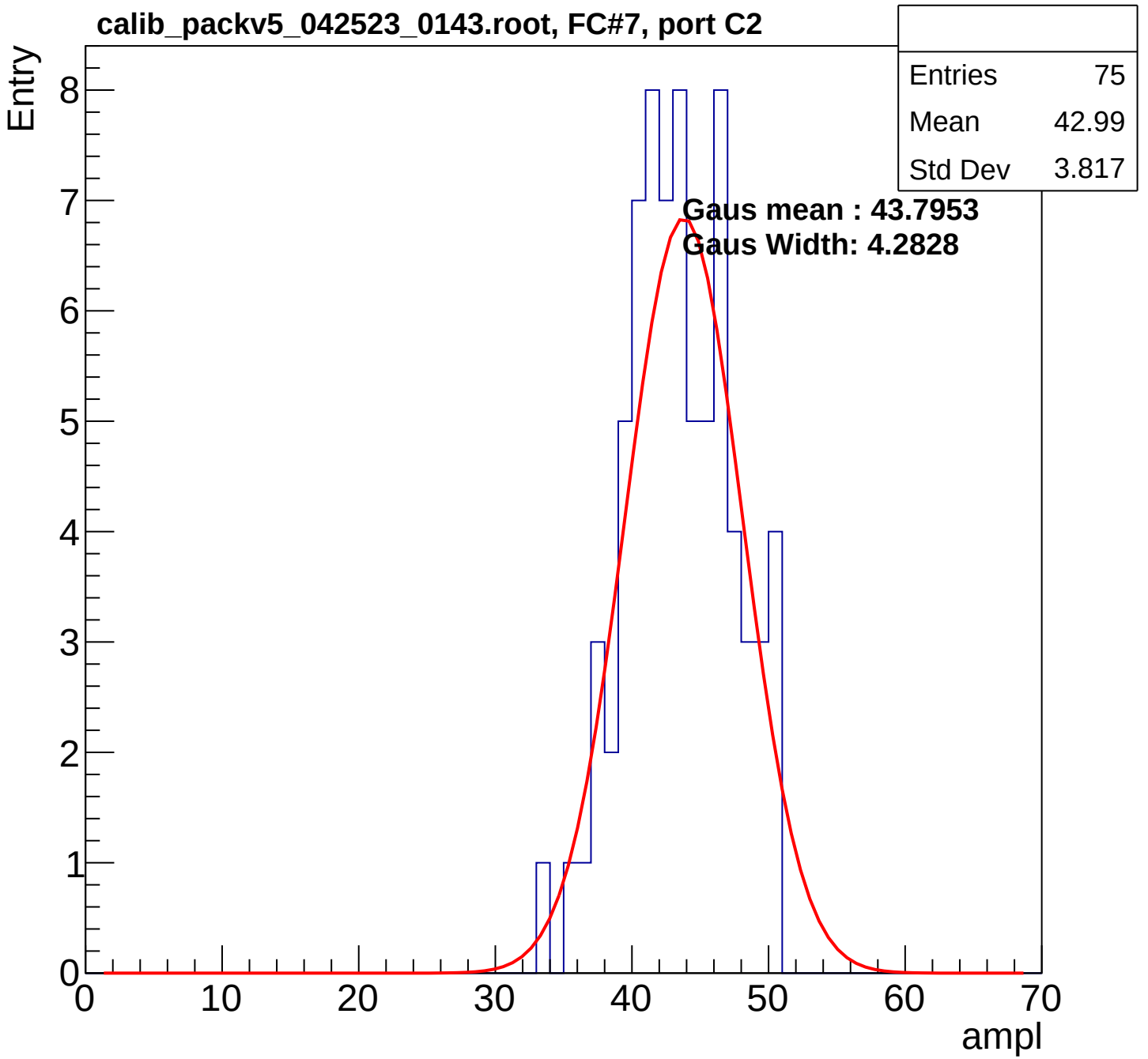
B1L103S, U13-ch70, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 42.99
Std Dev 3.817
Gaus mean : 43.7953
Gaus Width: 4.2828
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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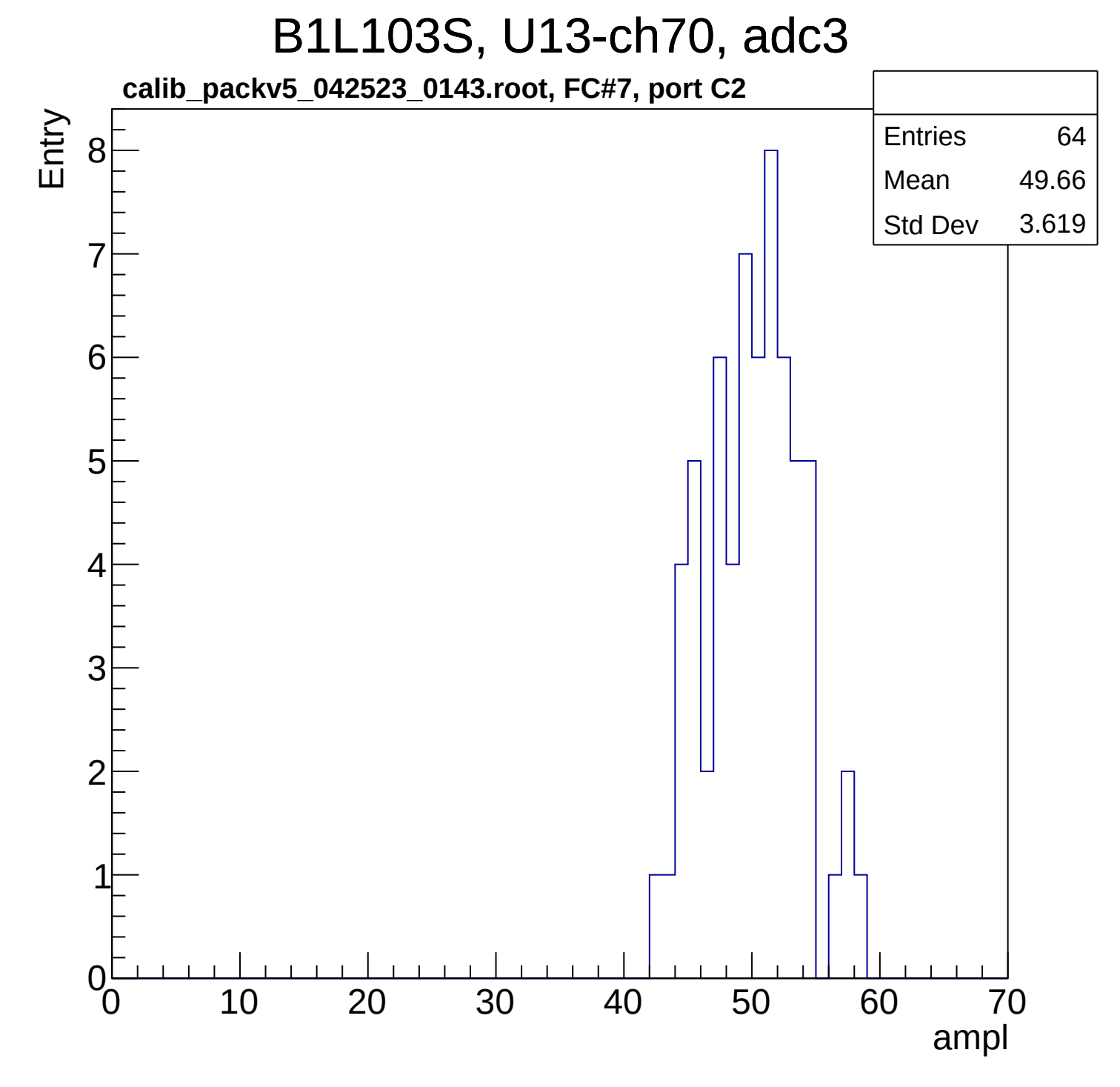
B1L103S, U13-ch70, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.66
Std Dev 3.619
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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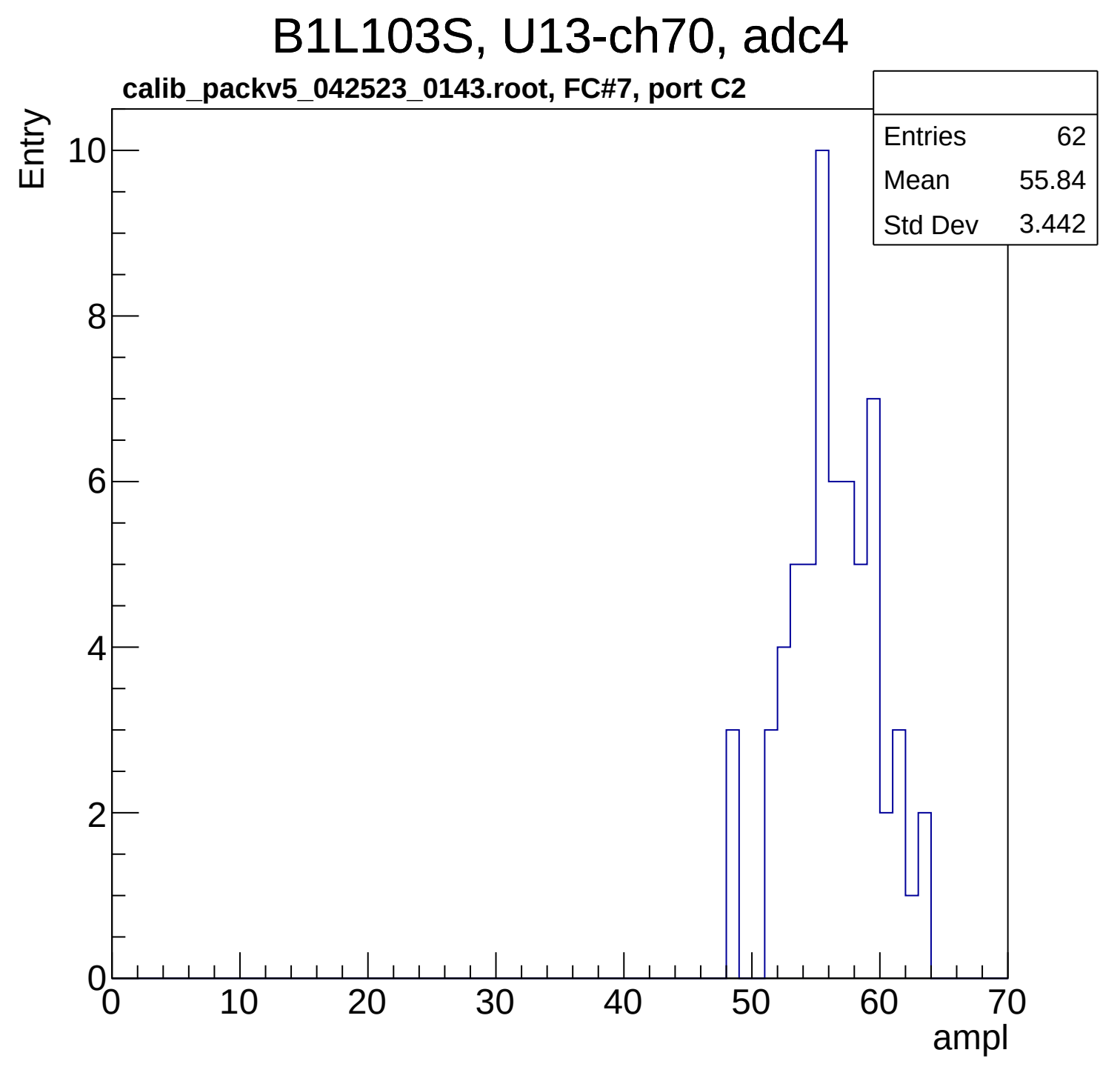
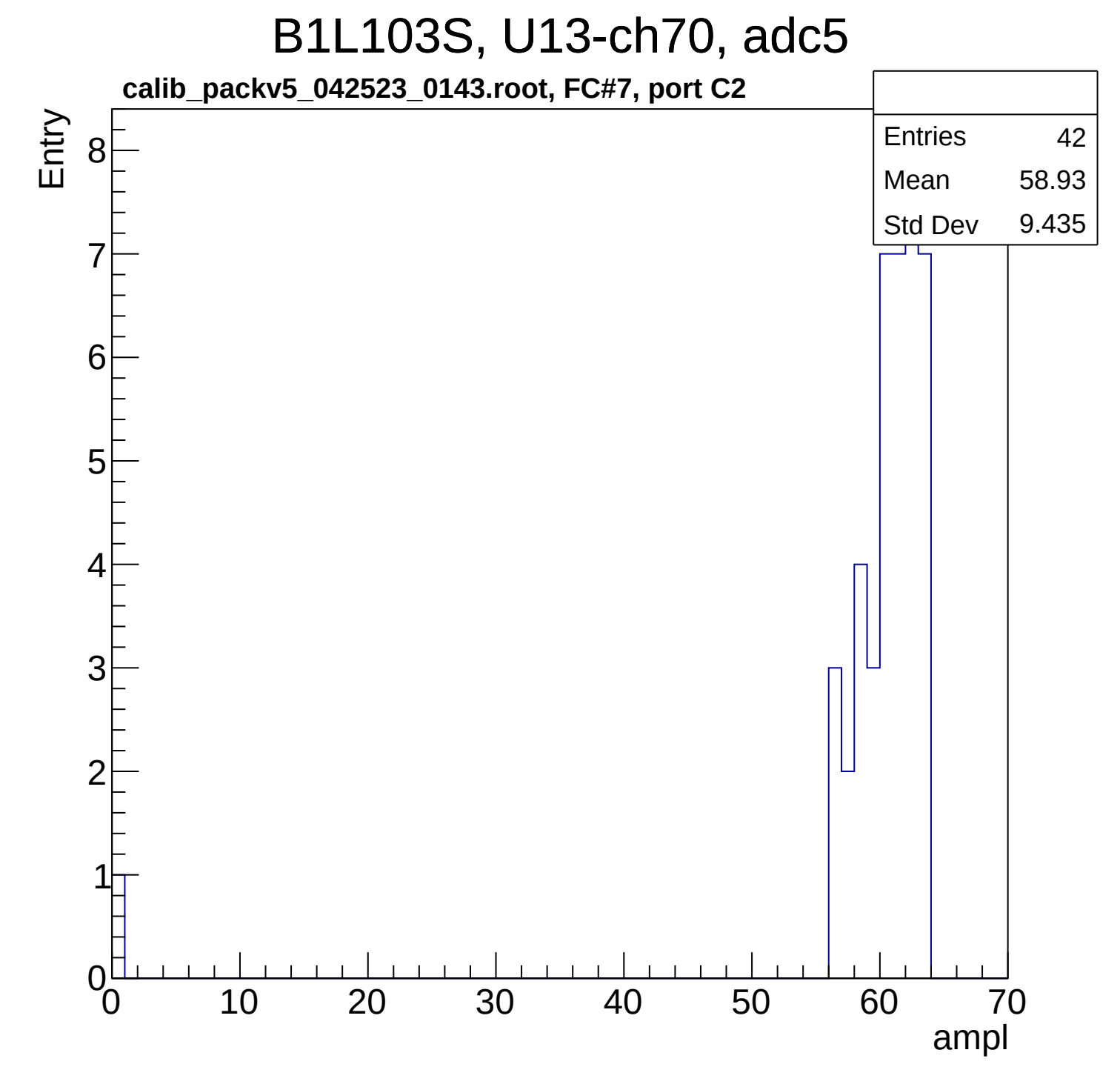

B1L103S, U13-ch70, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.84
Std Dev 3.442
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch70, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.93
Std Dev 9.435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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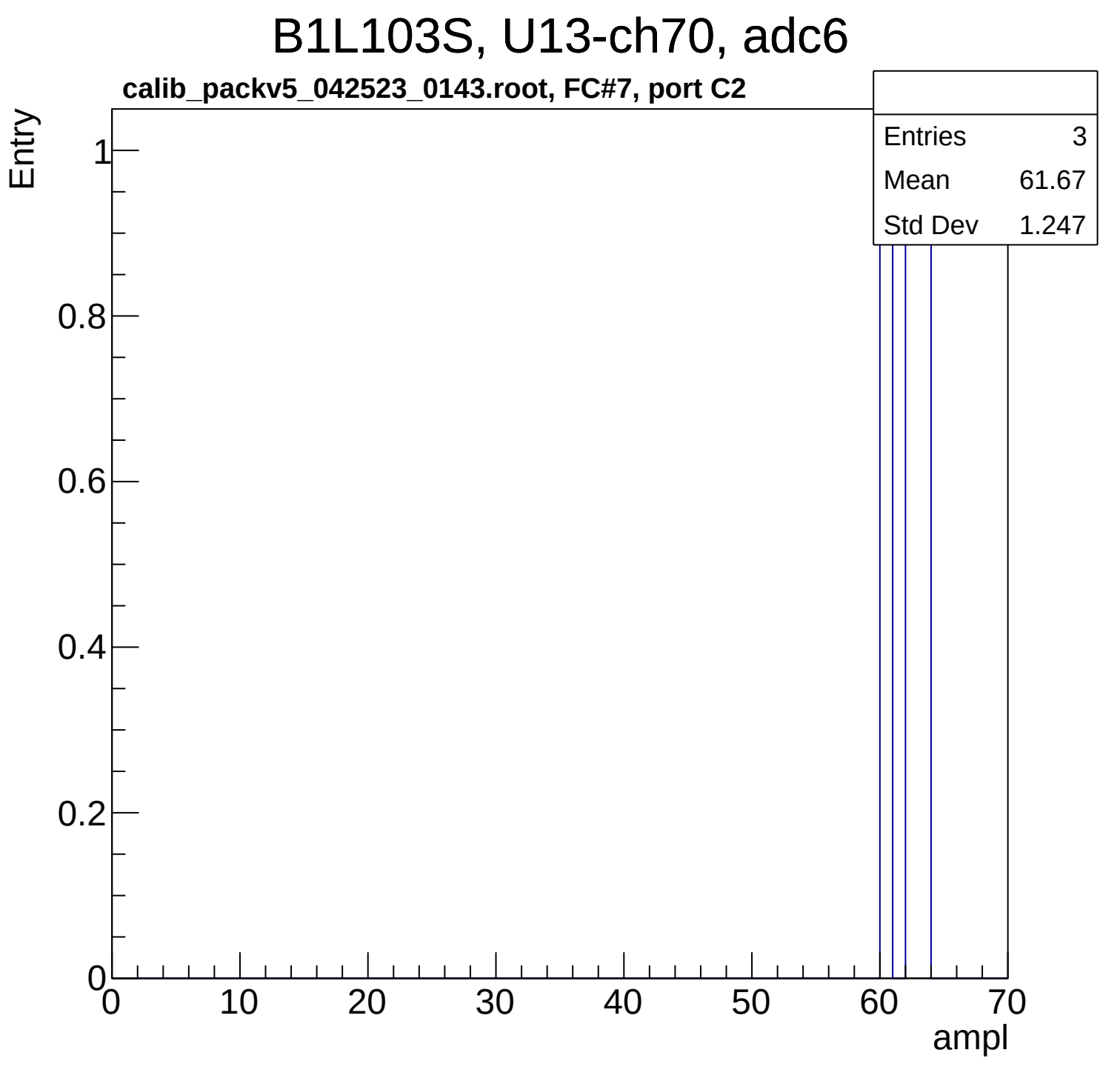
B1L103S, U13-ch70, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
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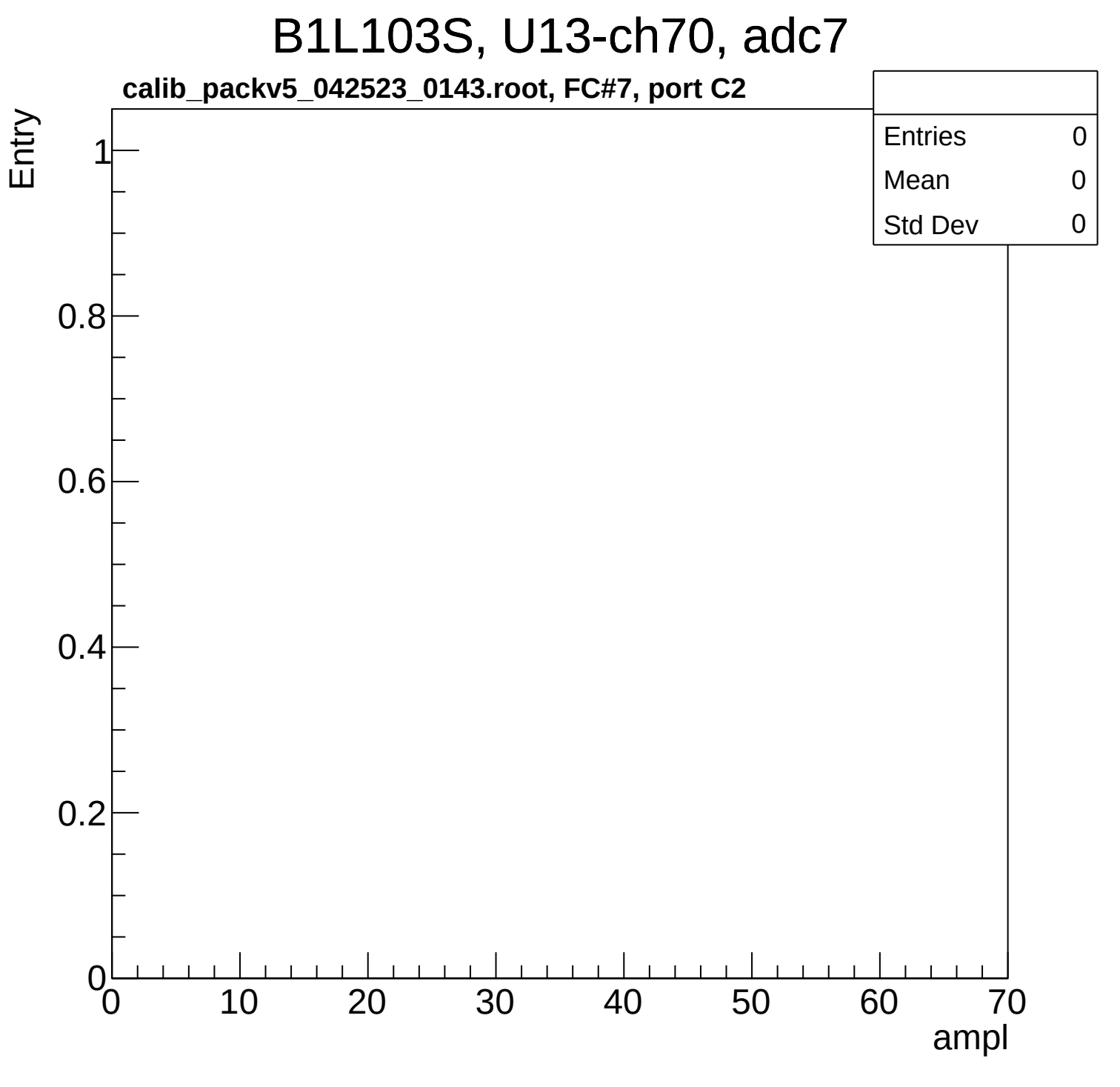
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch70, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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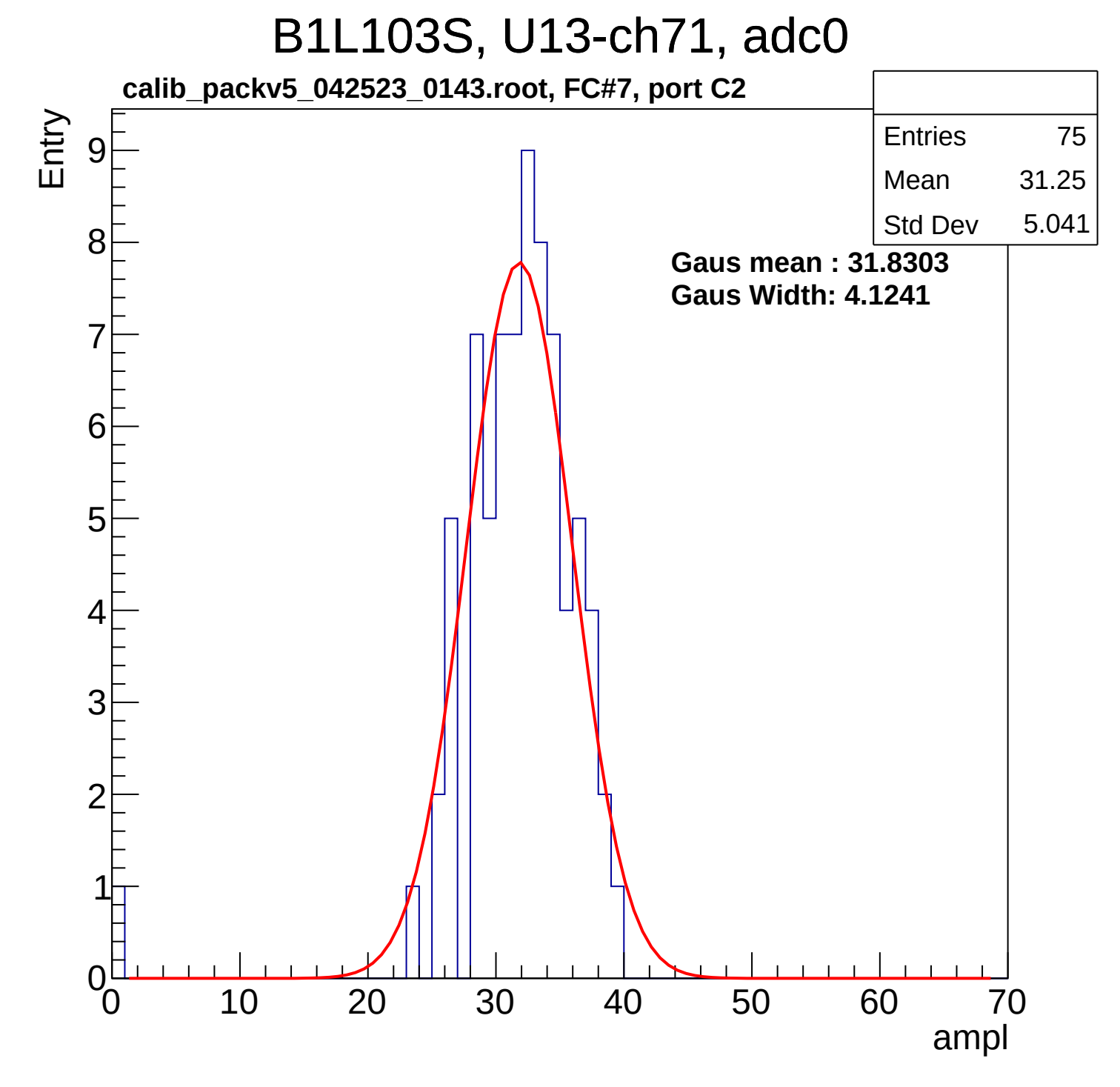
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch71, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 31.25
Std Dev 5.041
Gaus mean : 31.8303
Gaus Width: 4.1241
Entry
ampl
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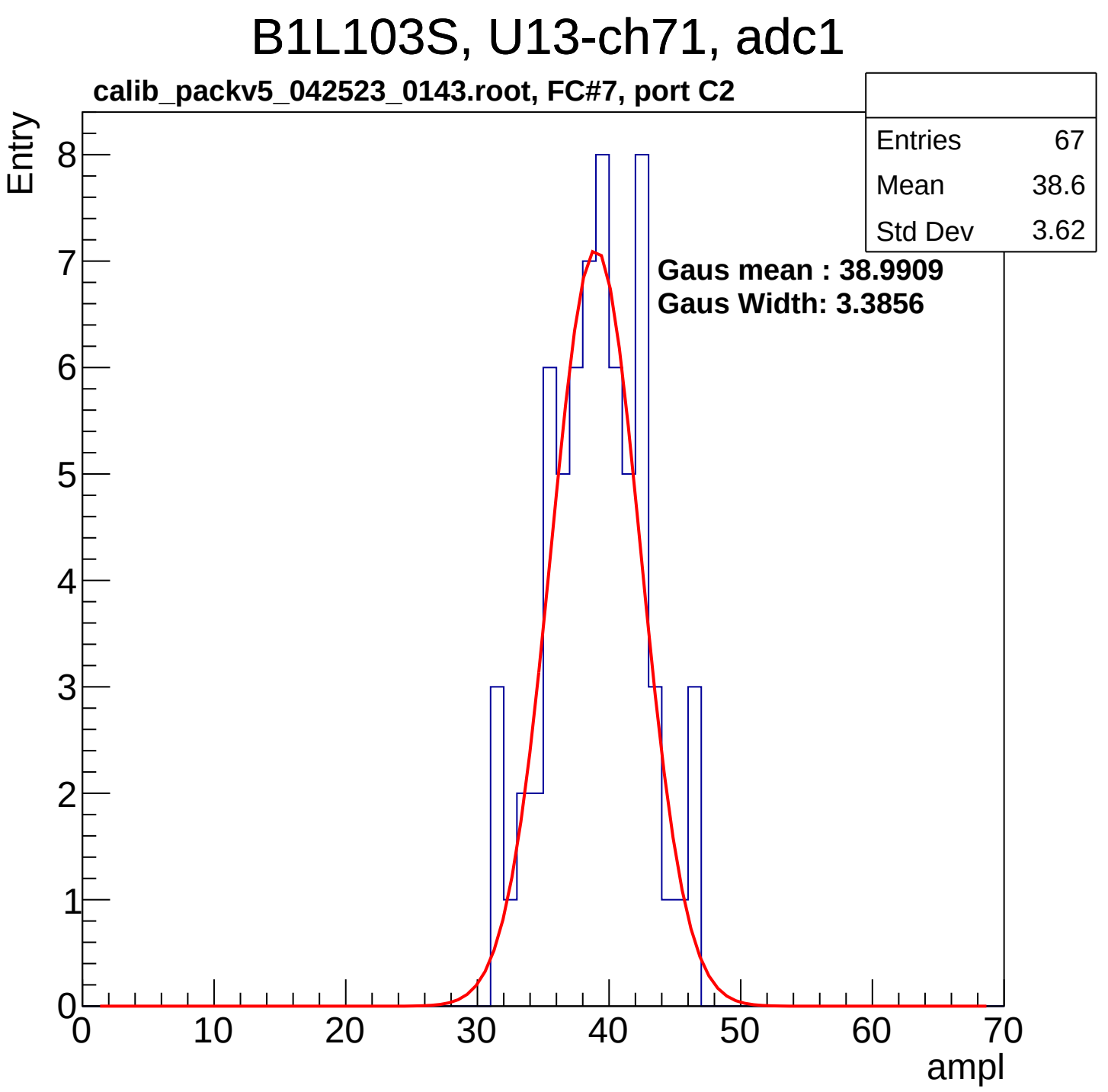
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch71, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 38.6
Std Dev 3.62
Gaus mean : 38.9909
Gaus Width: 3.3856
Entry
ampl
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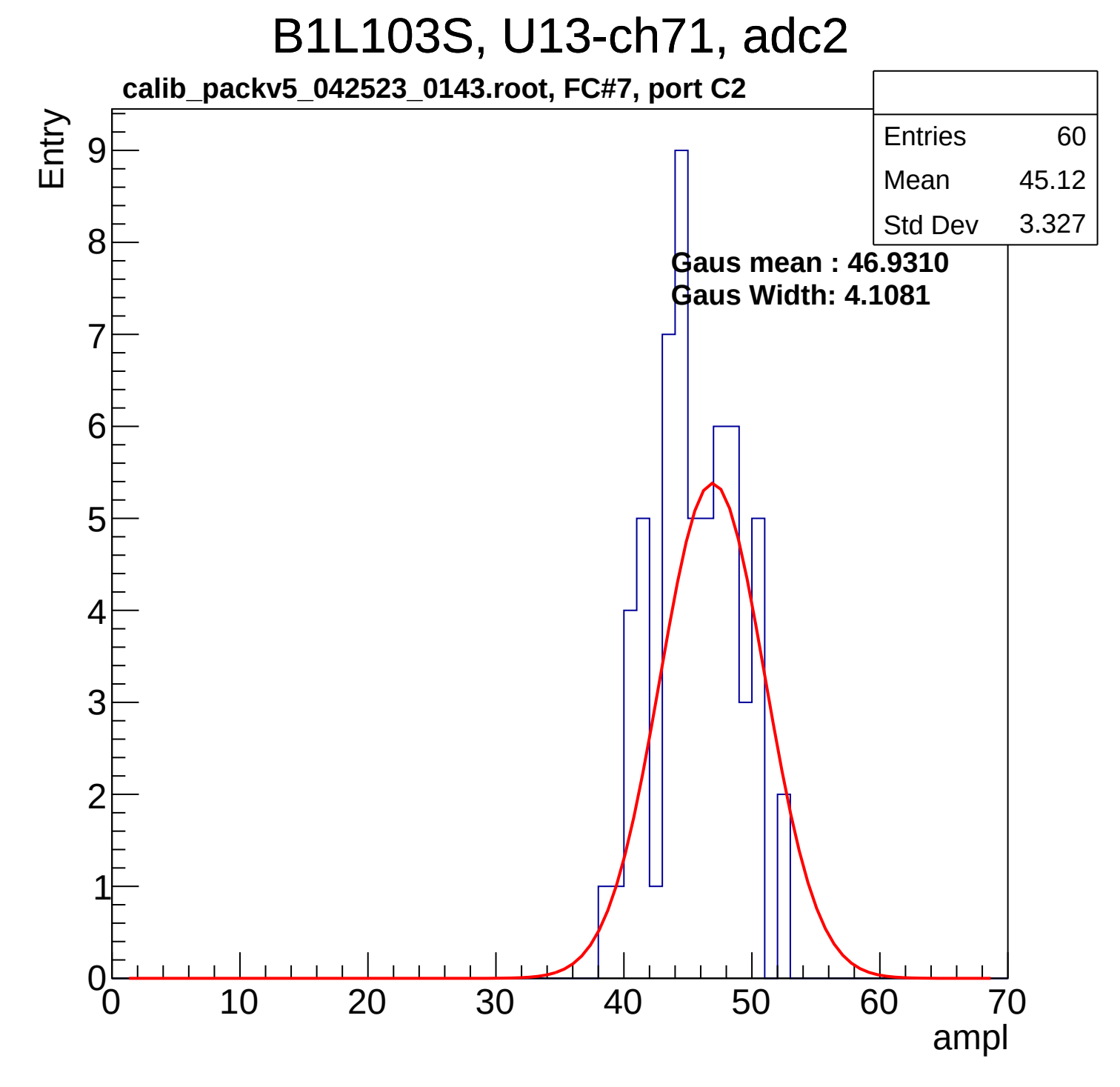
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch71, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 45.12
Std Dev 3.327
Gaus mean : 46.9310
Gaus Width: 4.1081
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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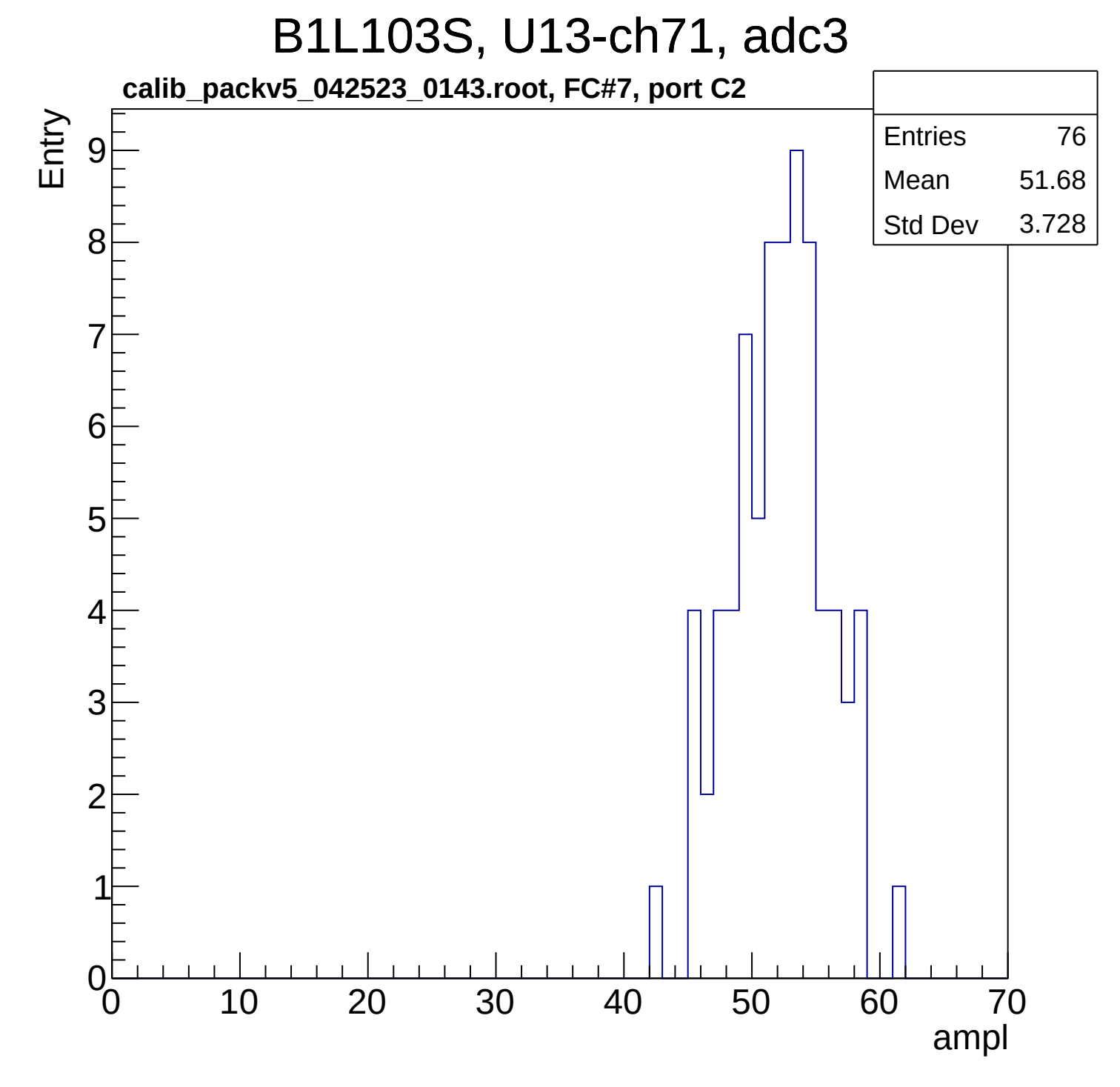
B1L103S, U13-ch71, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 51.68
Std Dev 3.728
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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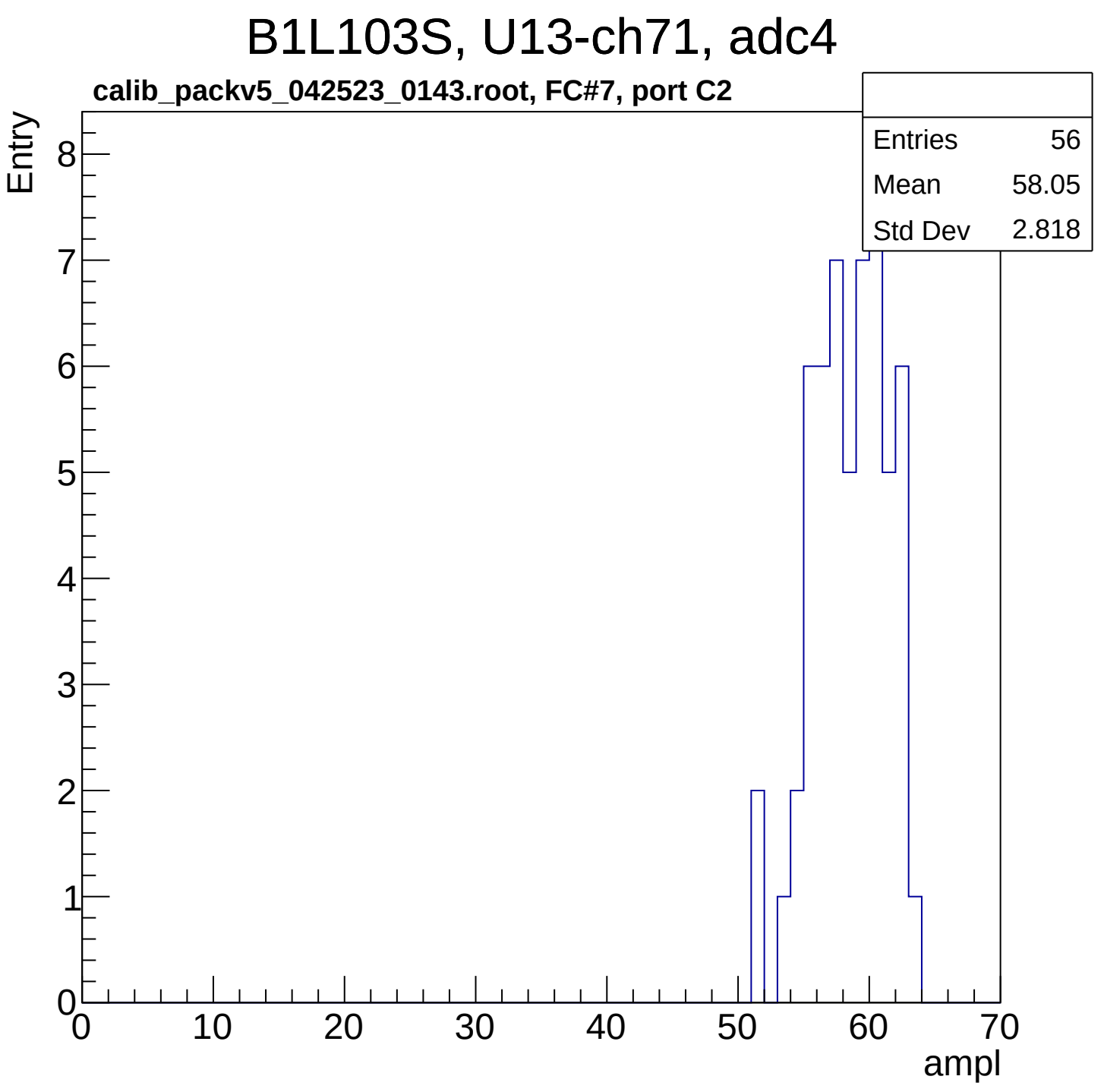
B1L103S, U13-ch71, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.05
Std Dev 2.818
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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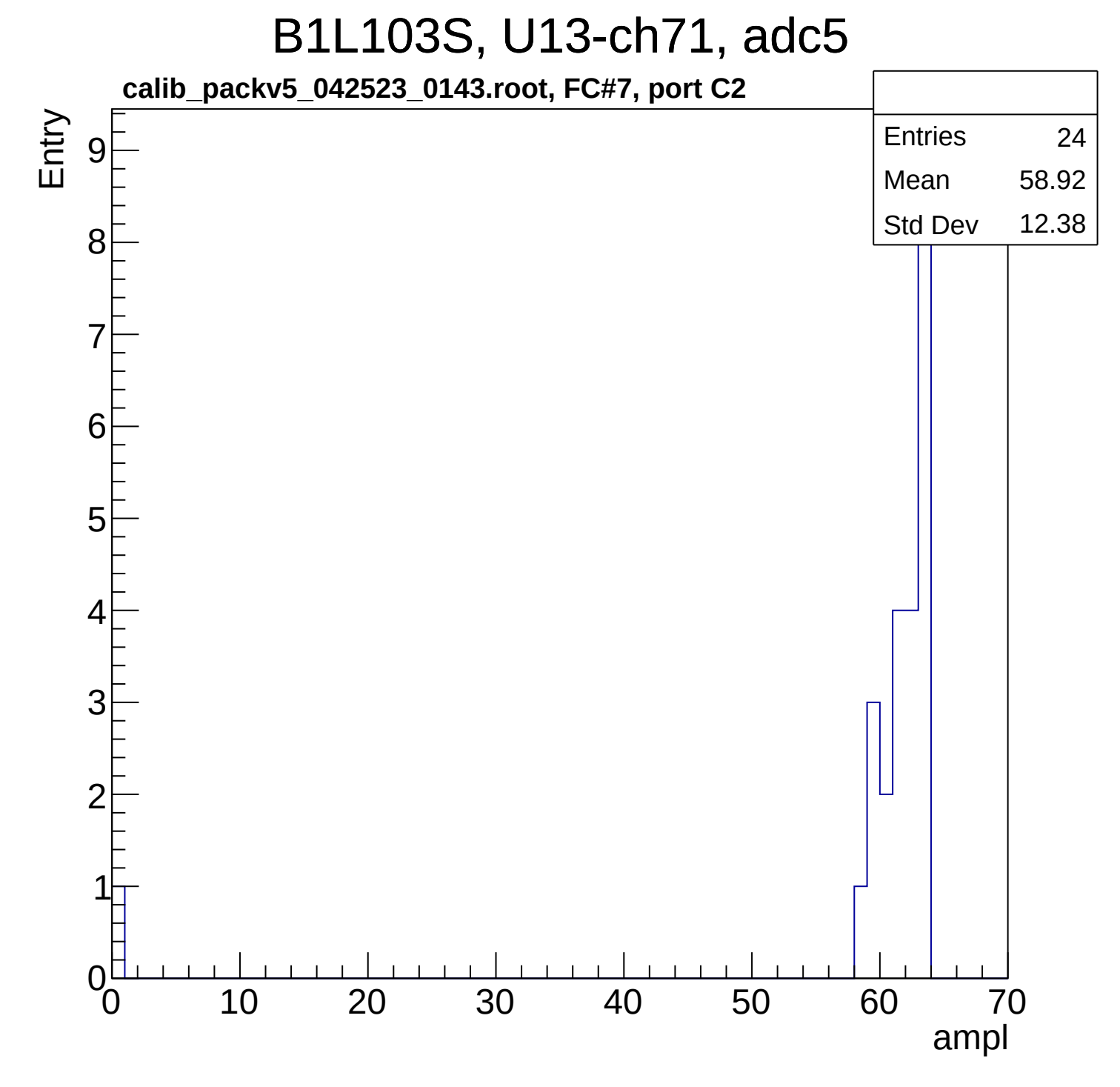
B1L103S, U13-ch71, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 58.92
Std Dev 12.38
Entry
ampl
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10
20
30
40
50
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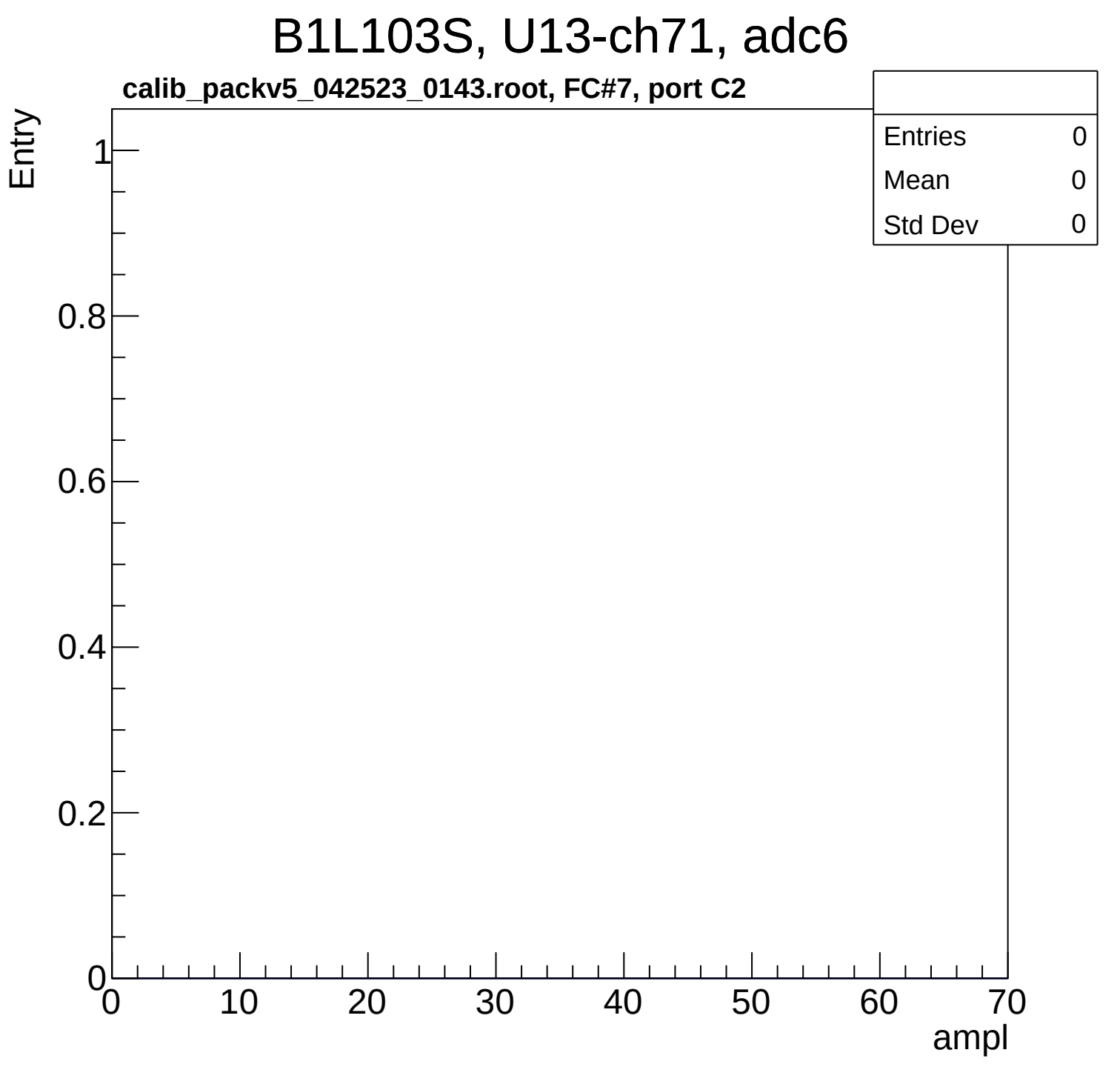
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch71, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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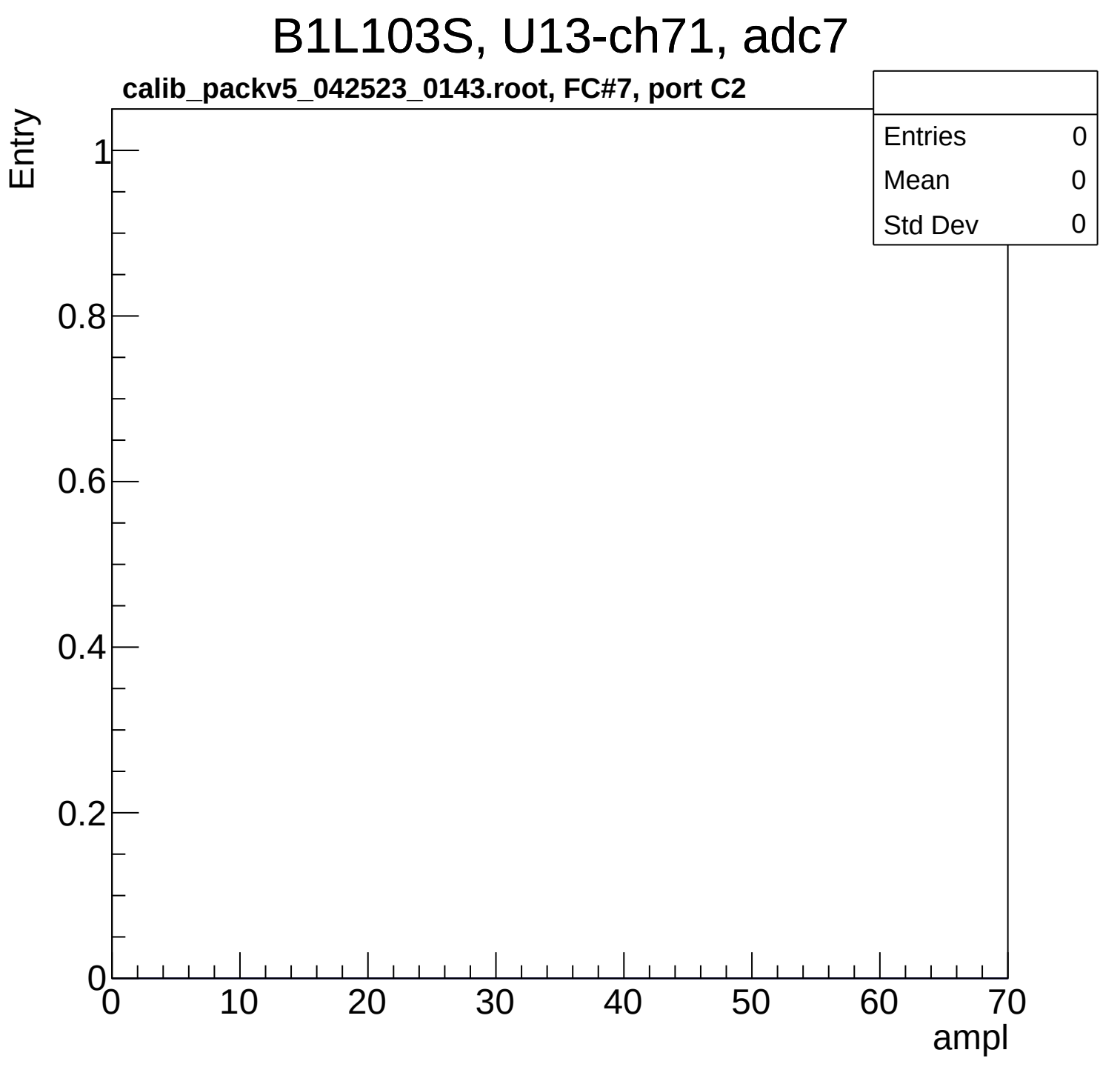
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch71, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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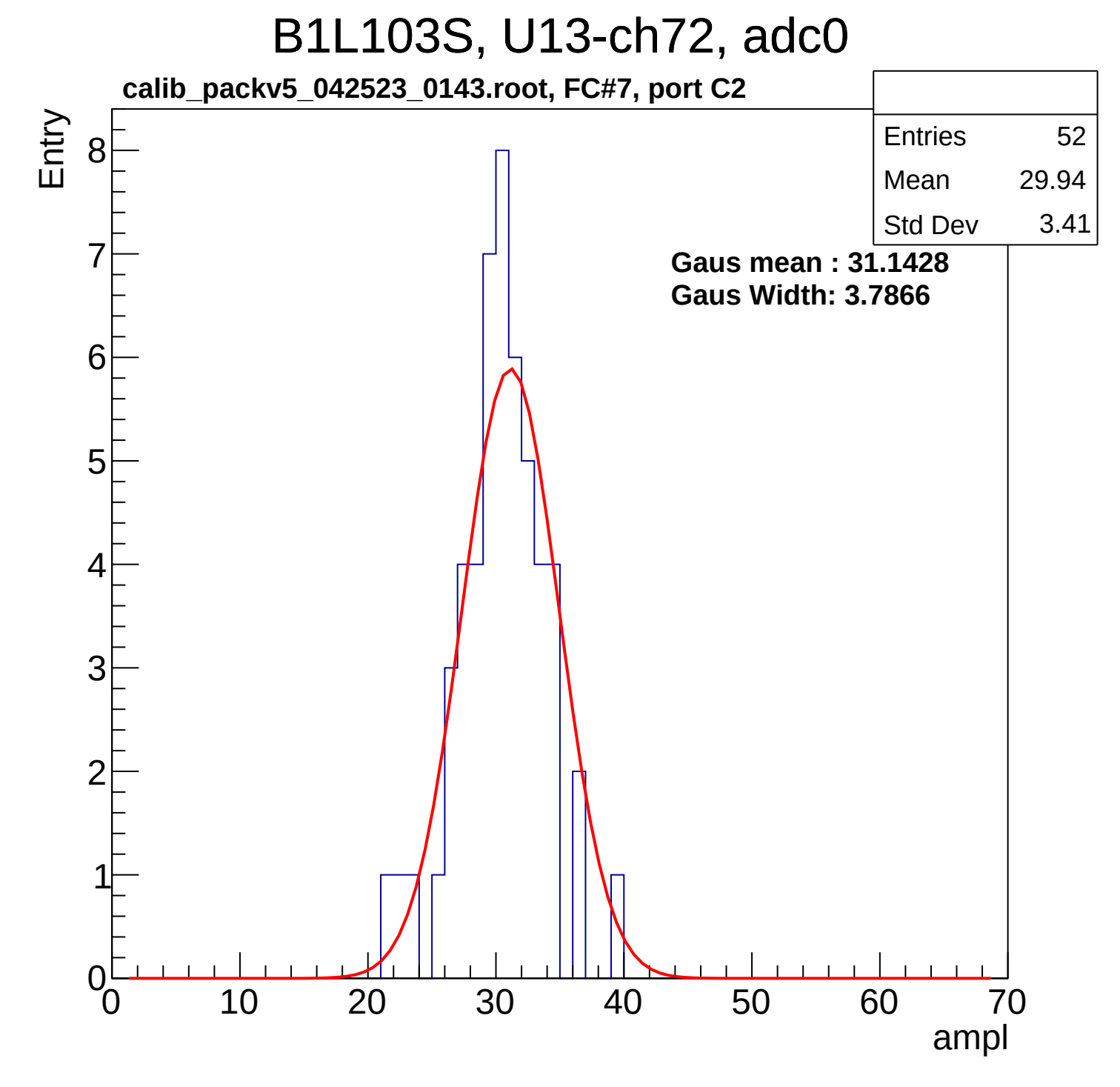
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch72, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 29.94
Std Dev 3.41
Gaus mean : 31.1428
Gaus Width: 3.7866
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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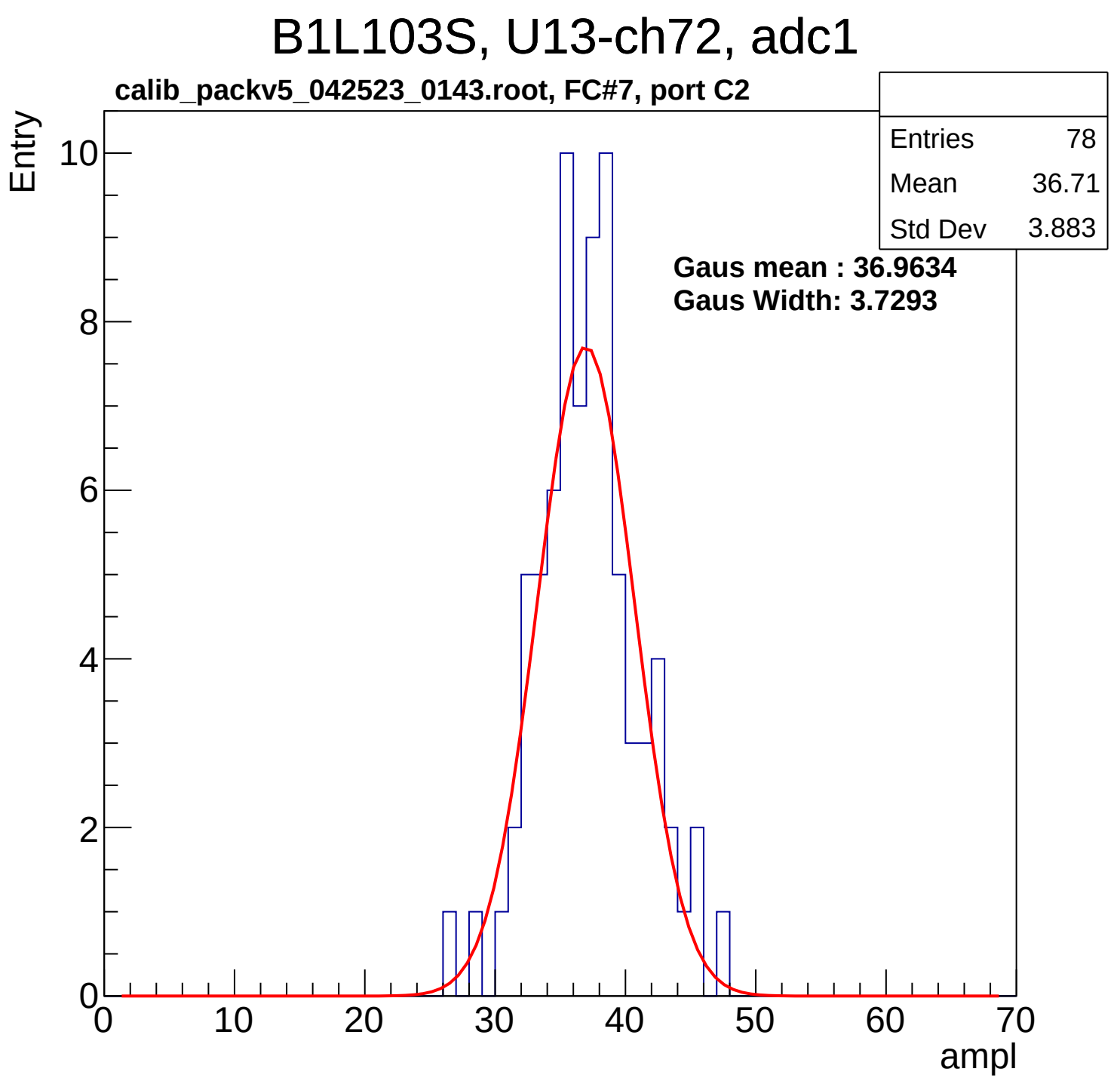
60
70


B1L103S, U13-ch72, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.71
Std Dev 3.883
Gaus mean : 36.9634
Gaus Width: 3.7293
Entry
ampl
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4
6
8
10
0
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60
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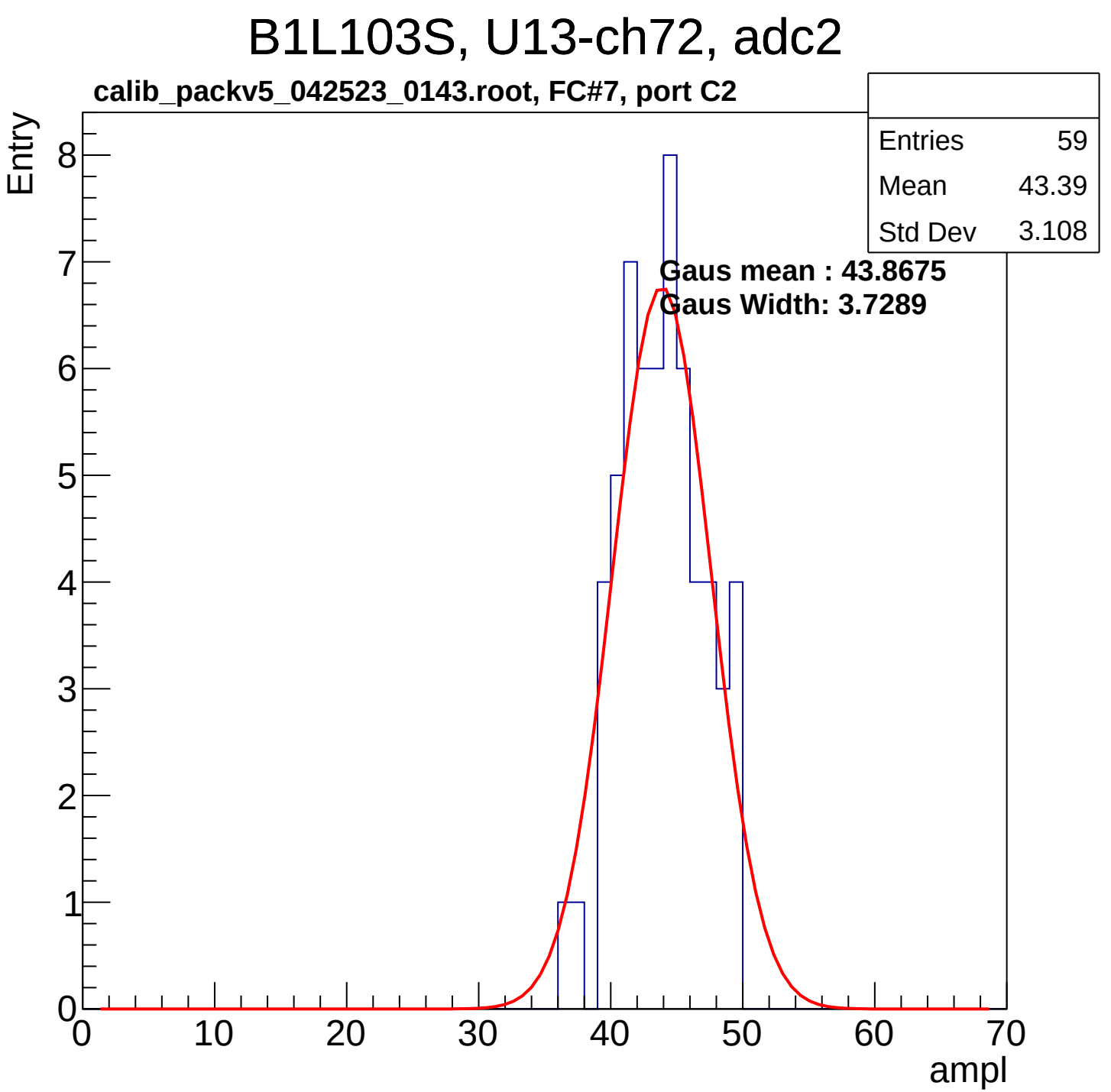
B1L103S, U13-ch72, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 43.39
Std Dev 3.108
Gaus mean : 43.8675
Gaus Width: 3.7289
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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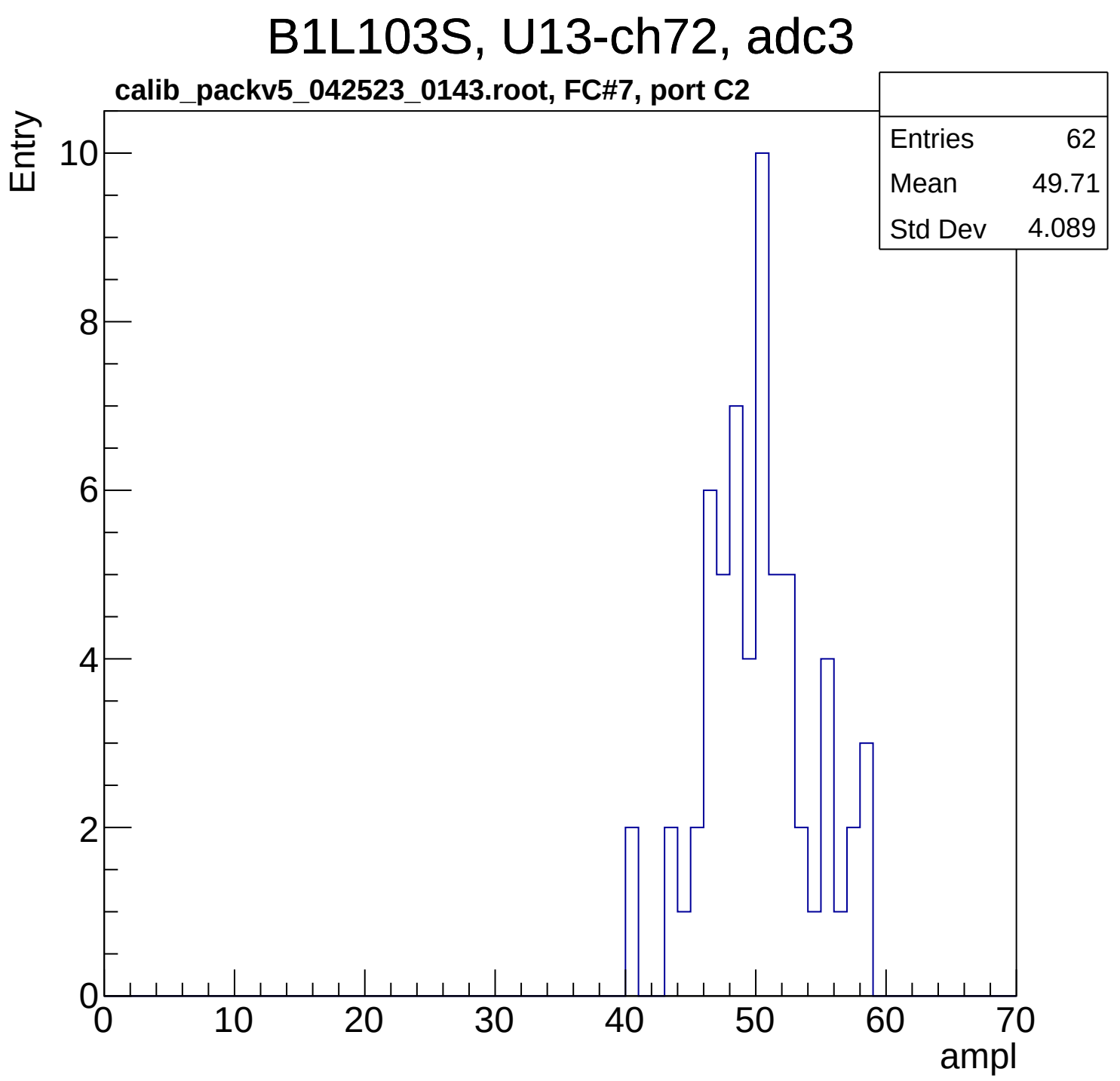
B1L103S, U13-ch72, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.71
Std Dev 4.089
Entry
ampl
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2
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0
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40
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60
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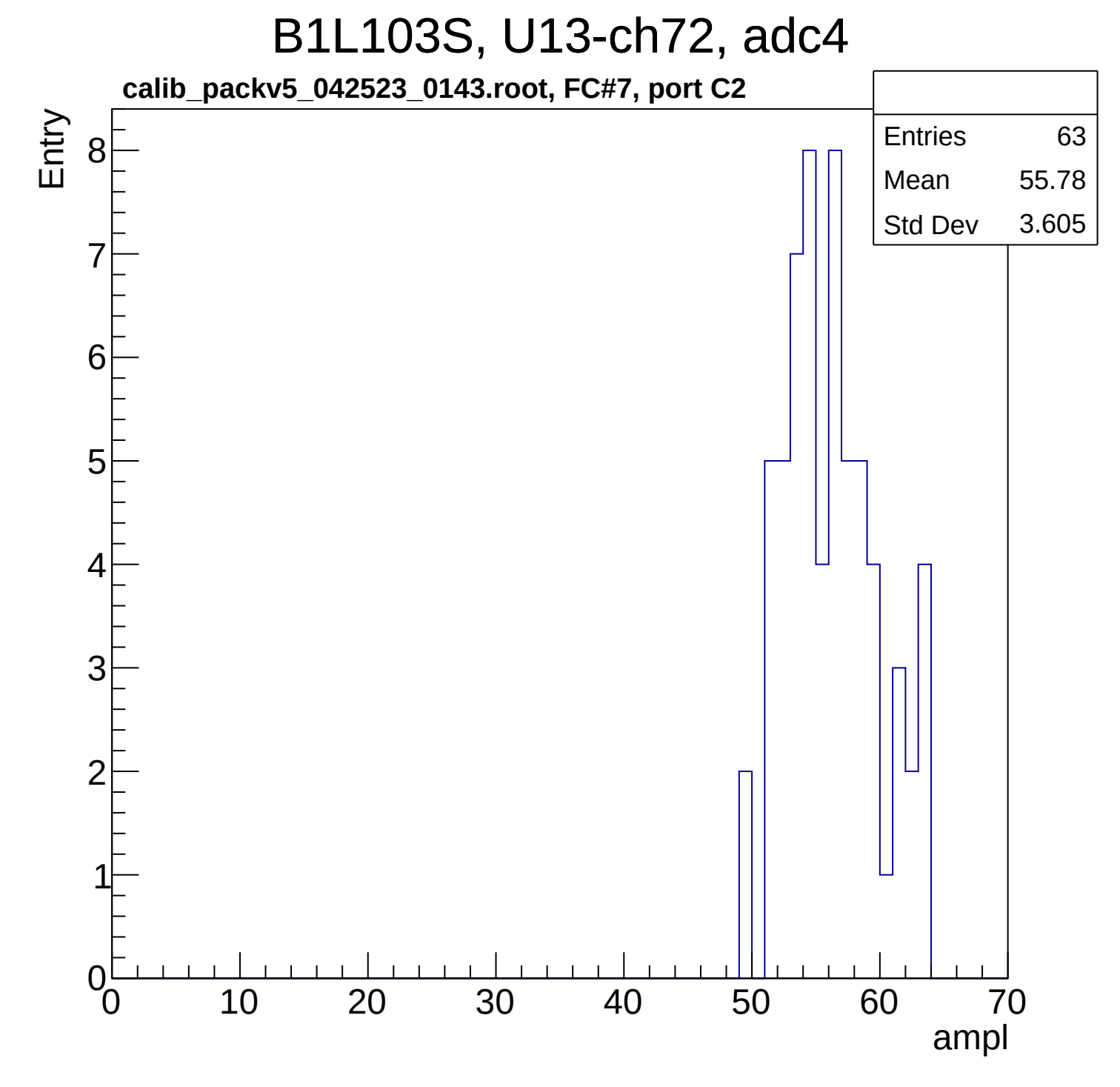
B1L103S, U13-ch72, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 55.78
Std Dev 3.605
Entry
ampl
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0
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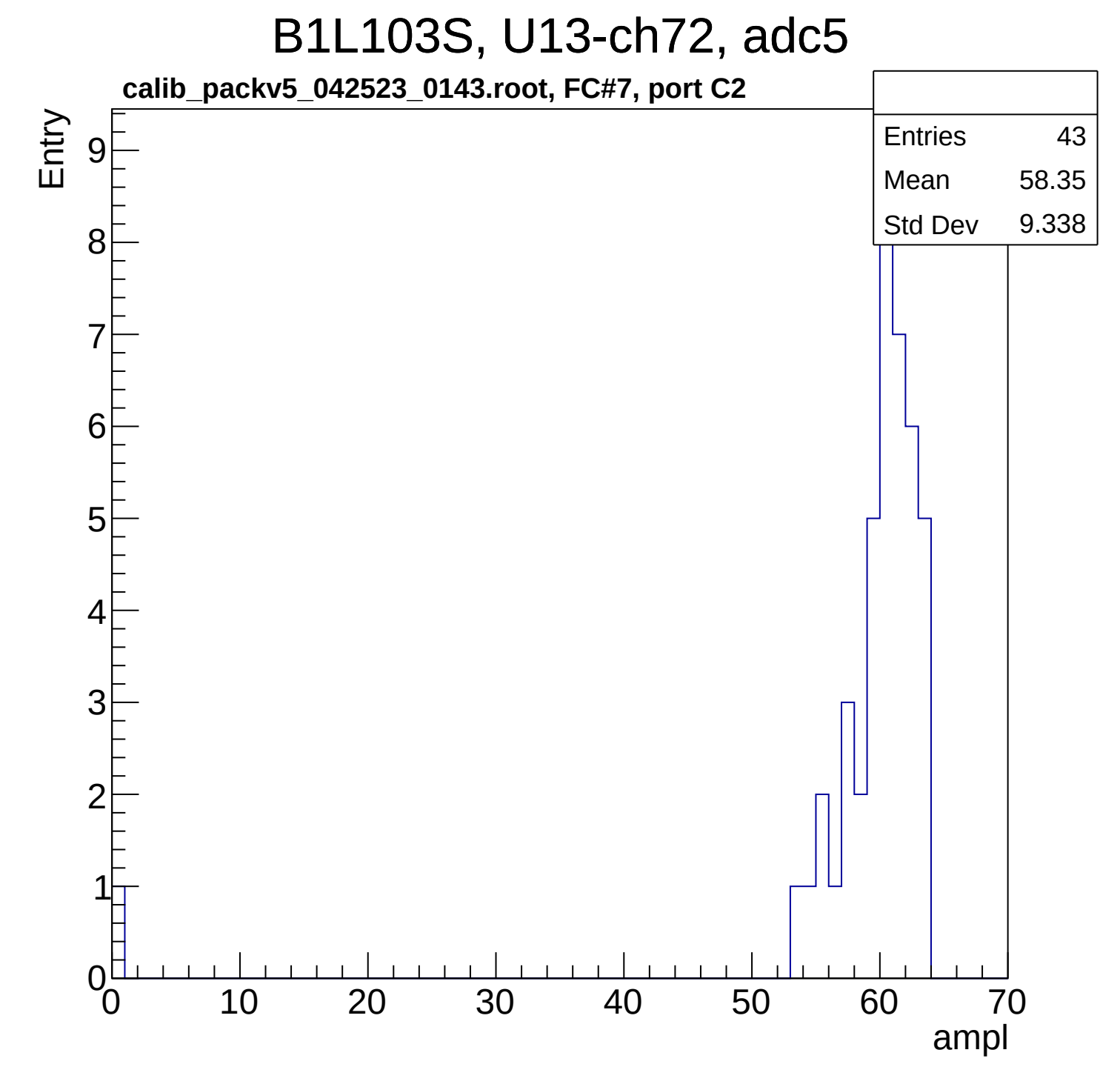
B1L103S, U13-ch72, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 58.35
Std Dev 9.338
Entry
ampl
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2
3
4
5
6
7
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0
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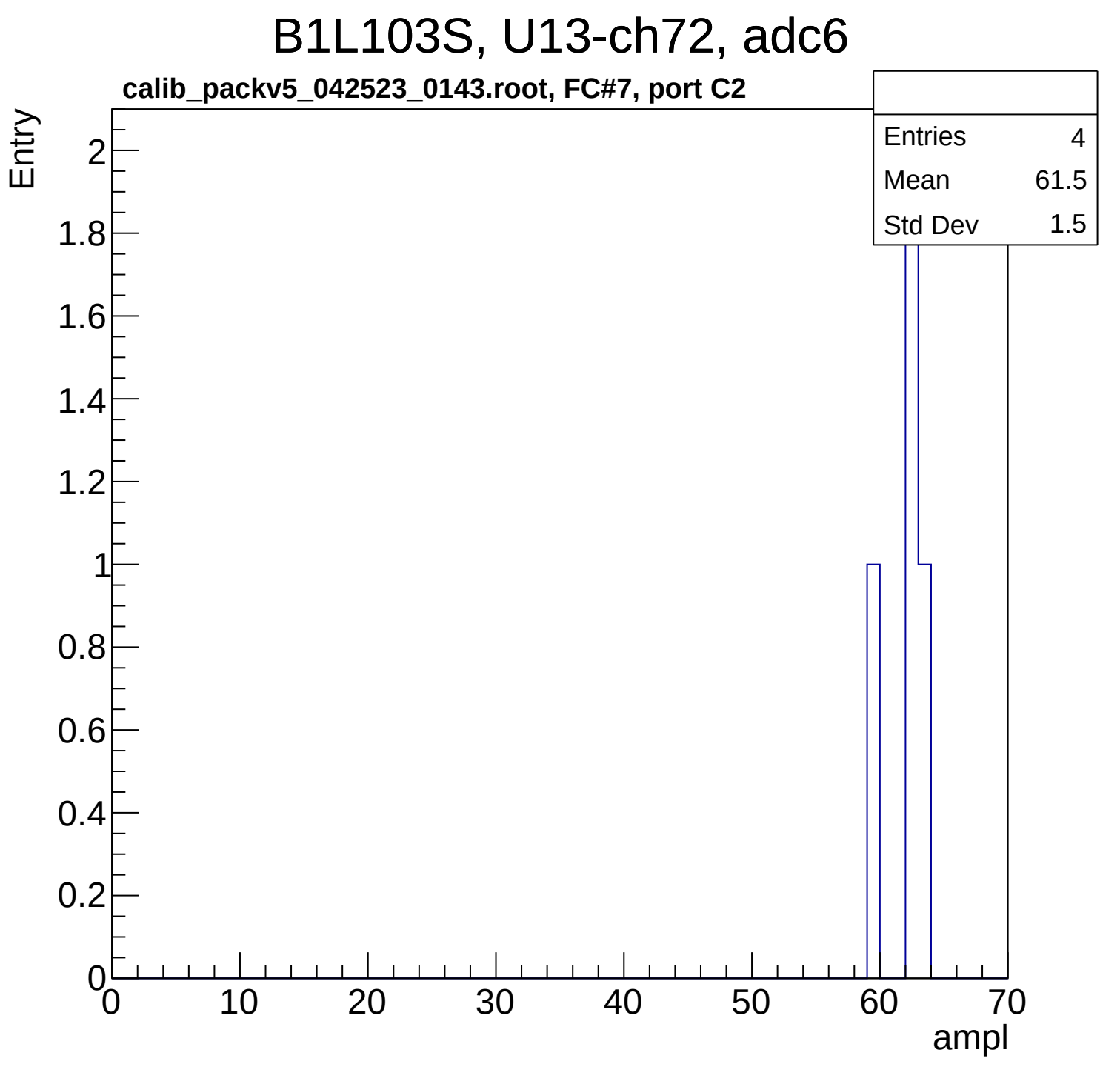
B1L103S, U13-ch72, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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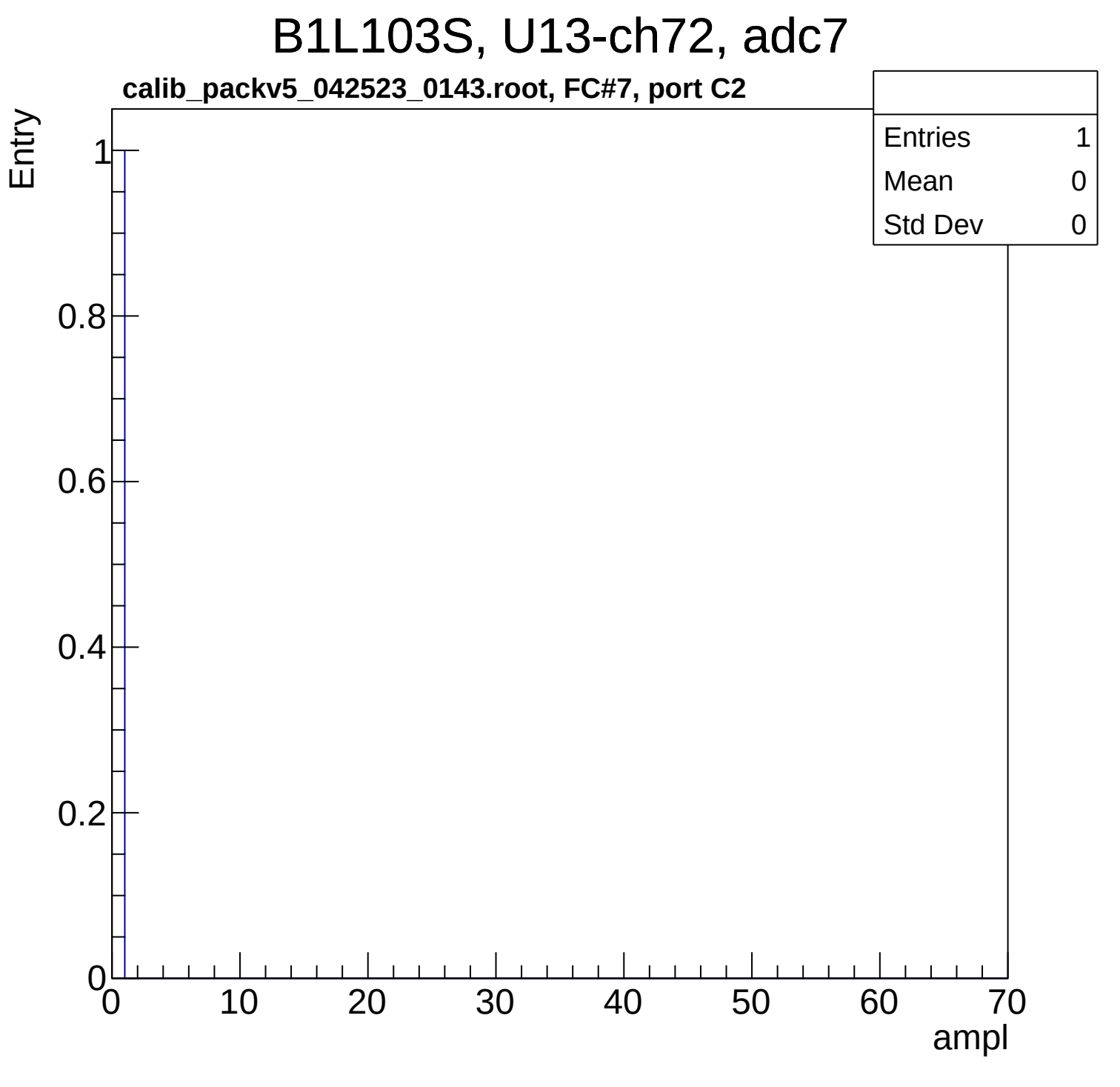
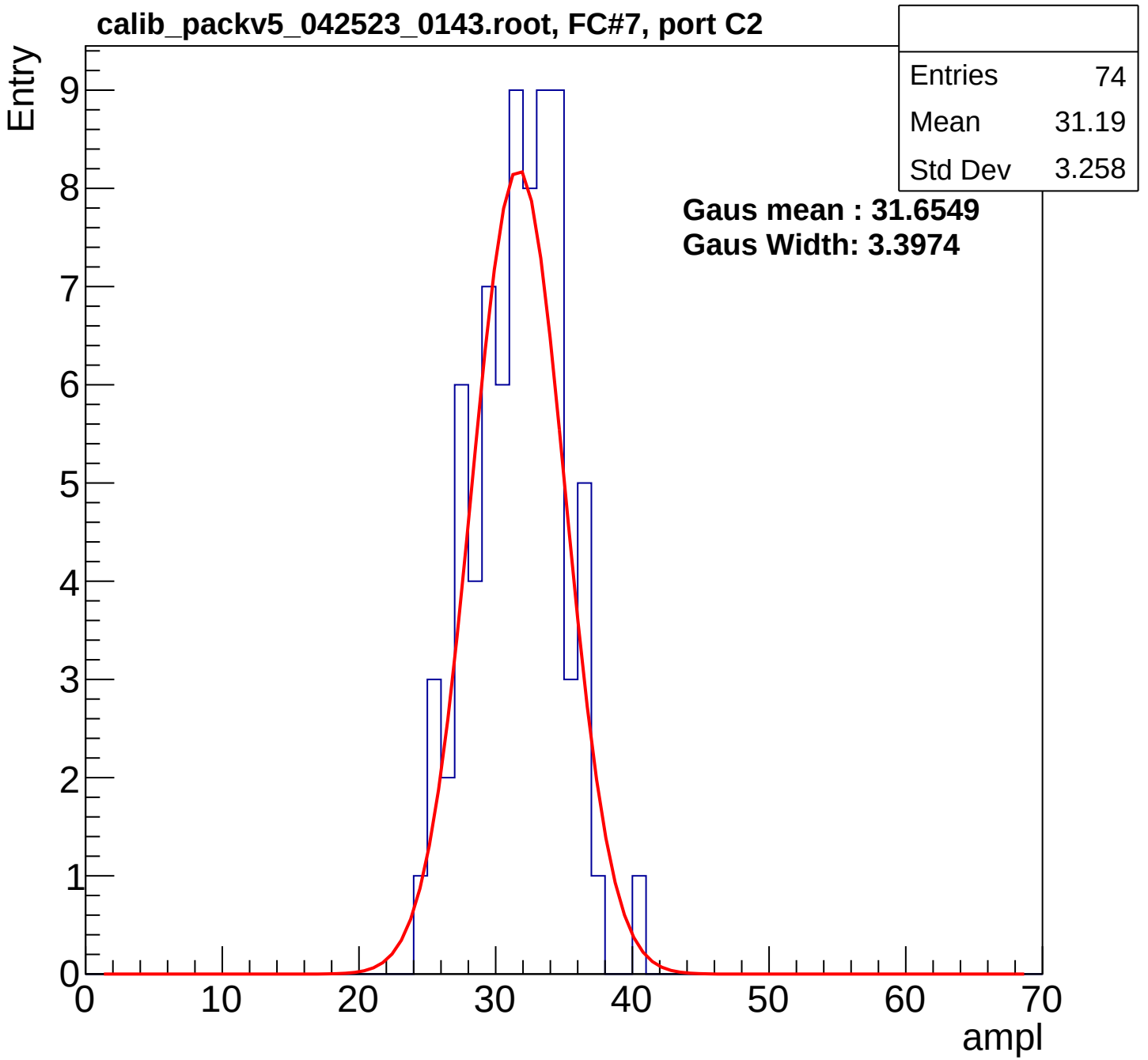

B1L103S, U13-ch72, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch73, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 31.19
Std Dev 3.258
Gaus mean : 31.6549
Gaus Width: 3.3974
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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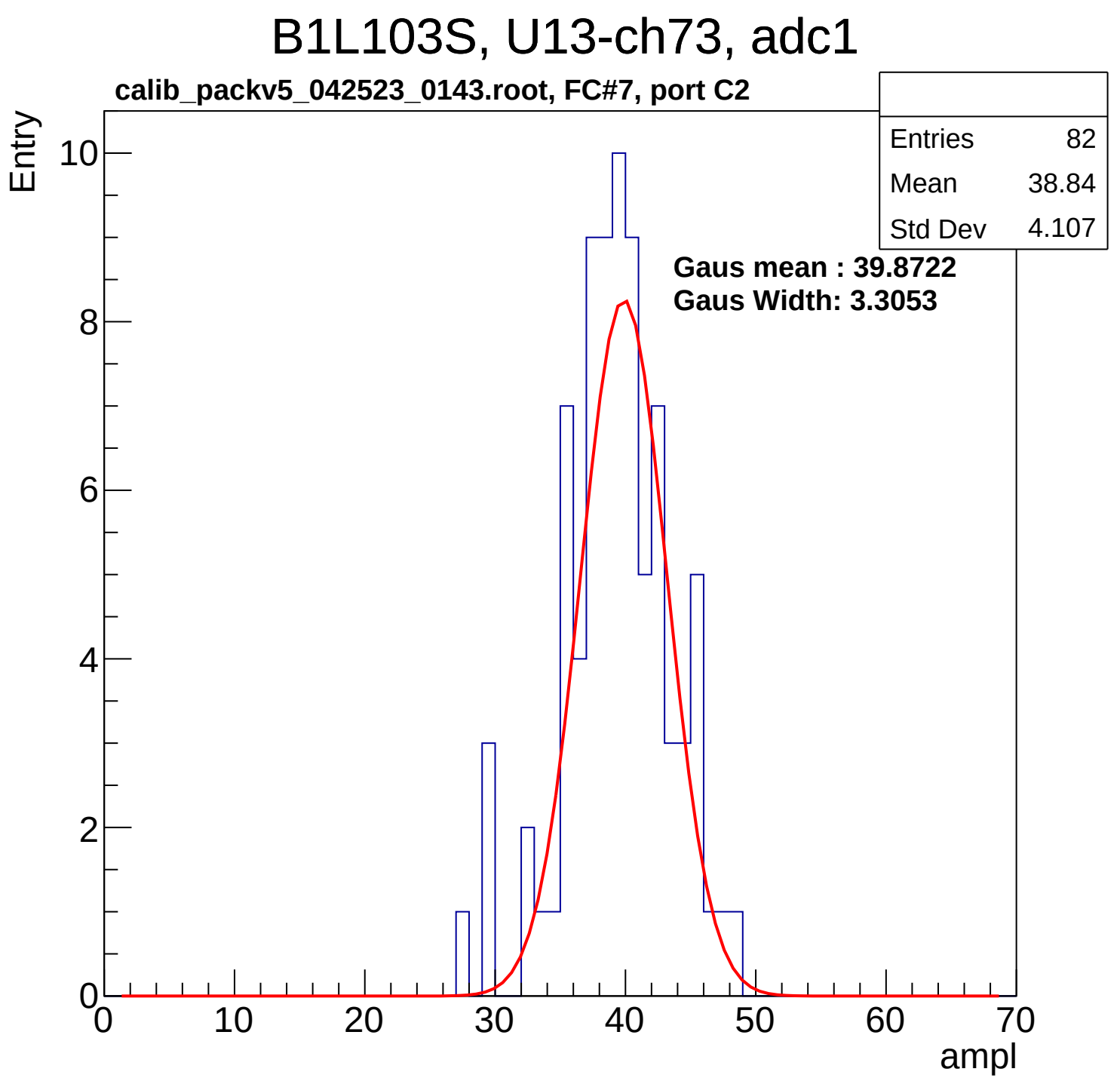
B1L103S, U13-ch73, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 38.84
Std Dev 4.107
Gaus mean : 39.8722
Gaus Width: 3.3053
Entry
ampl
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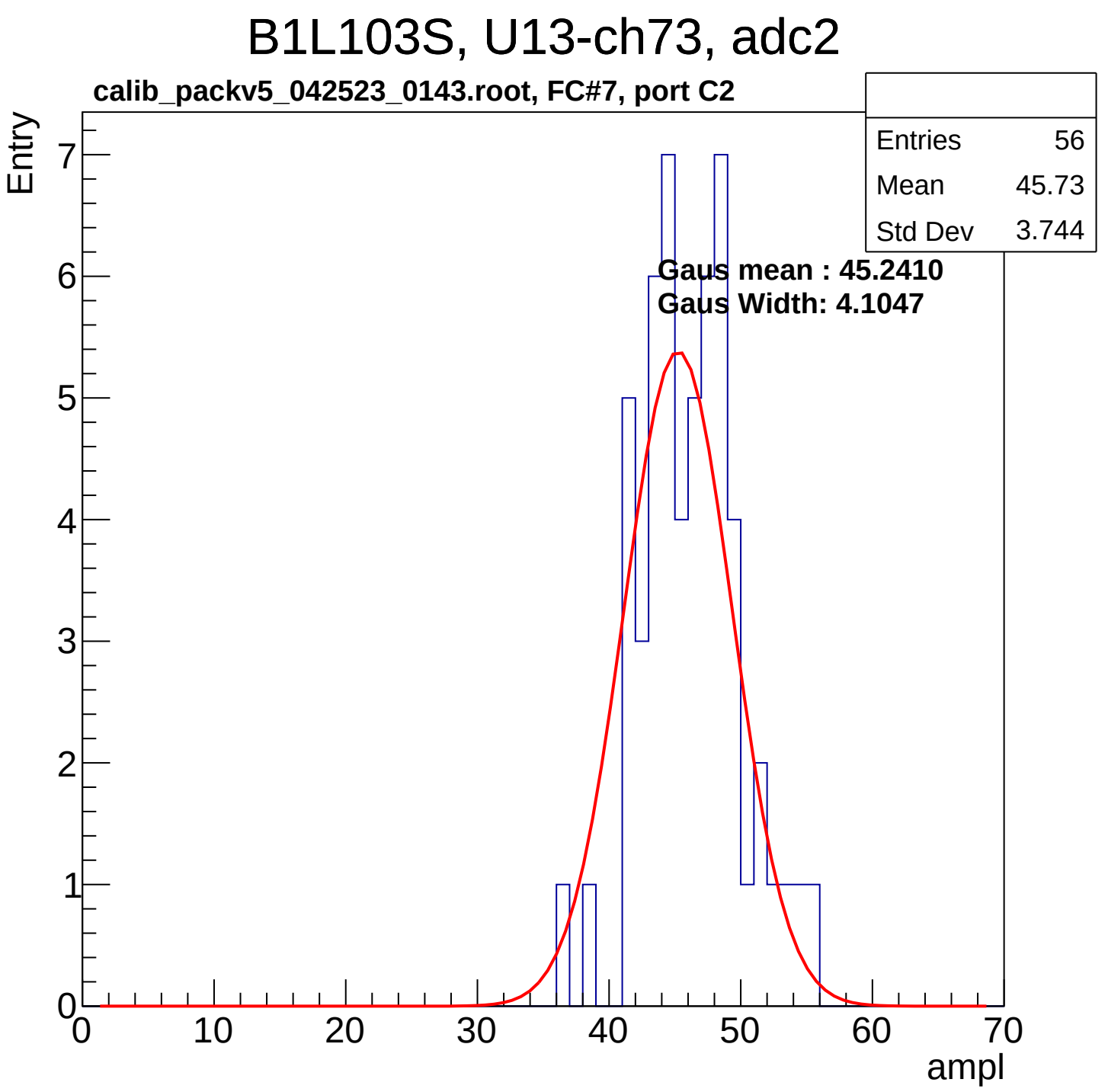
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch73, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 45.73
Std Dev 3.744
Gaus mean : 45.2410
Gaus Width: 4.1047
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
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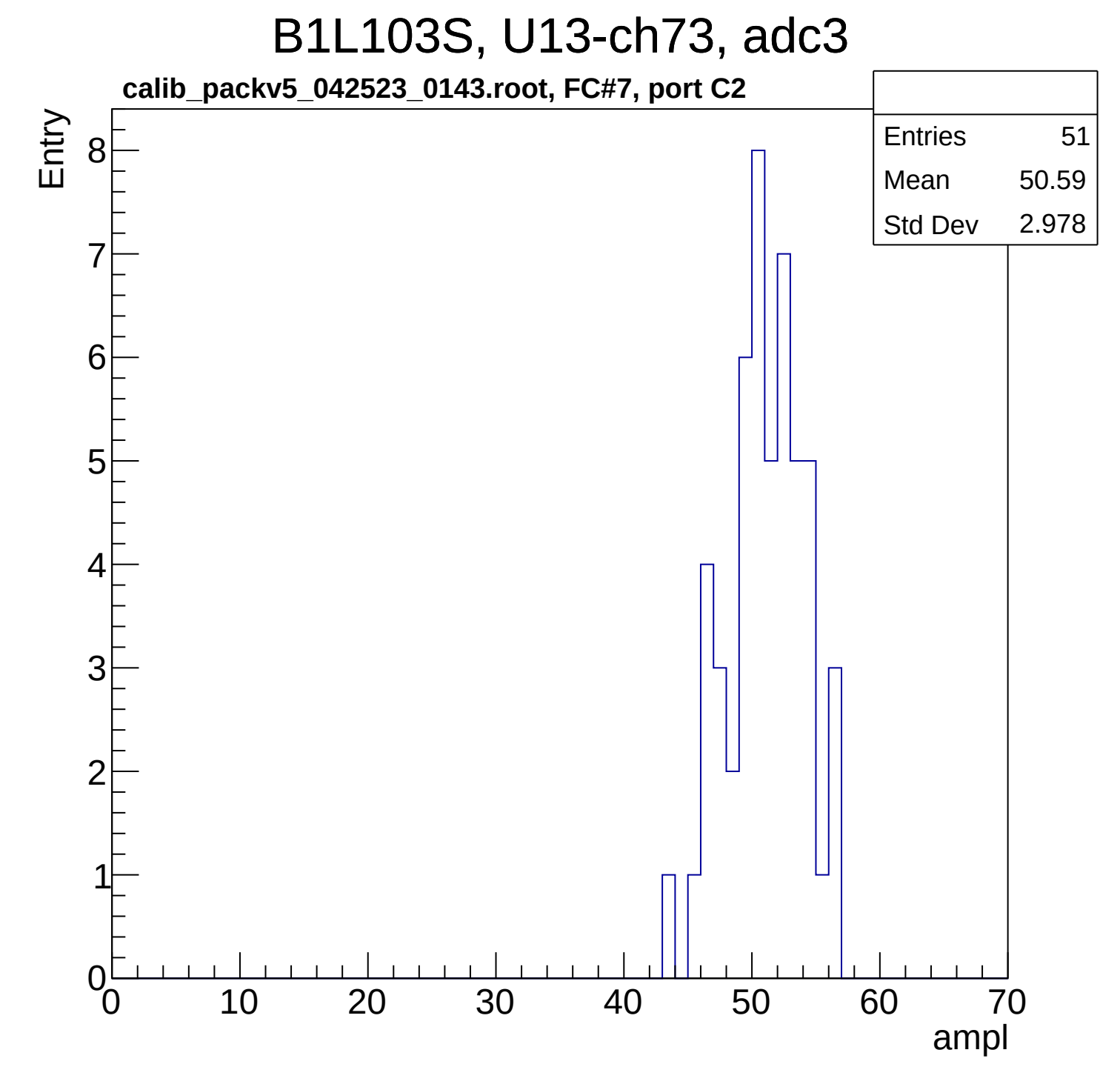
B1L103S, U13-ch73, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 50.59
Std Dev 2.978
Entry
0
1
2
3
4
5
6
7
8
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10
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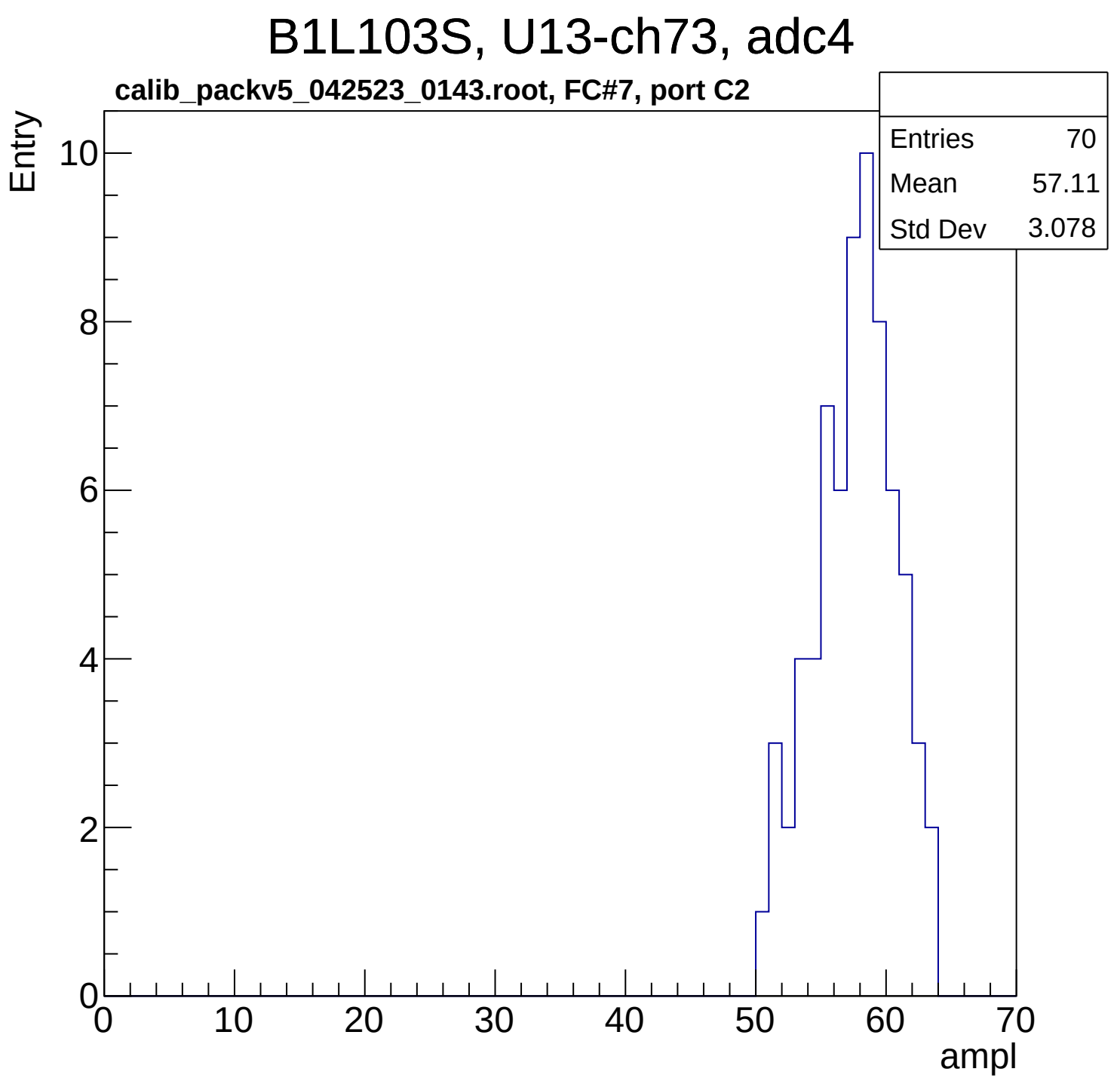
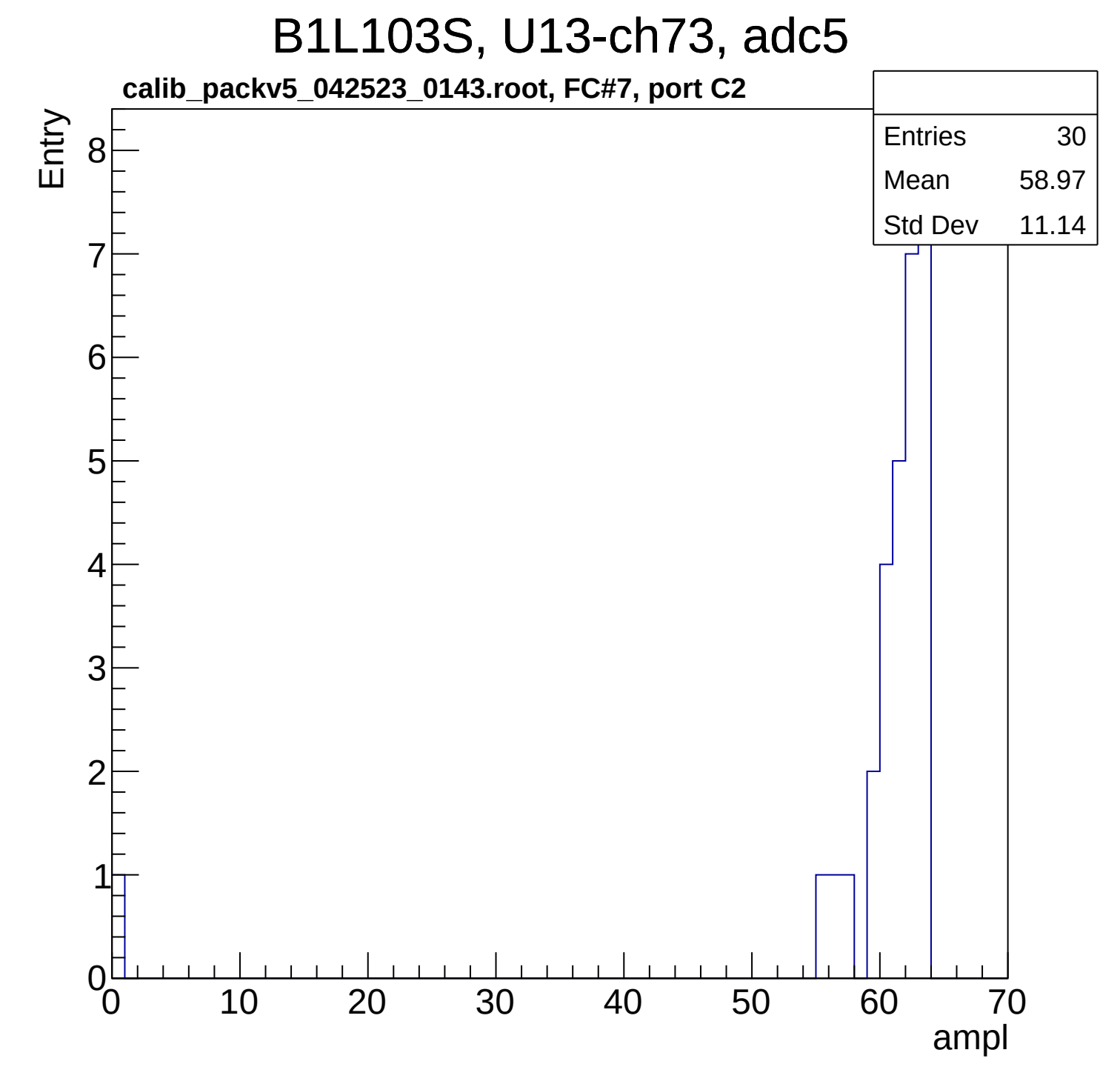
70
ampl


B1L103S, U13-ch73, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 57.11
Std Dev 3.078
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch73, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 58.97
Std Dev 11.14
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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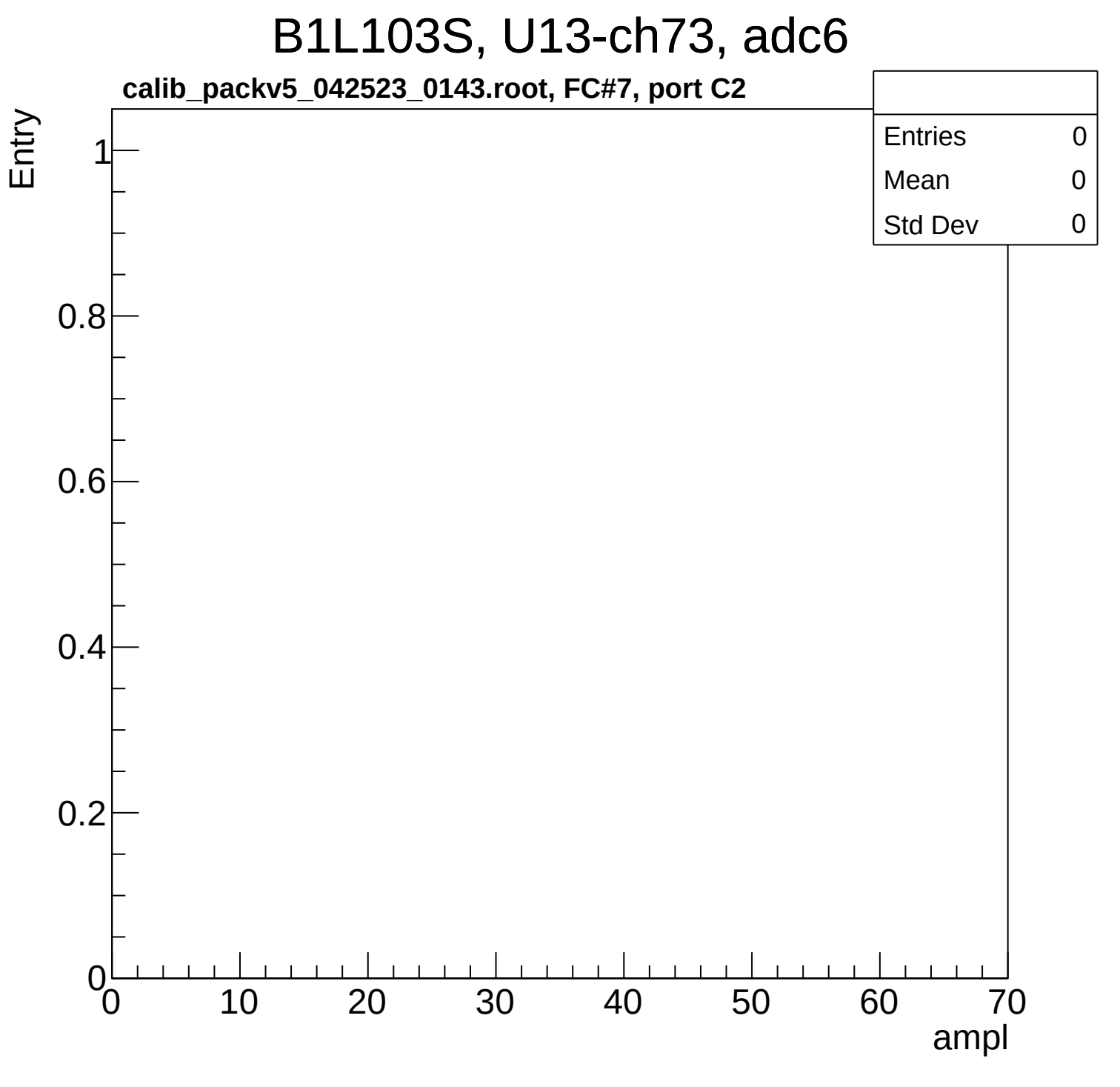
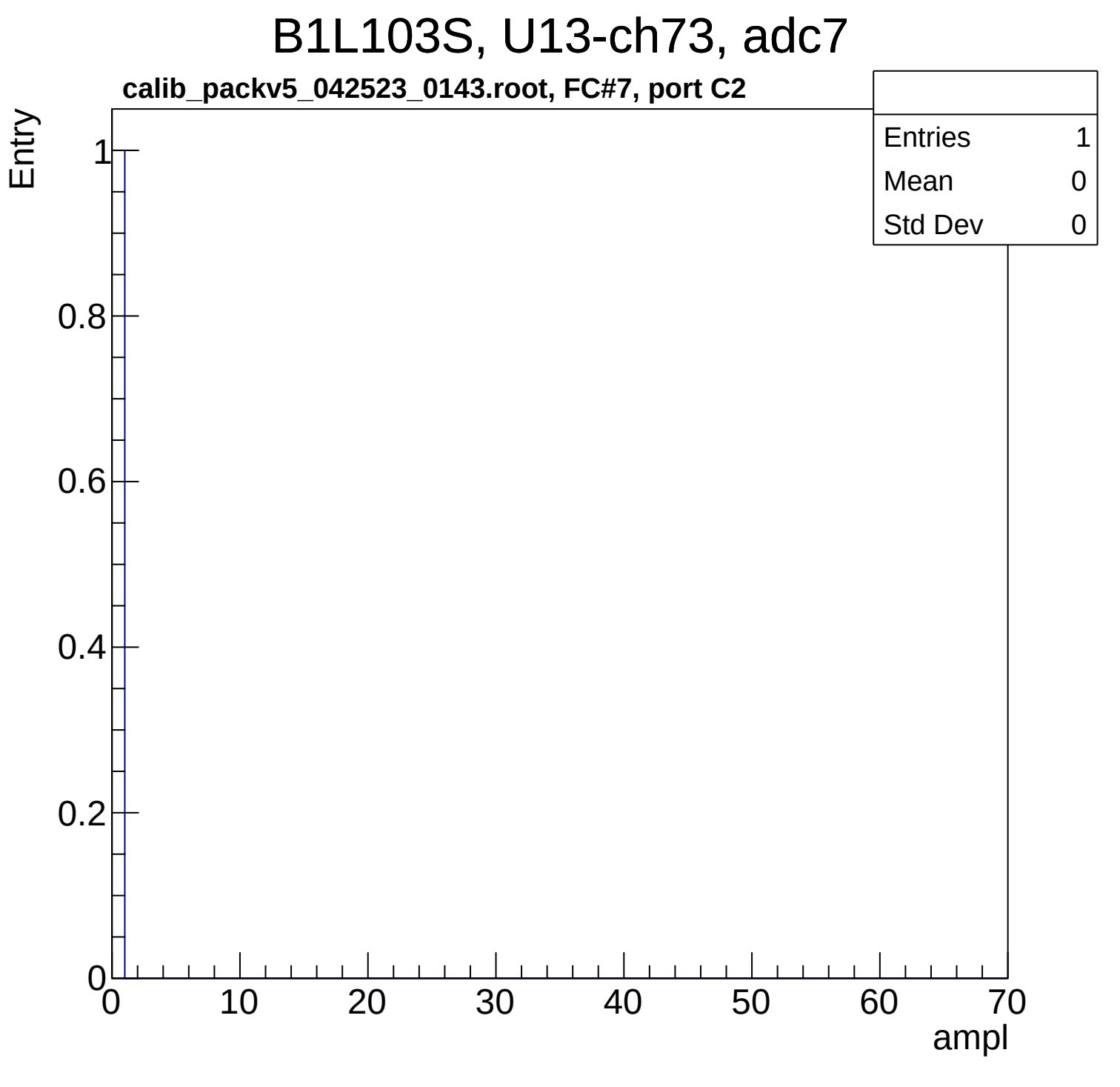
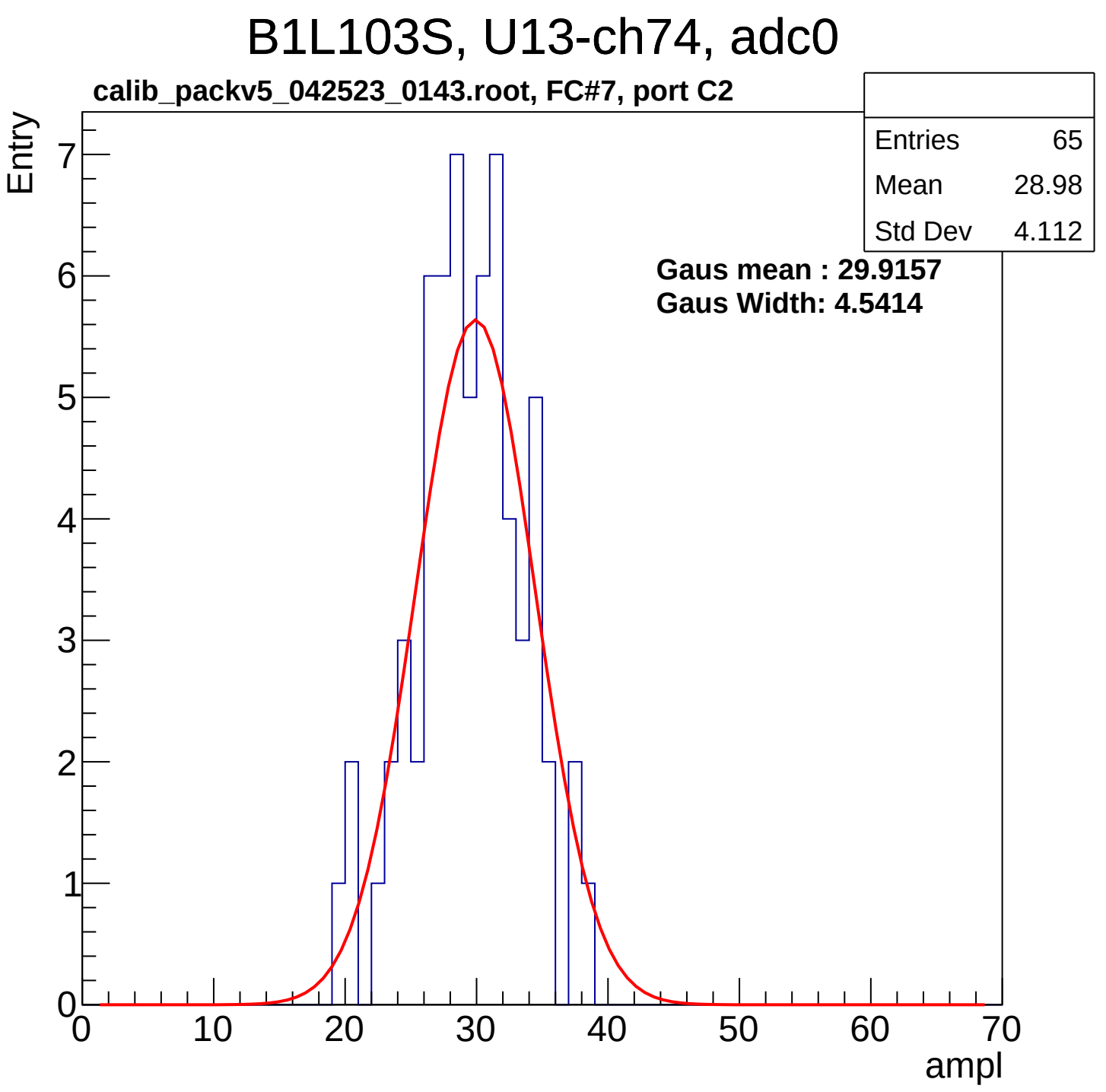
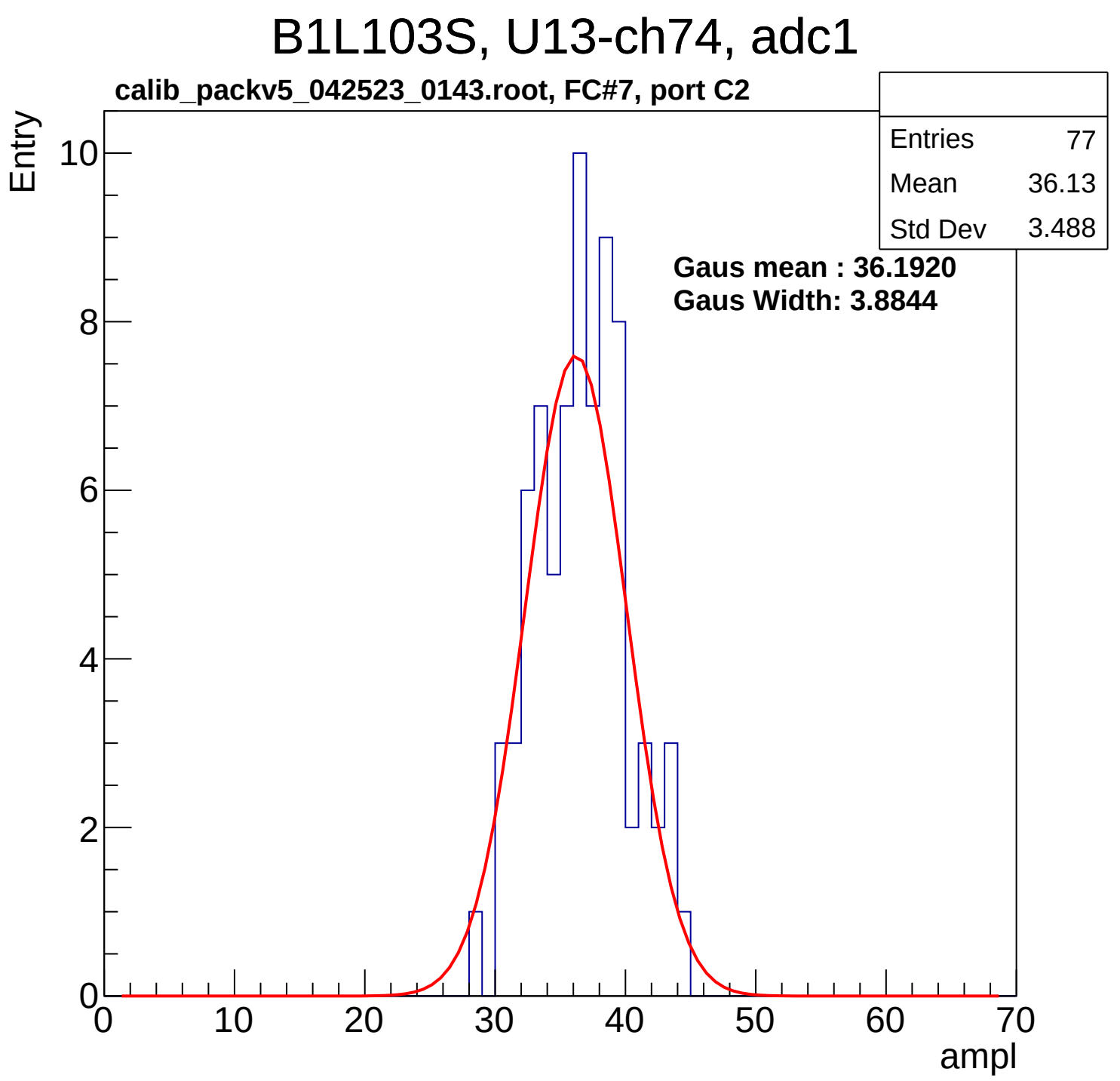
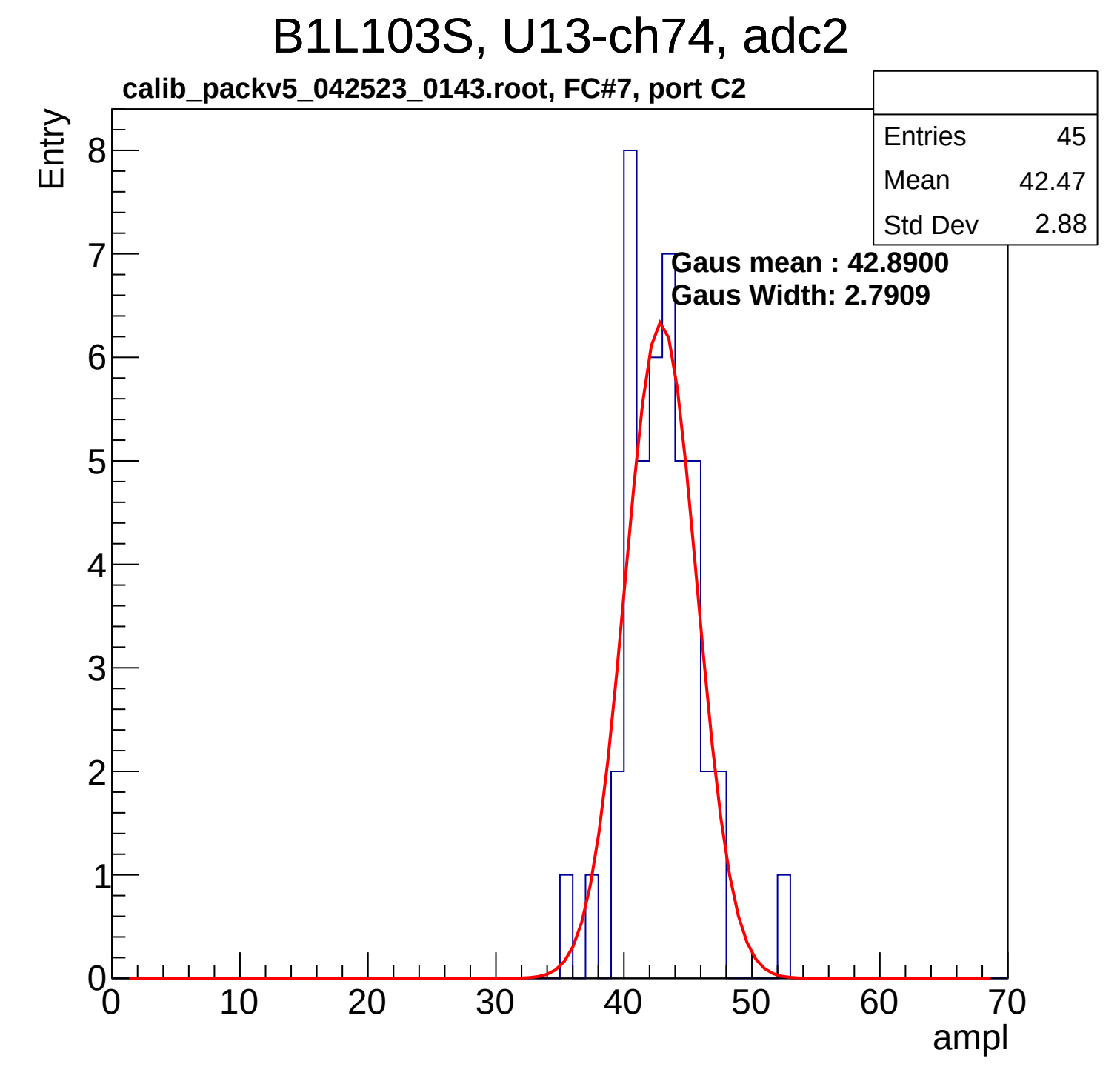

B1L103S, U13-ch73, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch73, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch74, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 28.98
Std Dev 4.112
Gaus mean : 29.9157
Gaus Width: 4.5414
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch74, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 36.13
Std Dev 3.488
Gaus mean : 36.1920
Gaus Width: 3.8844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch74, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 42.47
Std Dev 2.88
Gaus mean : 42.8900
Gaus Width: 2.7909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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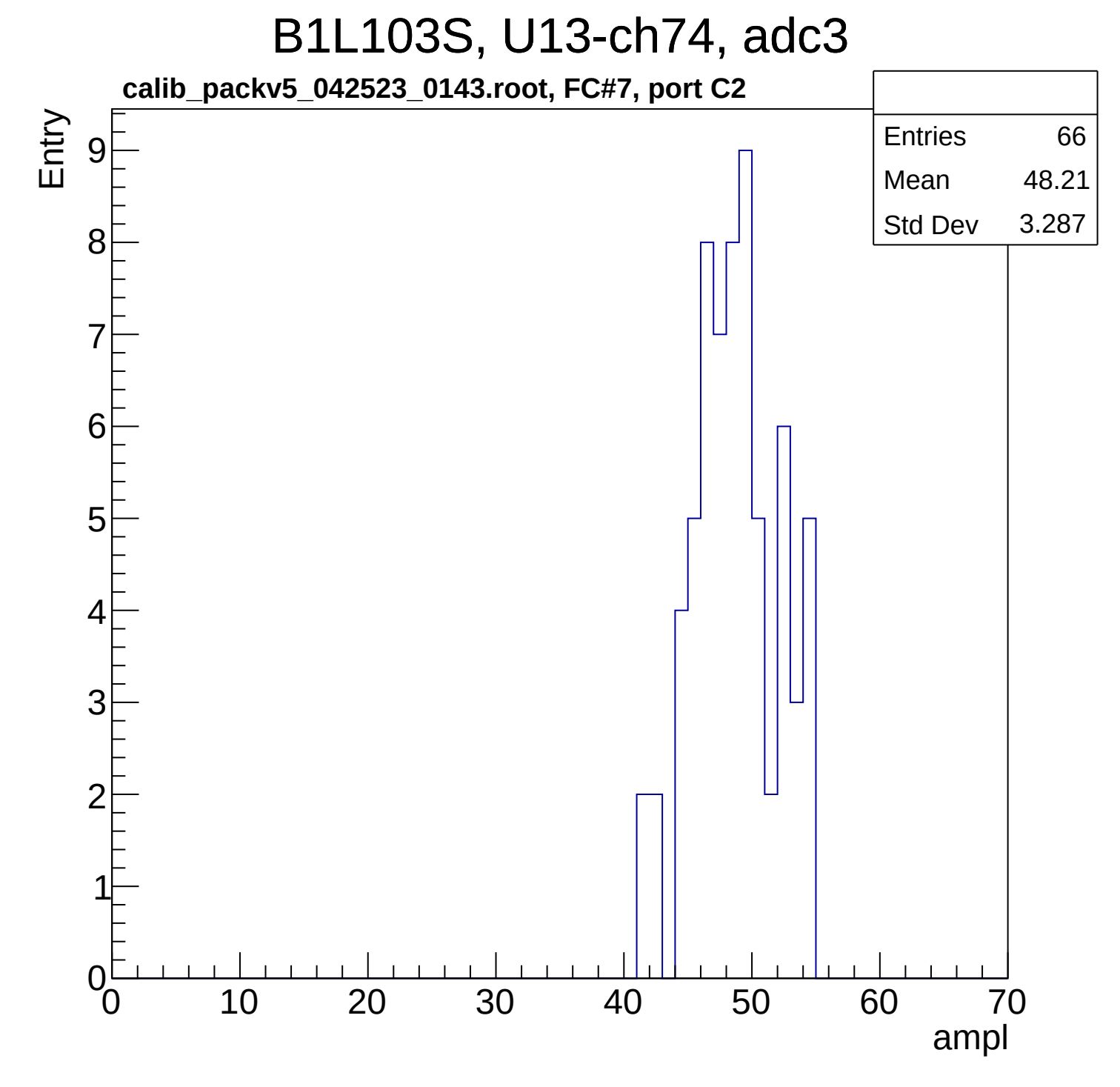
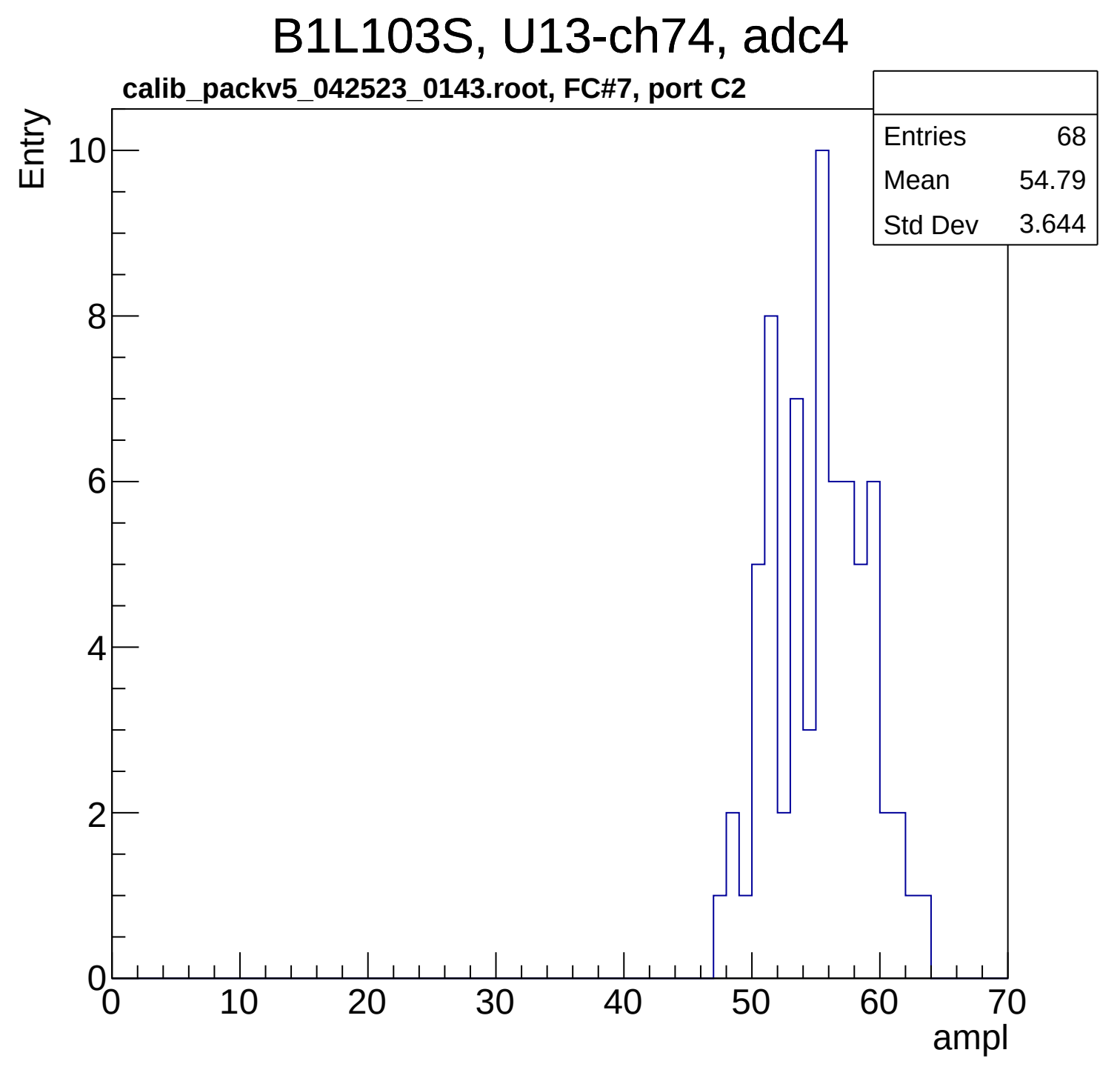
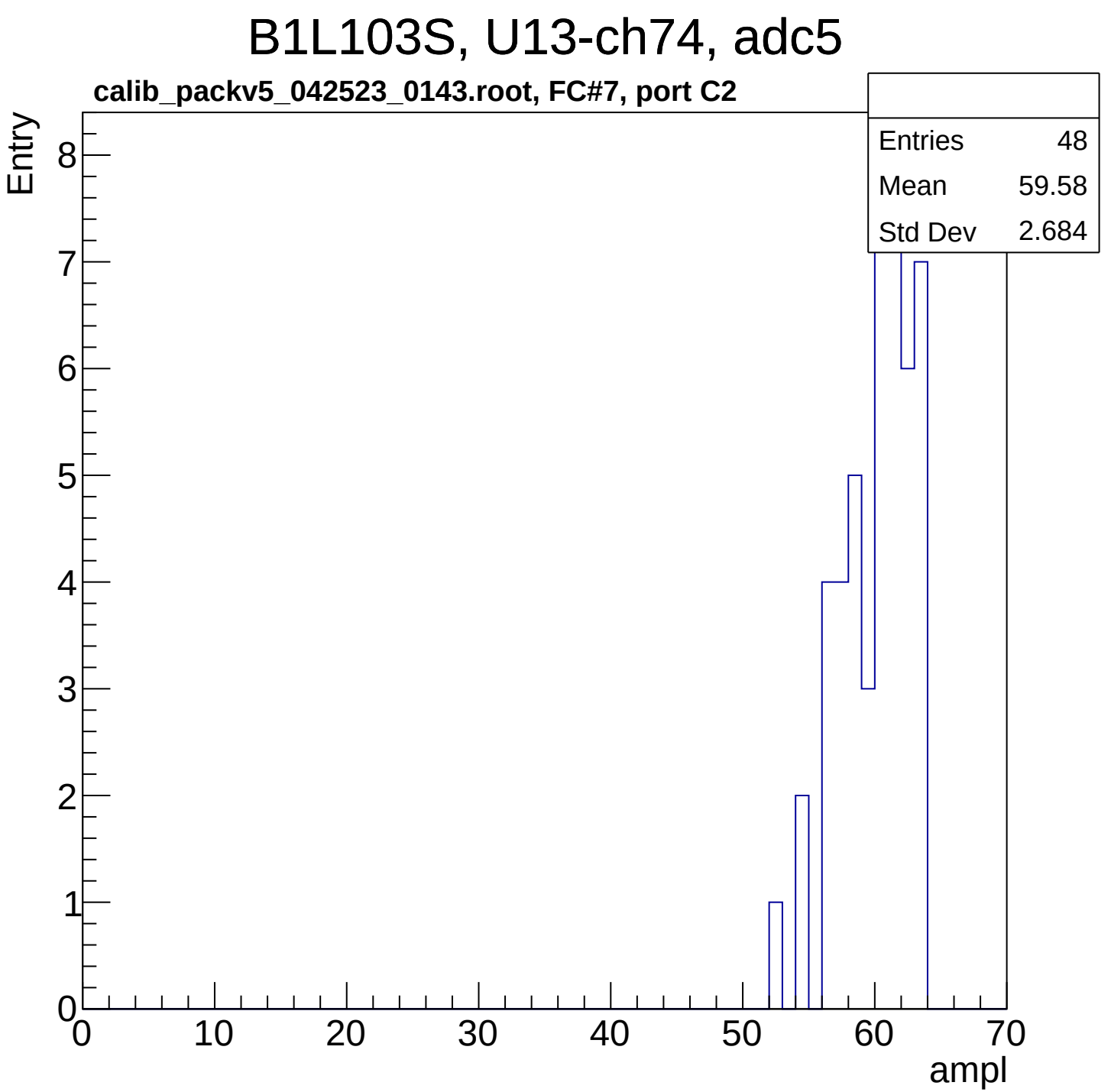

B1L103S, U13-ch74, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.21
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch74, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 54.79
Std Dev 3.644
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch74, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.58
Std Dev 2.684
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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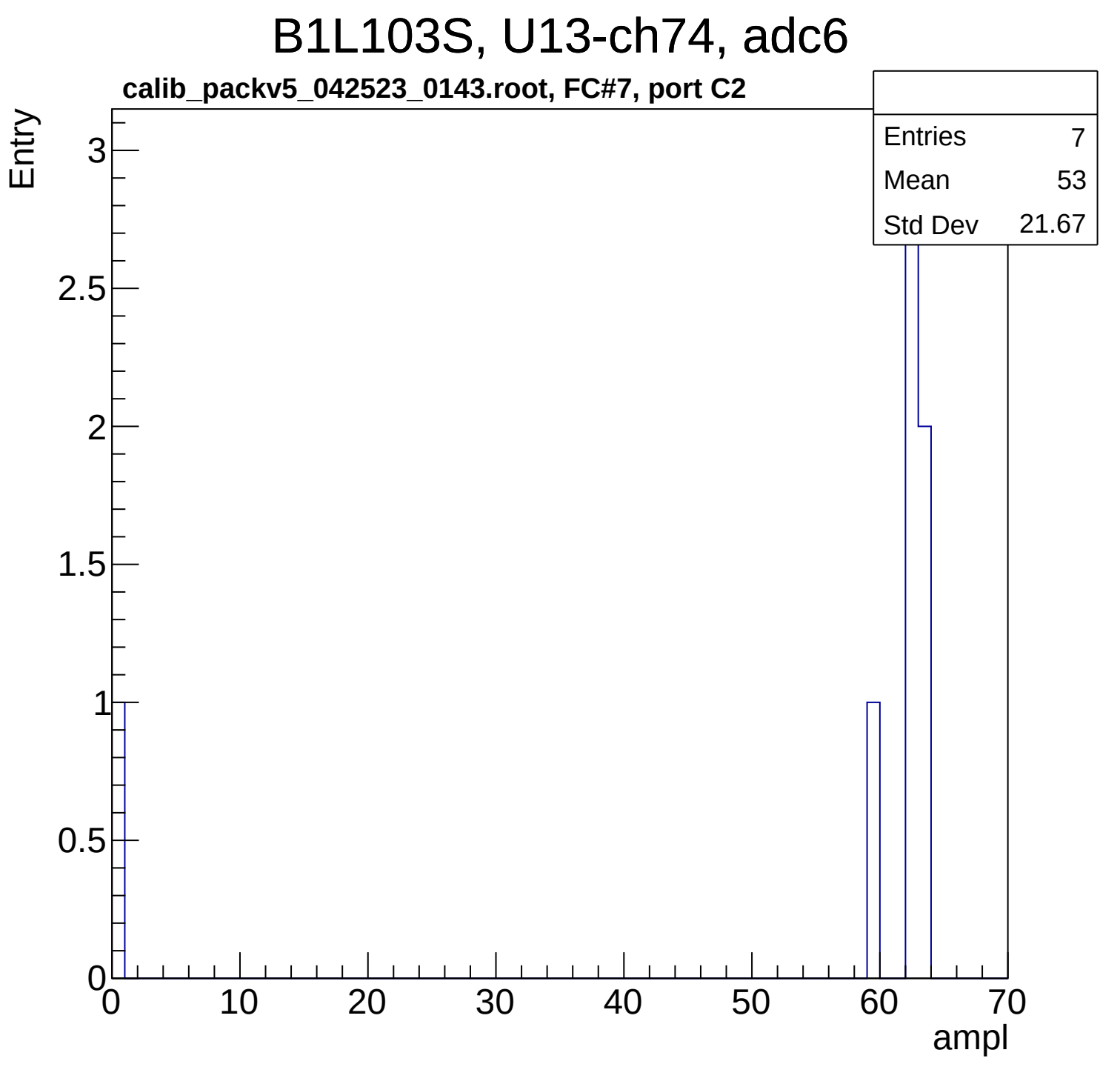
B1L103S, U13-ch74, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 53
Std Dev 21.67
Entry
ampl
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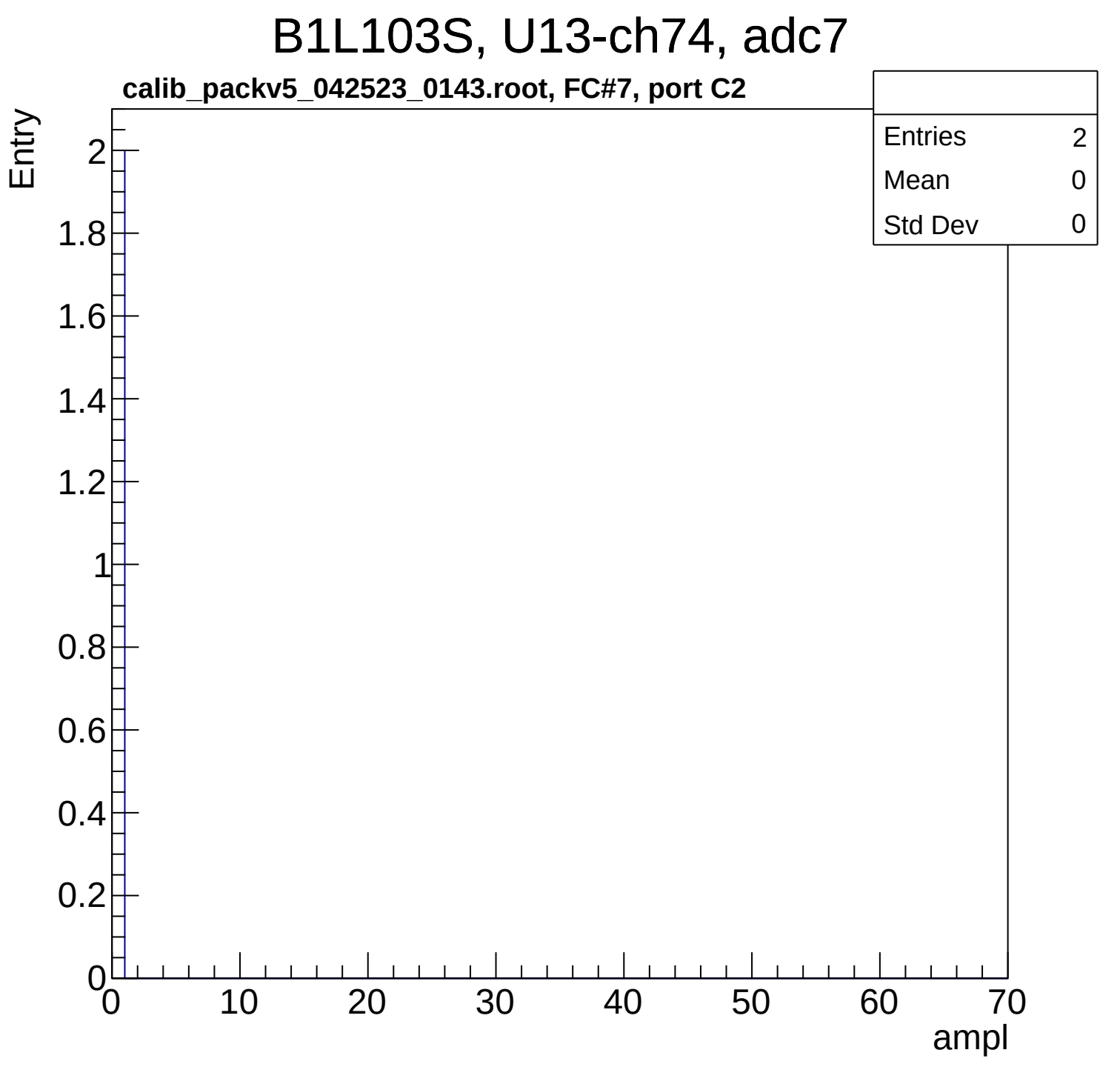
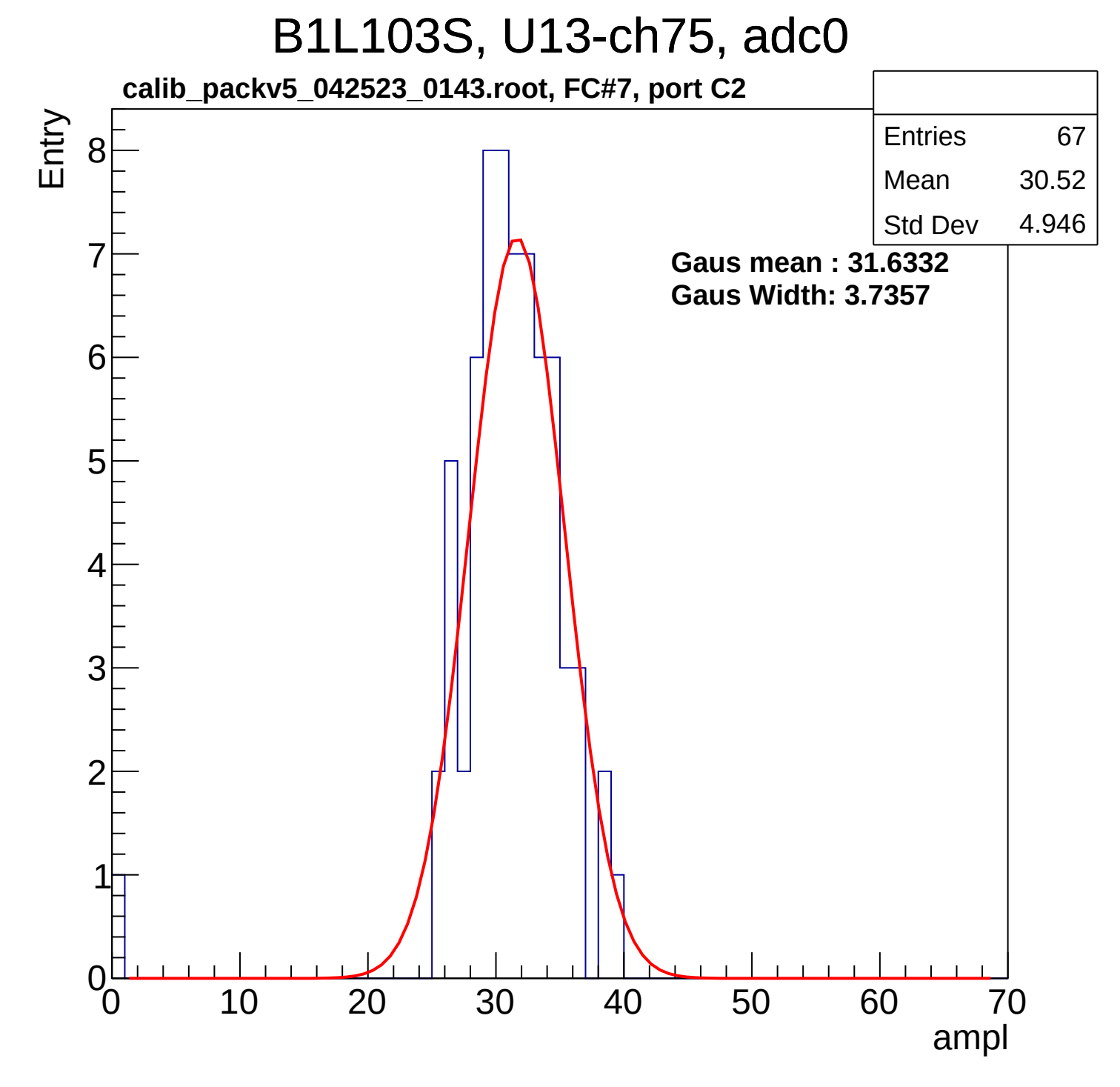
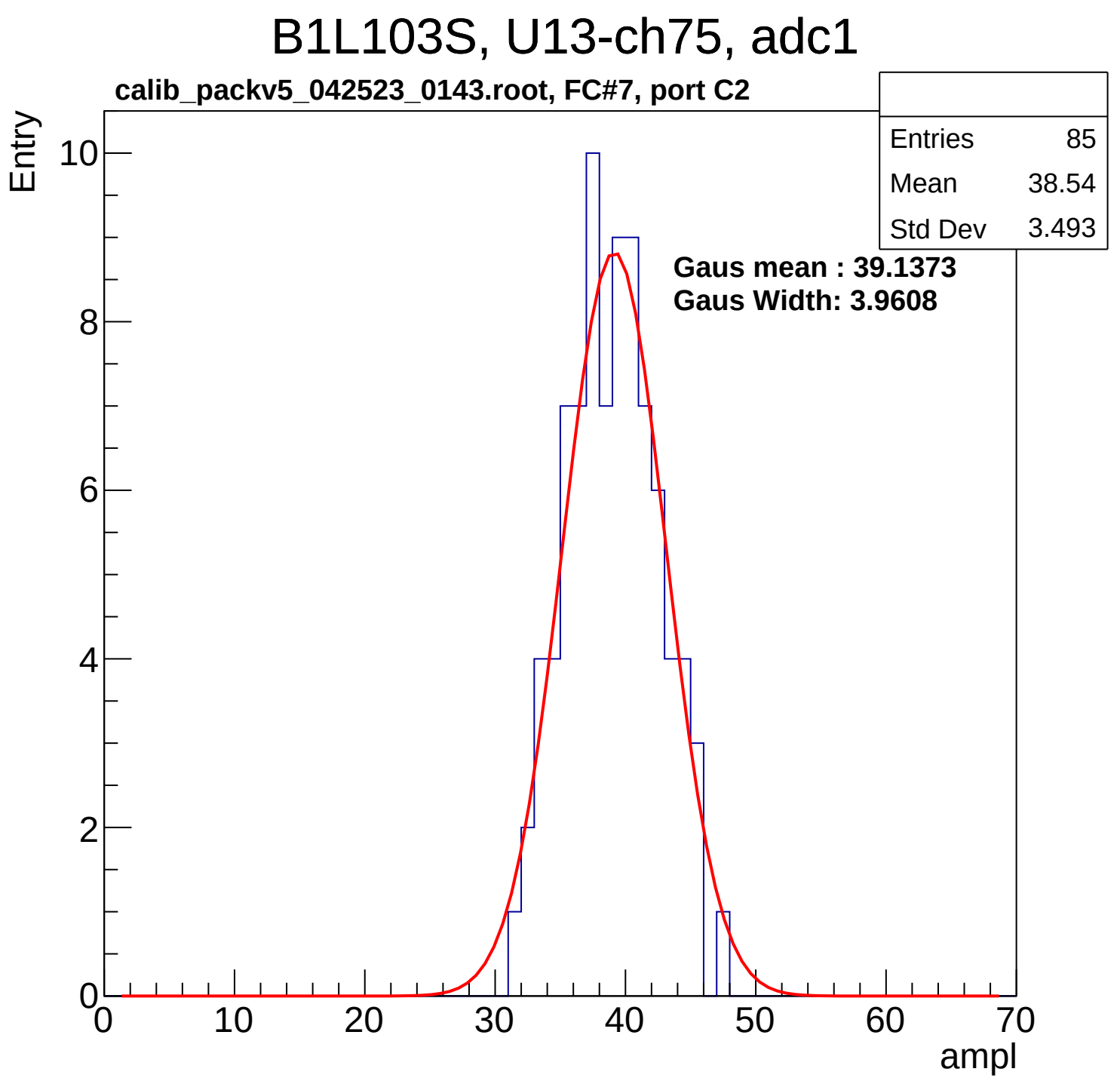
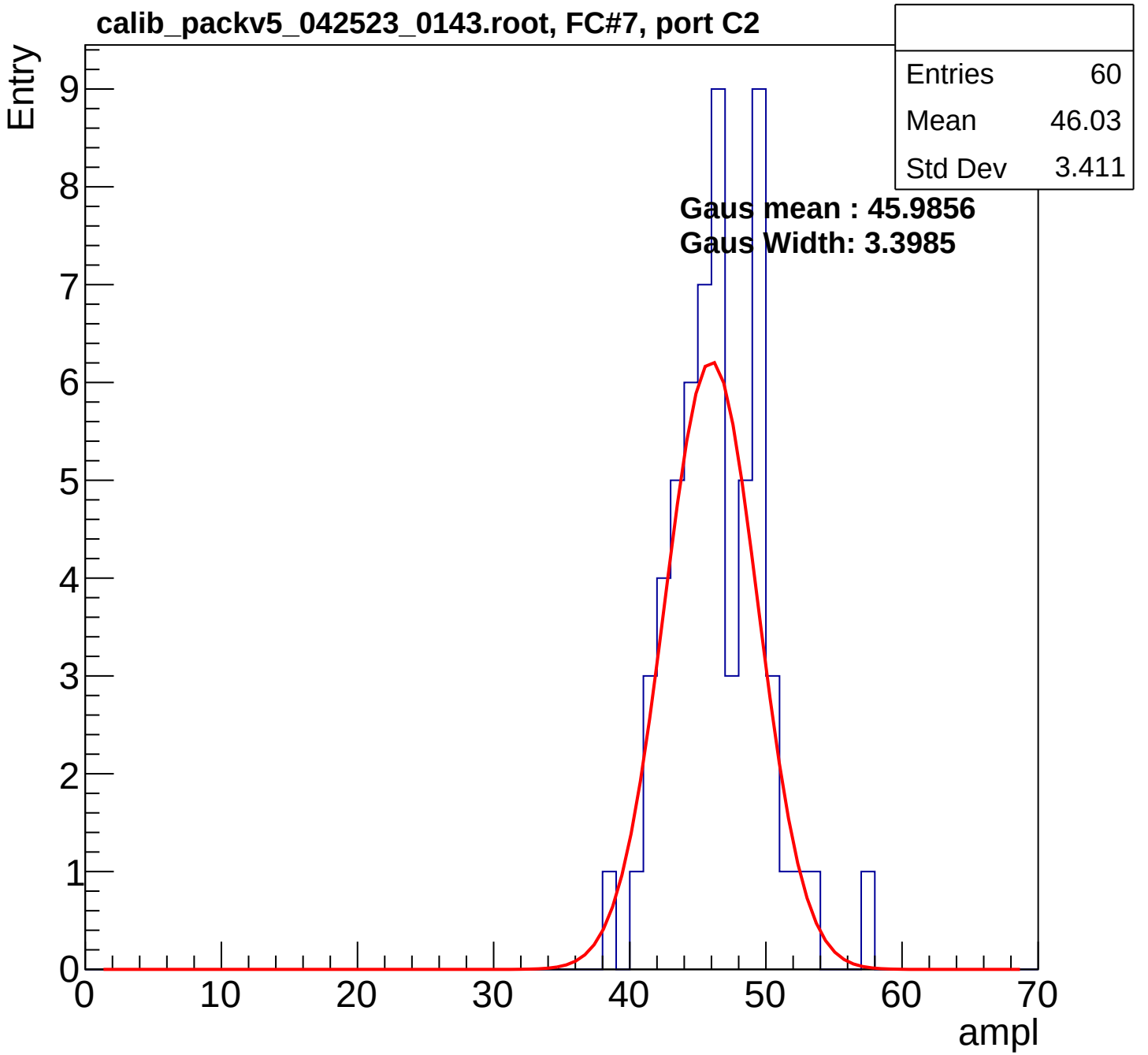
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch74, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch75, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 30.52
Std Dev 4.946
Gaus mean : 31.6332
Gaus Width: 3.7357
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch75, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 38.54
Std Dev 3.493
Gaus mean : 39.1373
Gaus Width: 3.9608
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch75, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 46.03
Std Dev 3.411
Gaus mean : 45.9856
Gaus Width: 3.3985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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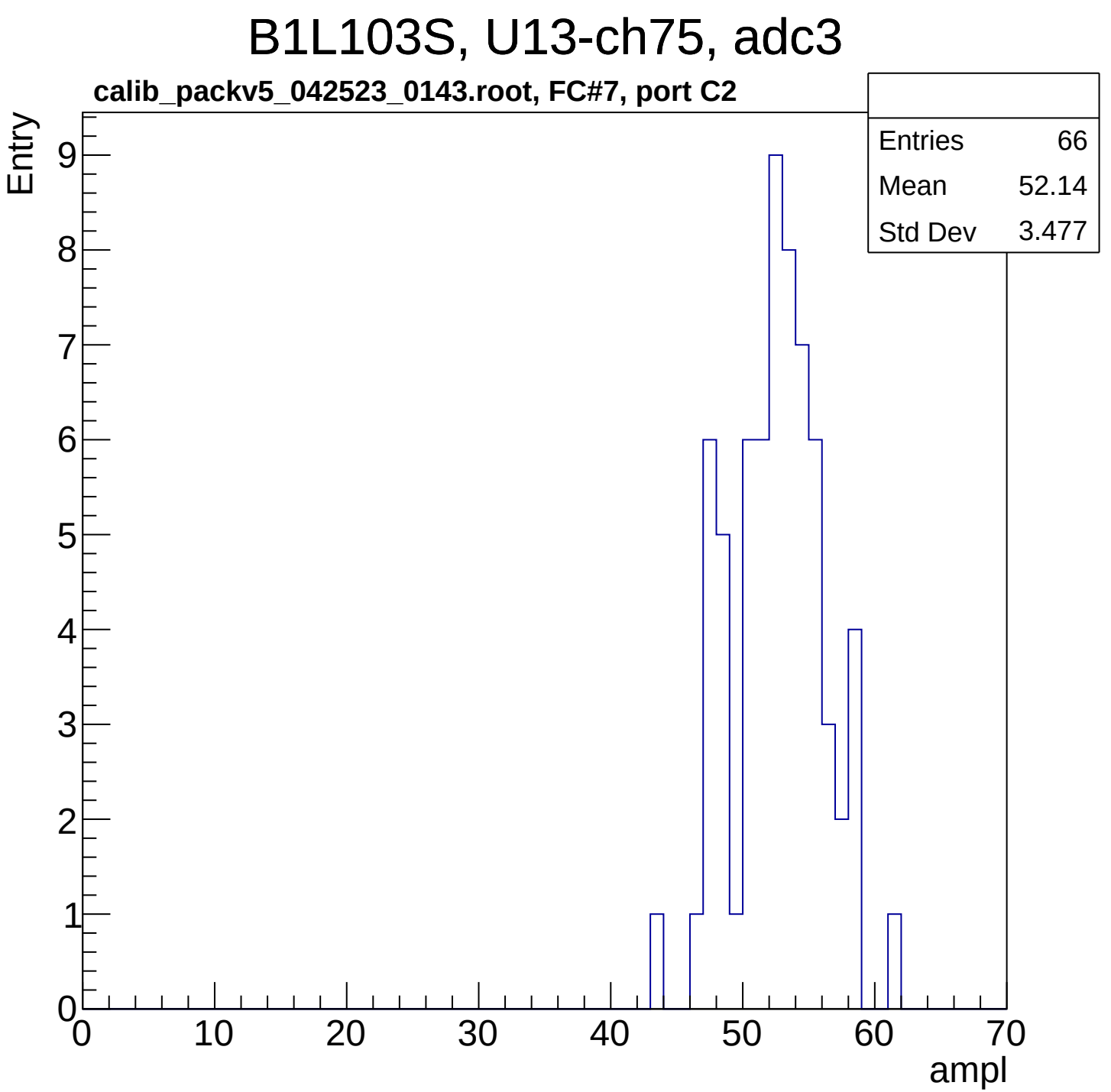
B1L103S, U13-ch75, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 52.14
Std Dev 3.477
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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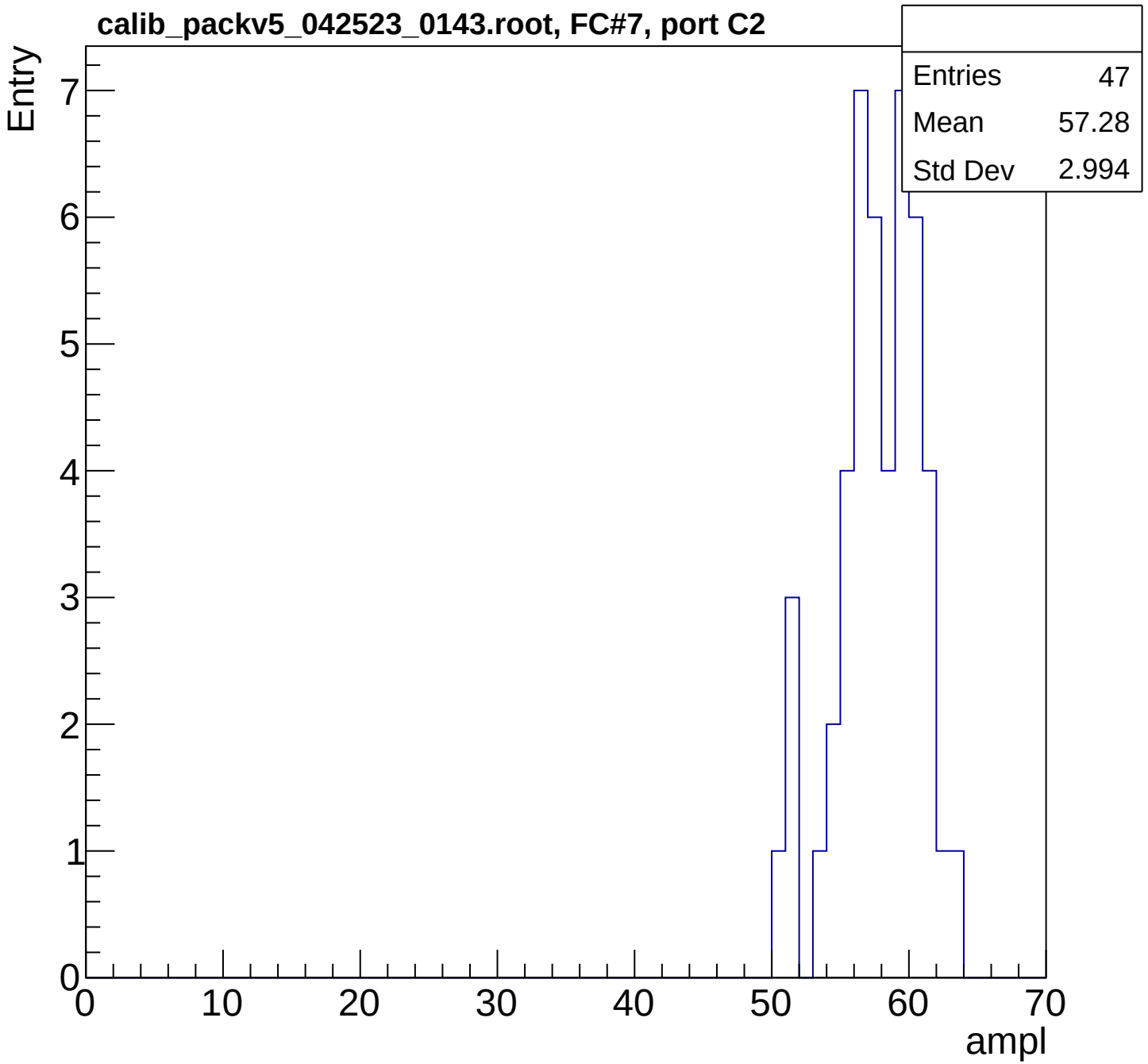
B1L103S, U13-ch75, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.28
Std Dev 2.994
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
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60
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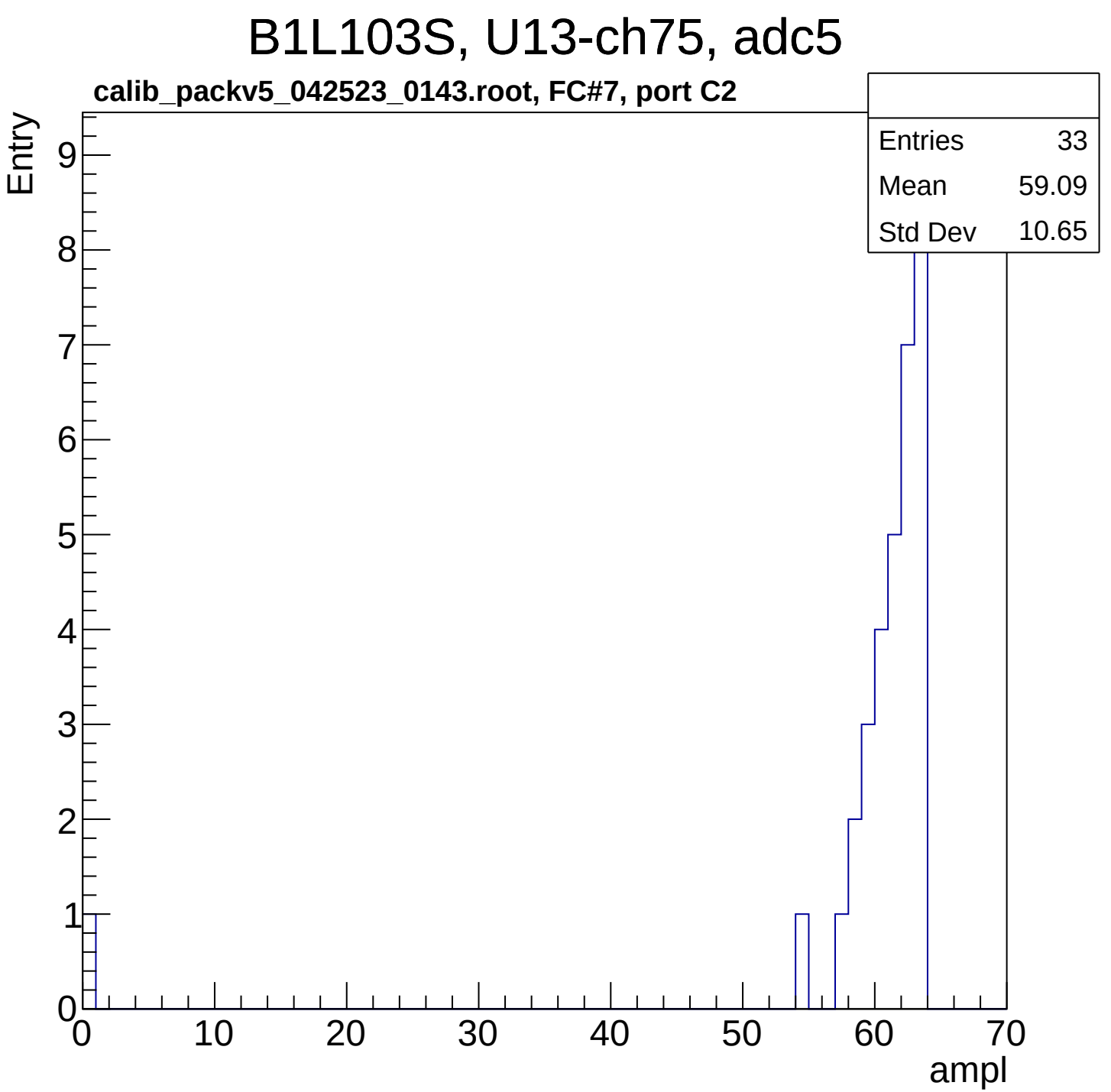
B1L103S, U13-ch75, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 59.09
Std Dev 10.65
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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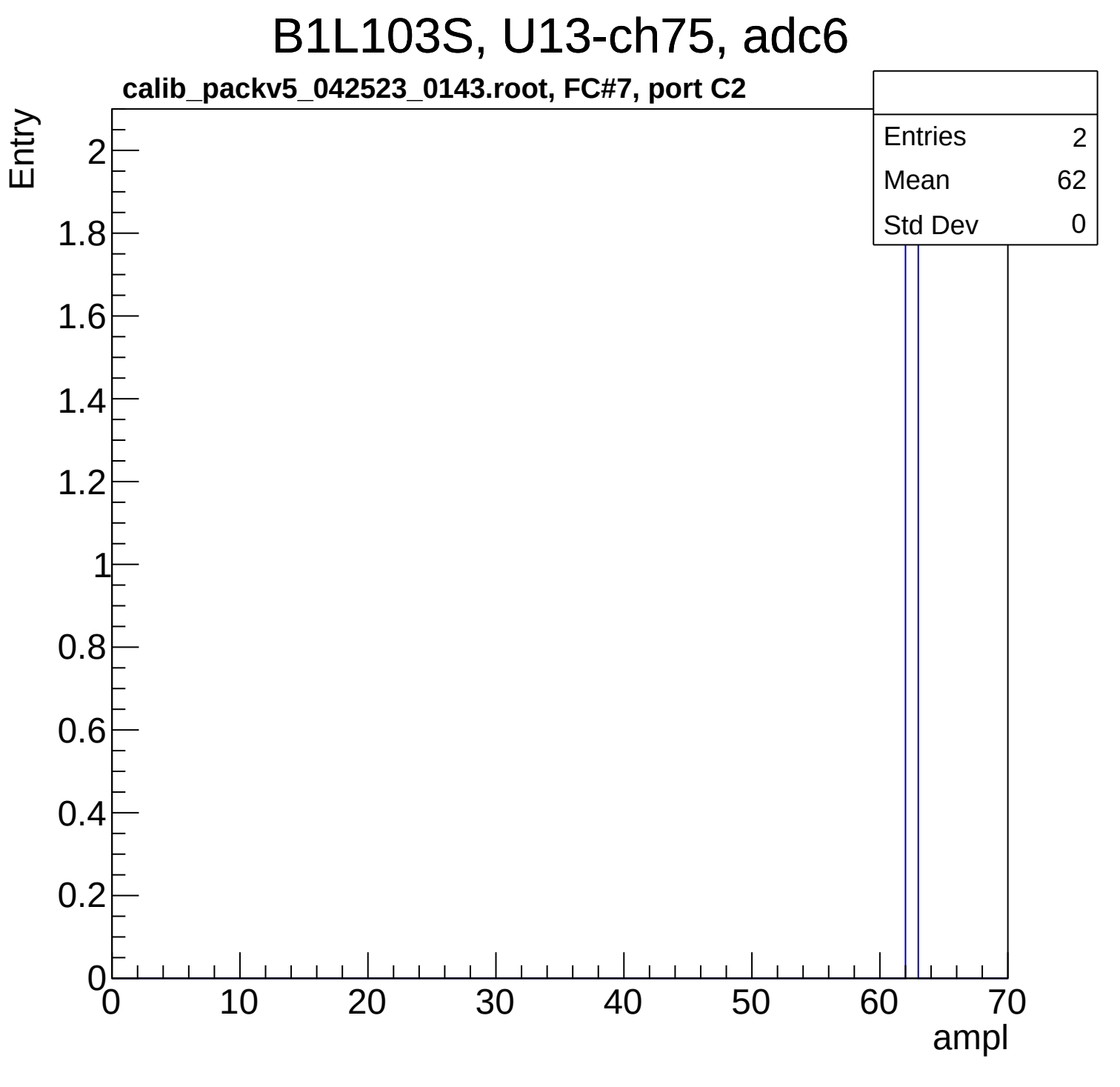
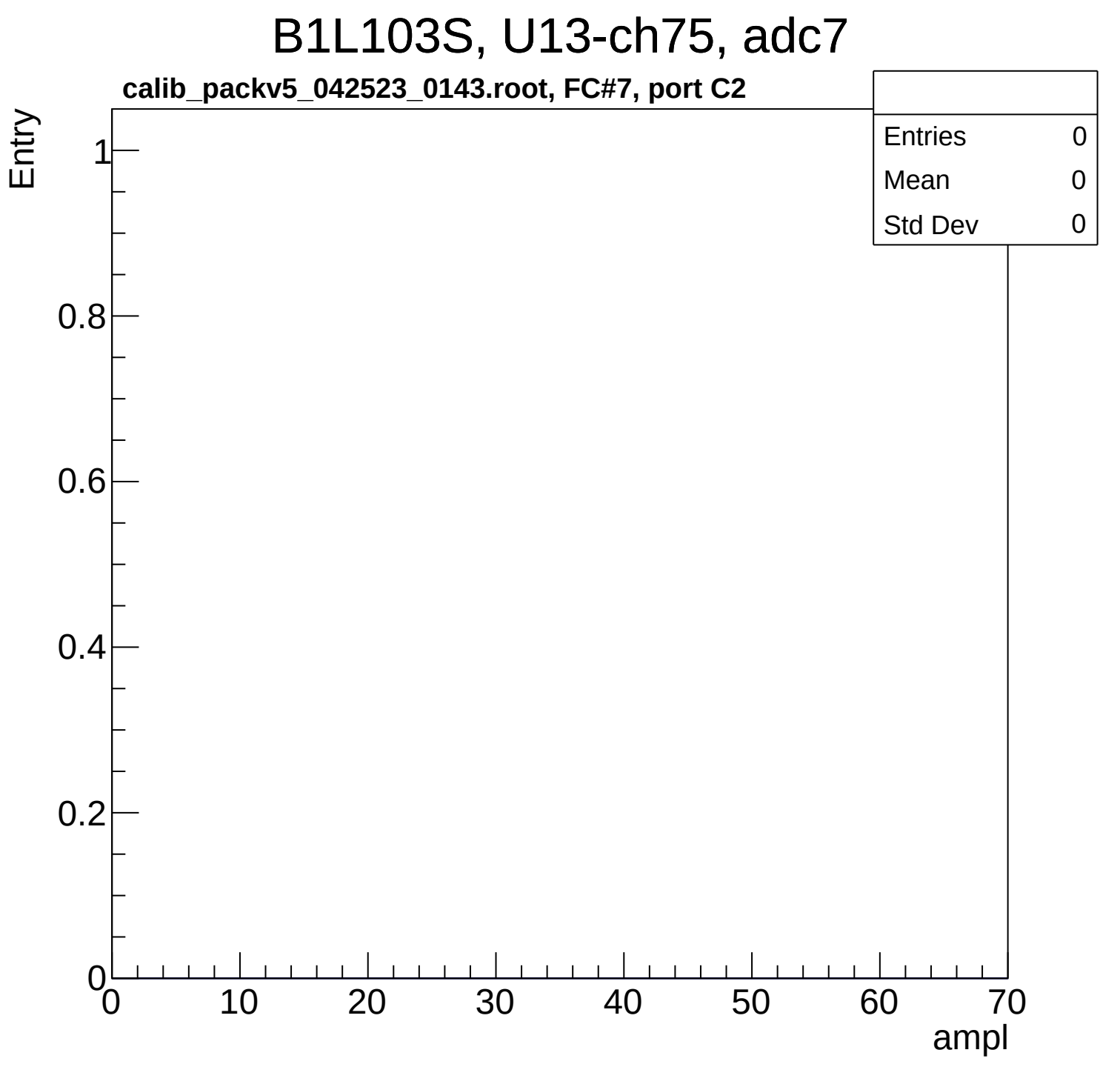
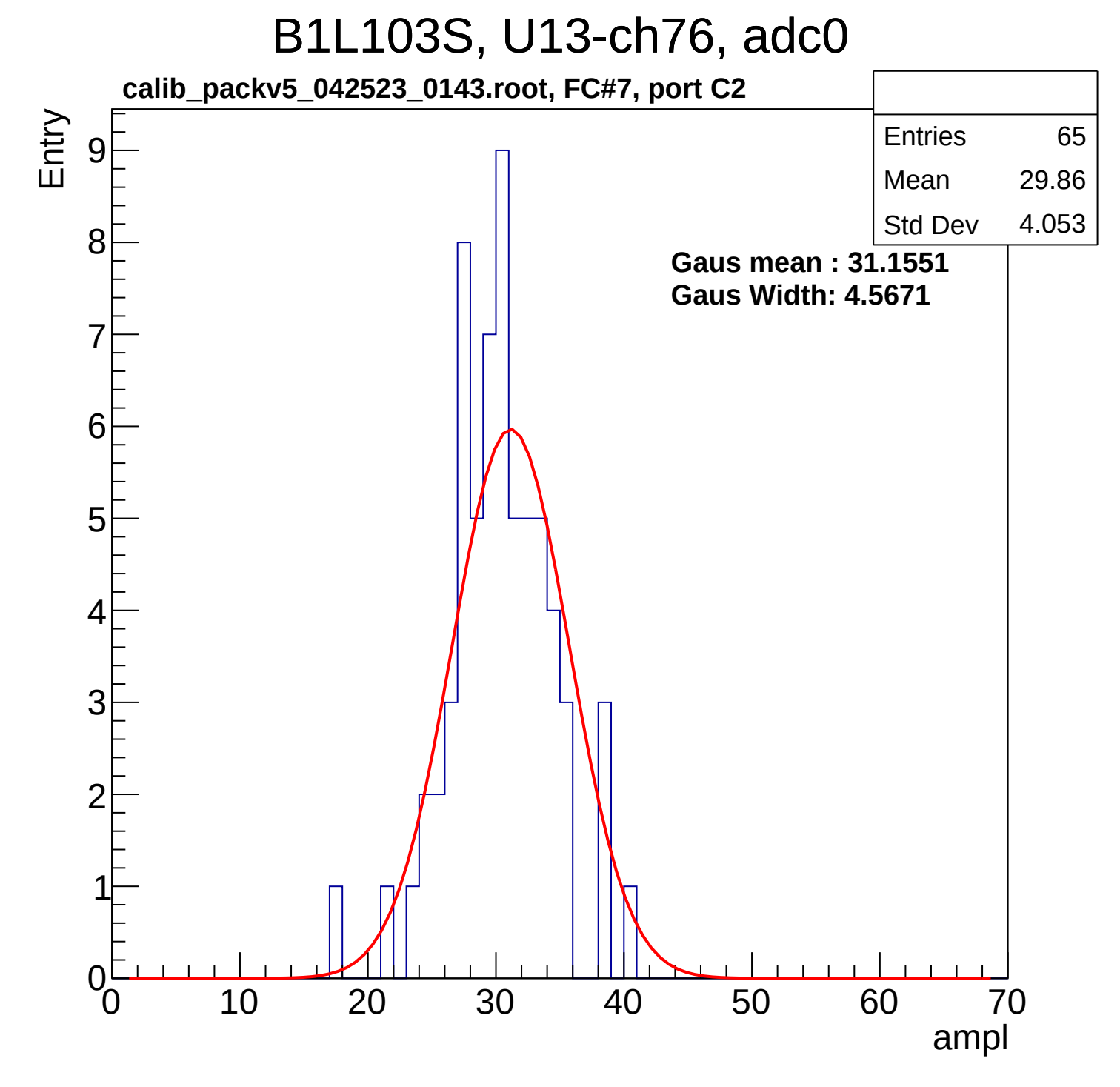
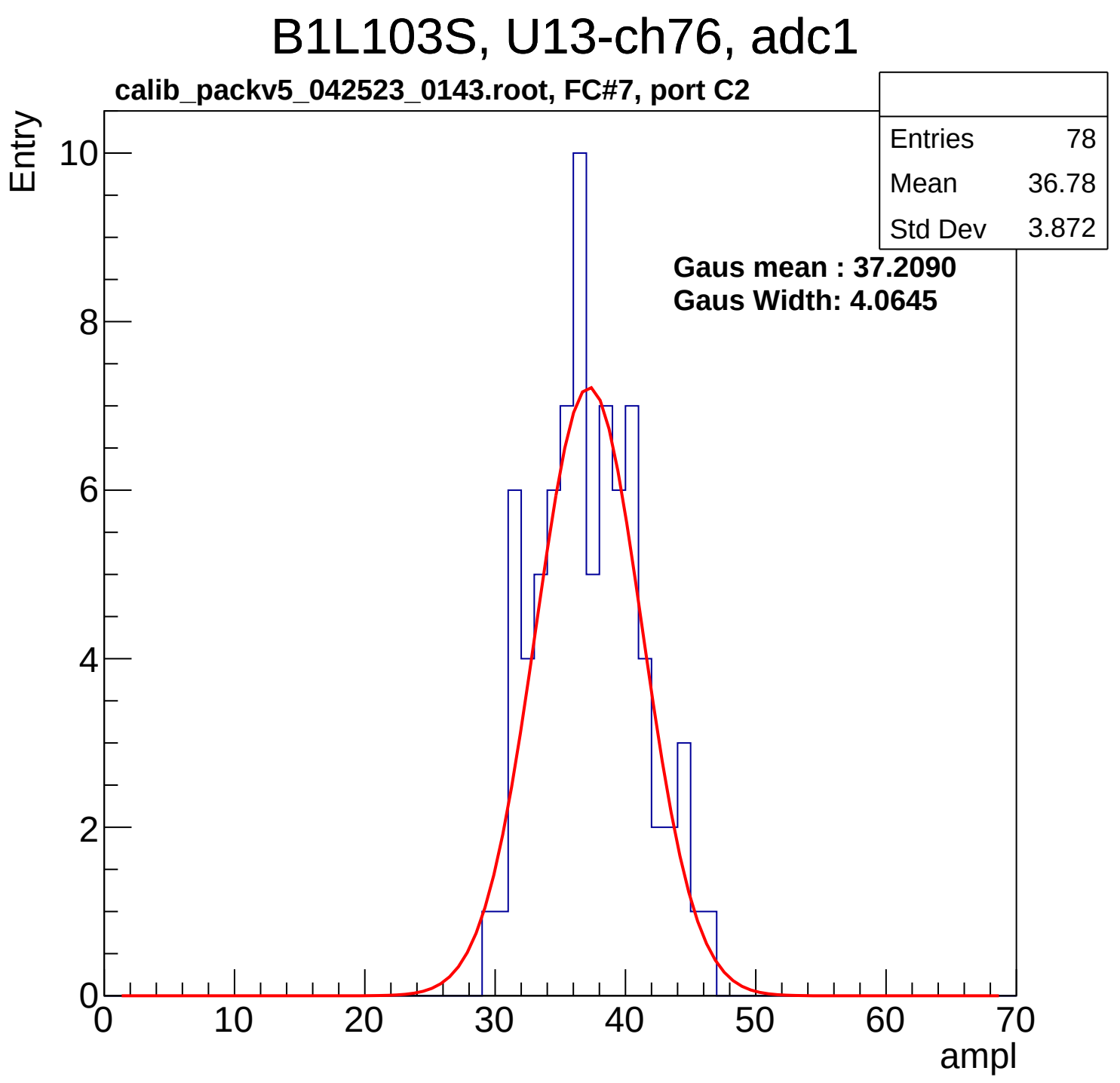
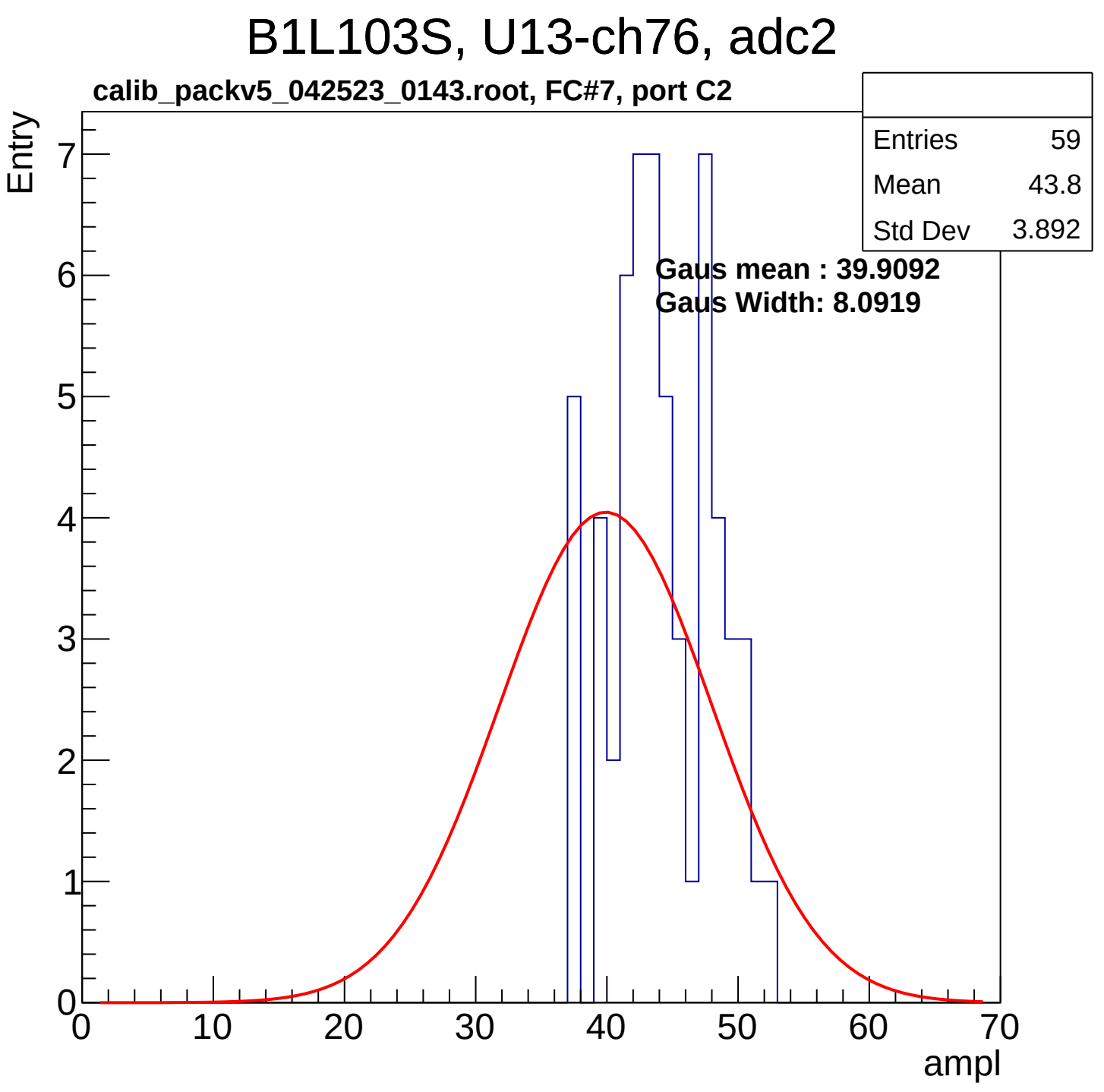
40
50
60
70


B1L103S, U13-ch75, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch75, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch76, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 29.86
Std Dev 4.053
Gaus mean : 31.1551
Gaus Width: 4.5671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch76, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.78
Std Dev 3.872
Gaus mean : 37.2090
Gaus Width: 4.0645
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch76, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 43.8
Std Dev 3.892
Gaus mean : 39.9092
Gaus Width: 8.0919
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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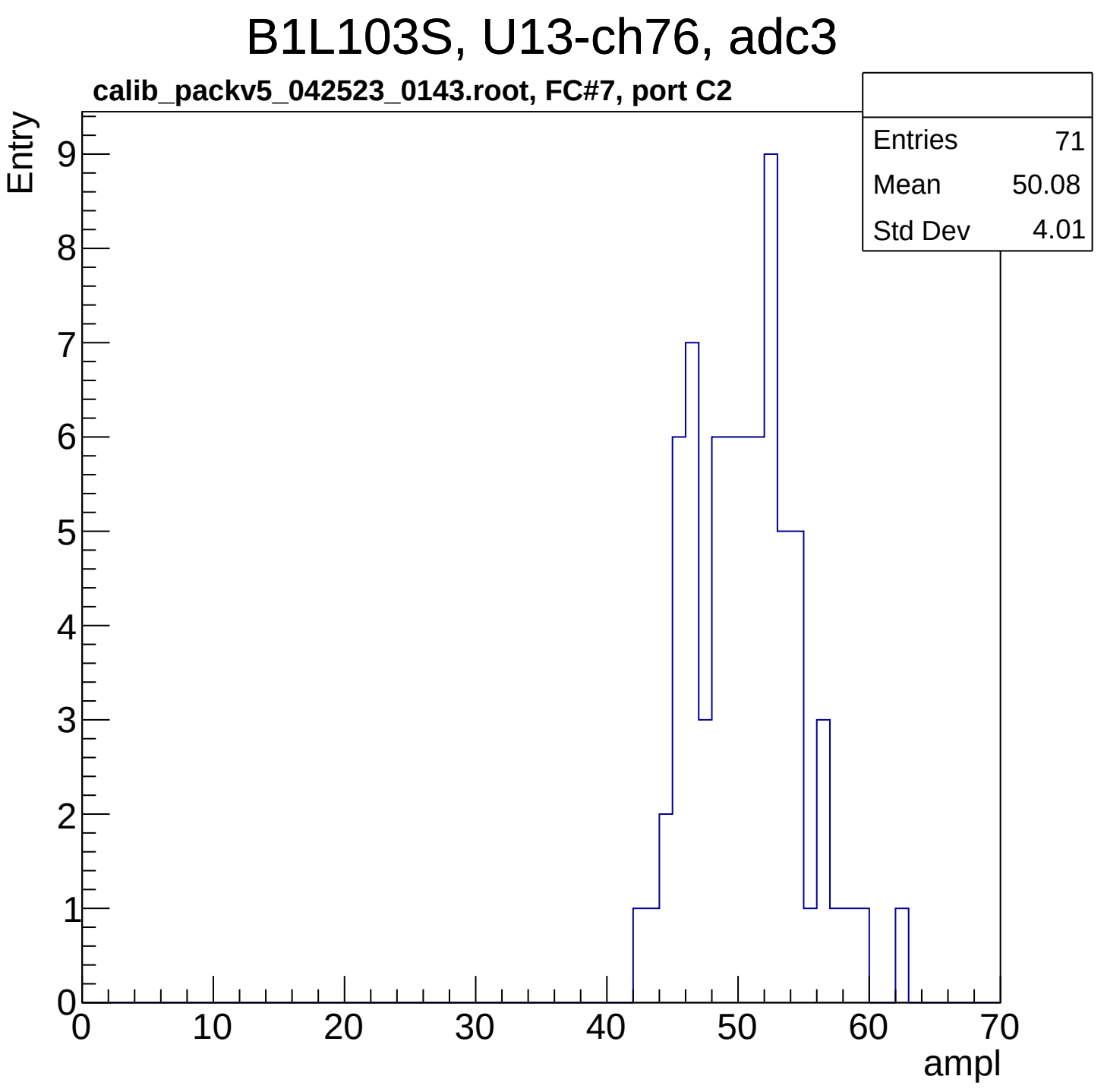
40
50
60
70


B1L103S, U13-ch76, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 50.08
Std Dev 4.01
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
50
60
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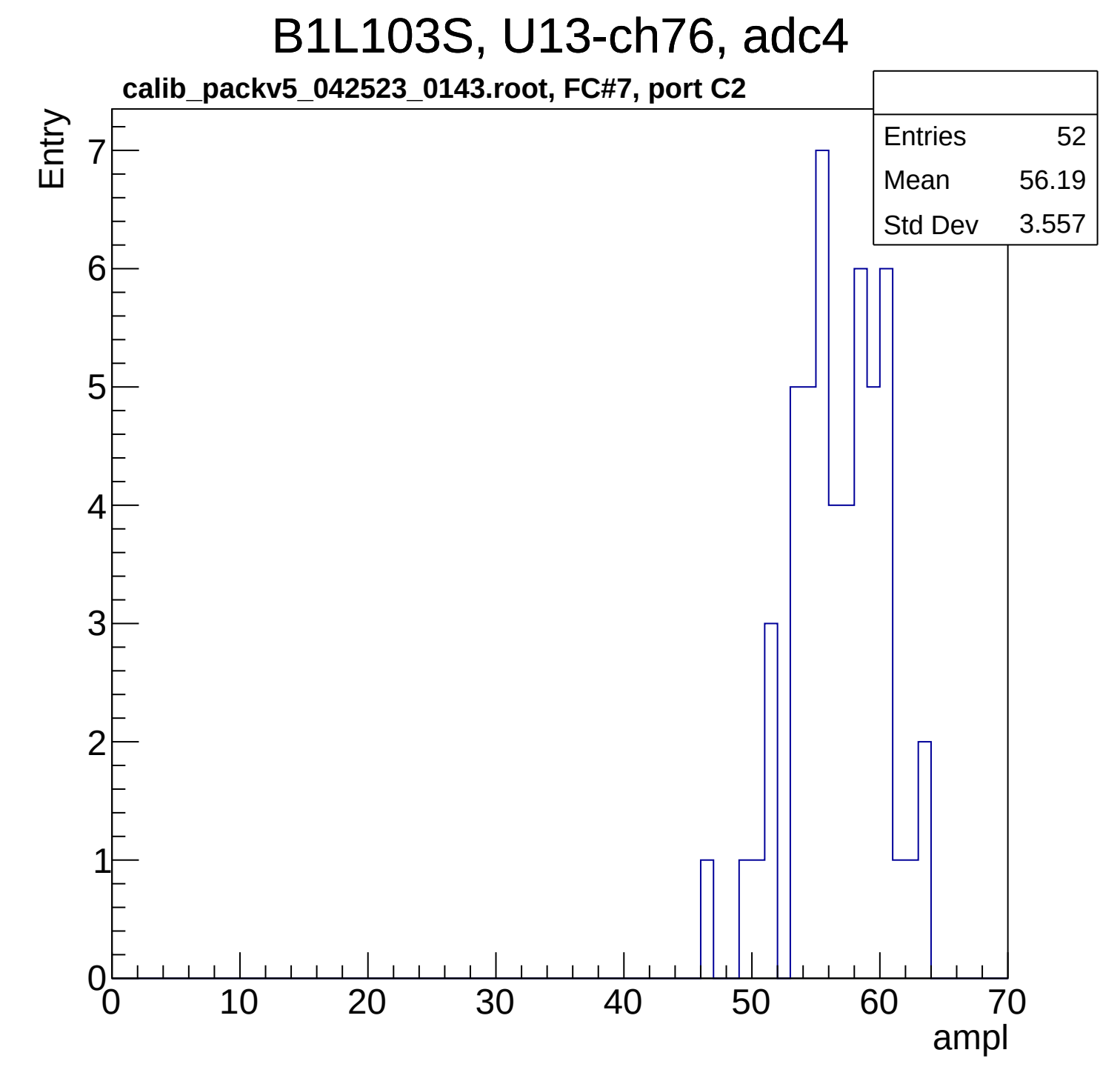
B1L103S, U13-ch76, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 56.19
Std Dev 3.557
Entry
ampl
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1
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3
4
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60
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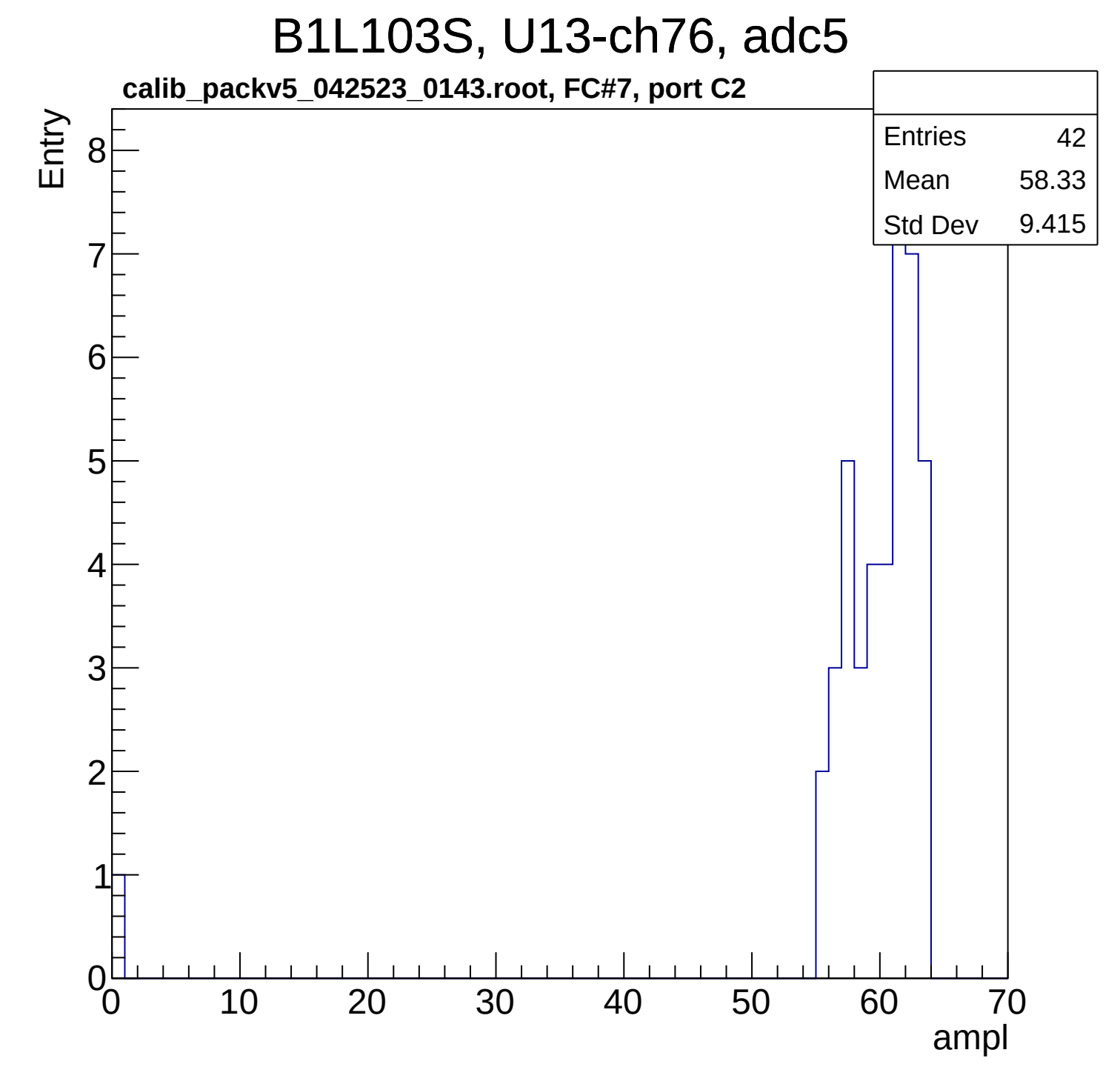
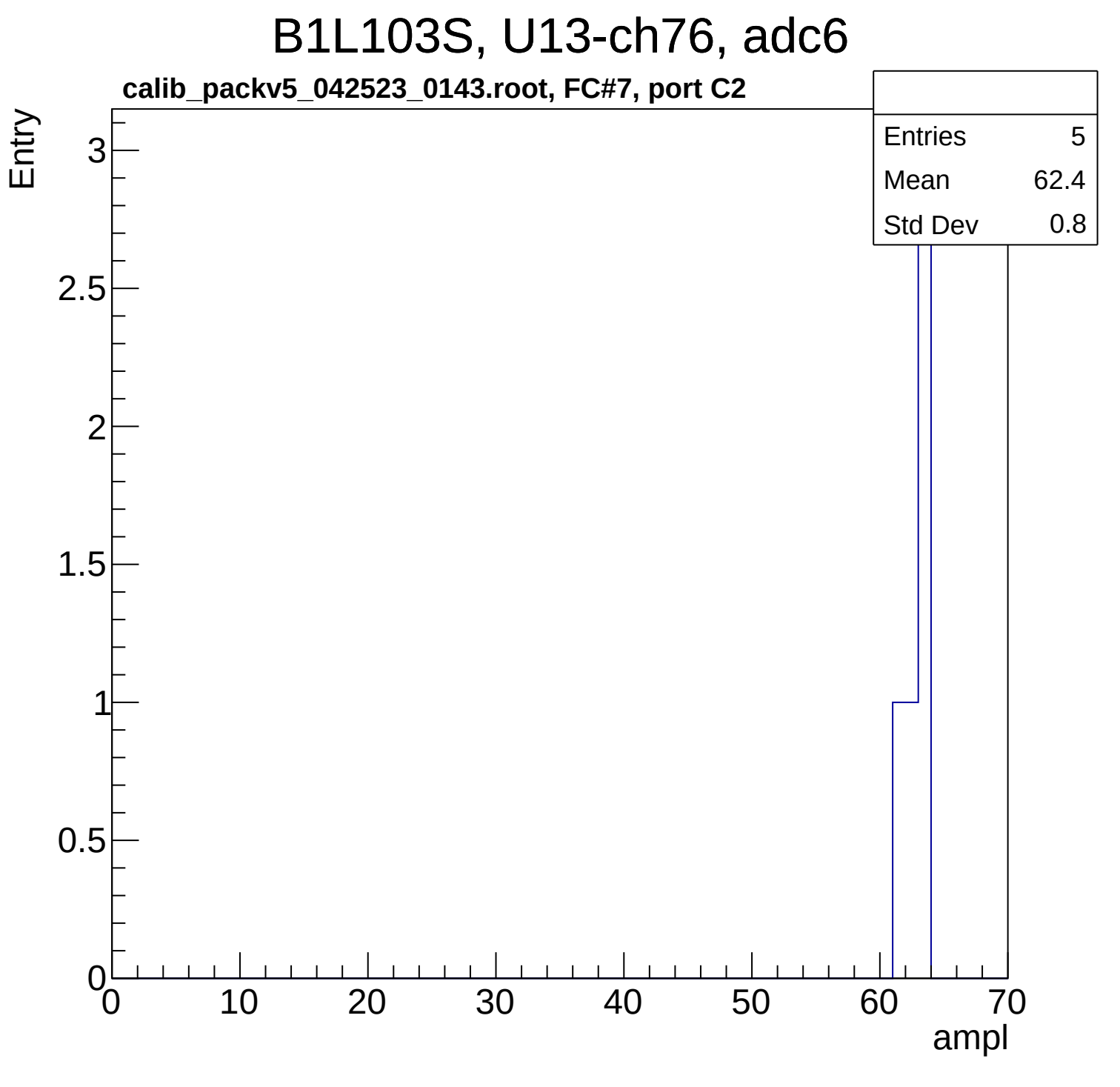
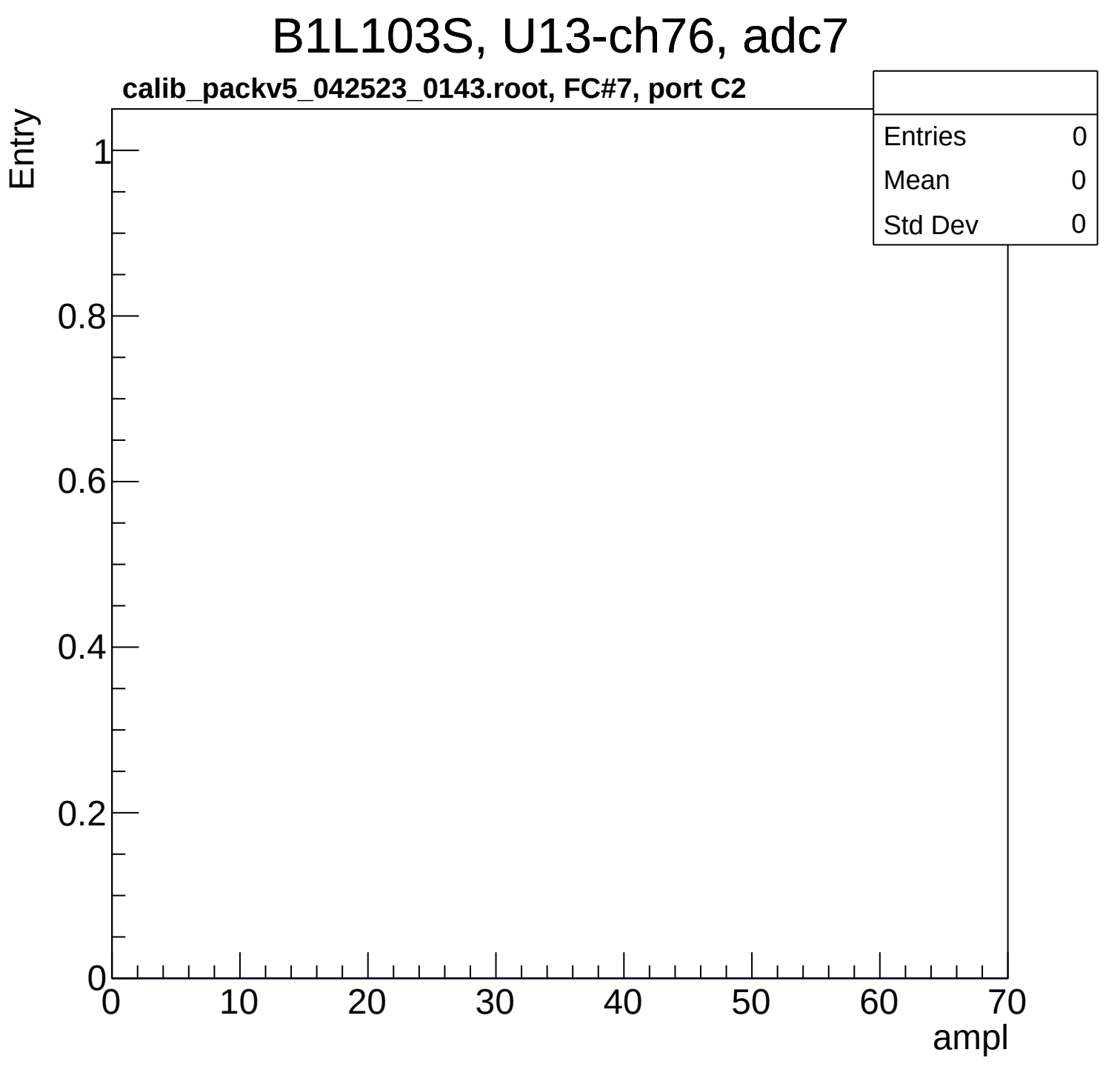

B1L103S, U13-ch76, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.33
Std Dev 9.415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch76, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch76, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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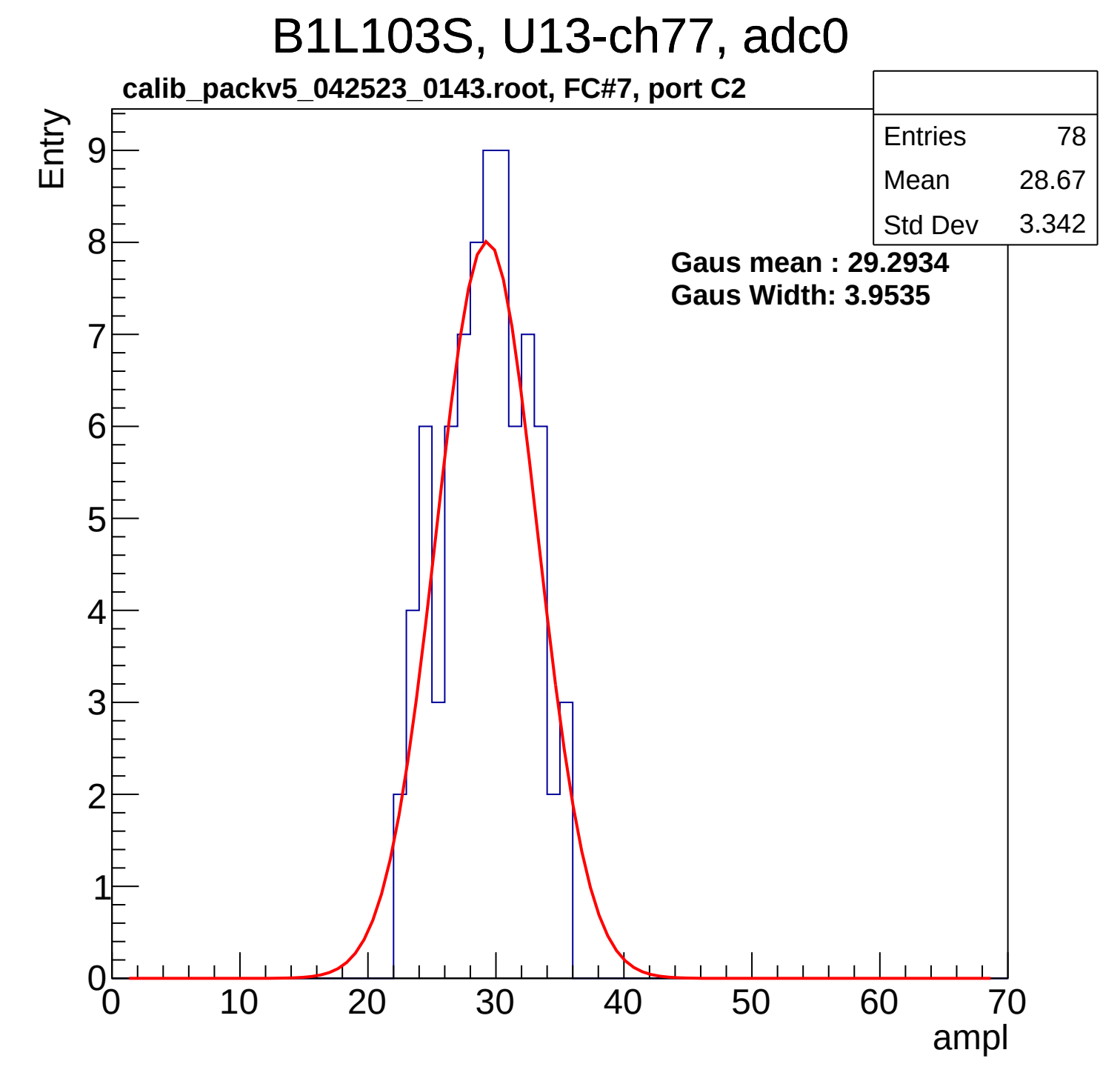
50
60
70
ampl


B1L103S, U13-ch77, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 28.67
Std Dev 3.342
Gaus mean : 29.2934
Gaus Width: 3.9535
Entry
ampl
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10
20
30
40
50
60
70
1
2
3
4
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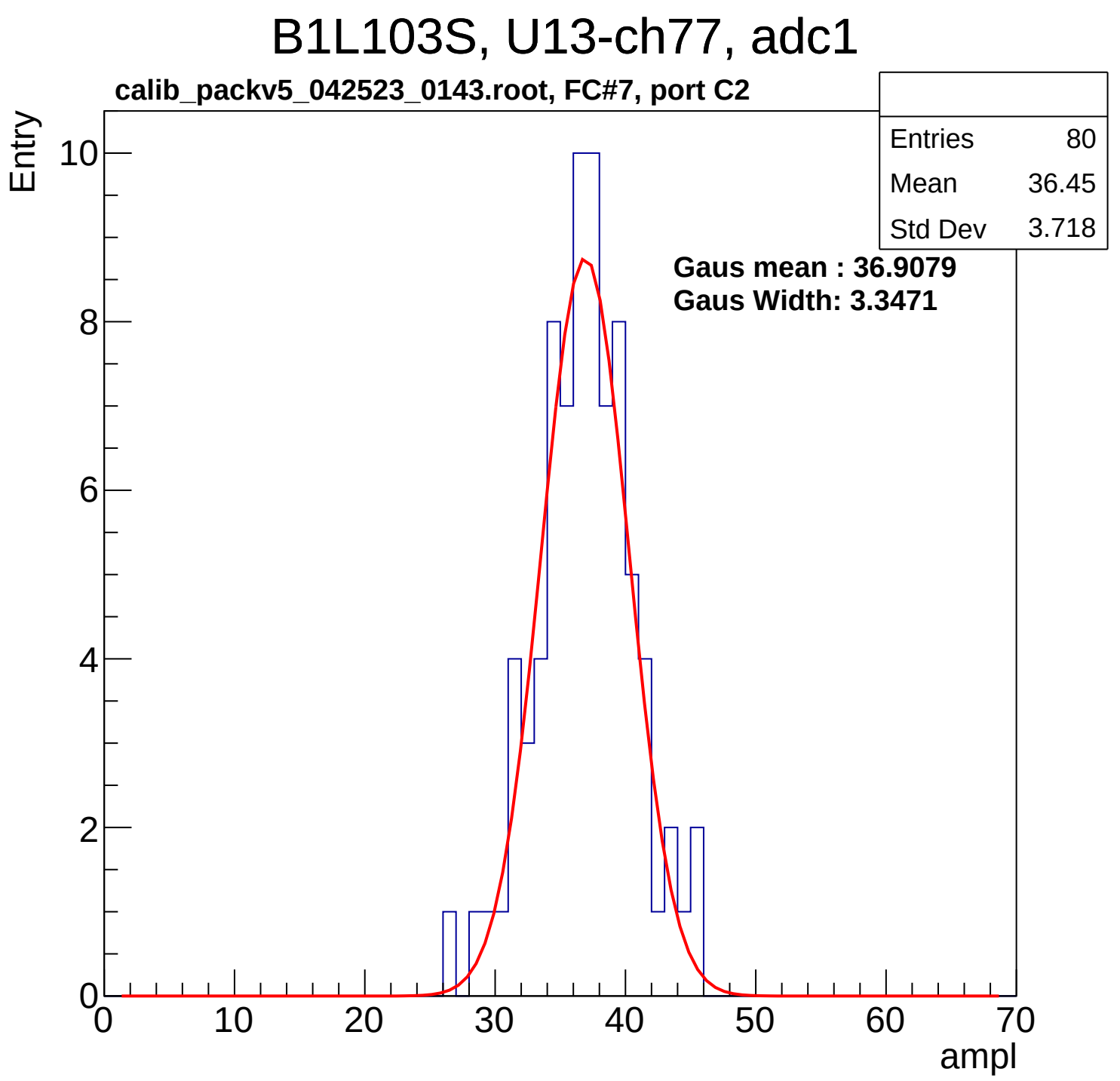
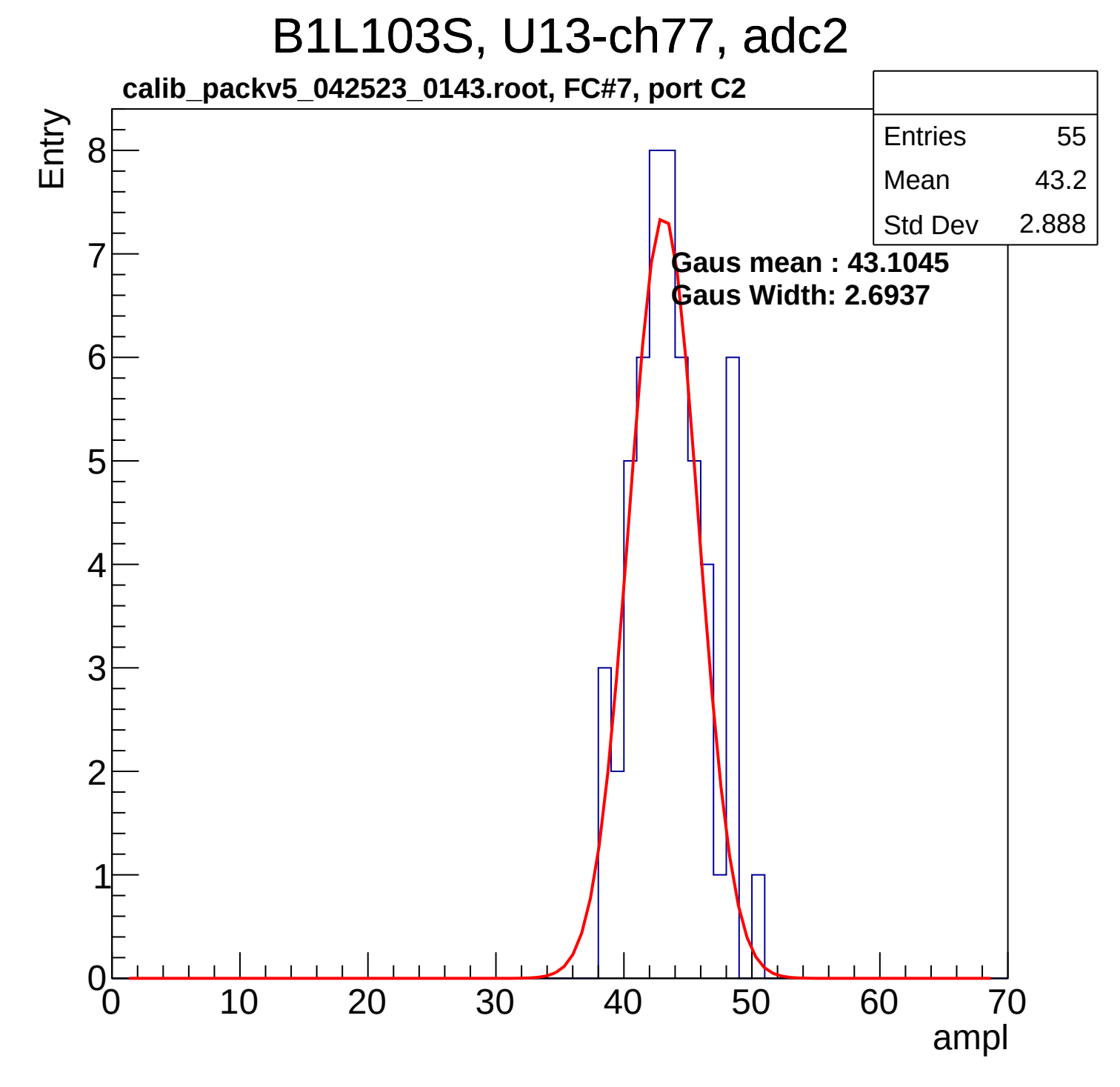
6
7
8
9


B1L103S, U13-ch77, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 36.45
Std Dev 3.718
Gaus mean : 36.9079
Gaus Width: 3.3471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch77, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 43.2
Std Dev 2.888
Gaus mean : 43.1045
Gaus Width: 2.6937
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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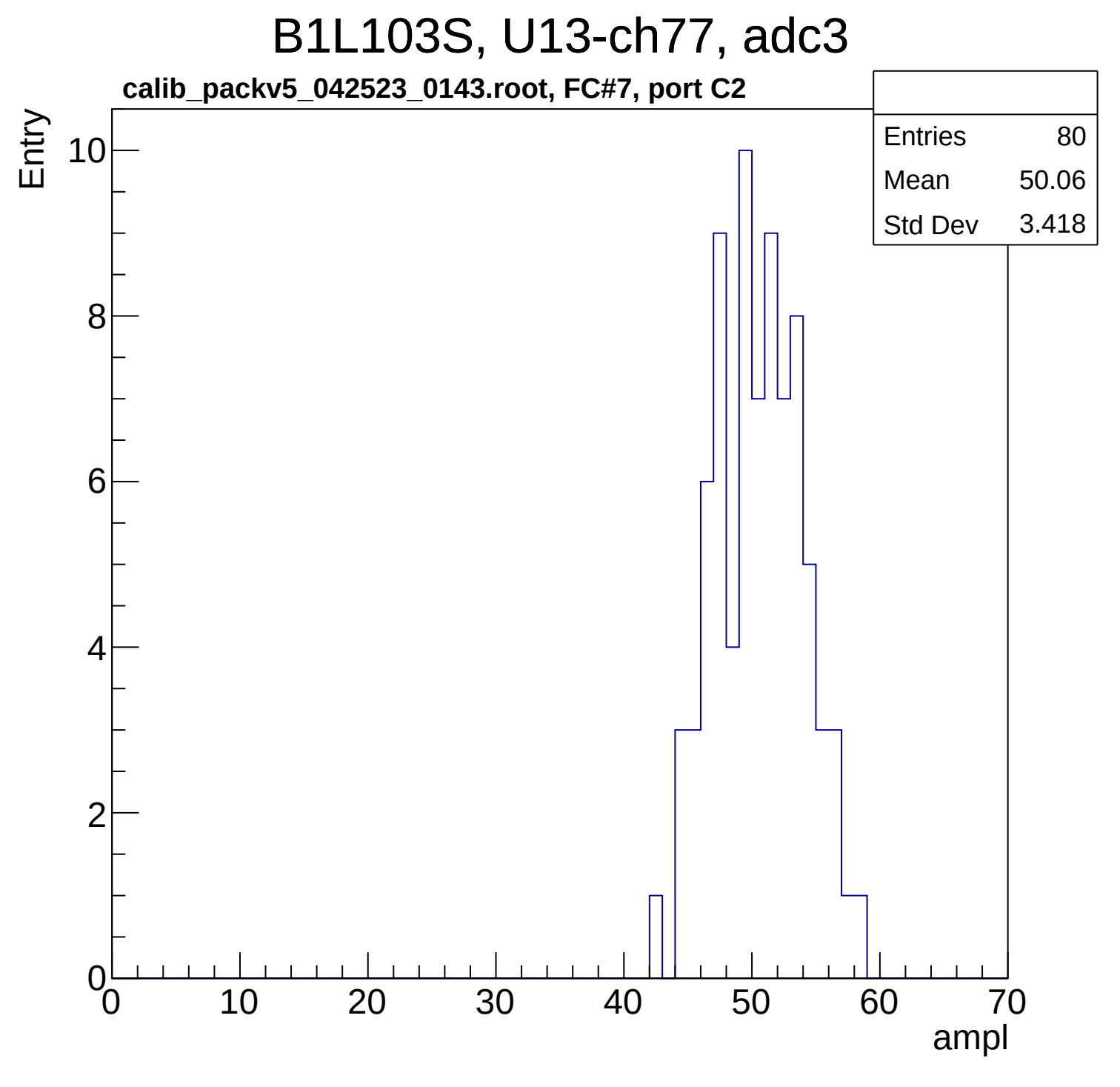
B1L103S, U13-ch77, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 50.06
Std Dev 3.418
Entry
ampl
0
2
4
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8
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0
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60
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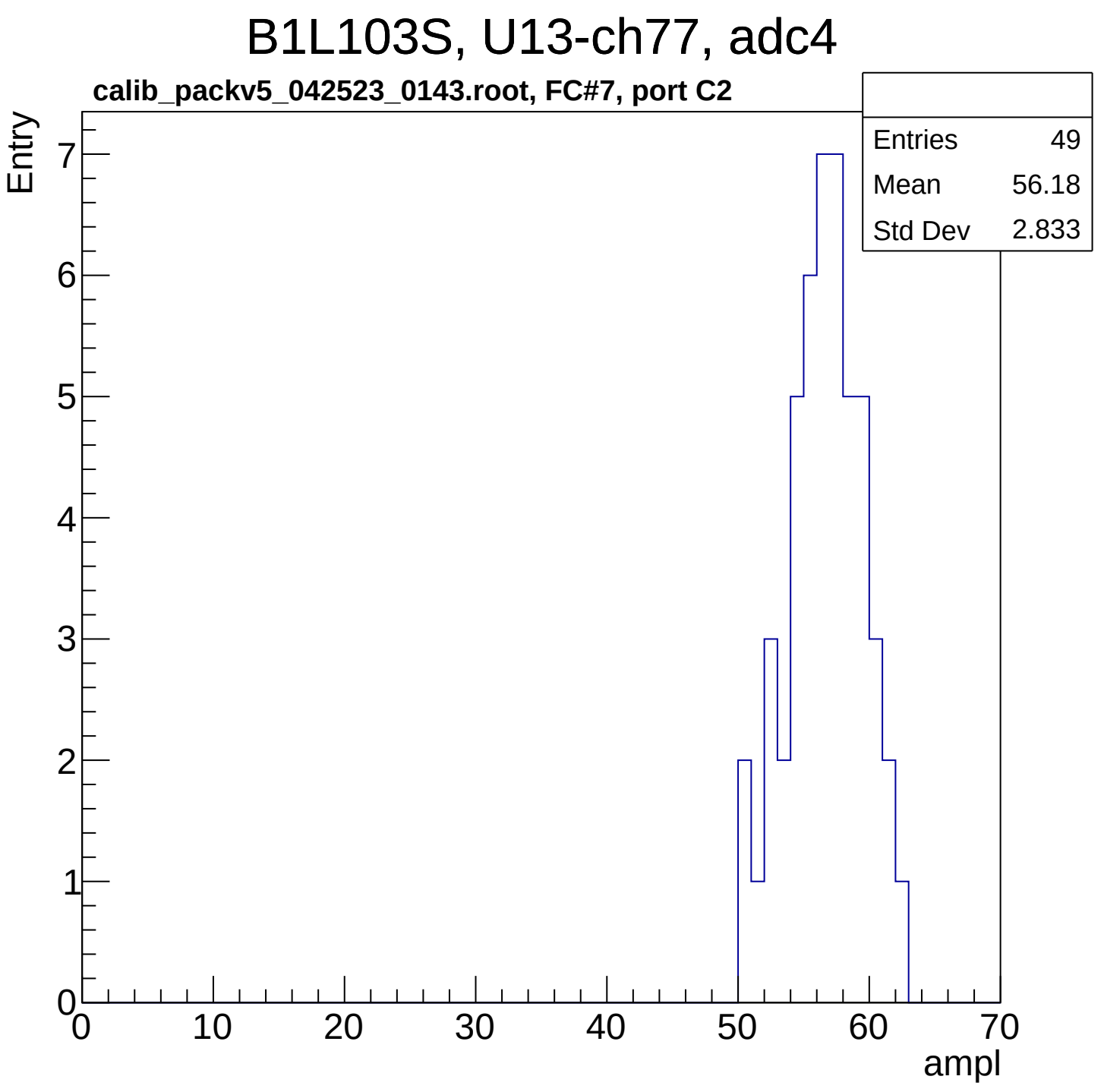
B1L103S, U13-ch77, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 56.18
Std Dev 2.833
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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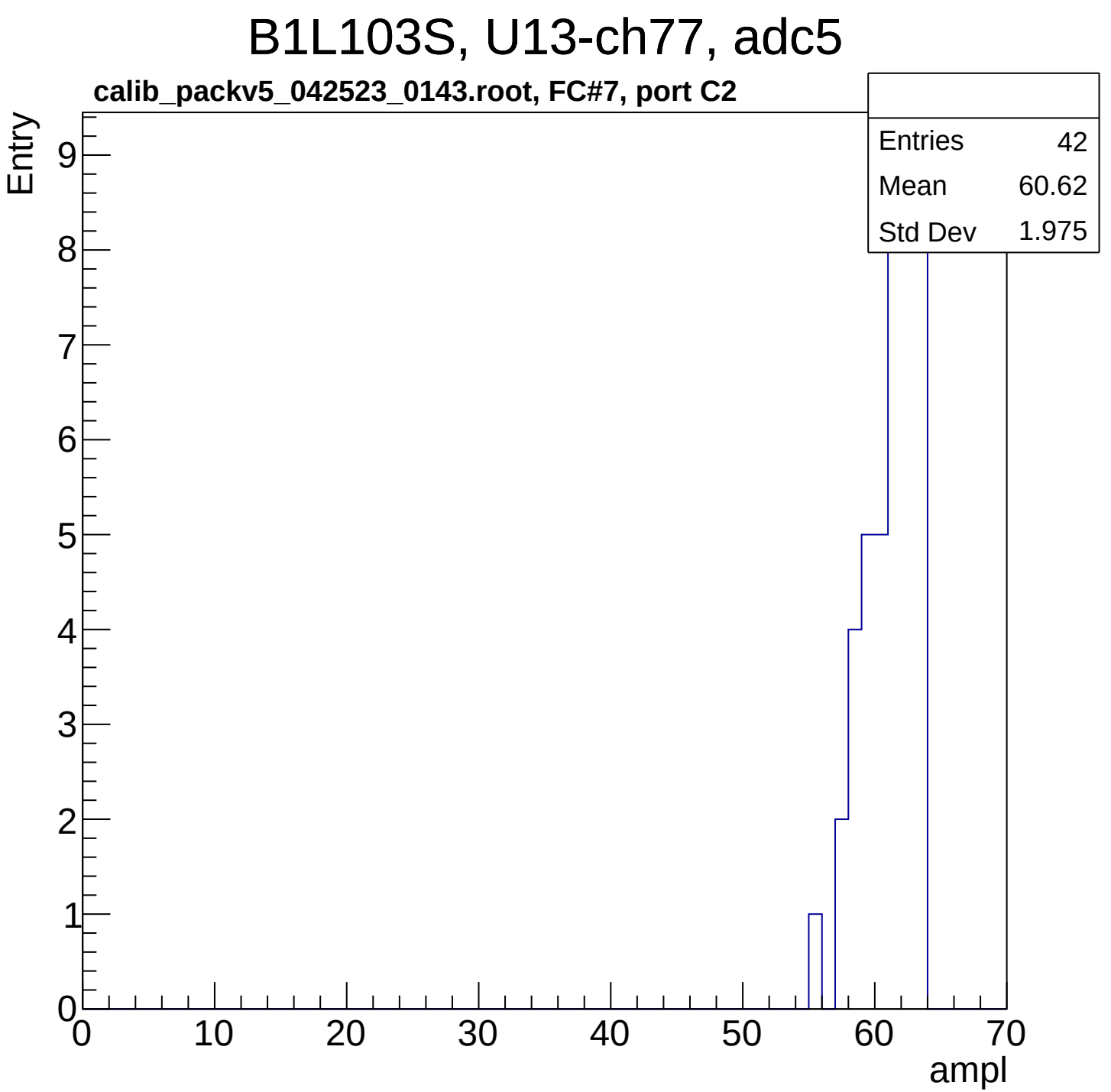
B1L103S, U13-ch77, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 60.62
Std Dev 1.975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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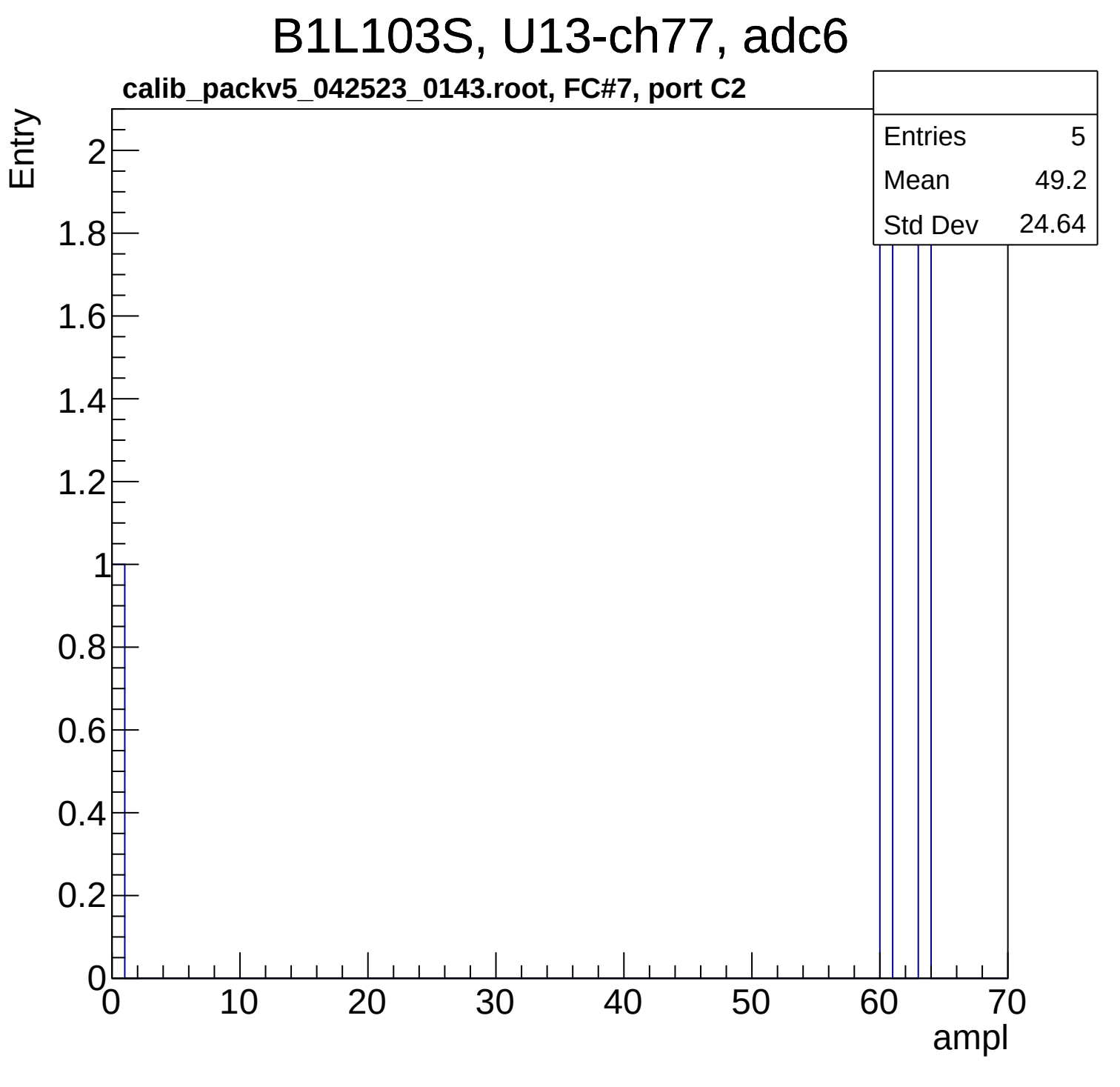
B1L103S, U13-ch77, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.2
Std Dev 24.64
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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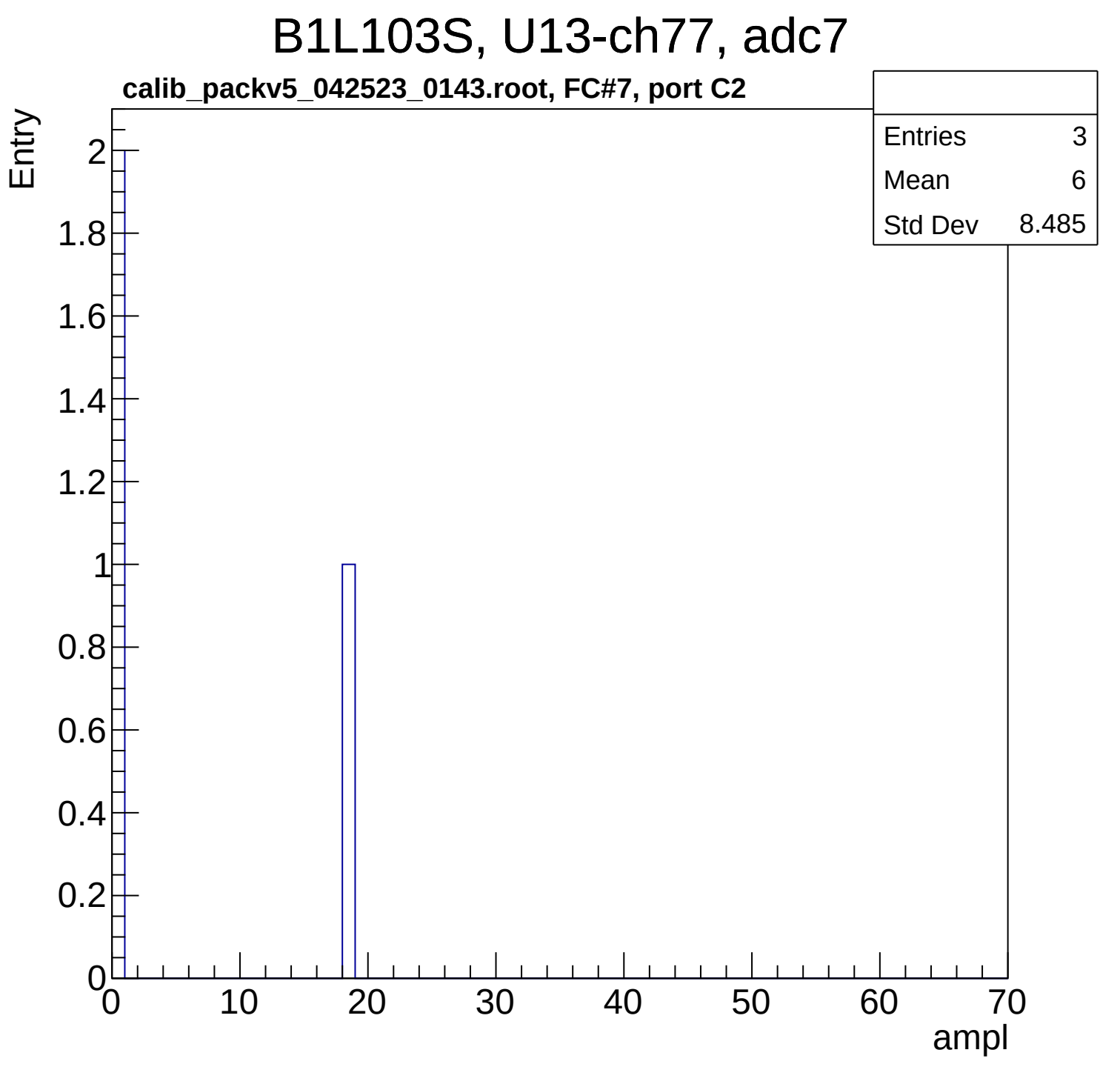
B1L103S, U13-ch77, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 6
Std Dev 8.485
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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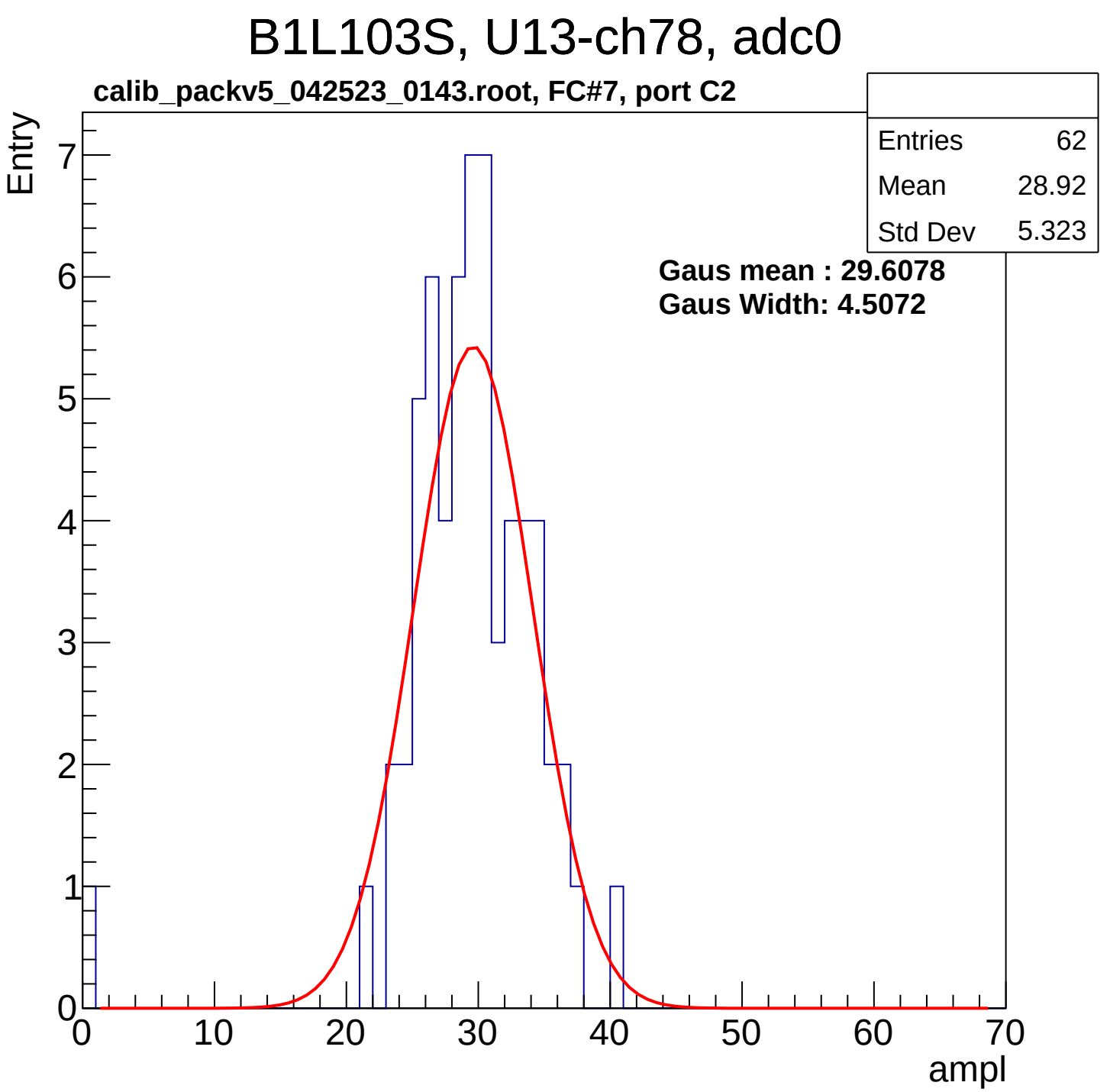
B1L103S, U13-ch78, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28.92
Std Dev 5.323
Gaus mean : 29.6078
Gaus Width: 4.5072
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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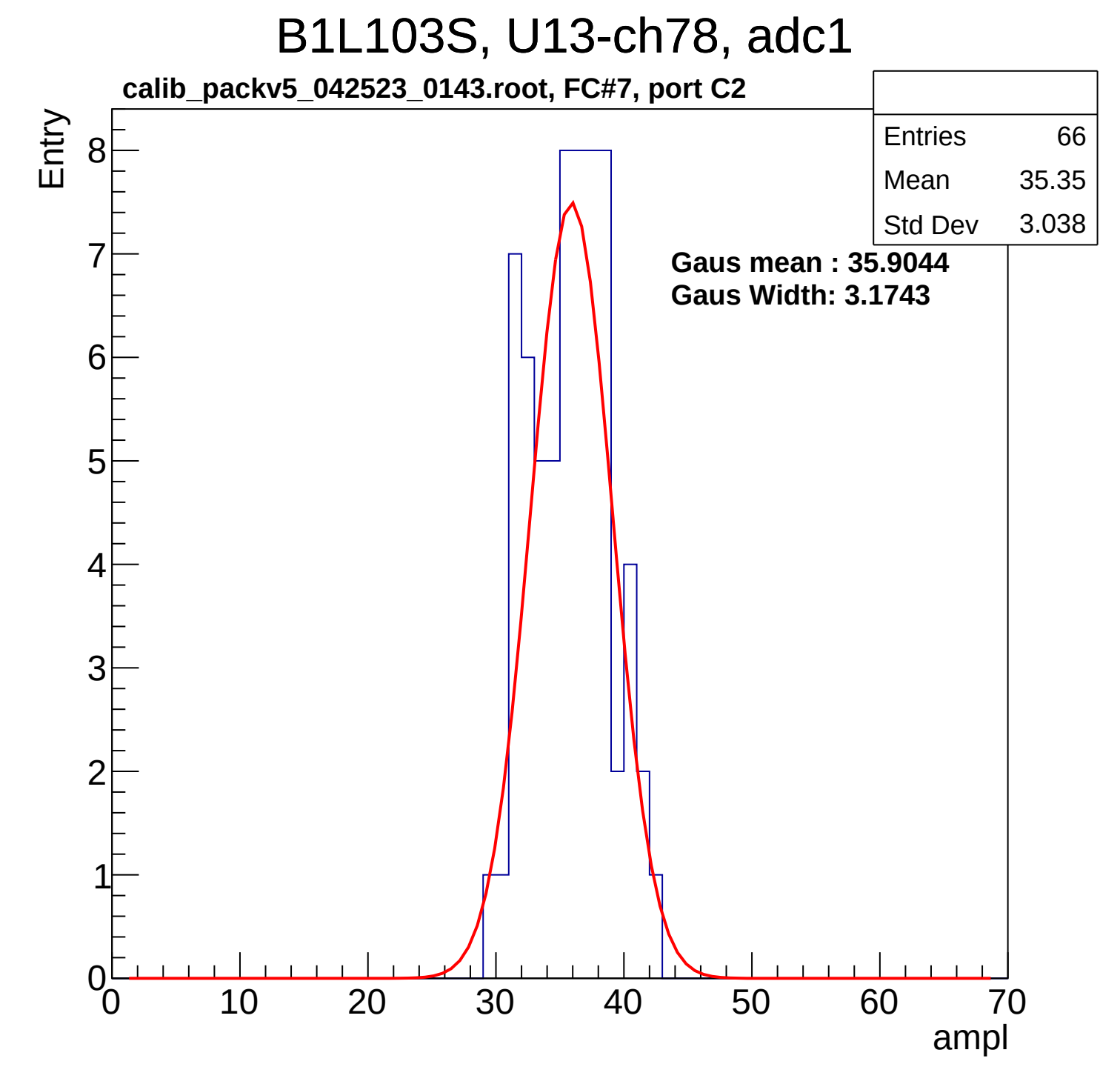
B1L103S, U13-ch78, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 35.35
Std Dev 3.038
Gaus mean : 35.9044
Gaus Width: 3.1743
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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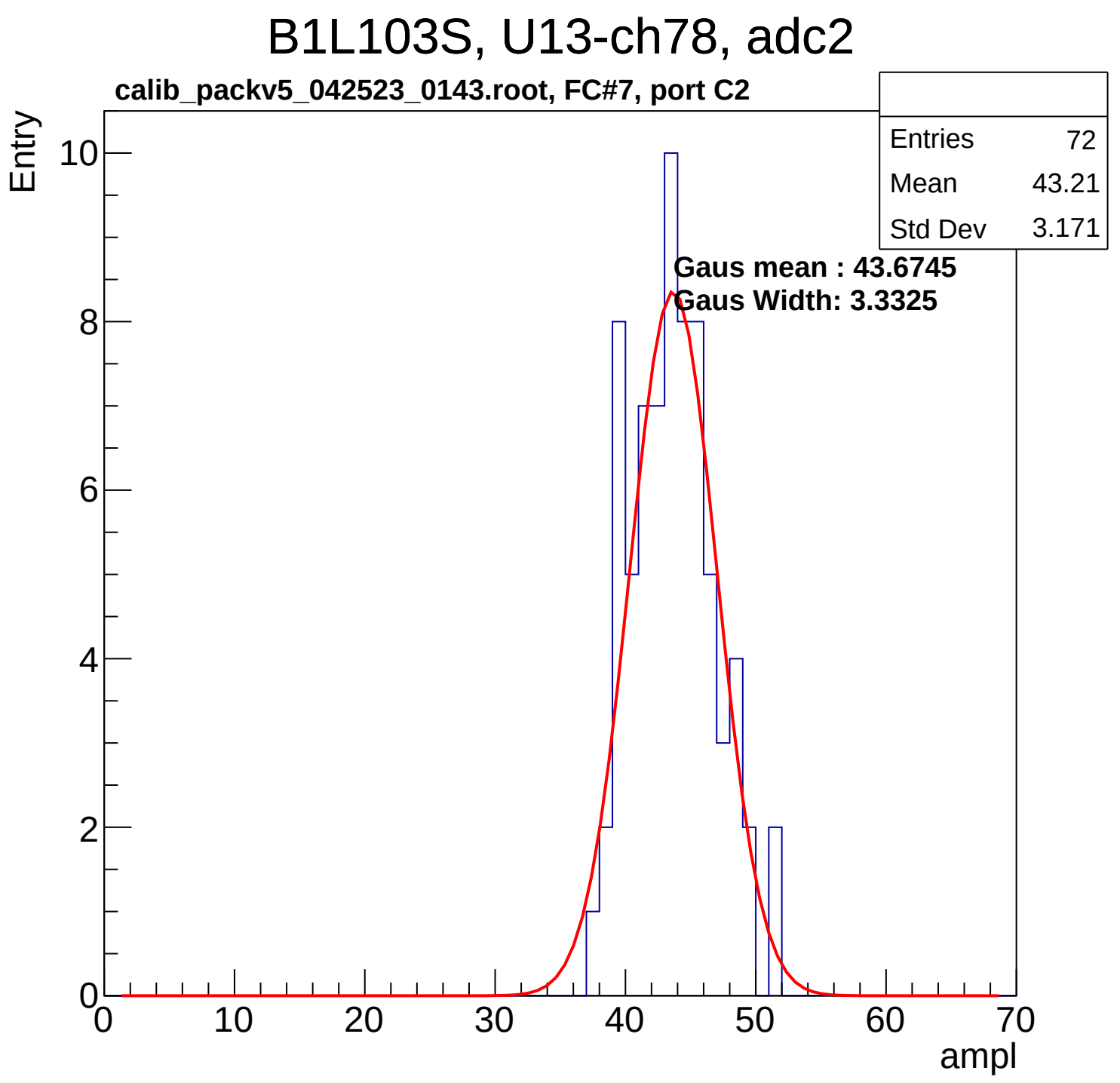
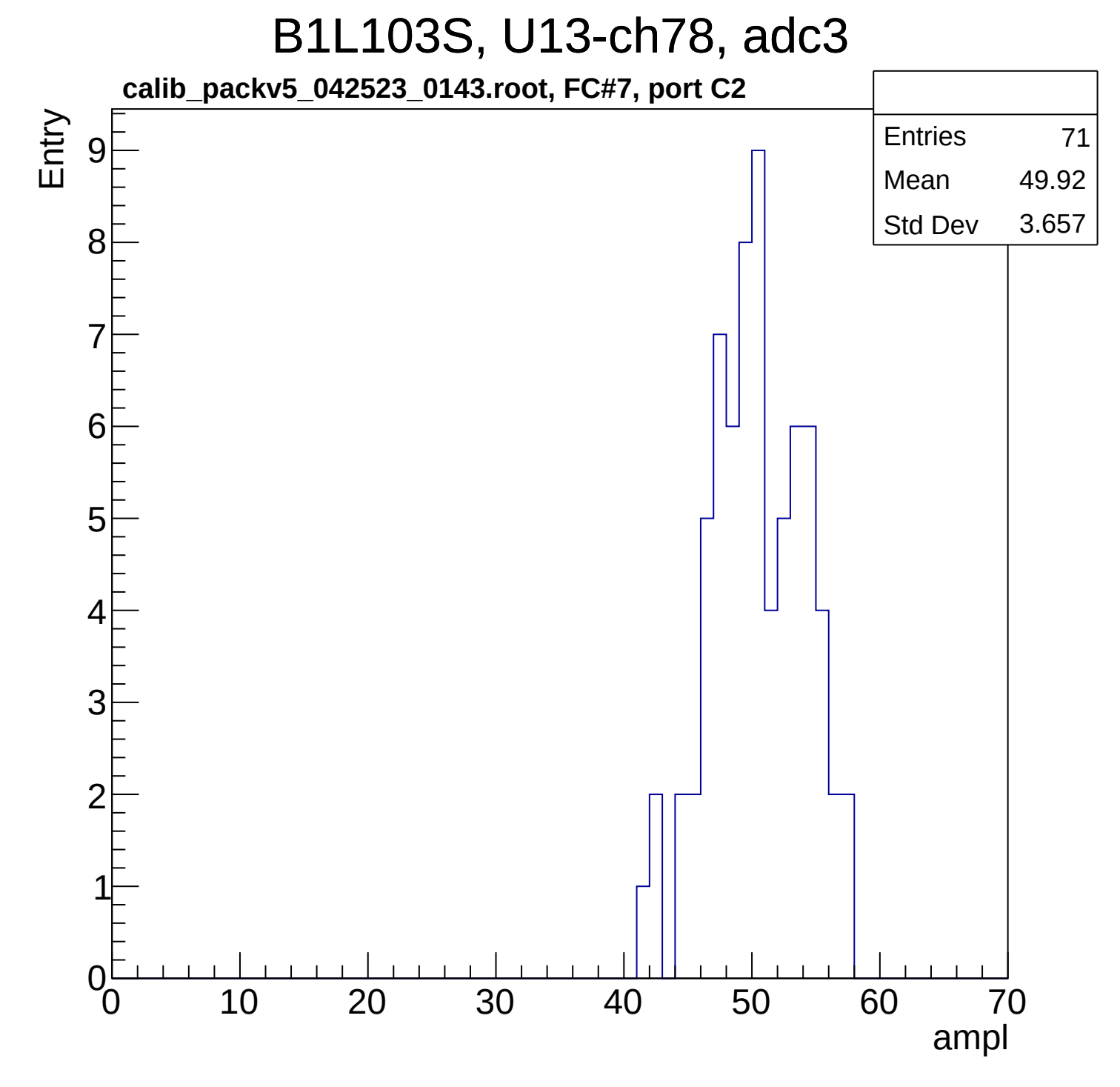

B1L103S, U13-ch78, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.21
Std Dev 3.171
Gaus mean : 43.6745
Gaus Width: 3.3325
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch78, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.92
Std Dev 3.657
Entry
ampl
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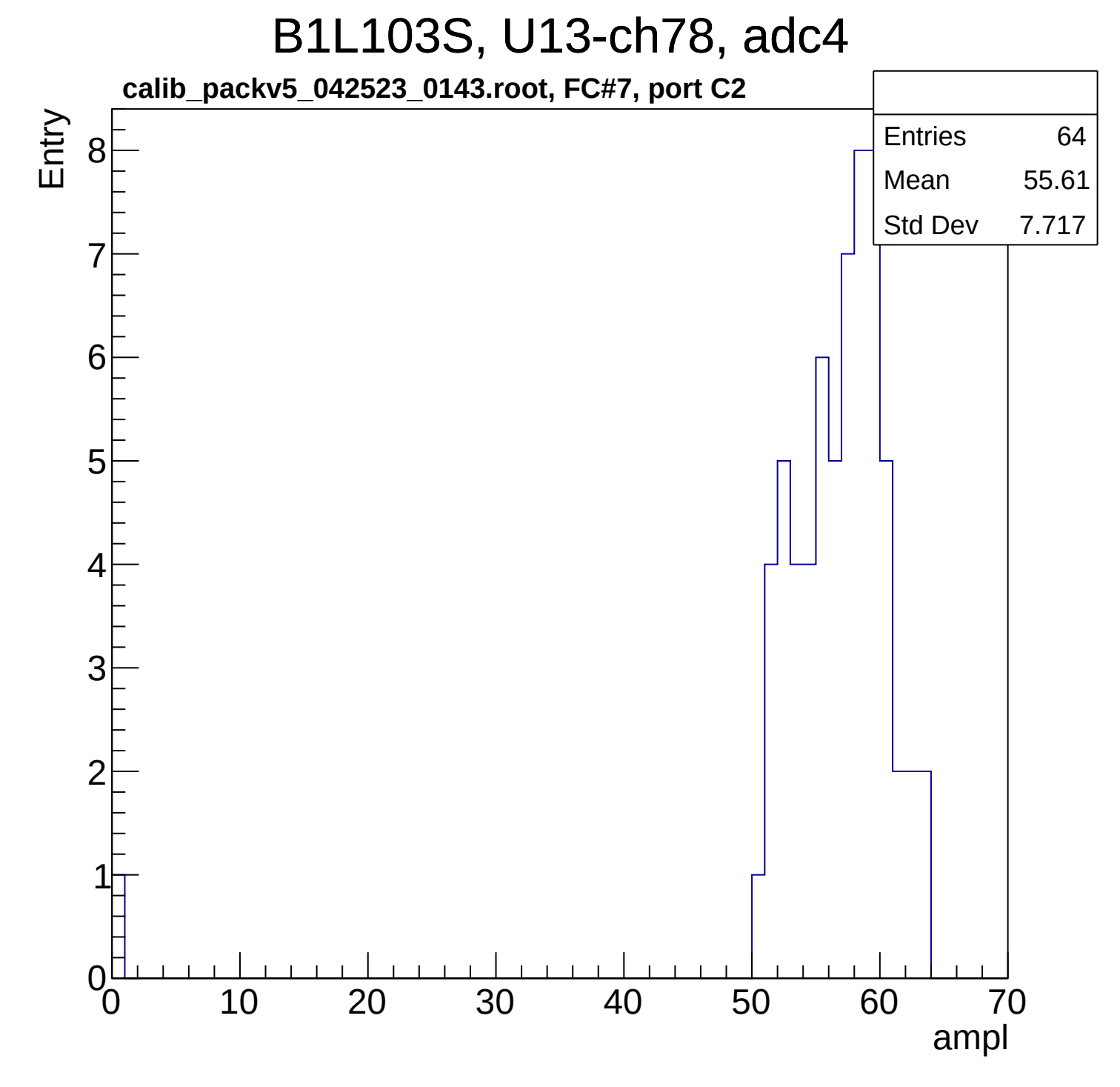
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch78, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.61
Std Dev 7.717
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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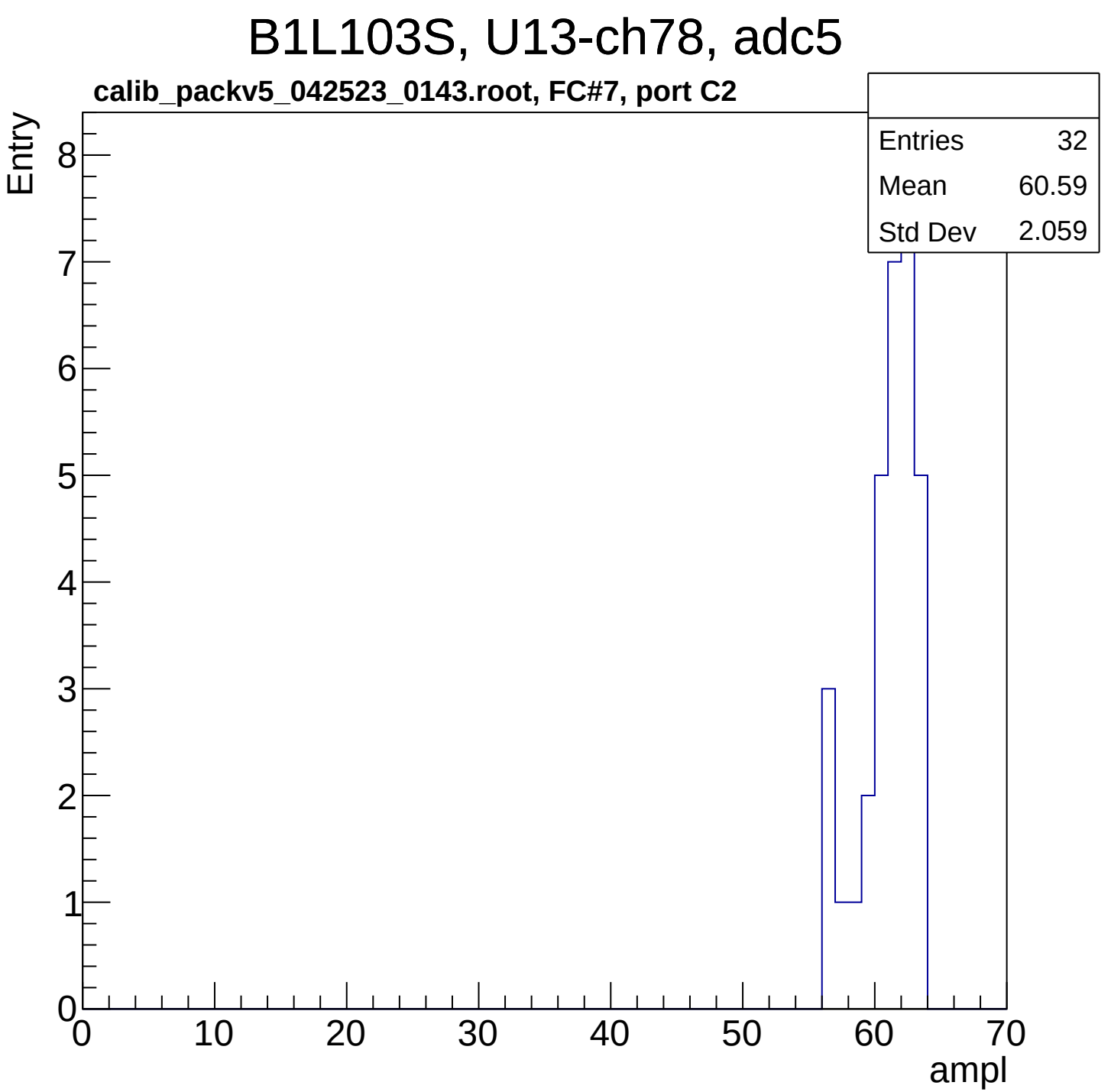
B1L103S, U13-ch78, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 60.59
Std Dev 2.059
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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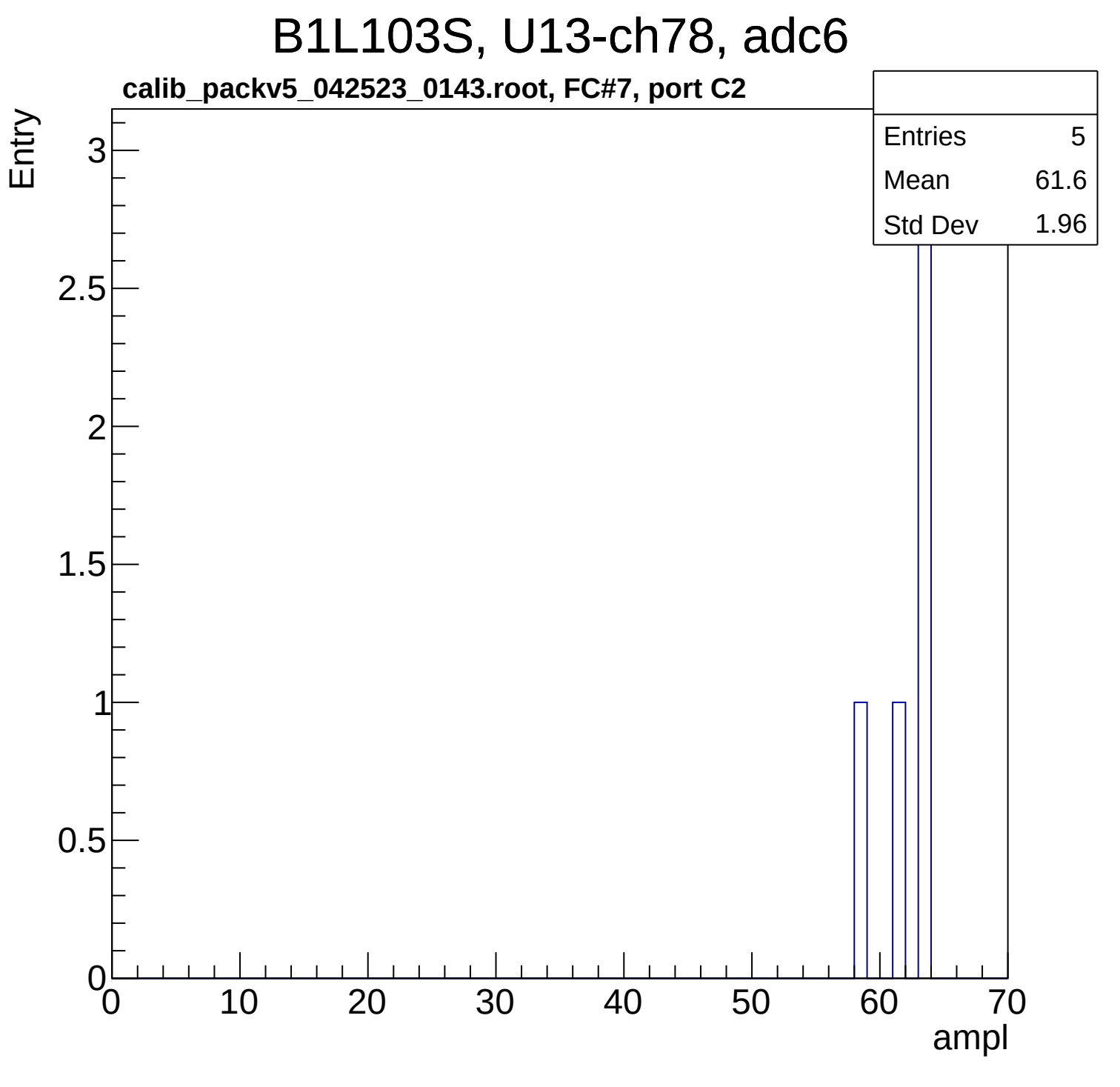
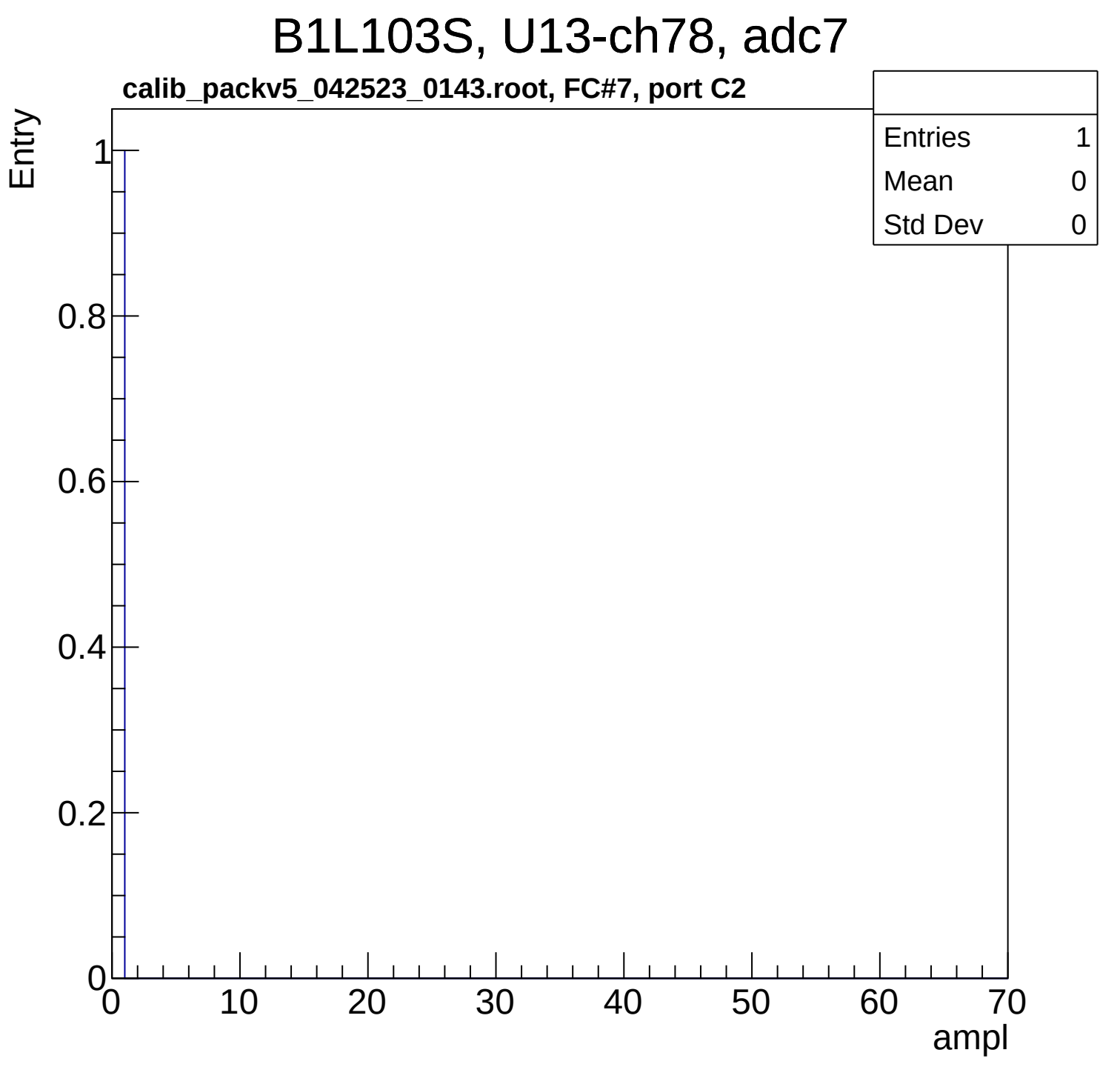
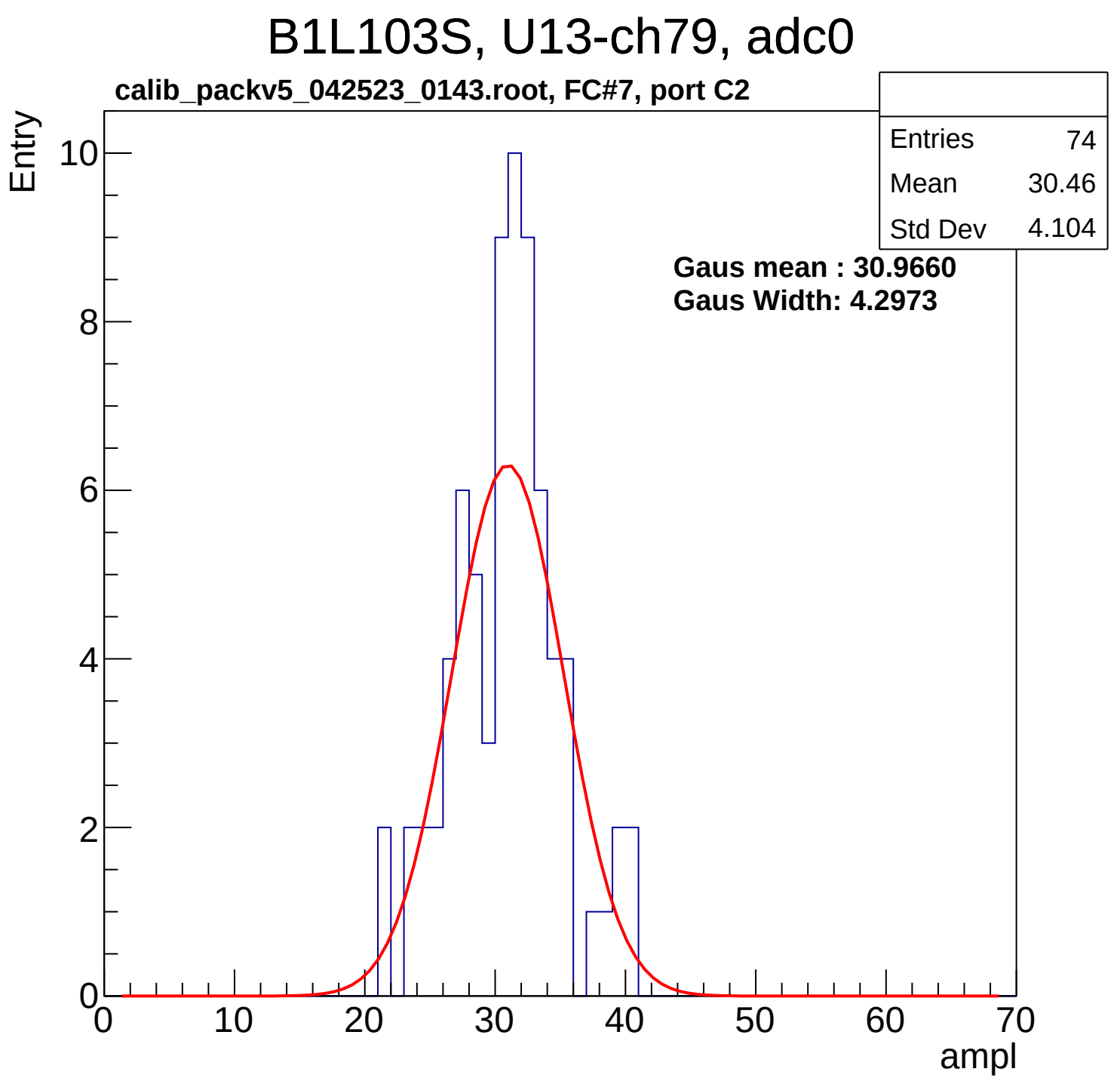

B1L103S, U13-ch78, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.6
Std Dev 1.96
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch78, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch79, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.46
Std Dev 4.104
Gaus mean : 30.9660
Gaus Width: 4.2973
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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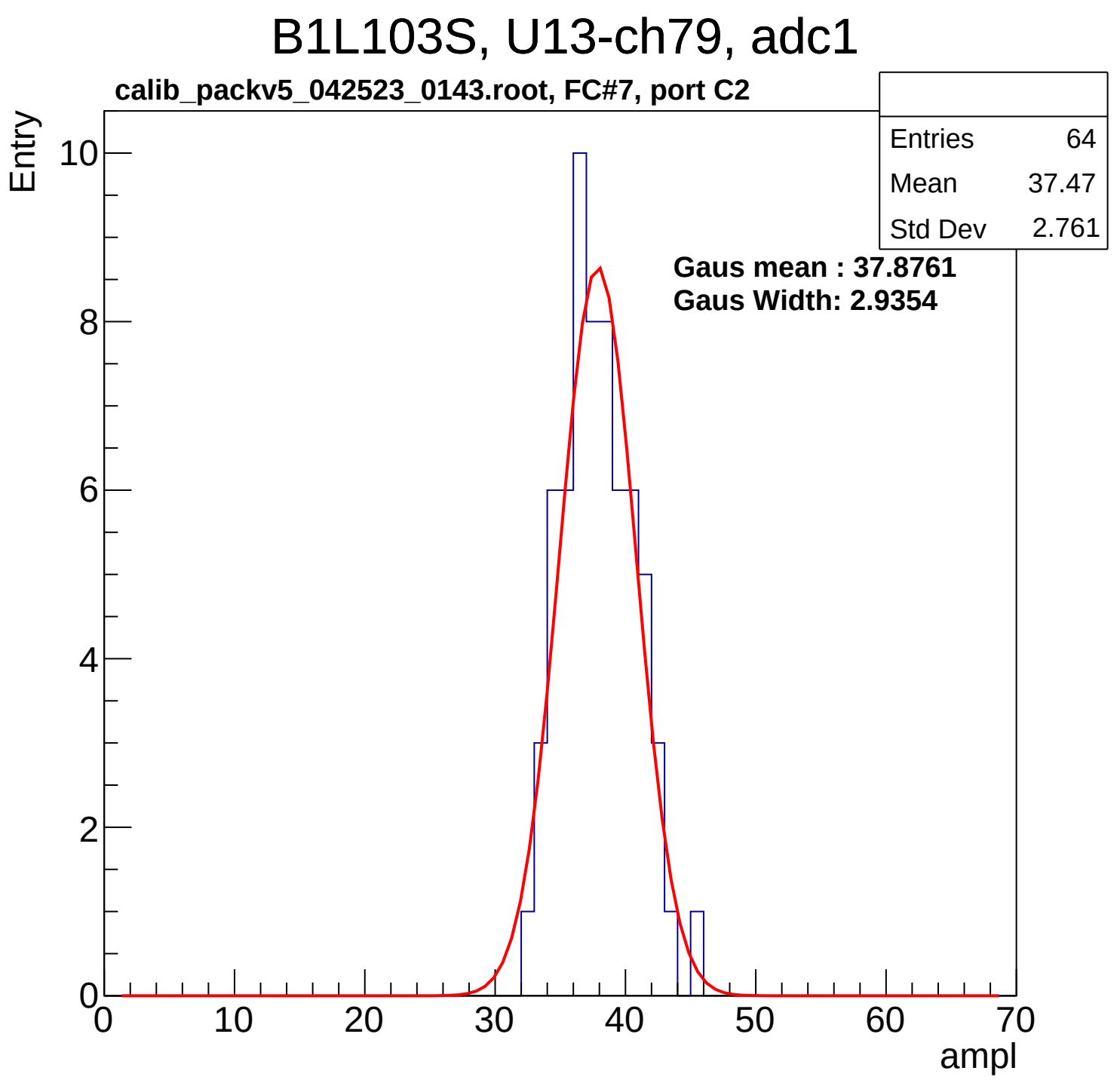
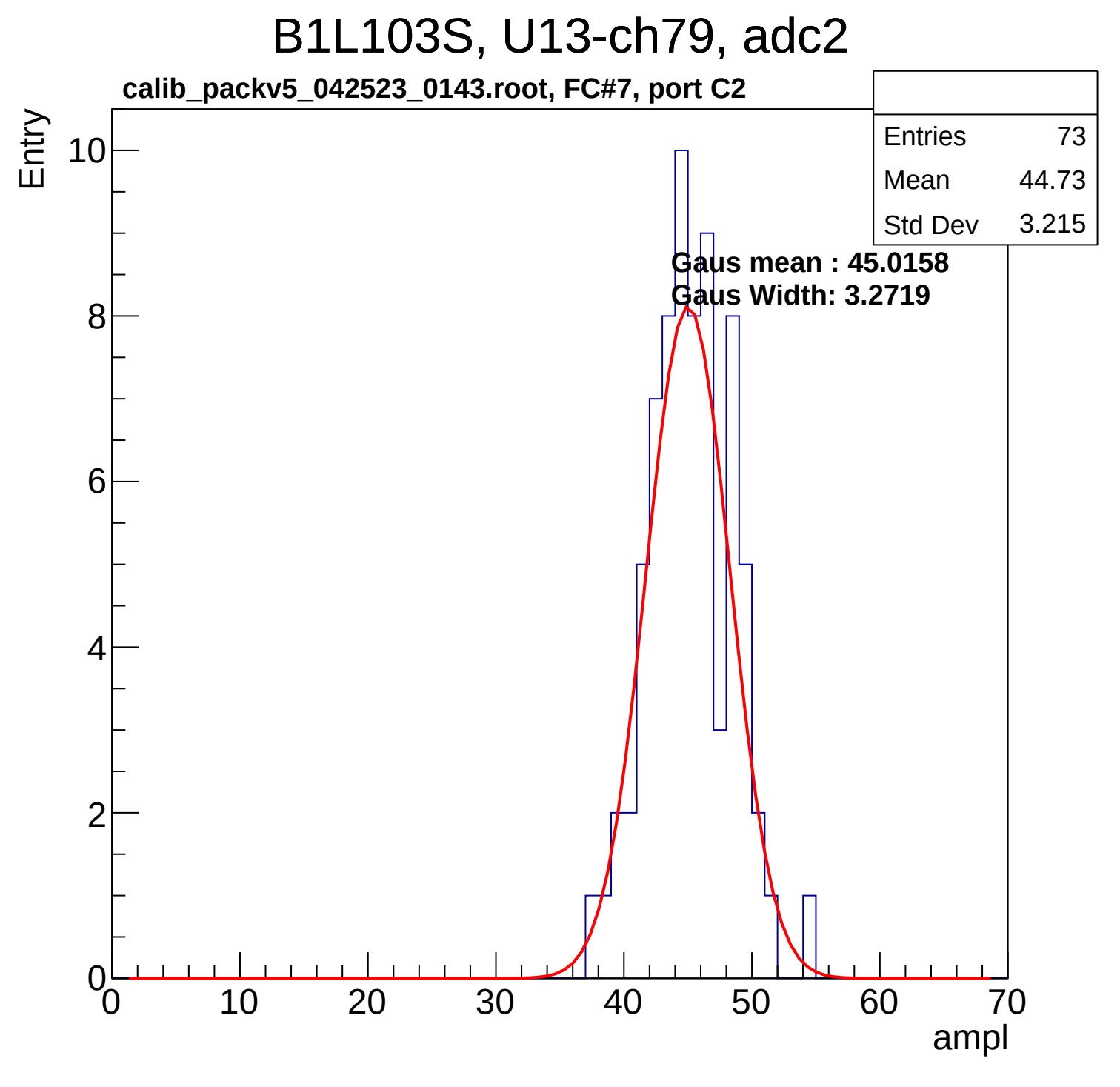
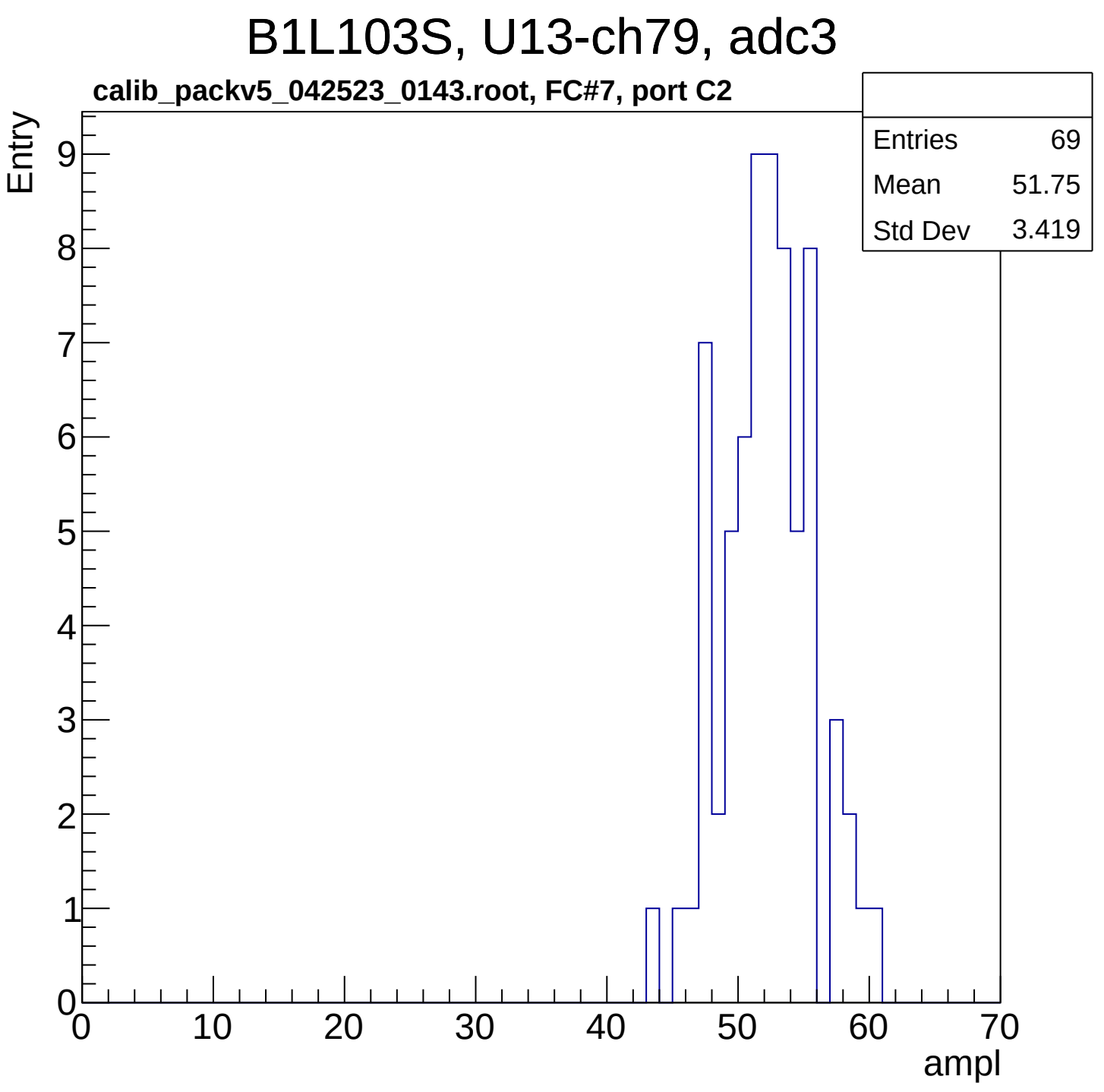

B1L103S, U13-ch79, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 37.47
Std Dev 2.761
Gaus mean : 37.8761
Gaus Width: 2.9354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch79, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 44.73
Std Dev 3.215
Gaus mean : 45.0158
Gaus Width: 3.2719
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch79, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 51.75
Std Dev 3.419
Entry
ampl
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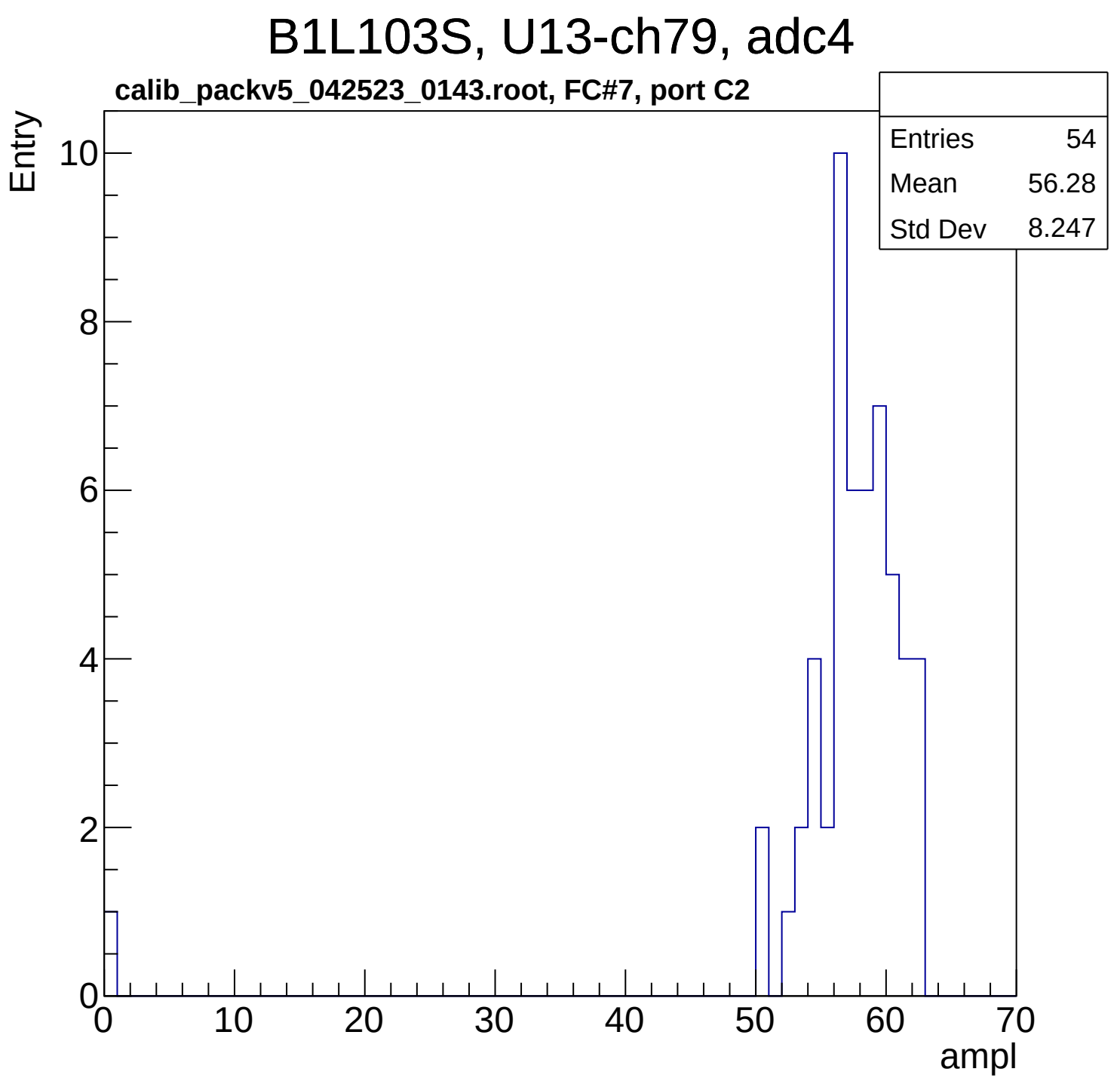
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch79, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.28
Std Dev 8.247
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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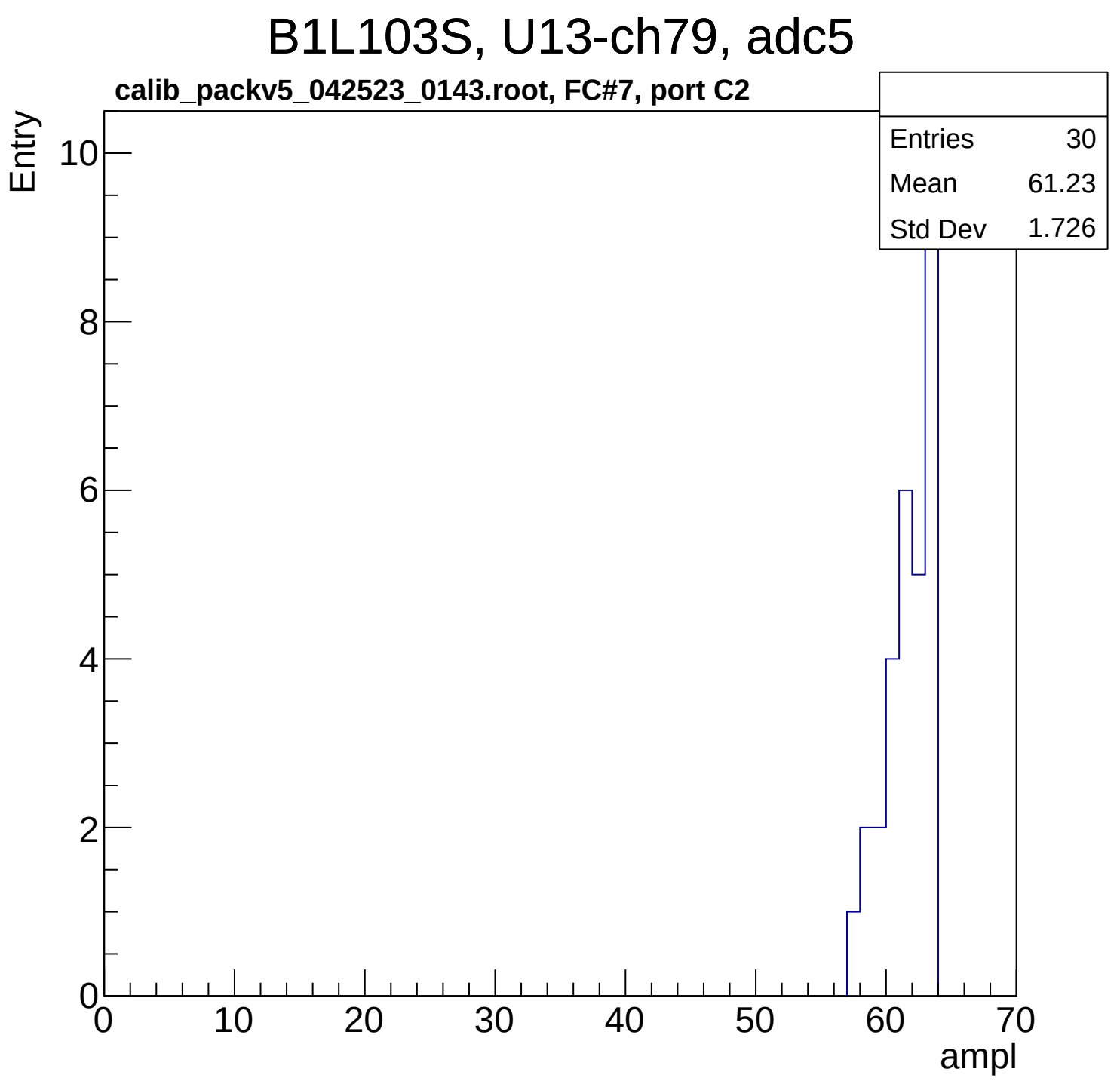
B1L103S, U13-ch79, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 61.23
Std Dev 1.726
Entry
0
2
4
6
8
10
0
10
20
30
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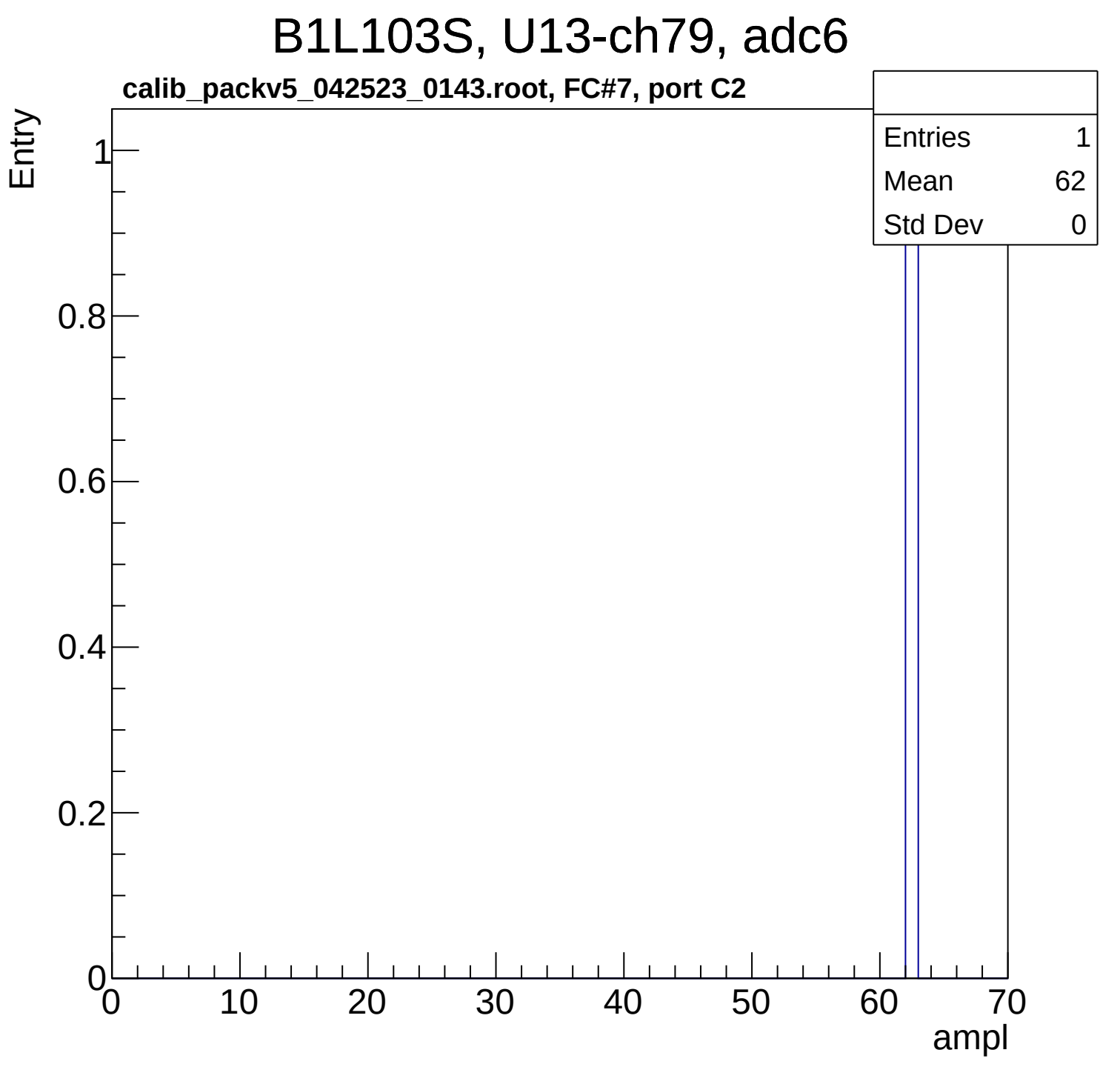
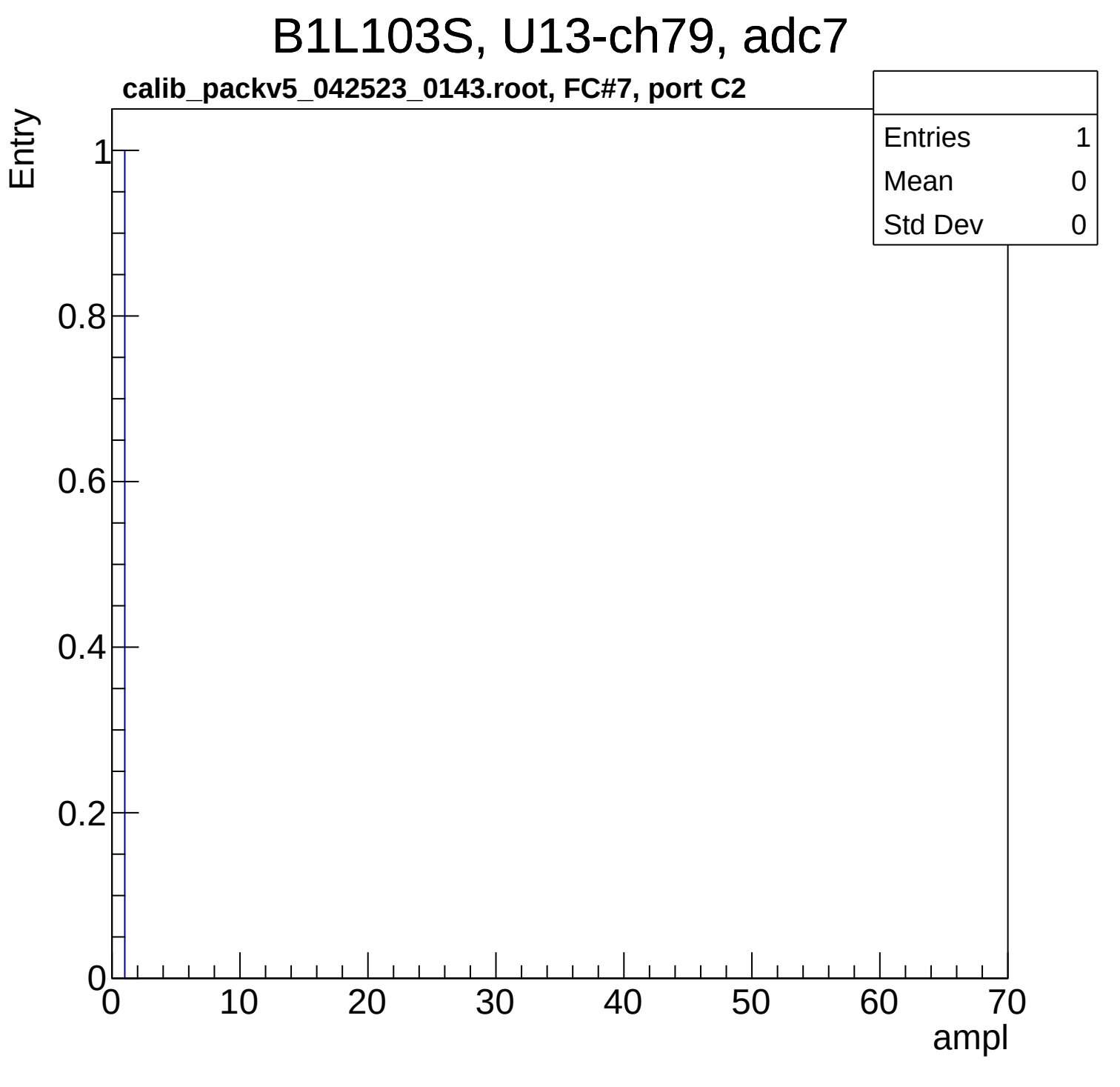
50
60
70
ampl


B1L103S, U13-ch79, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch79, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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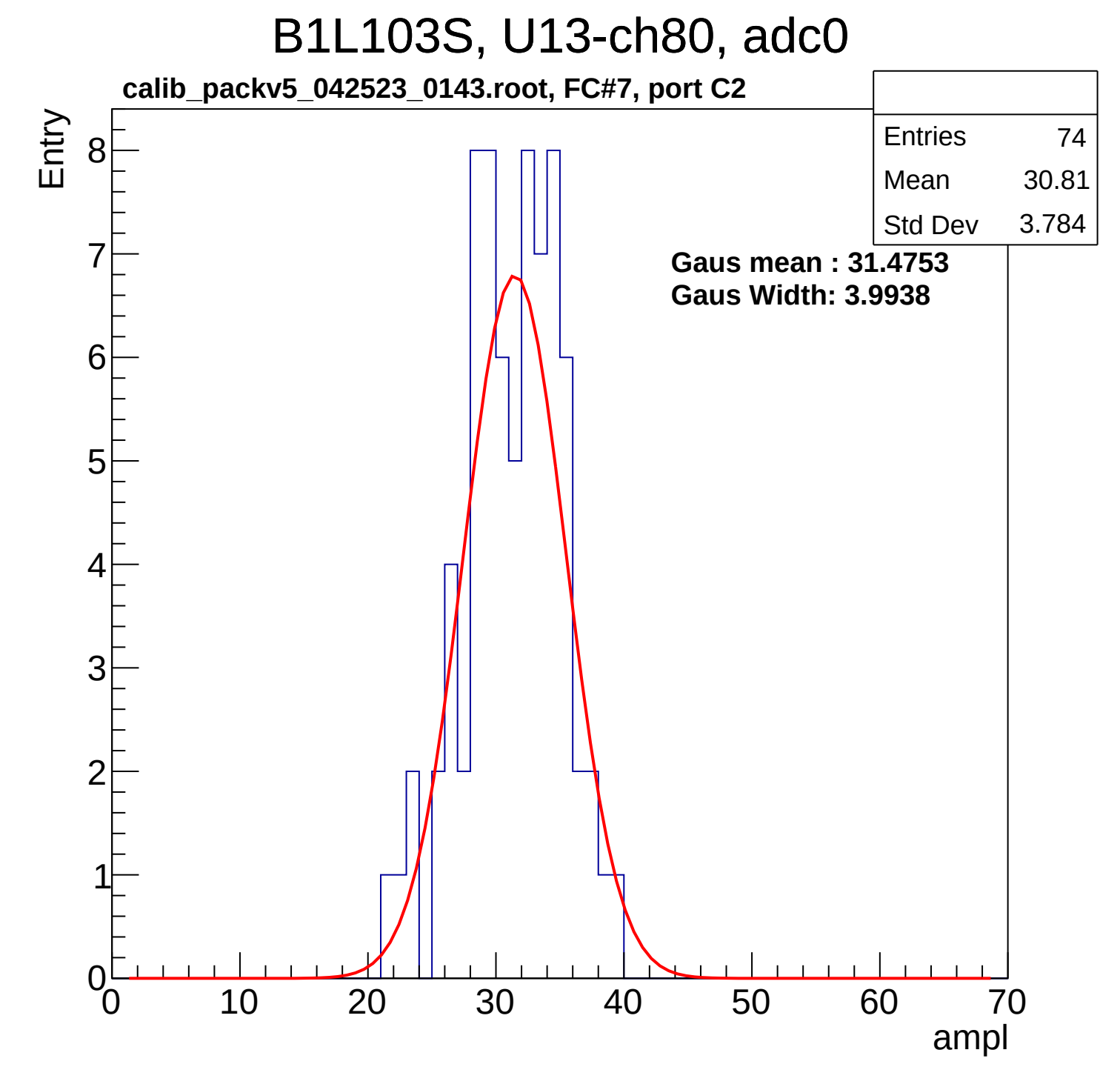
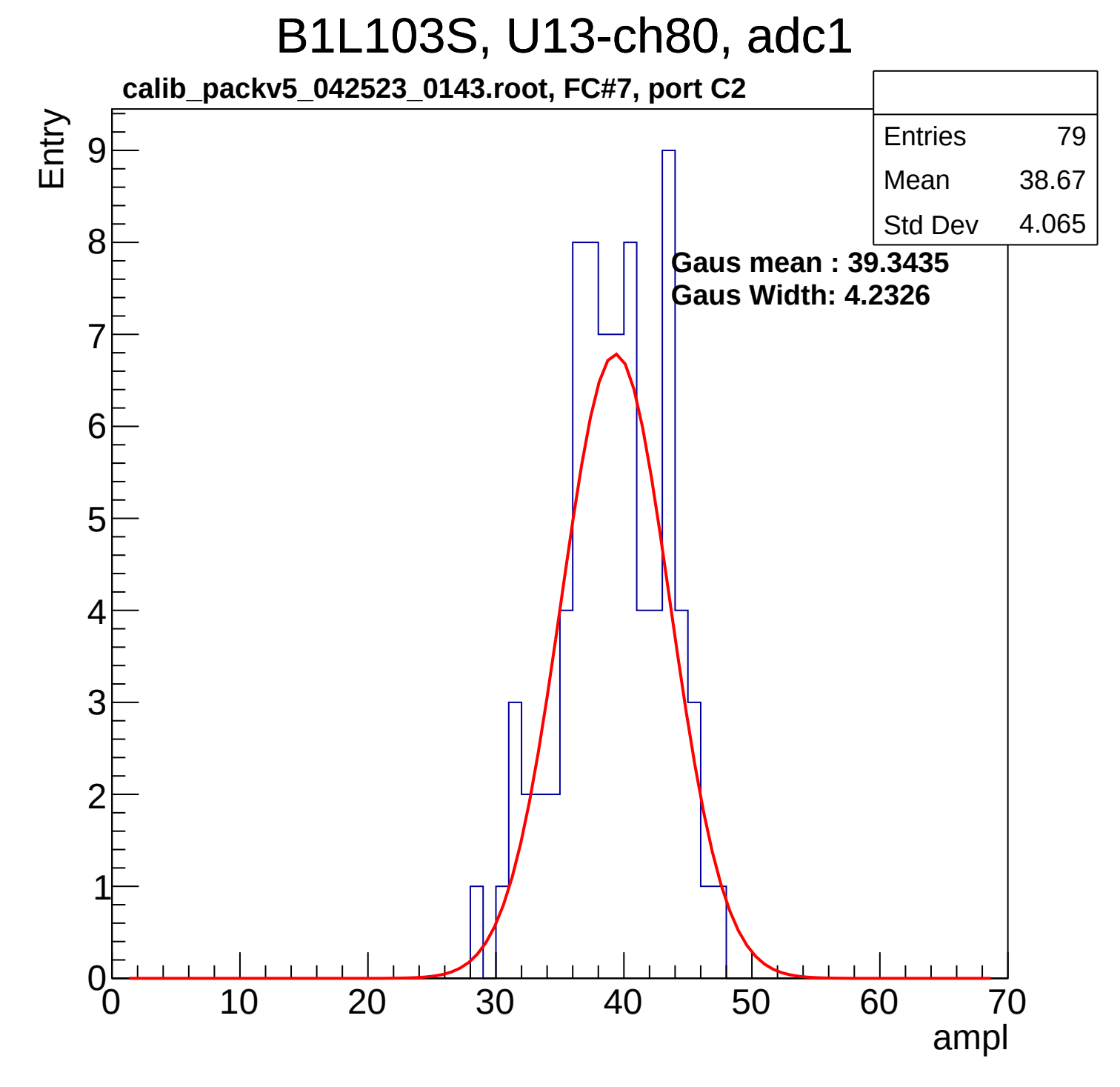
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch80, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.81
Std Dev 3.784
Gaus mean : 31.4753
Gaus Width: 3.9938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch80, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 38.67
Std Dev 4.065
Gaus mean : 39.3435
Gaus Width: 4.2326
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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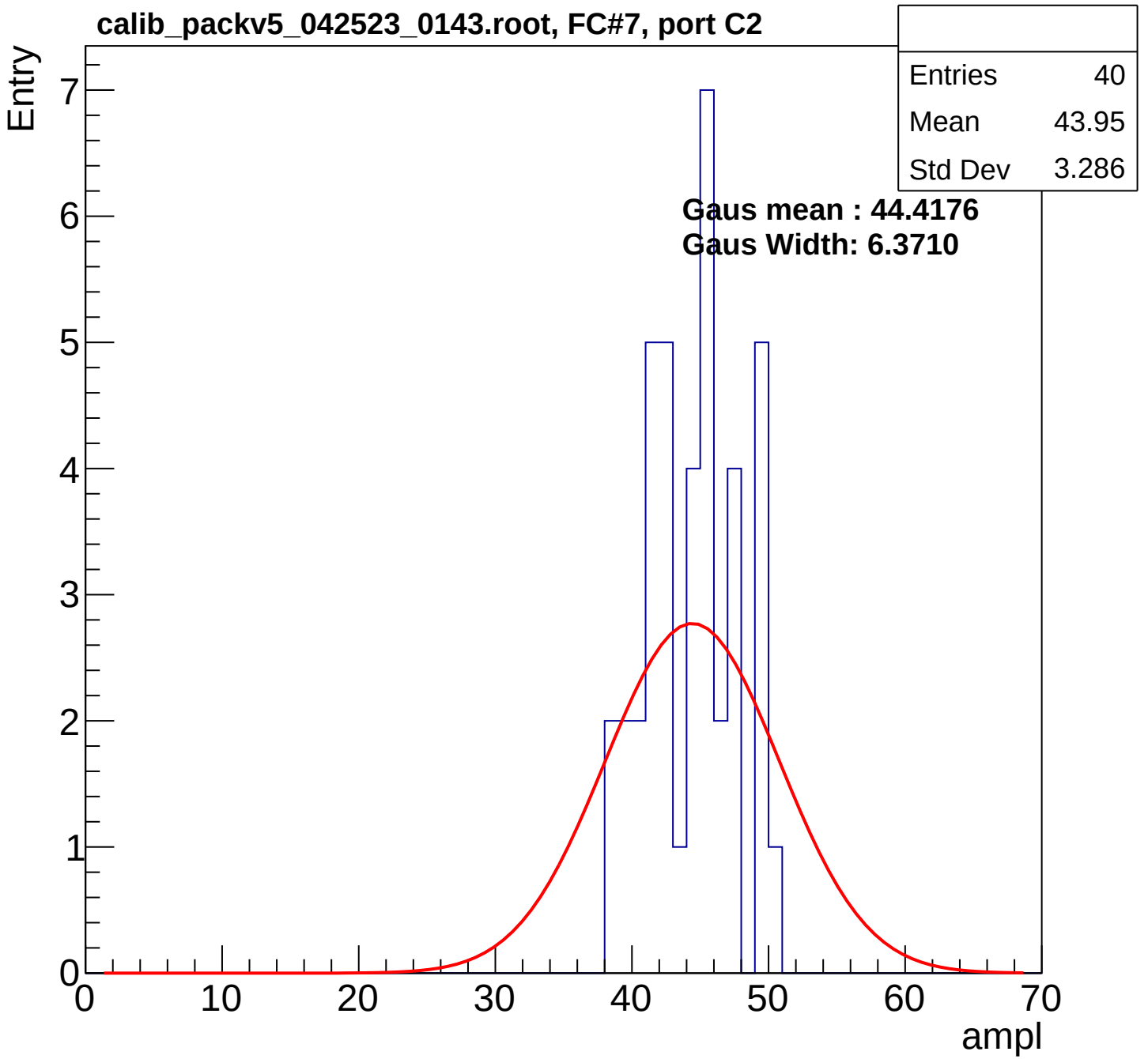
B1L103S, U13-ch80, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 43.95
Std Dev 3.286
Gaus mean : 44.4176
Gaus Width: 6.3710
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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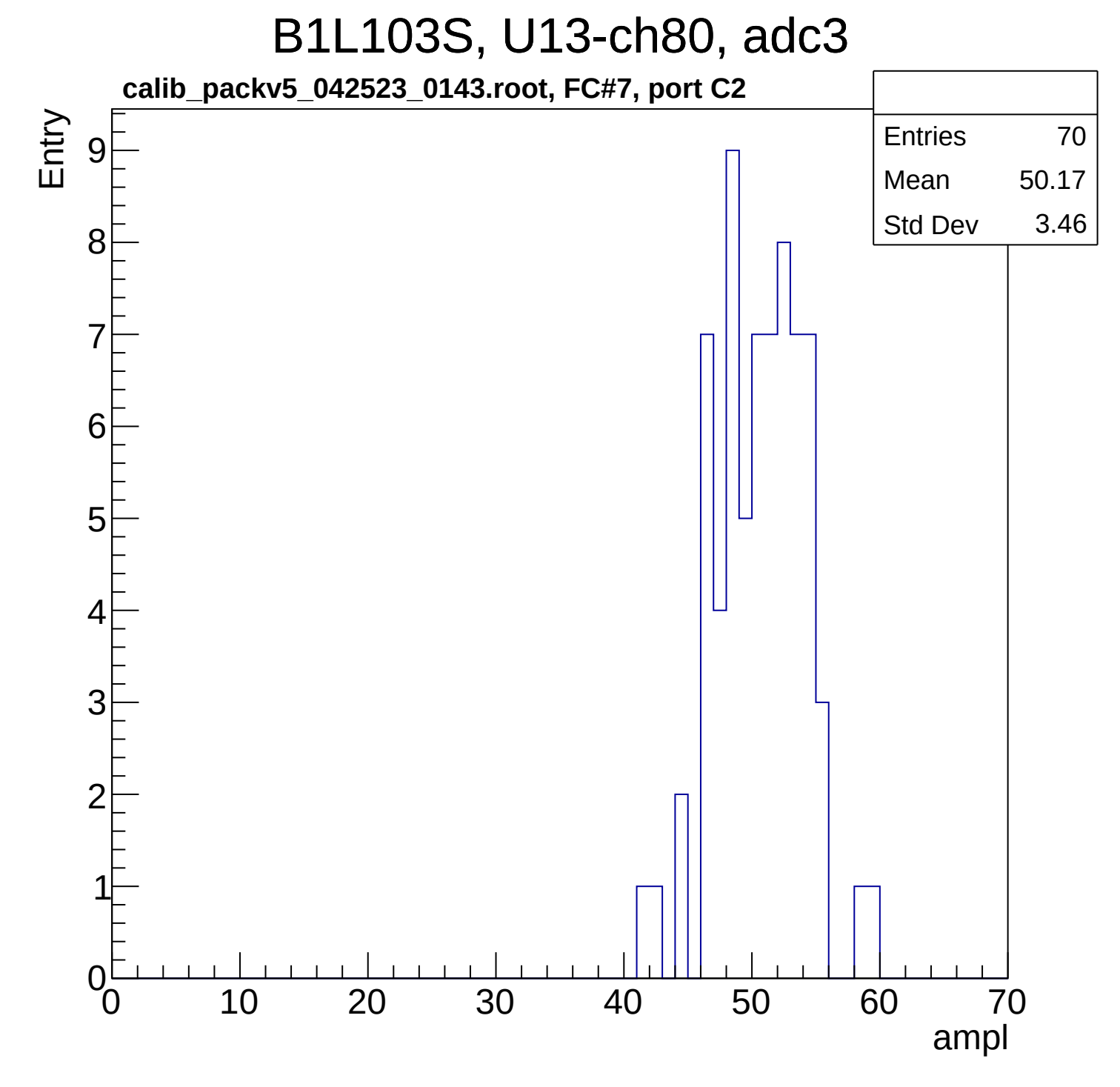
B1L103S, U13-ch80, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 50.17
Std Dev 3.46
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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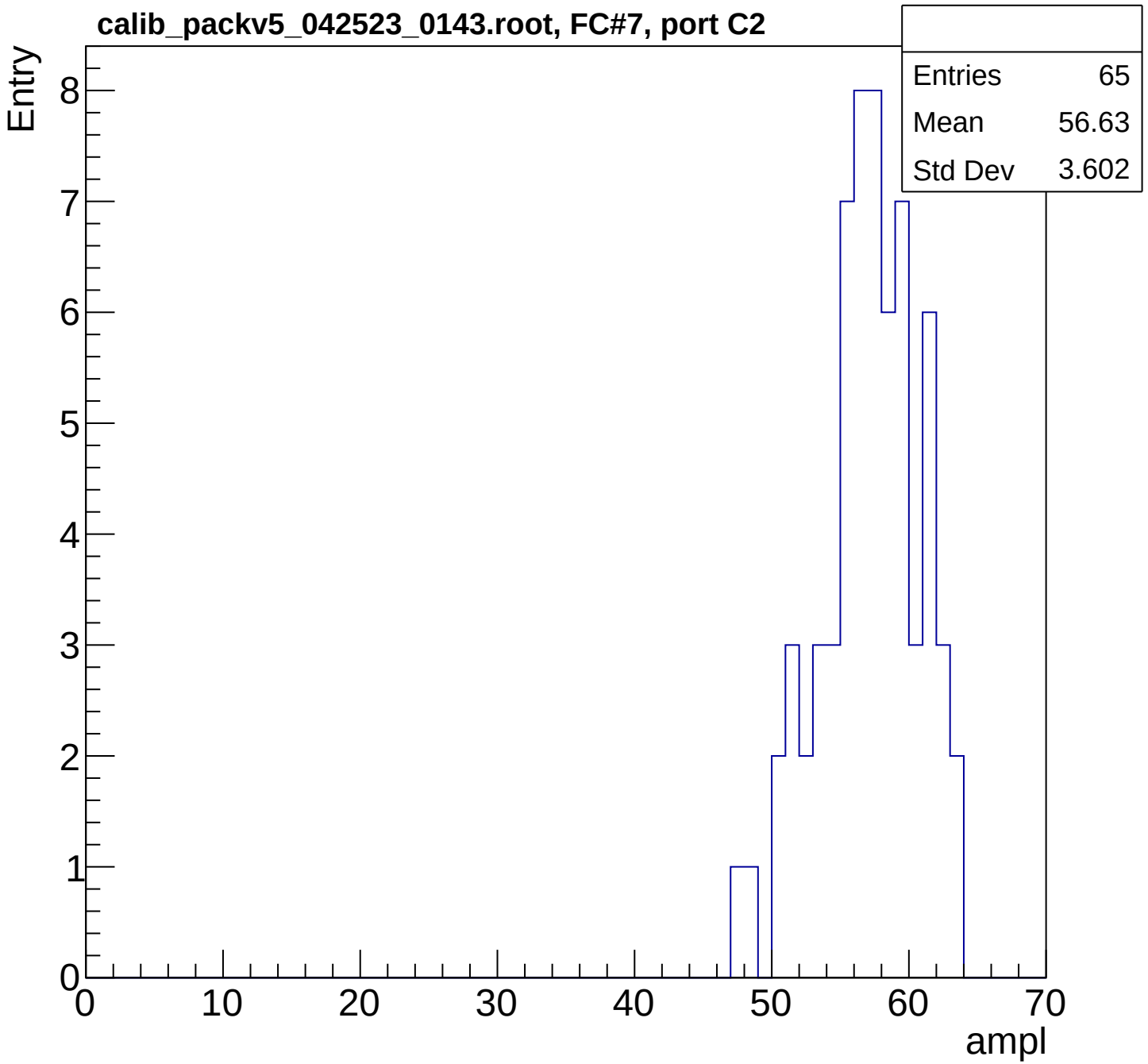
B1L103S, U13-ch80, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 56.63
Std Dev 3.602
Entry
0
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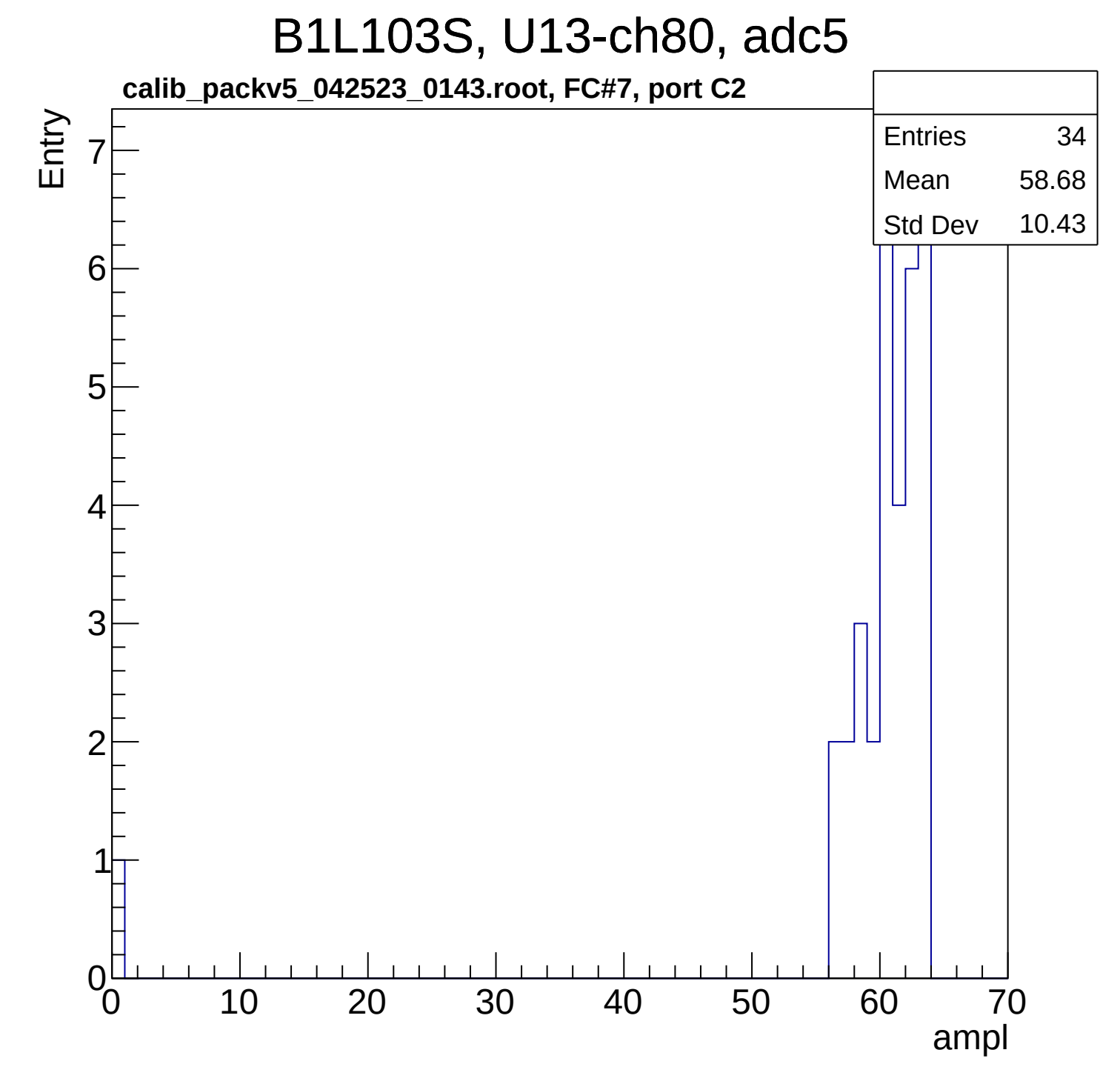
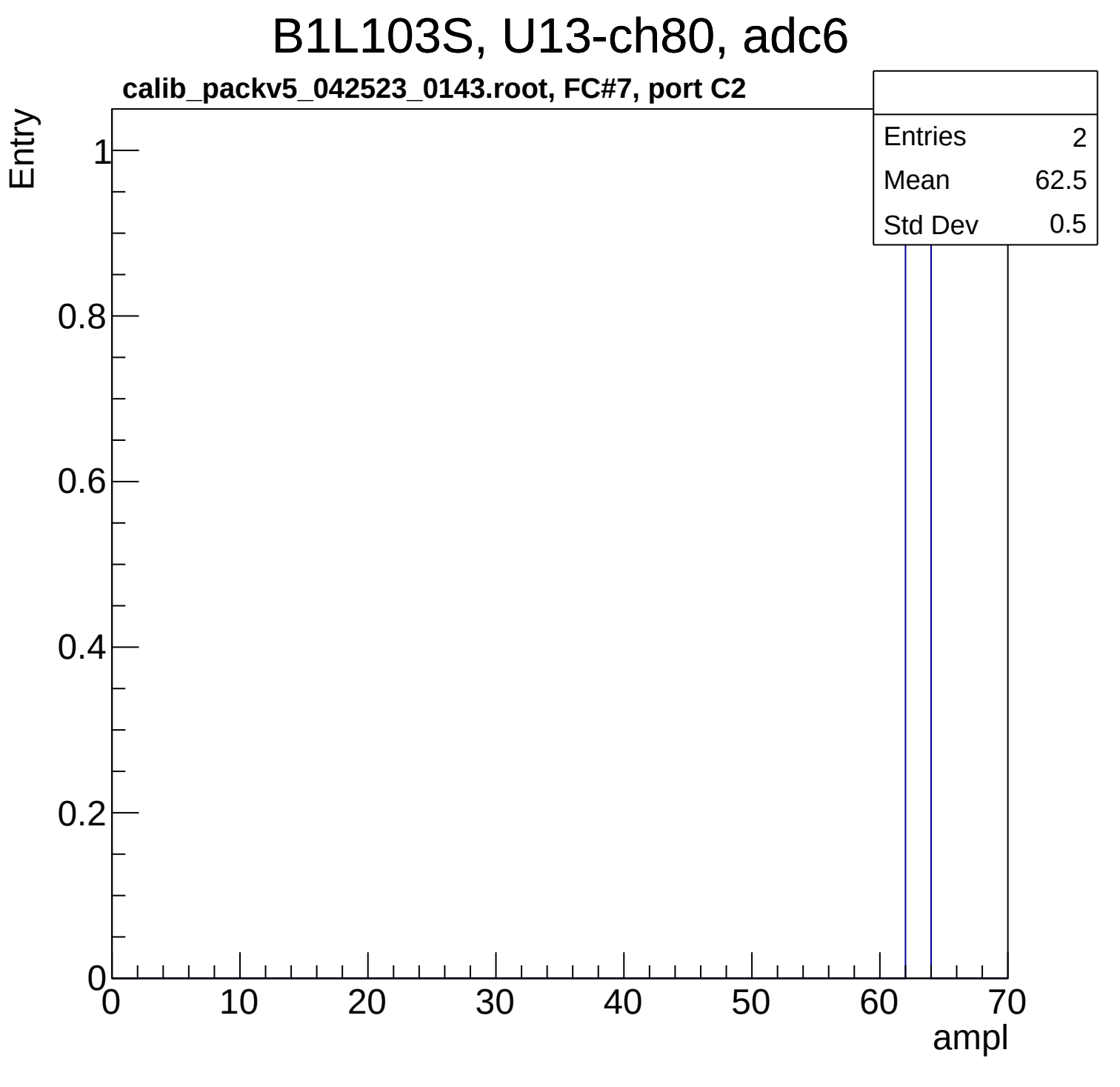
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch80, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.68
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch80, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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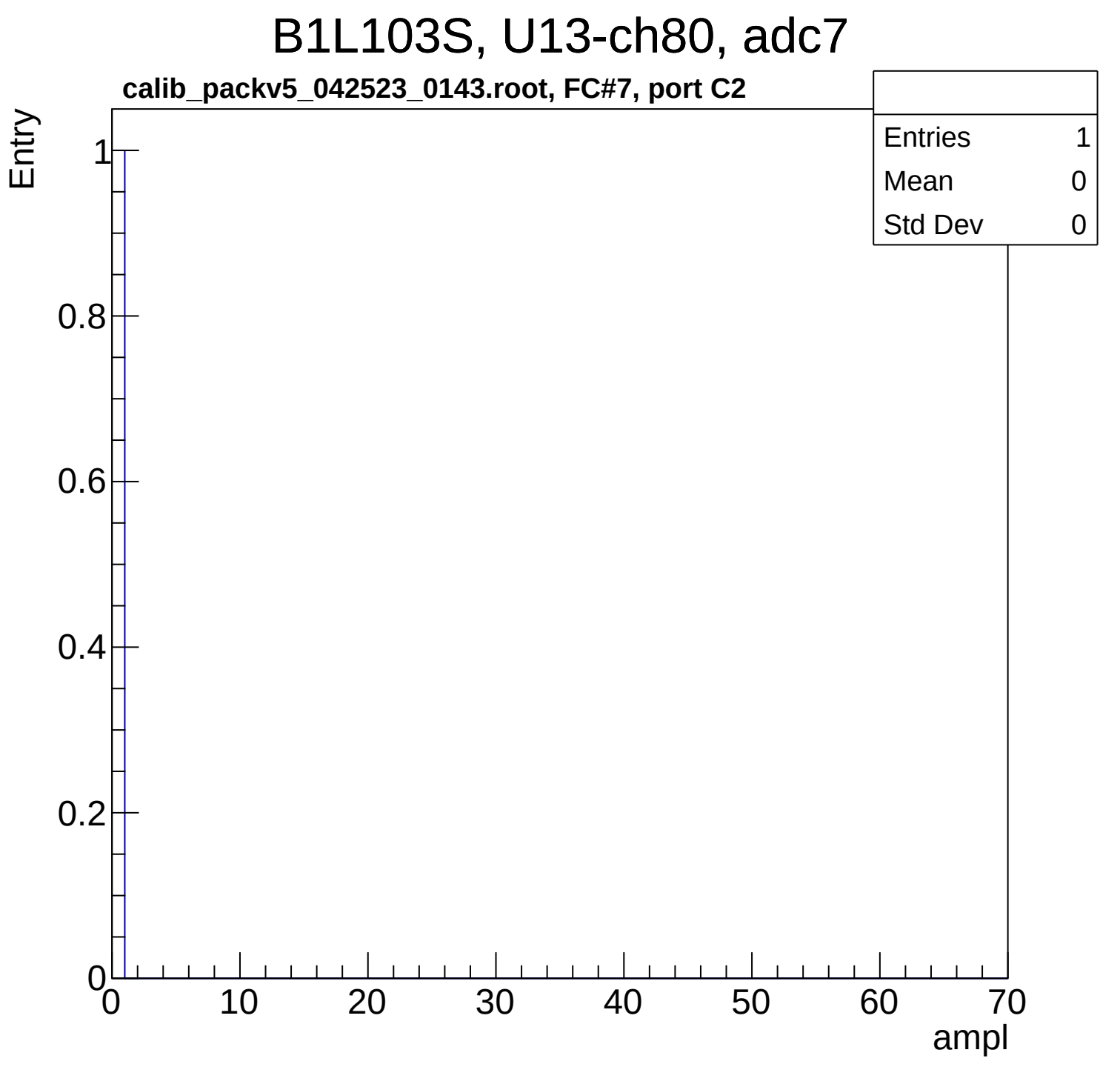
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch80, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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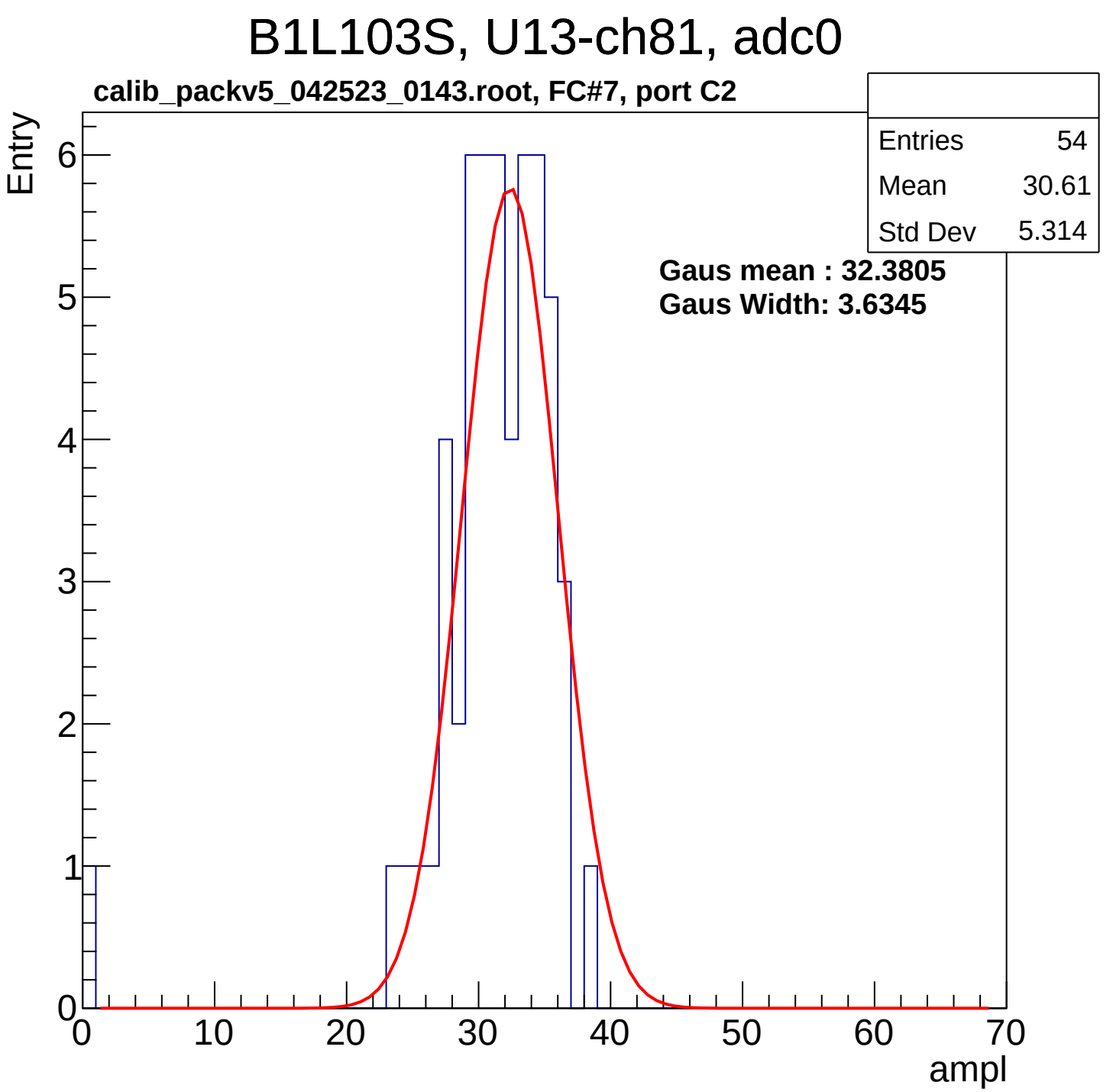
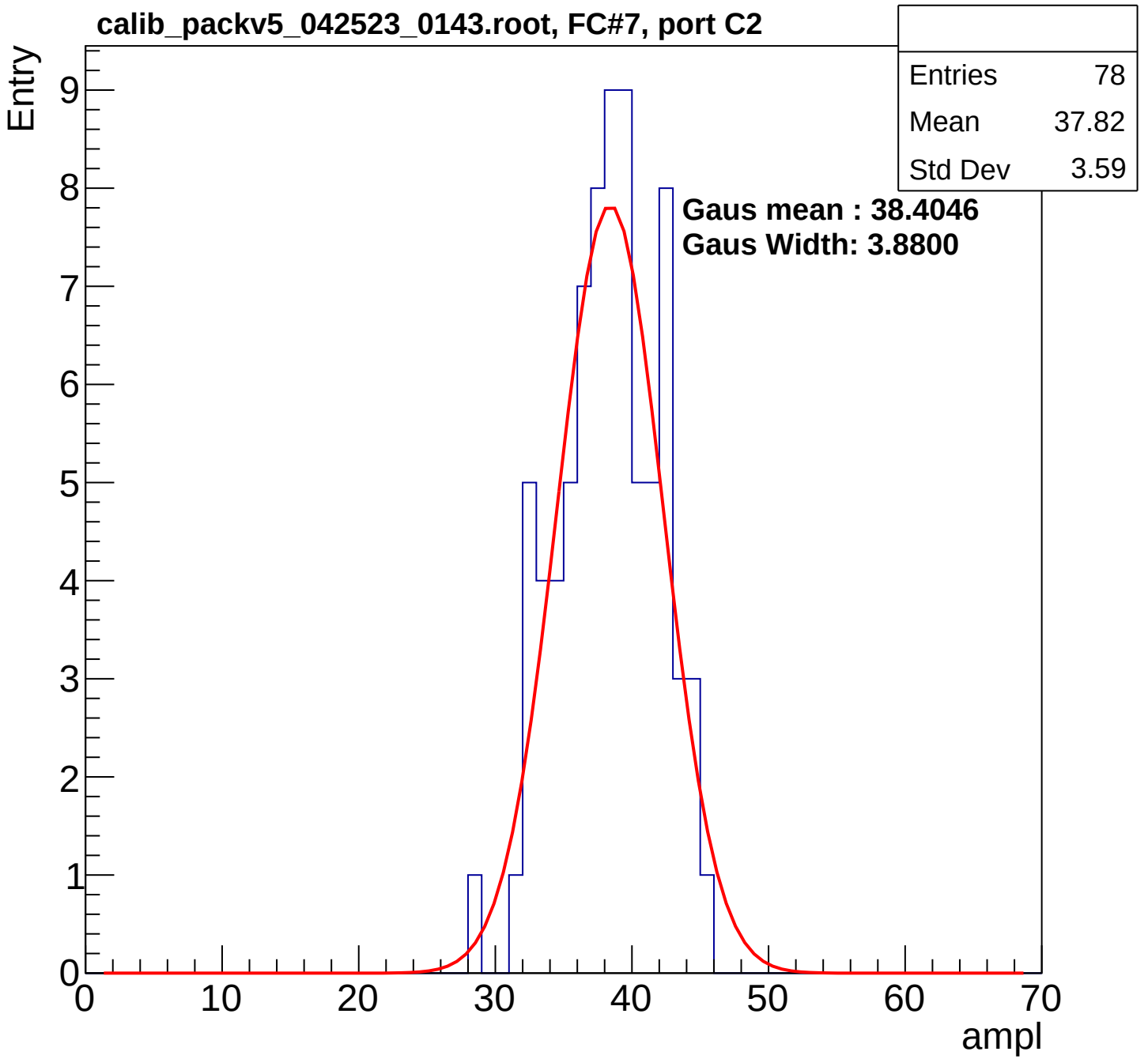
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch81, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 30.61
Std Dev 5.314
Gaus mean : 32.3805
Gaus Width: 3.6345
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch81, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 37.82
Std Dev 3.59
Gaus mean : 38.4046
Gaus Width: 3.8800
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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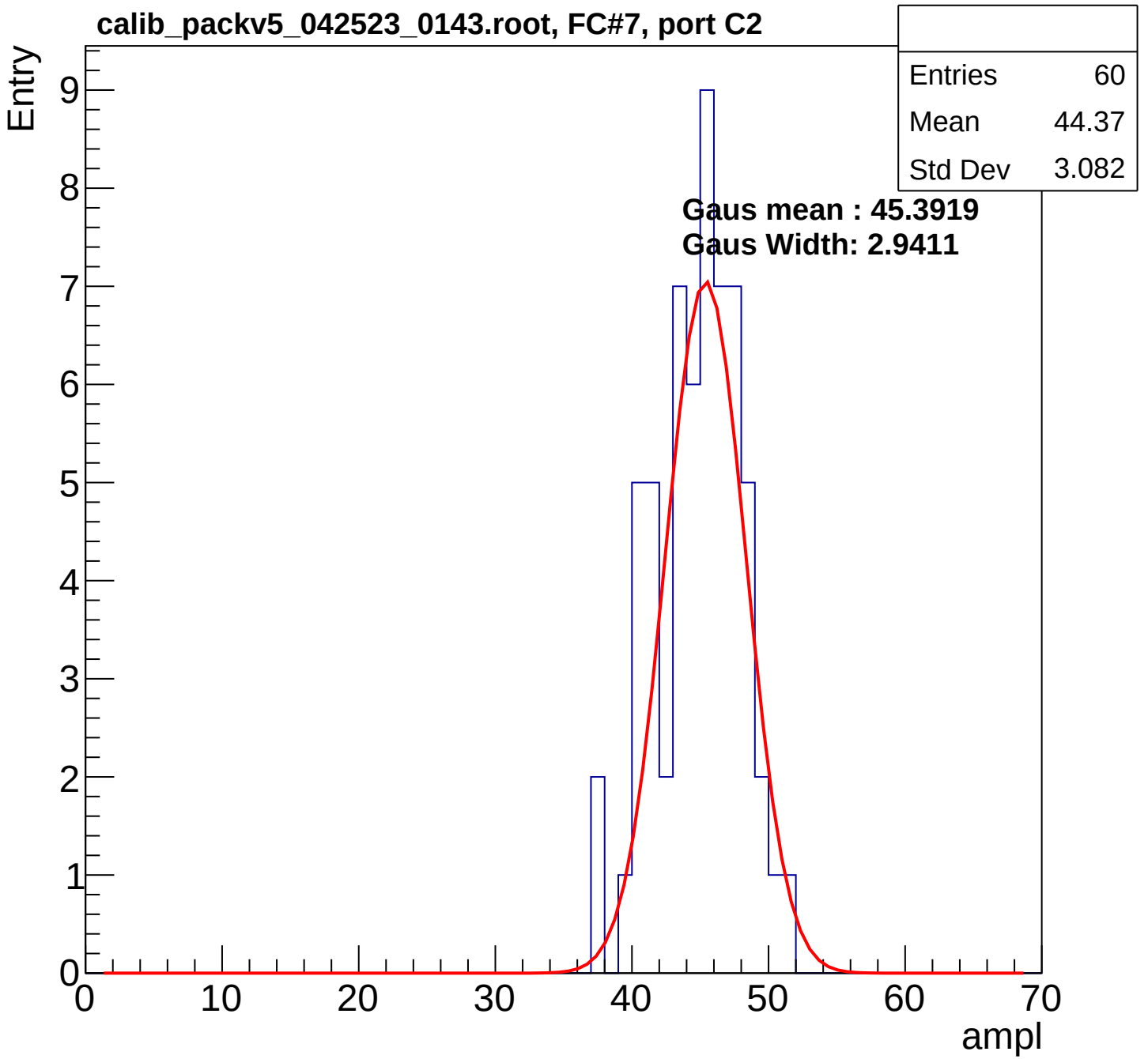
B1L103S, U13-ch81, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 44.37
Std Dev 3.082
Gaus mean : 45.3919
Gaus Width: 2.9411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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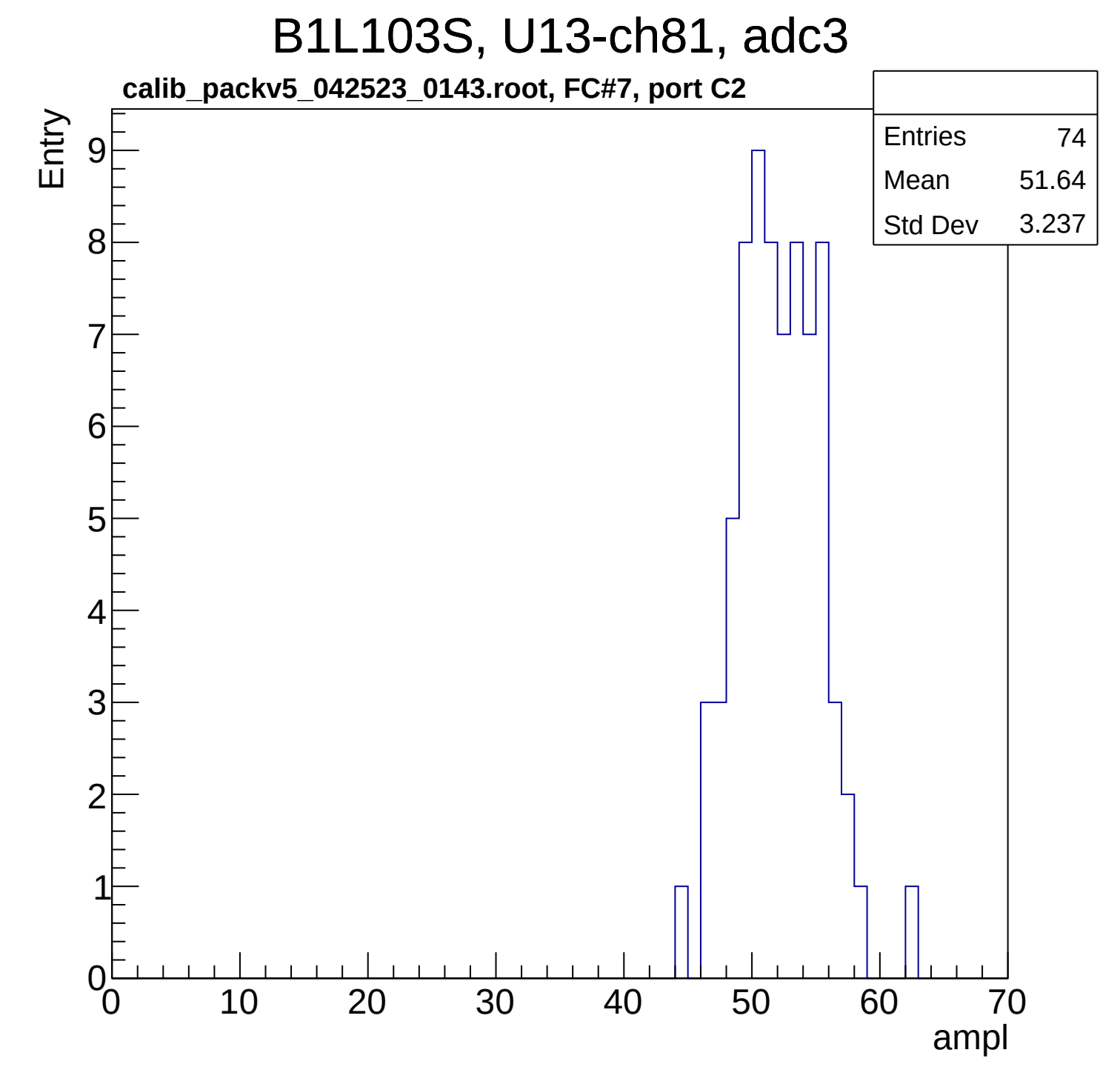
B1L103S, U13-ch81, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 51.64
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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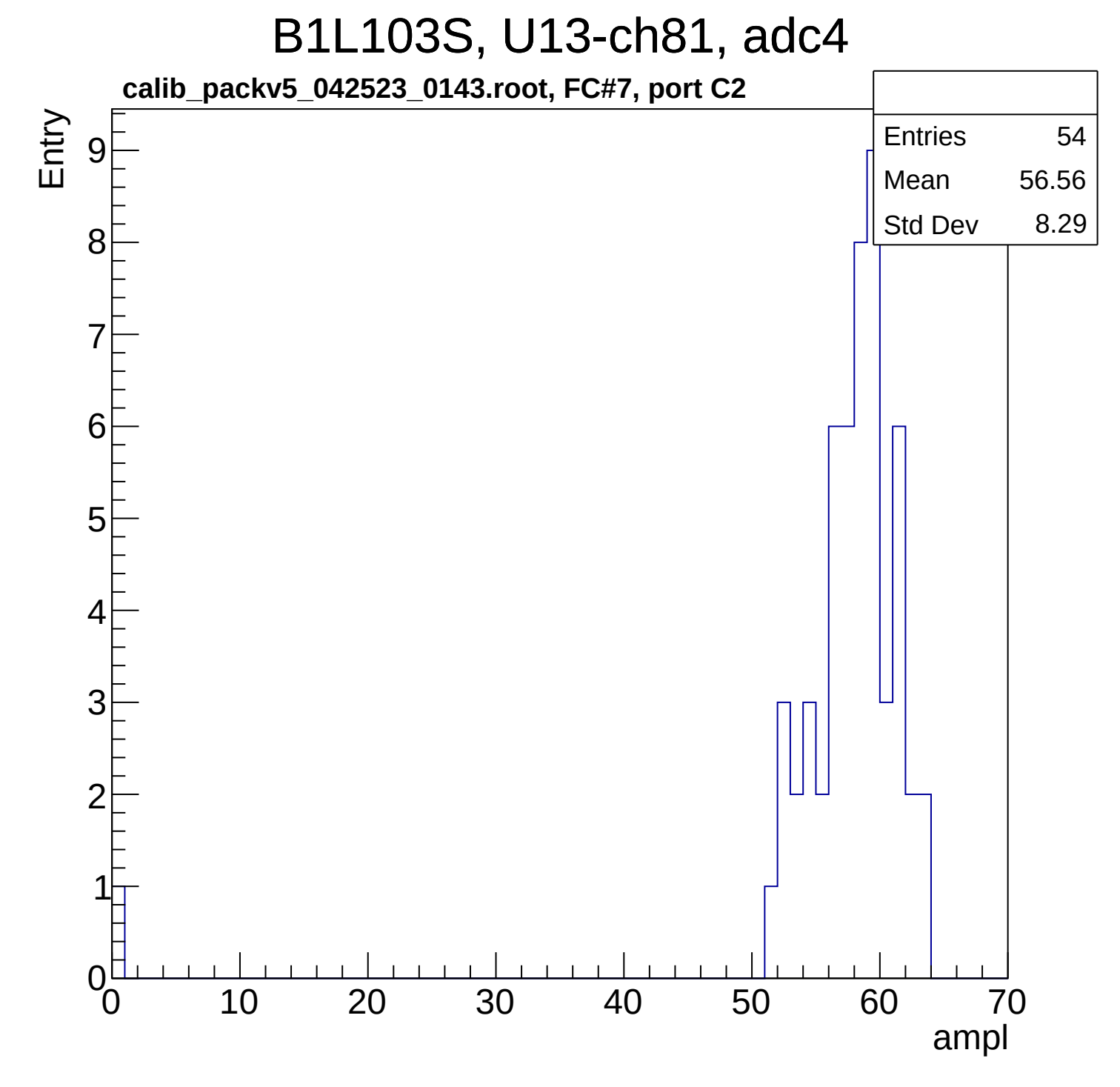
B1L103S, U13-ch81, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.56
Std Dev 8.29
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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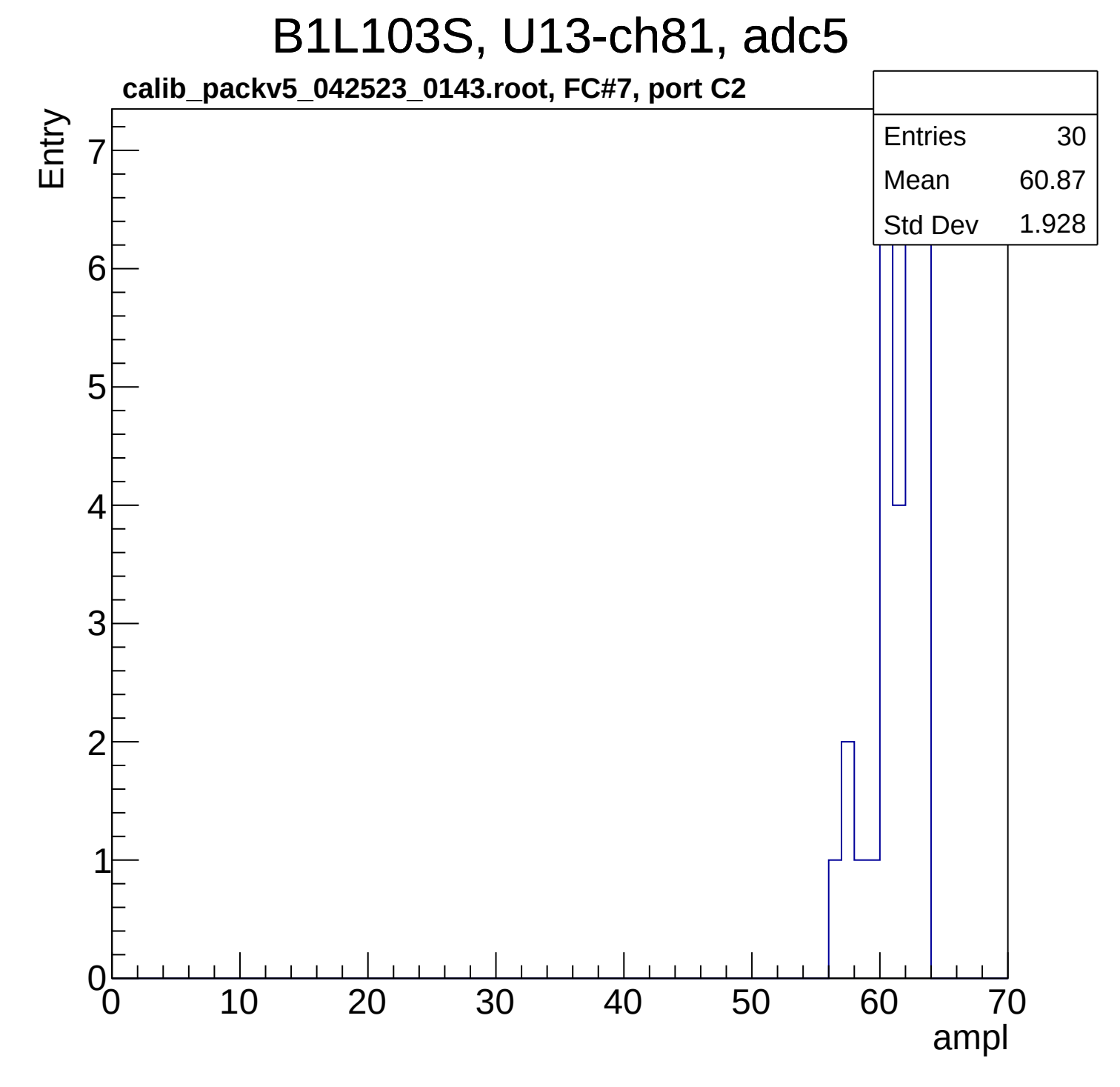
B1L103S, U13-ch81, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 60.87
Std Dev 1.928
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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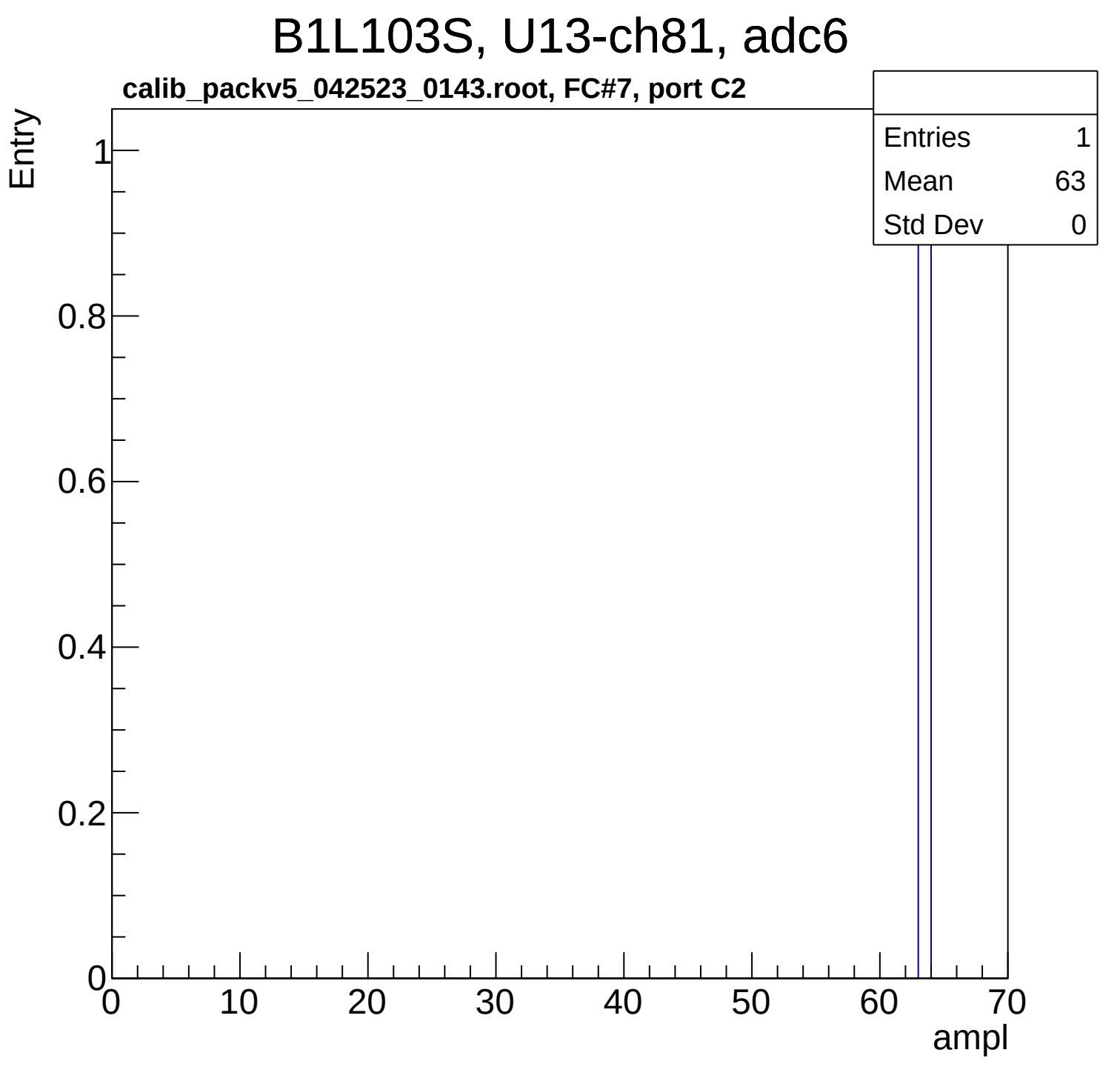
B1L103S, U13-ch81, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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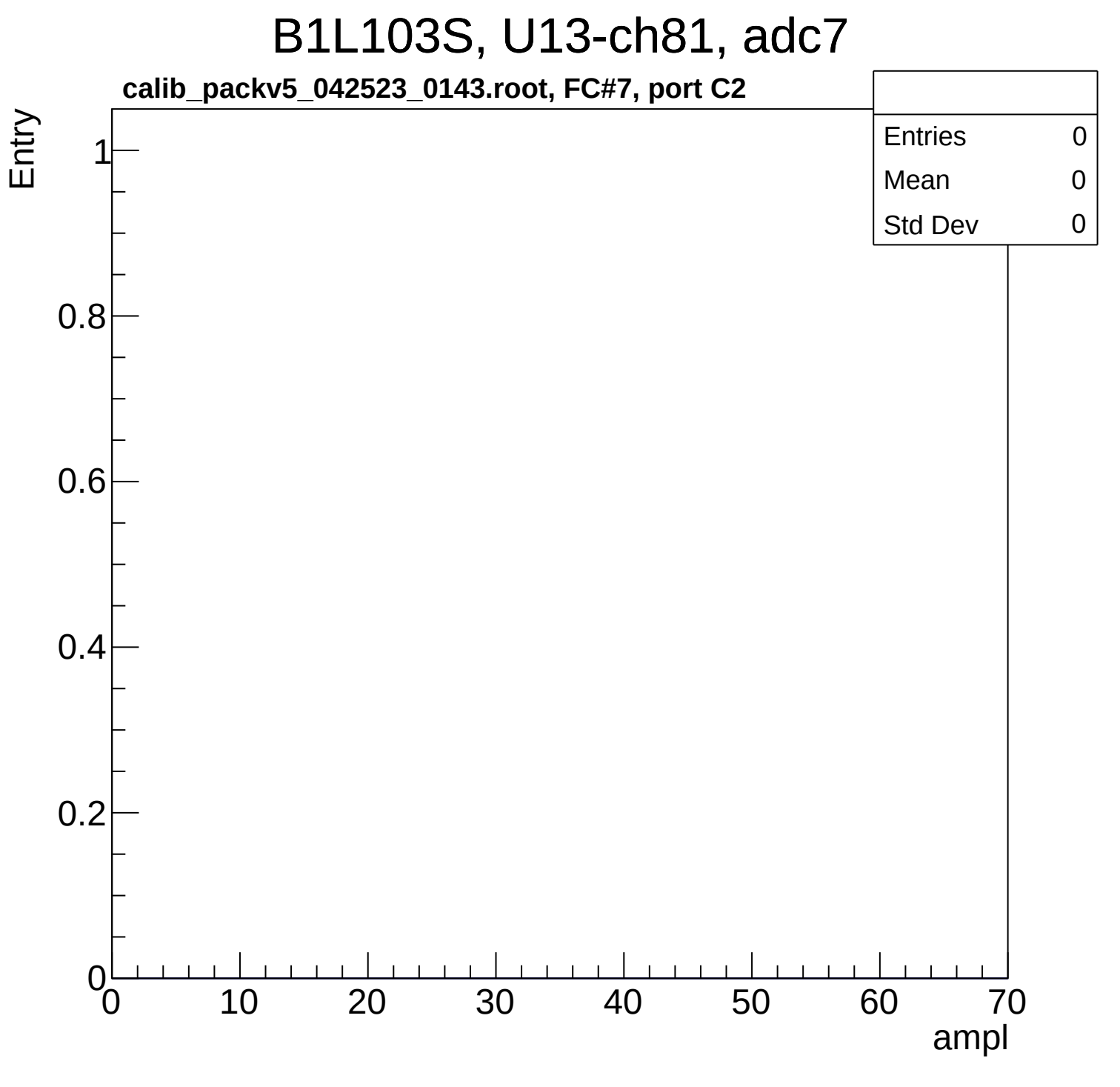
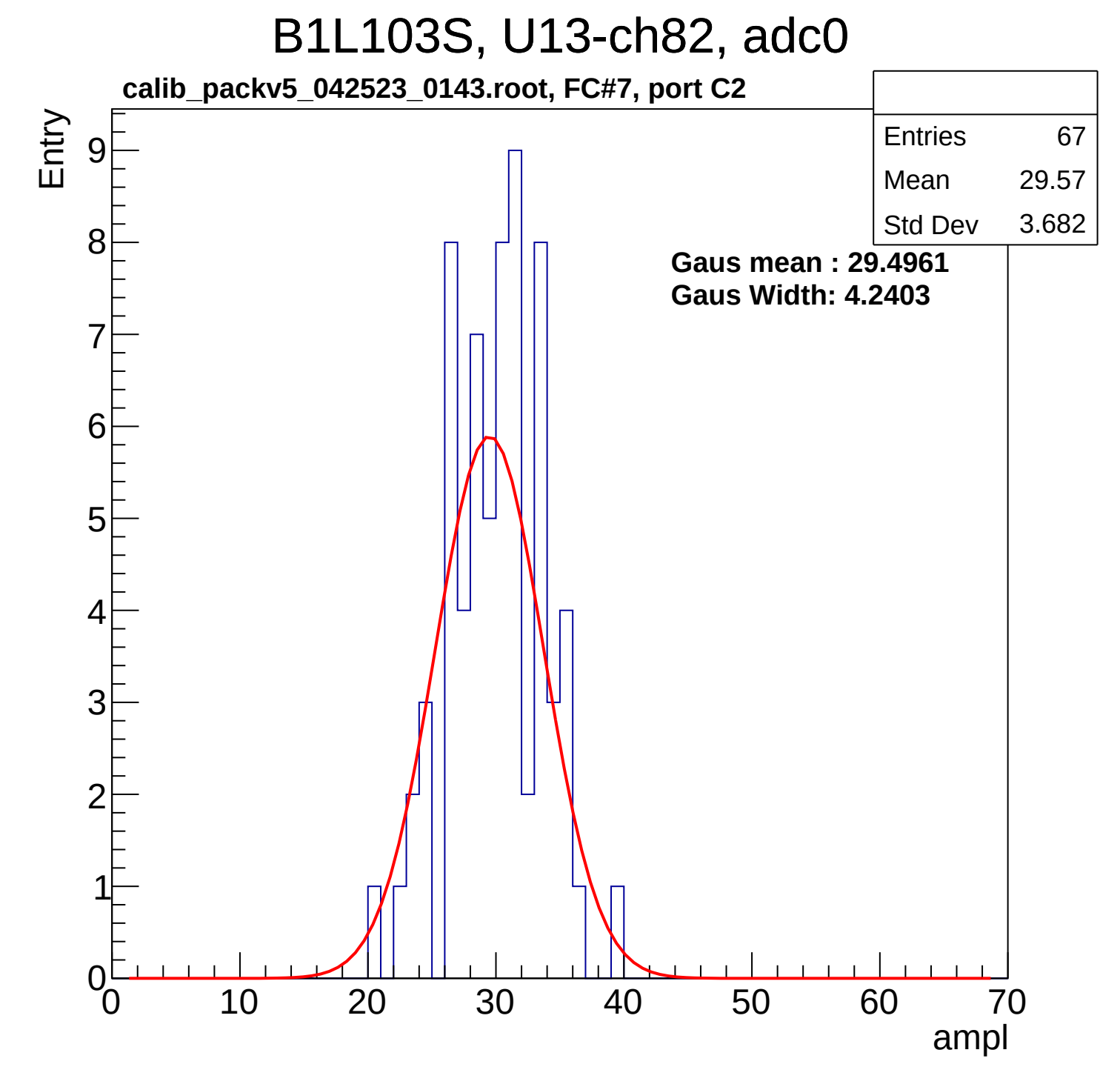
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch81, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch82, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.57
Std Dev 3.682
Gaus mean : 29.4961
Gaus Width: 4.2403
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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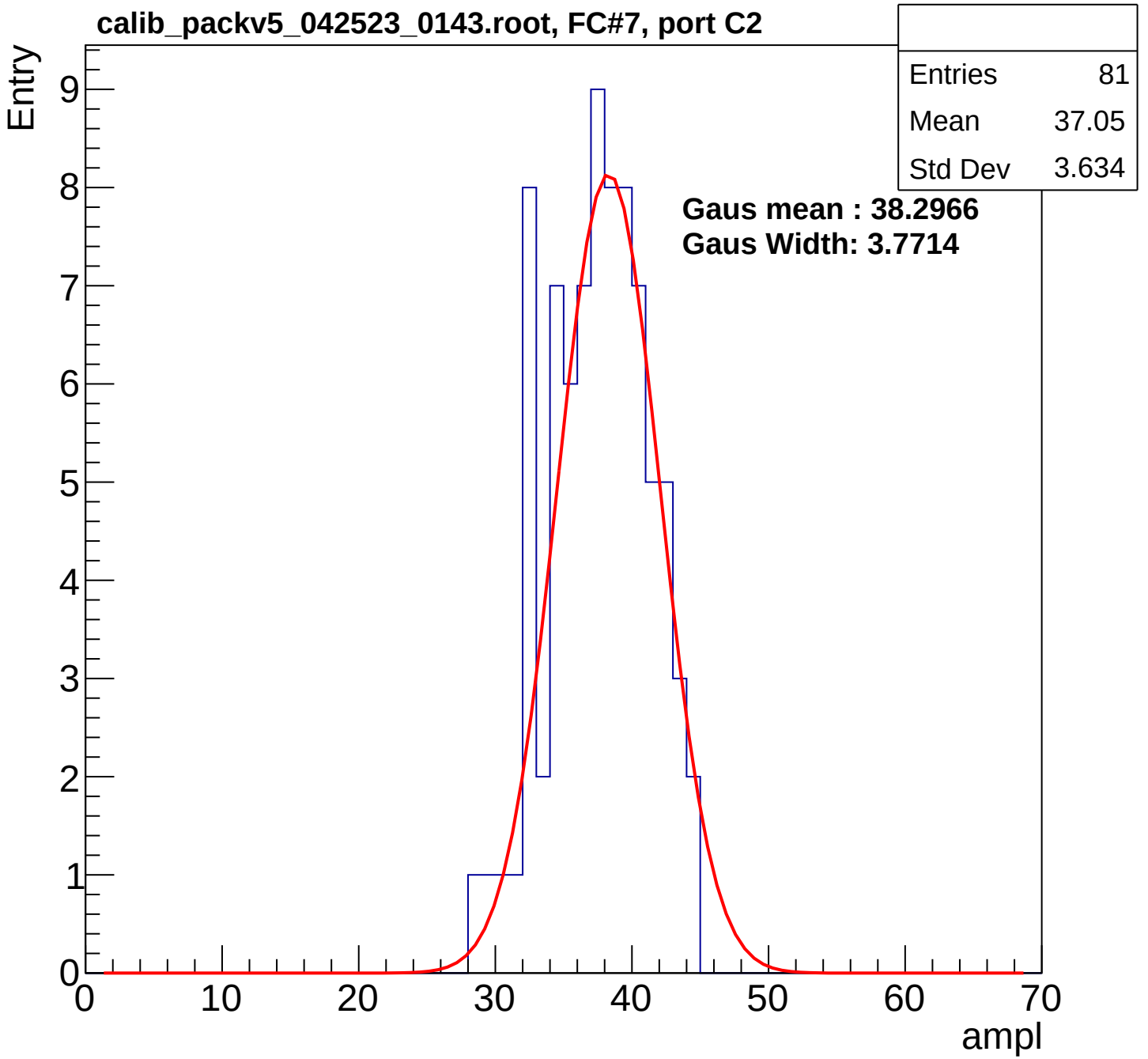
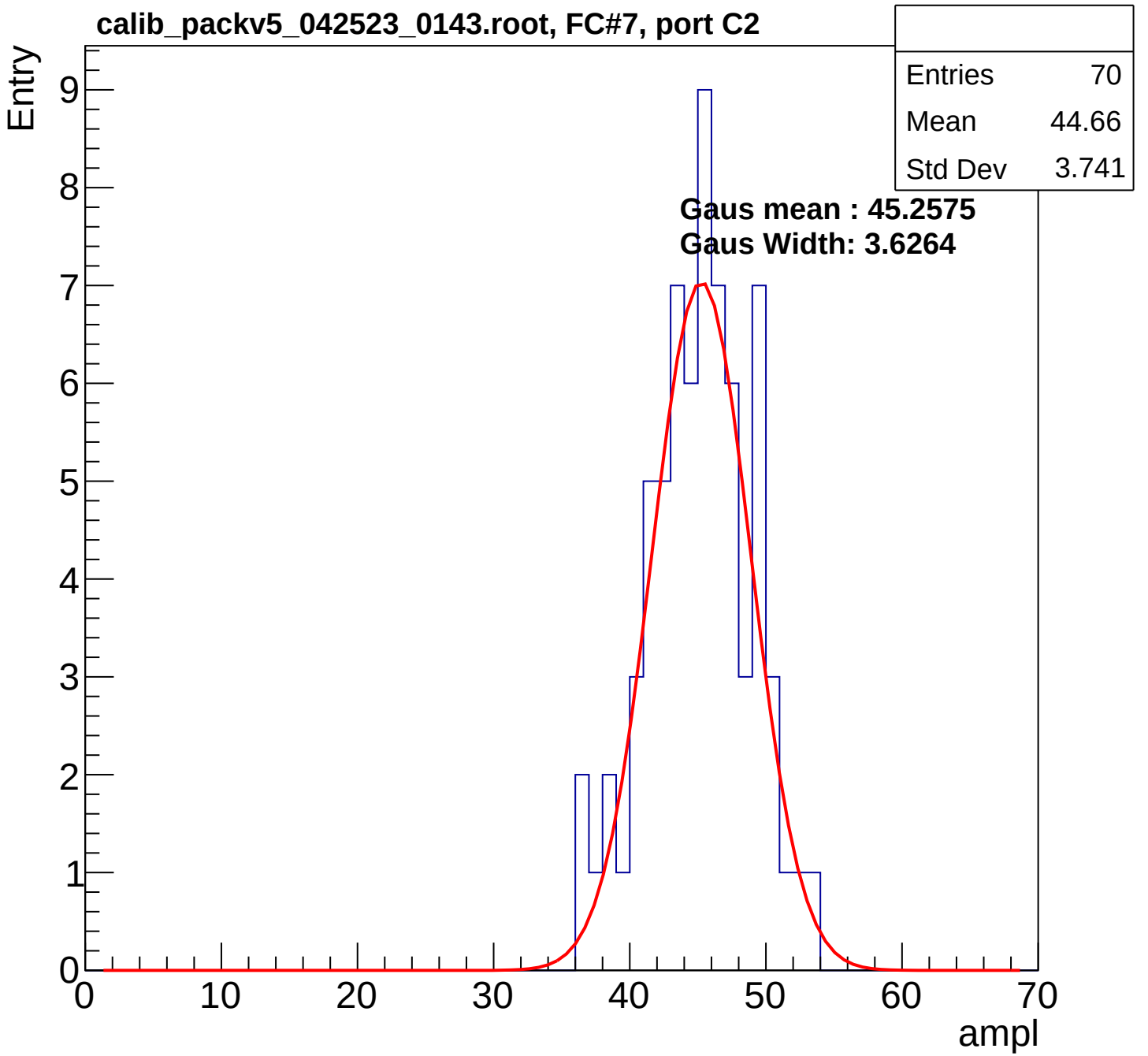

B1L103S, U13-ch82, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 37.05
Std Dev 3.634
Gaus mean : 38.2966
Gaus Width: 3.7714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch82, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 44.66
Std Dev 3.741
Gaus mean : 45.2575
Gaus Width: 3.6264
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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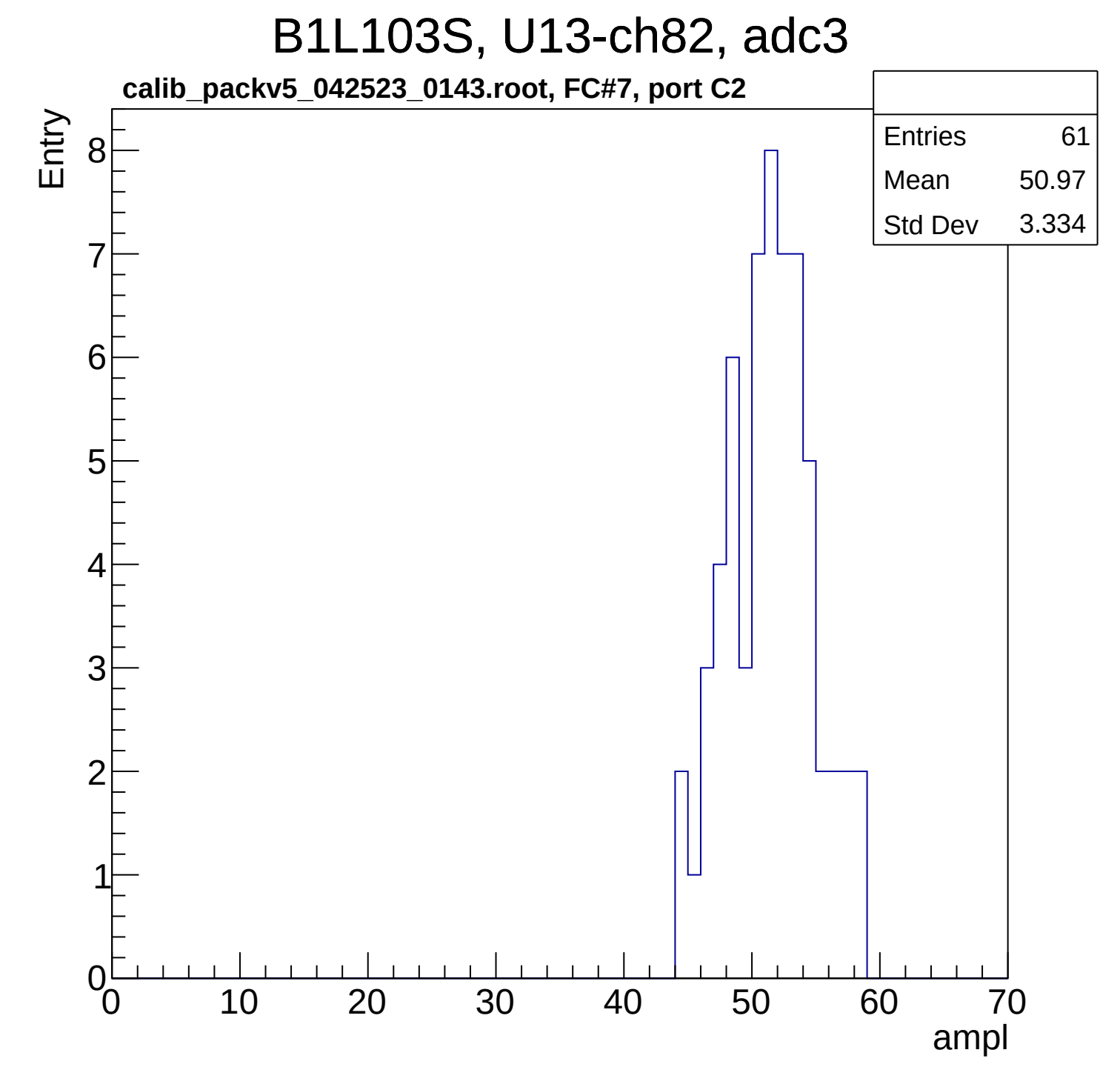
B1L103S, U13-ch82, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 50.97
Std Dev 3.334
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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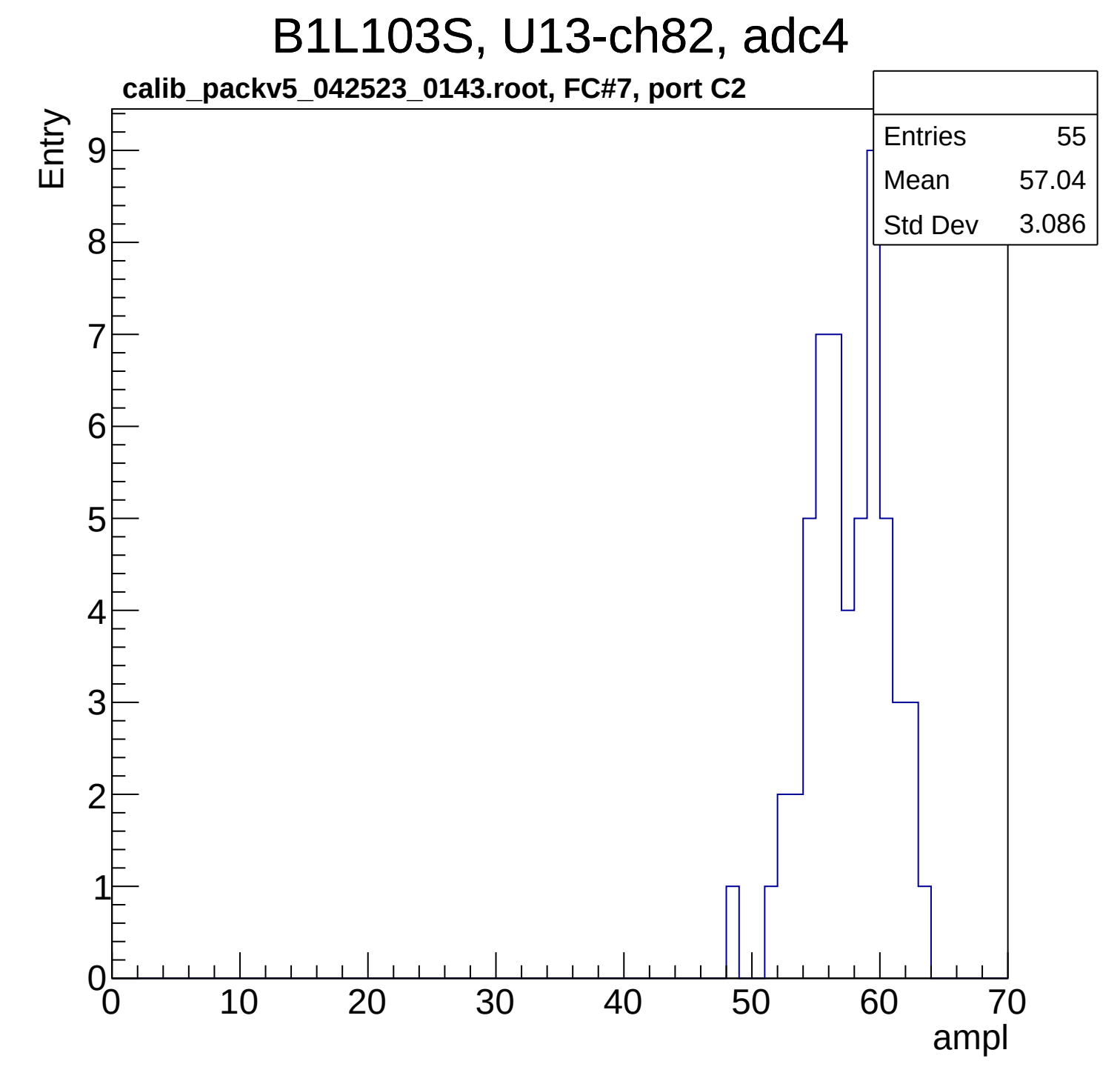
B1L103S, U13-ch82, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 57.04
Std Dev 3.086
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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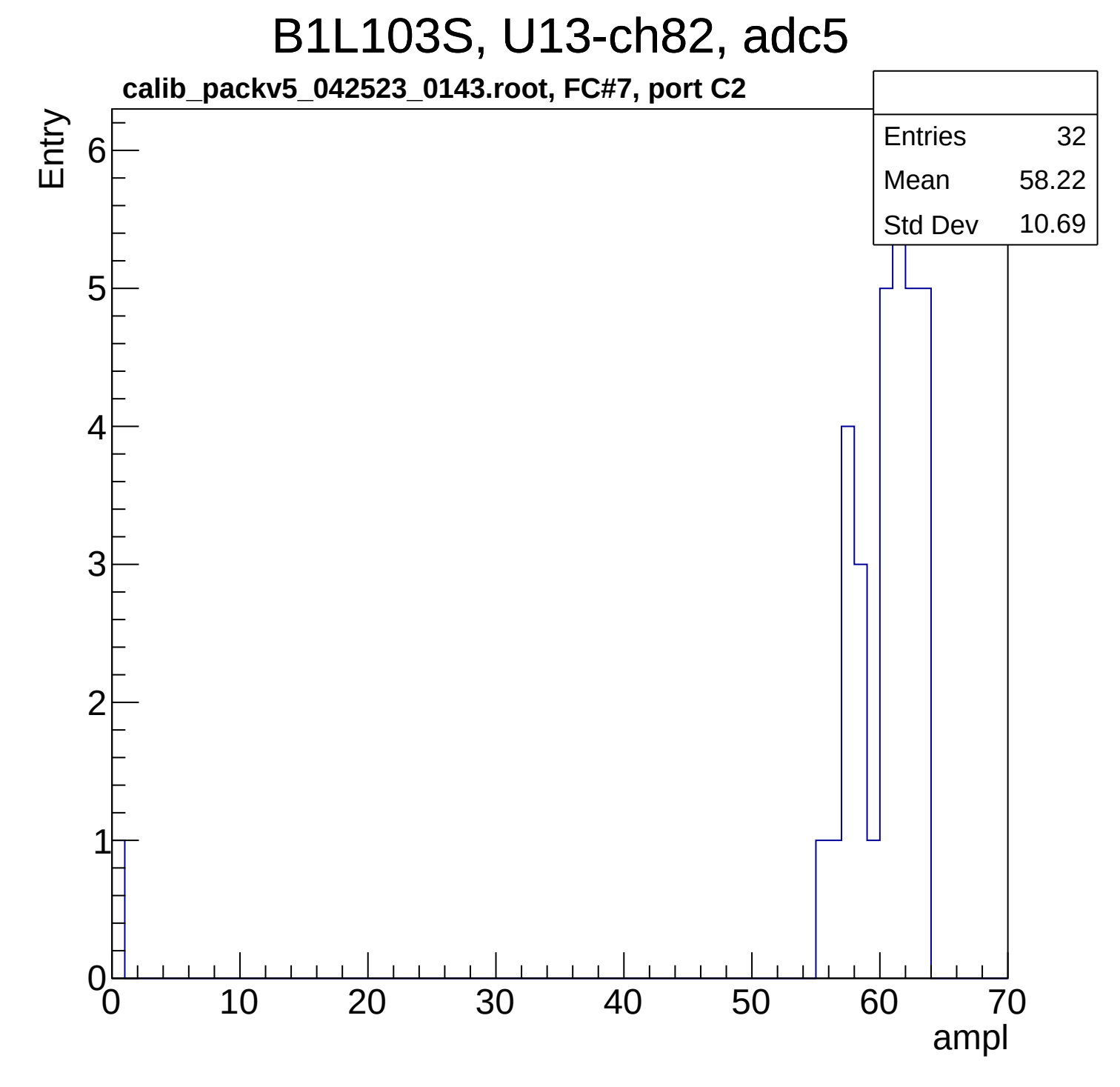
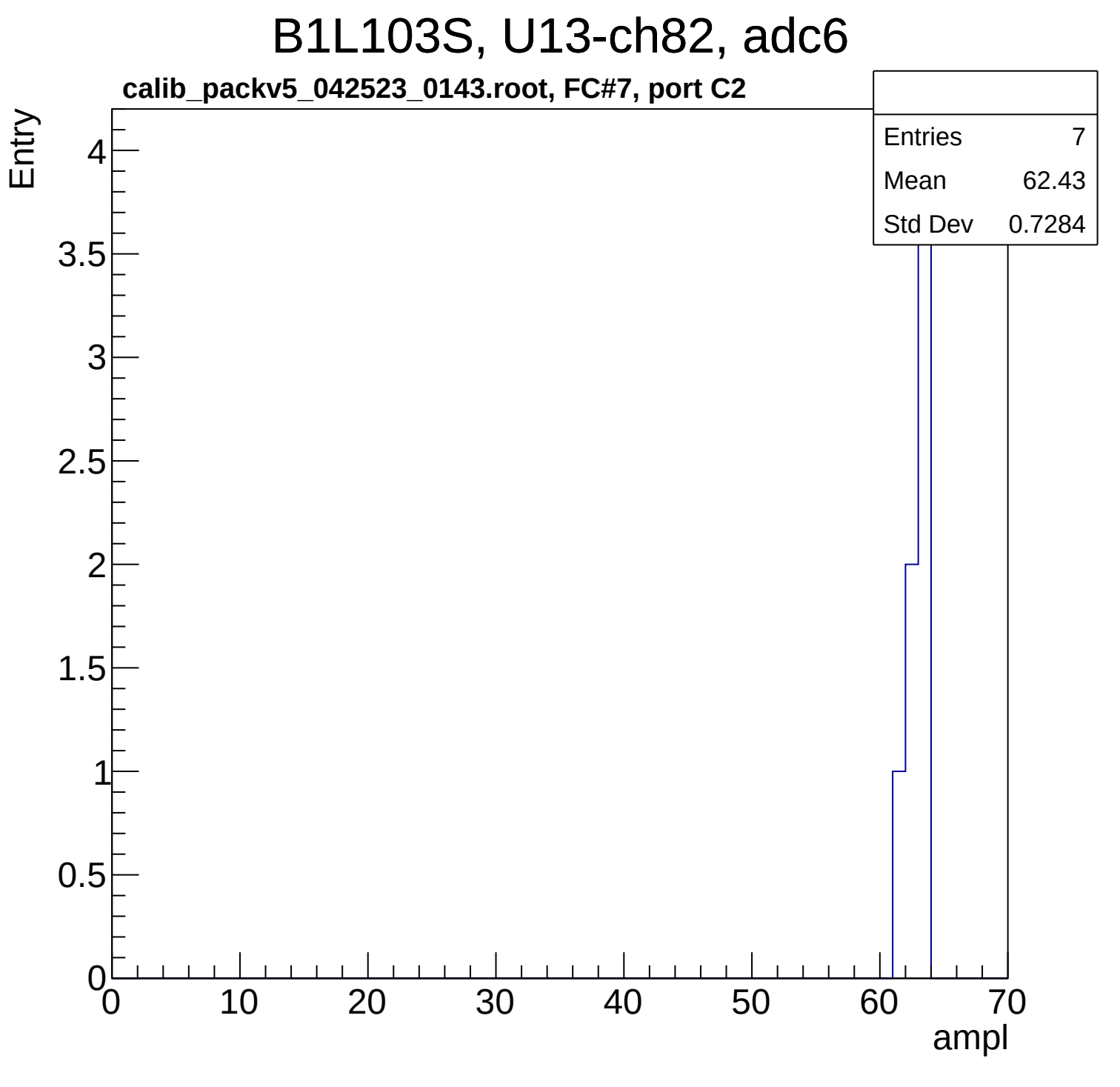
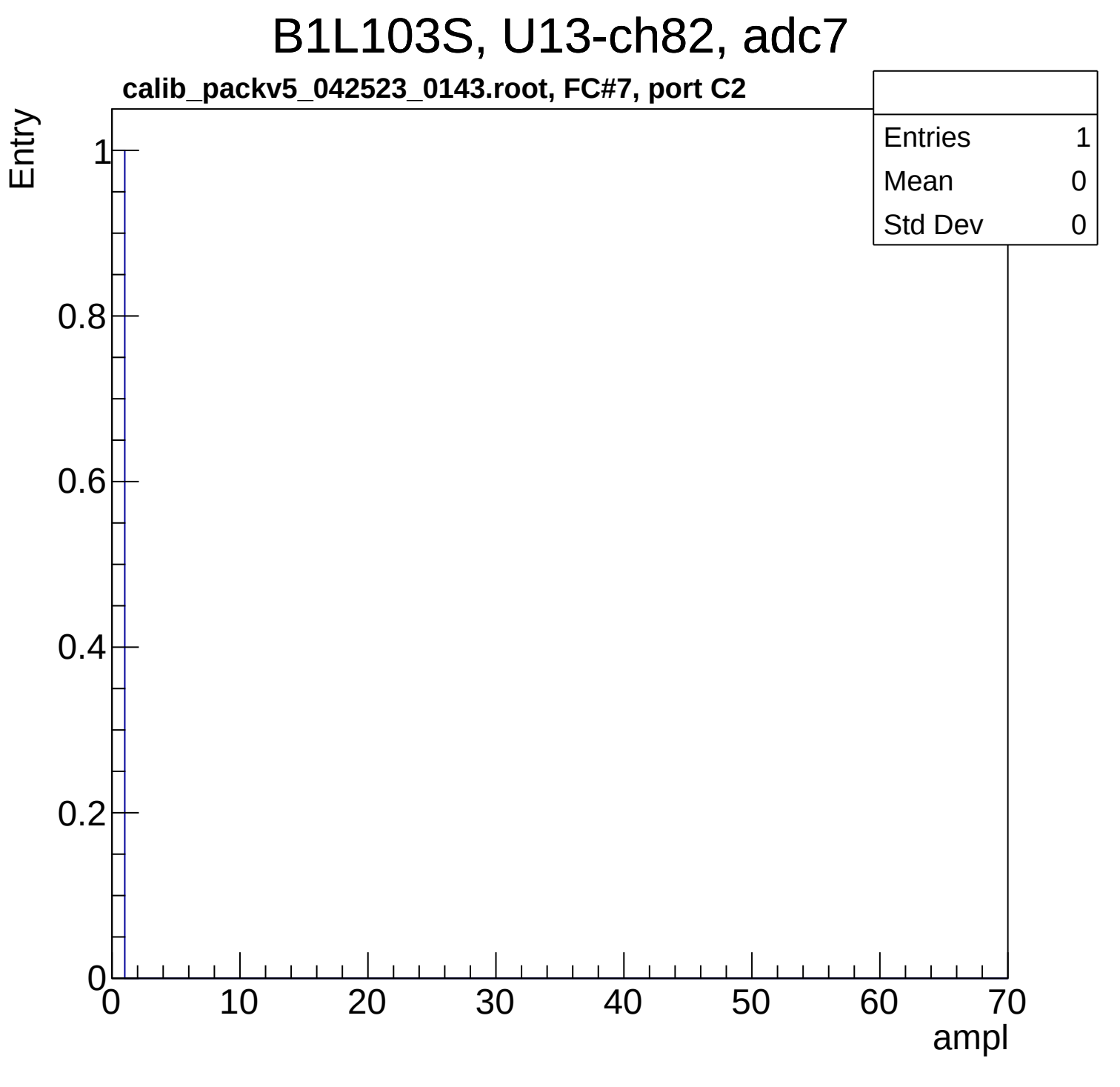

B1L103S, U13-ch82, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.22
Std Dev 10.69
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch82, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U13-ch82, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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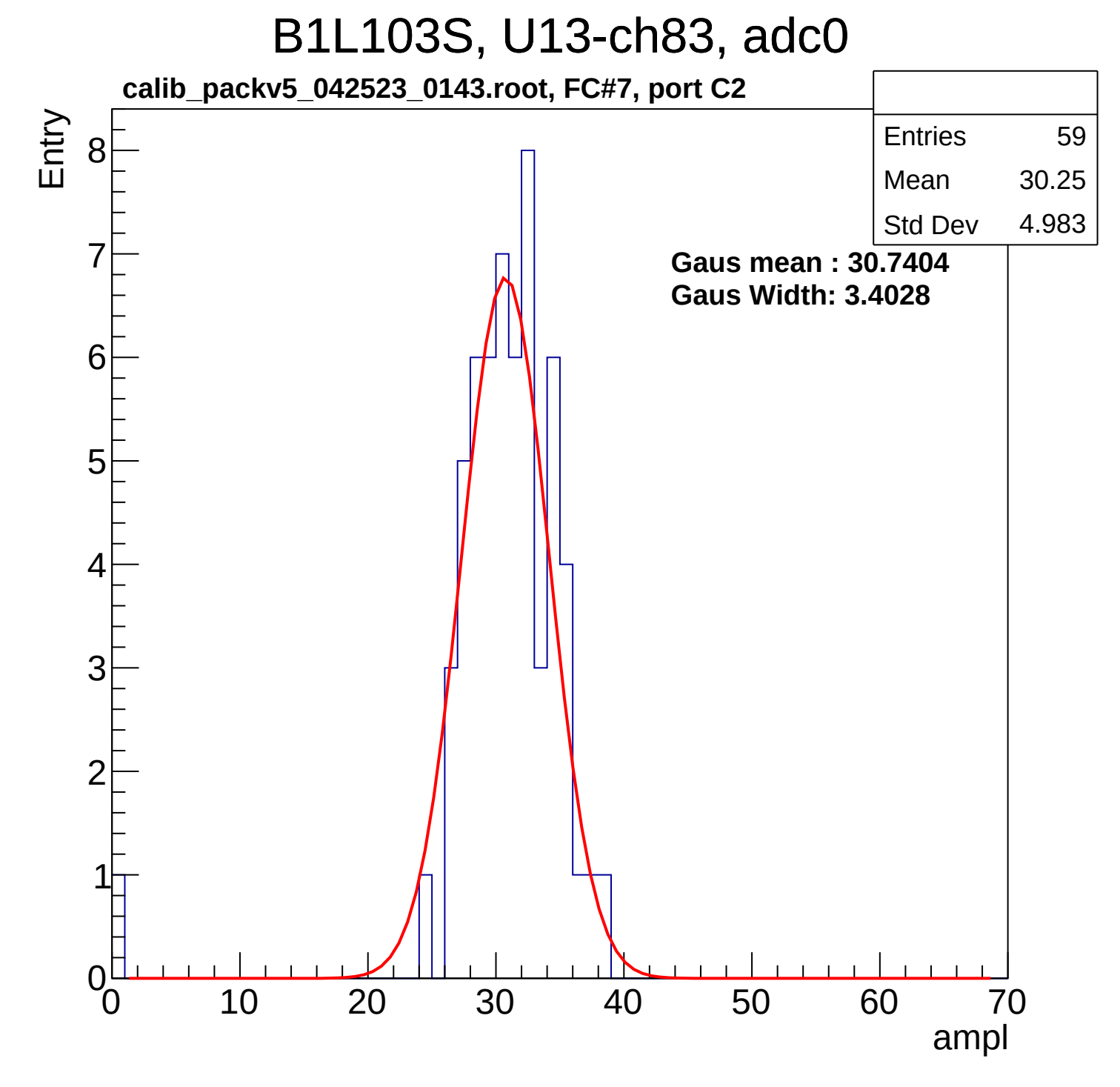
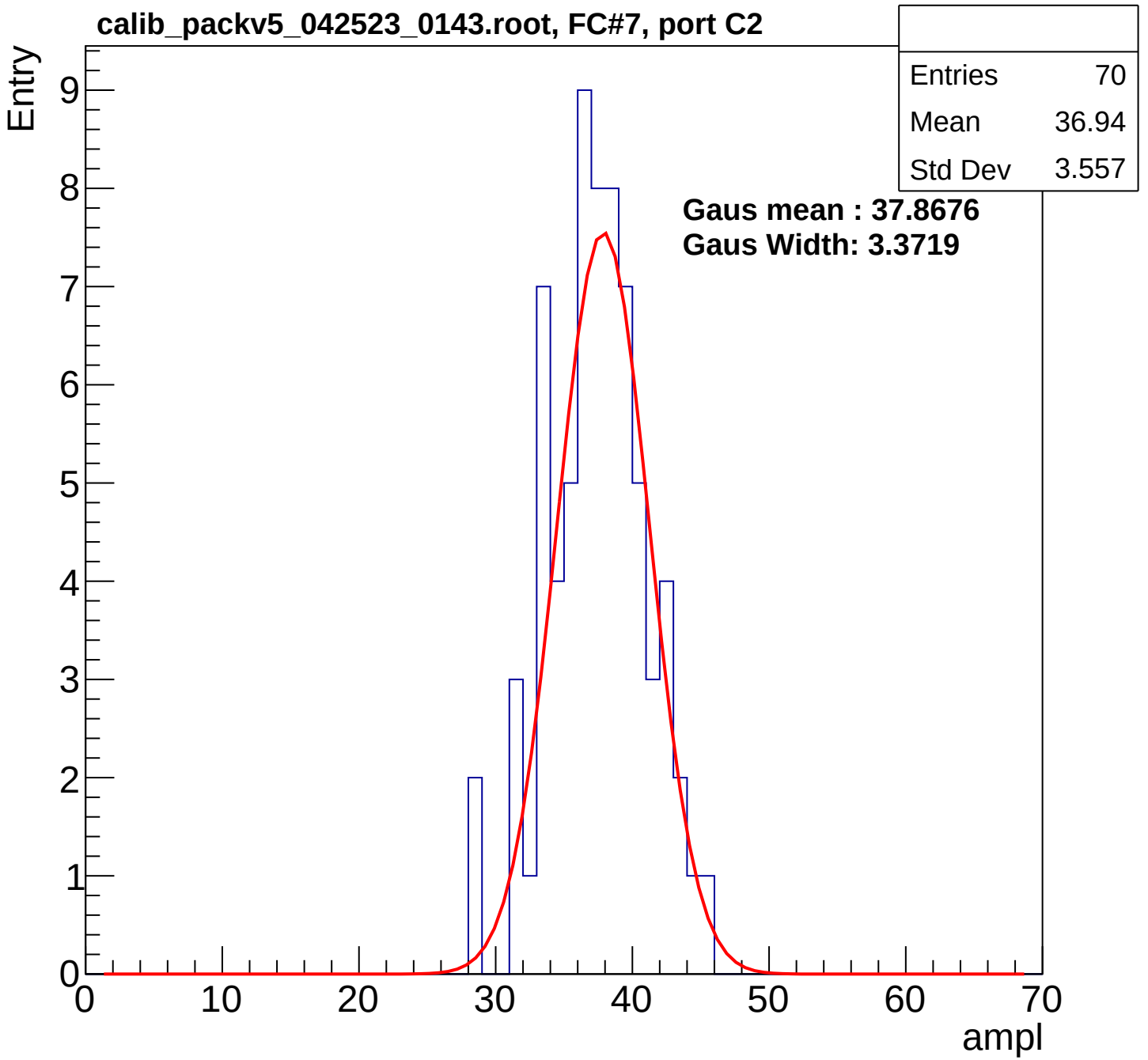
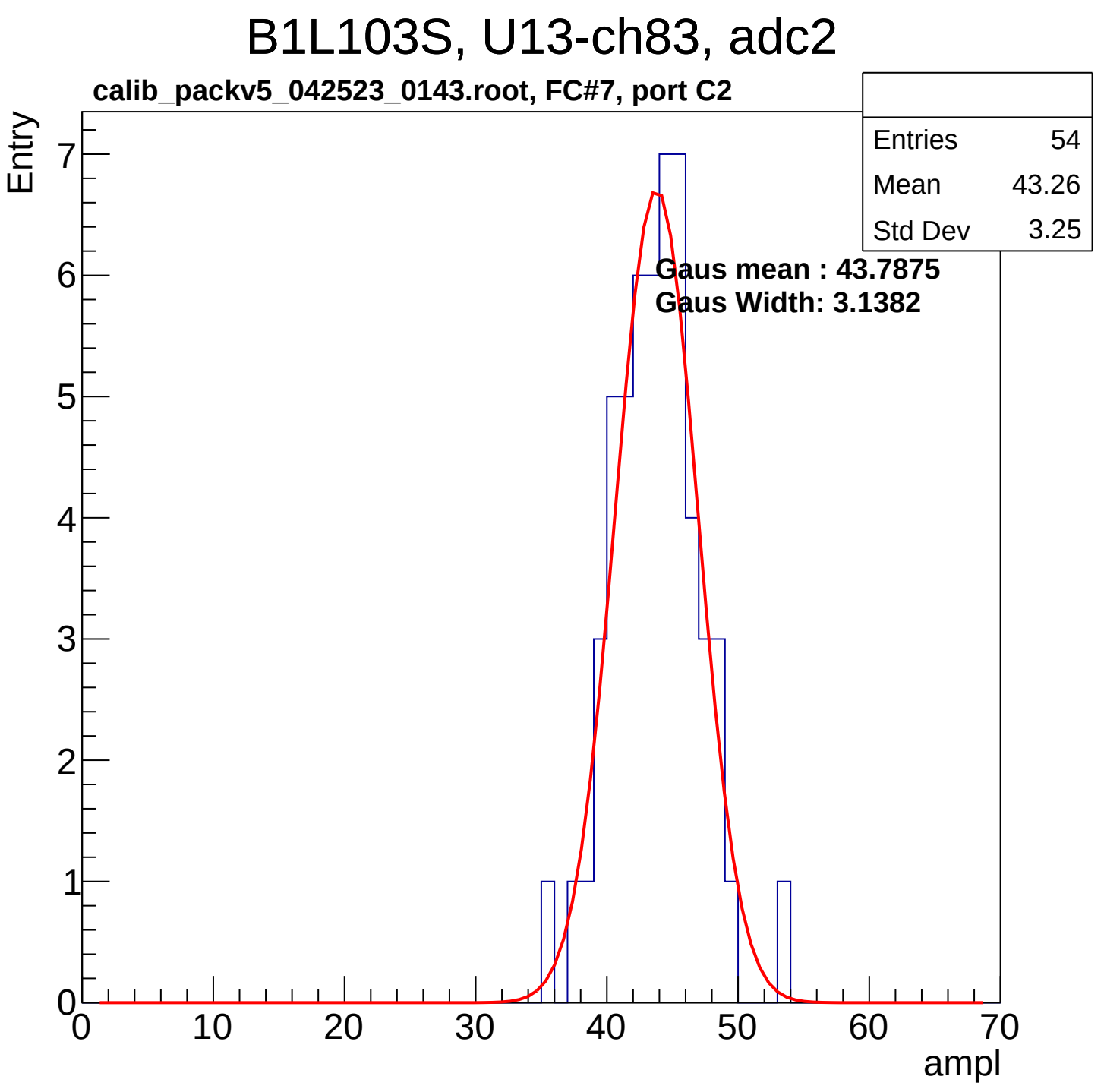
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch83, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 30.25
Std Dev 4.983
Gaus mean : 30.7404
Gaus Width: 3.4028
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch83, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 36.94
Std Dev 3.557
Gaus mean : 37.8676
Gaus Width: 3.3719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch83, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 43.26
Std Dev 3.25
Gaus mean : 43.7875
Gaus Width: 3.1382
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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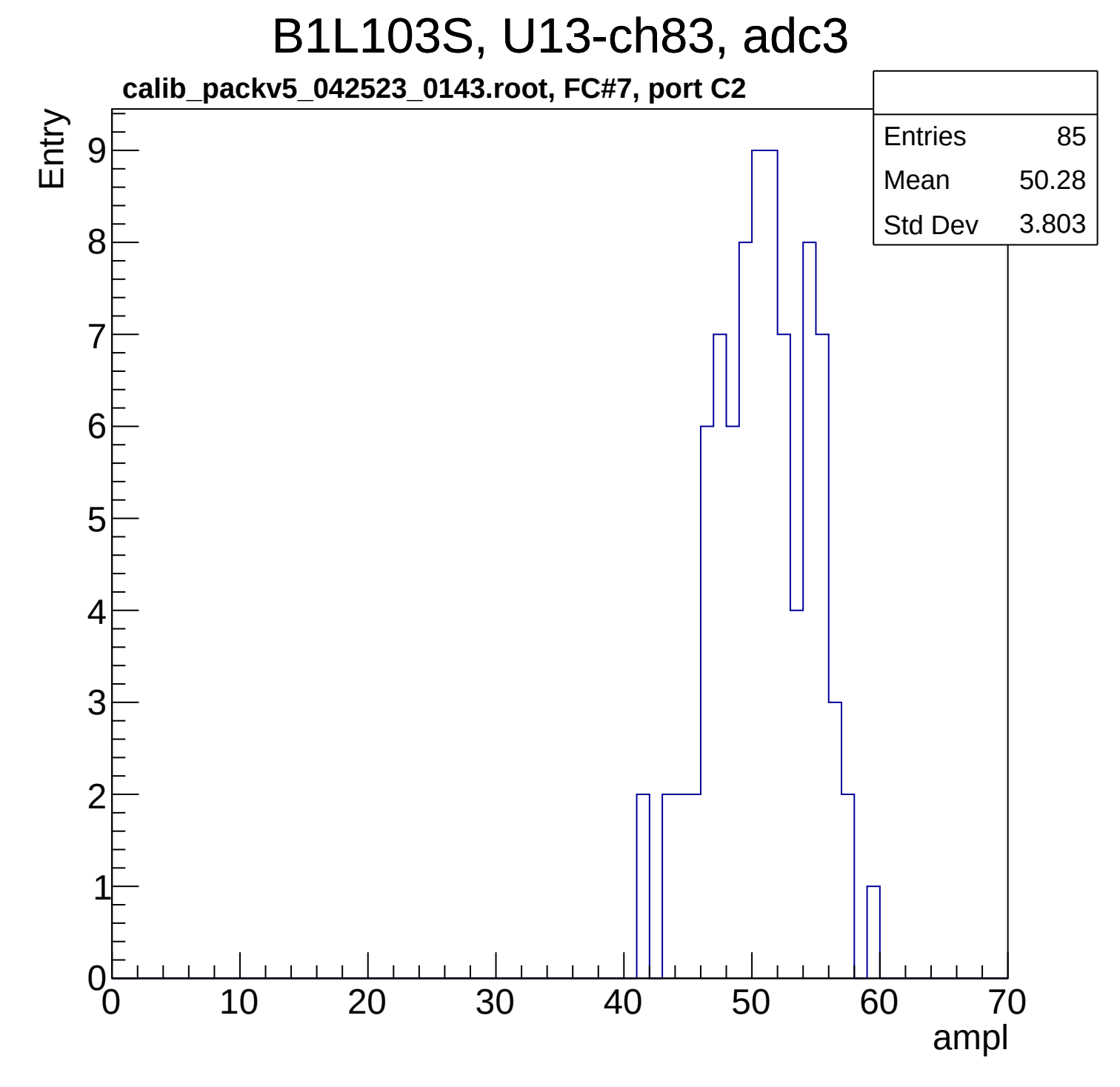
B1L103S, U13-ch83, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 50.28
Std Dev 3.803
Entry
ampl
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5
6
7
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0
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60
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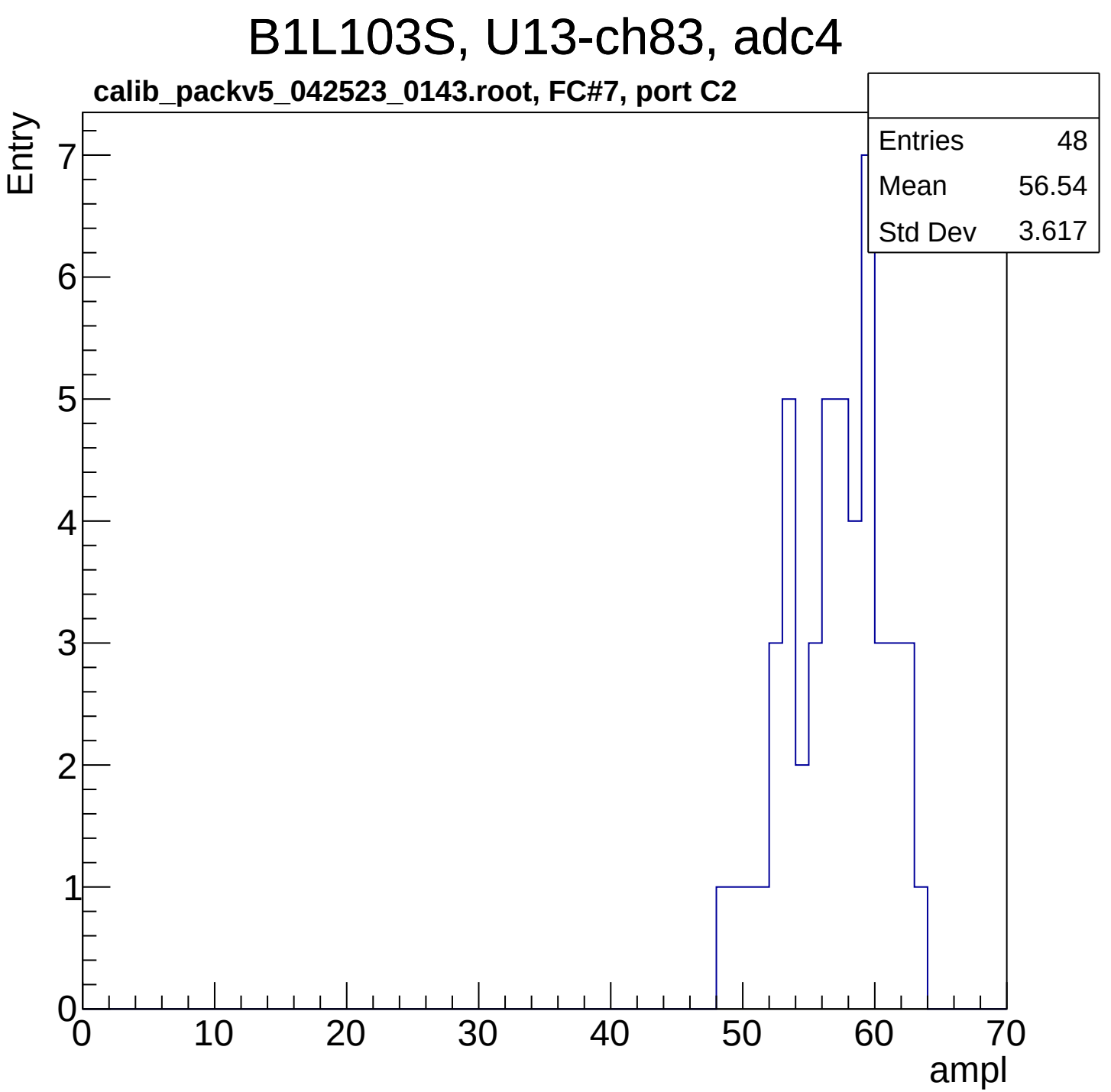
B1L103S, U13-ch83, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 56.54
Std Dev 3.617
Entry
ampl
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60
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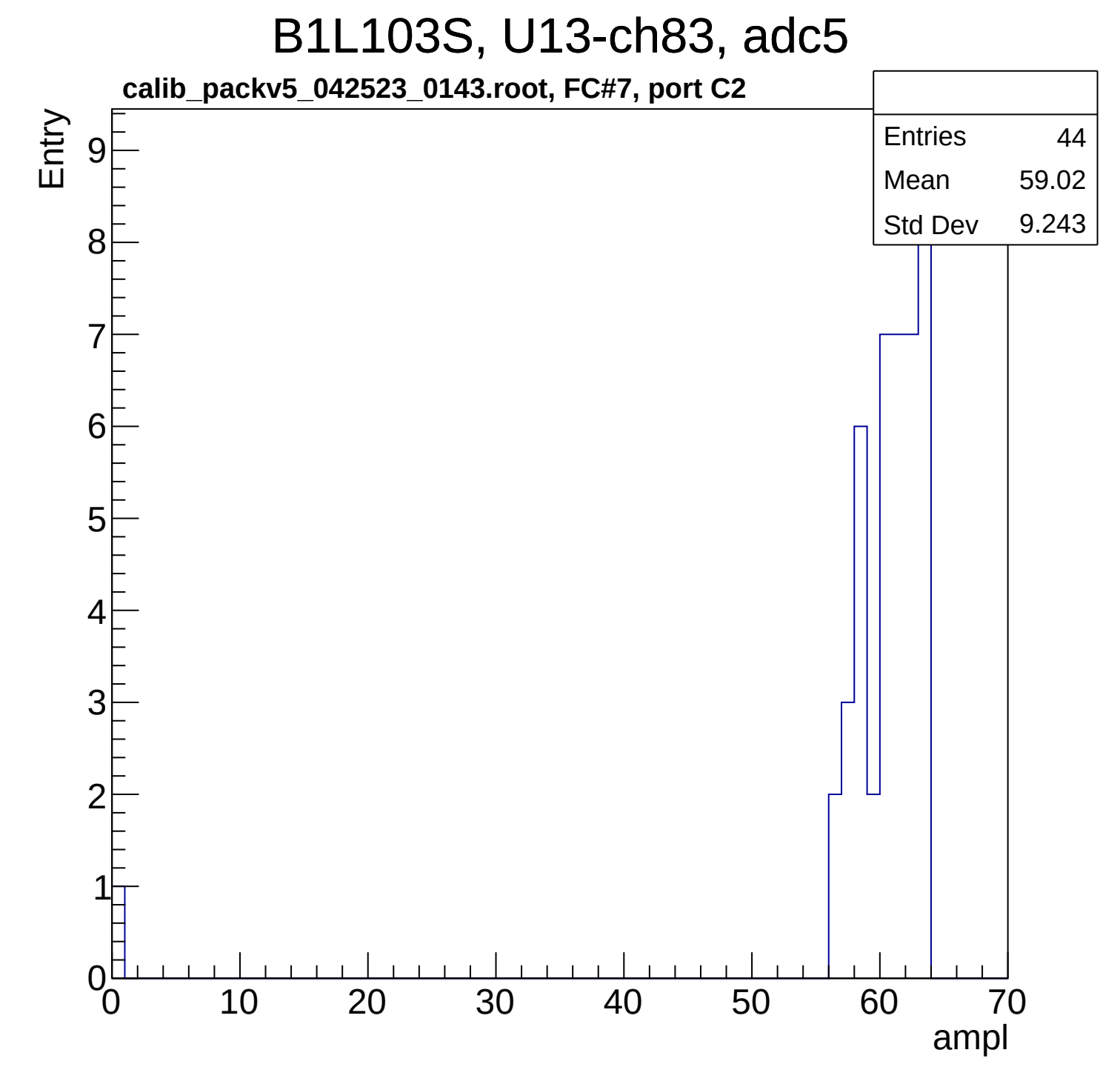
B1L103S, U13-ch83, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.02
Std Dev 9.243
Entry
ampl
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3
4
5
6
7
8
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0
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60
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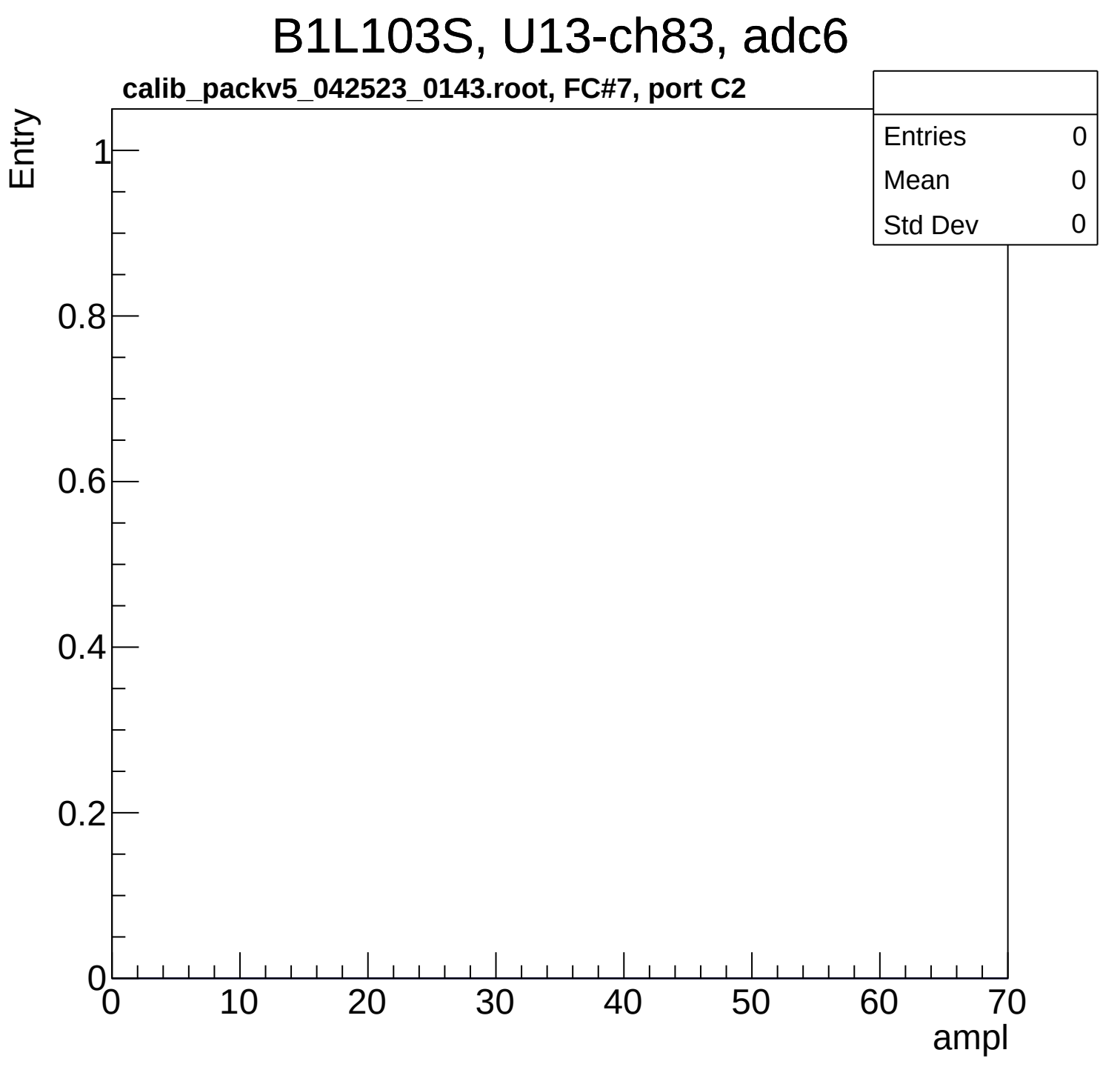
B1L103S, U13-ch83, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
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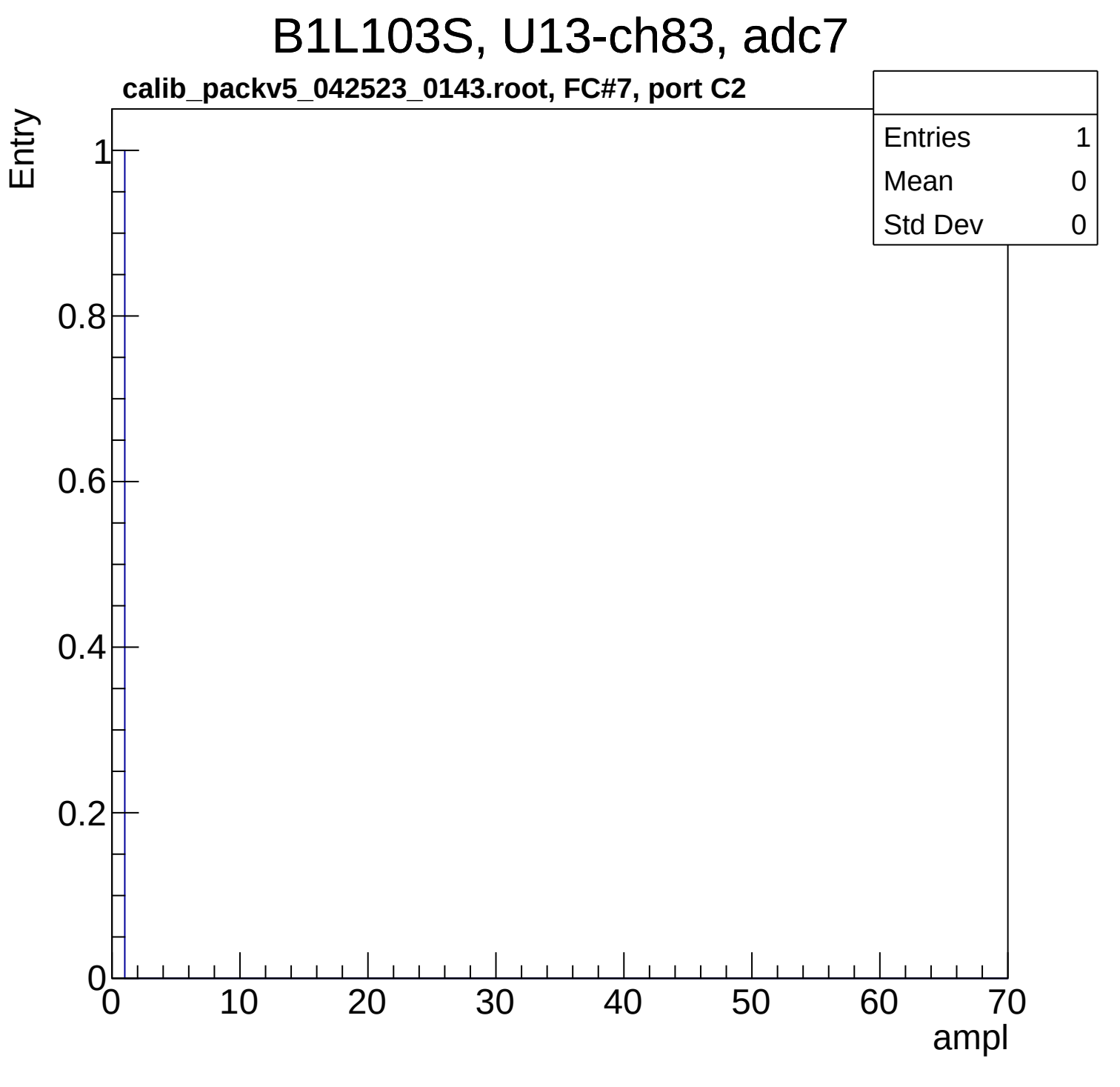
40
50
60
70
ampl


B1L103S, U13-ch83, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.4
0.2
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10
20
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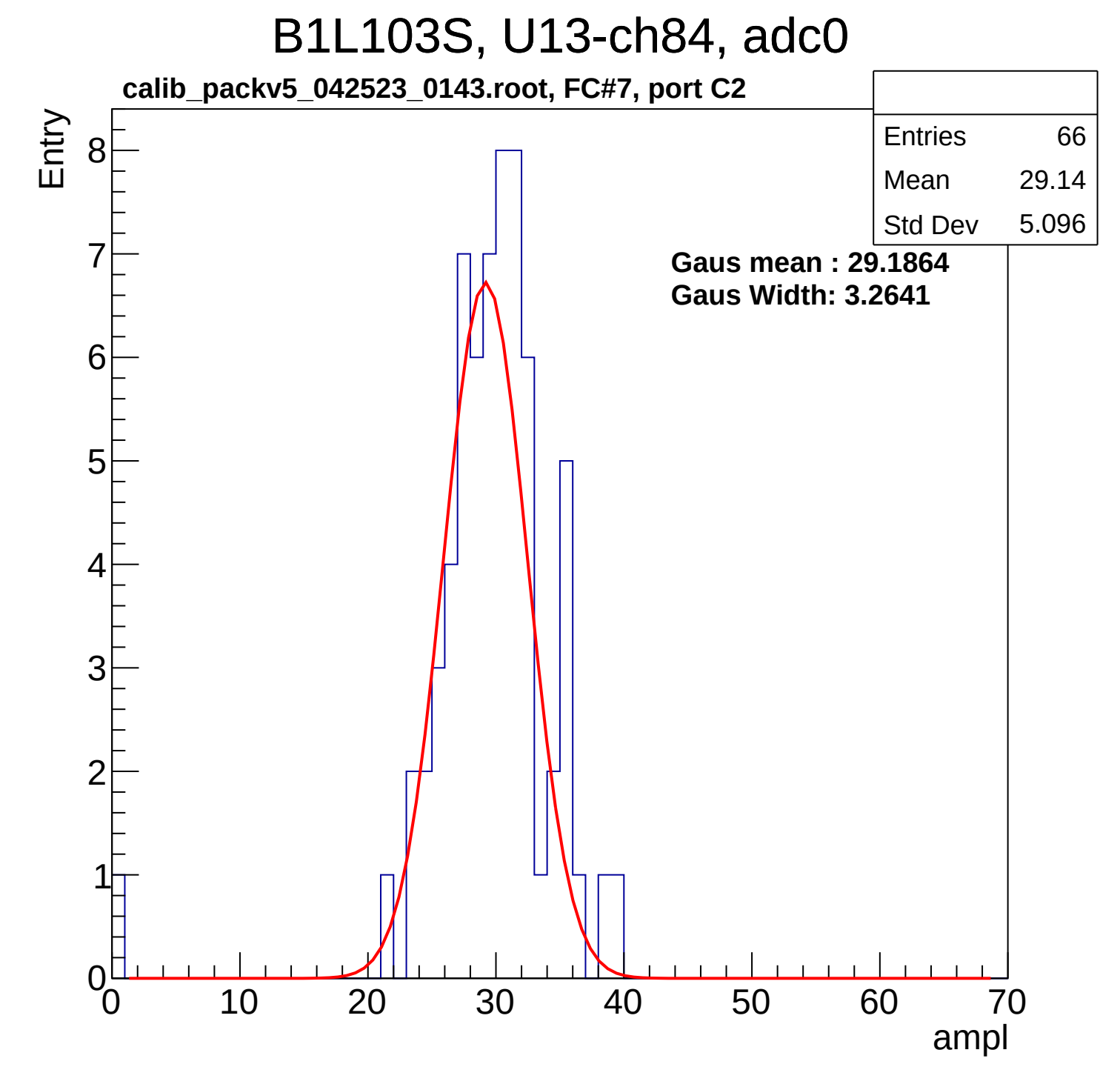
50
60
70
ampl


B1L103S, U13-ch84, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.14
Std Dev 5.096
Gaus mean : 29.1864
Gaus Width: 3.2641
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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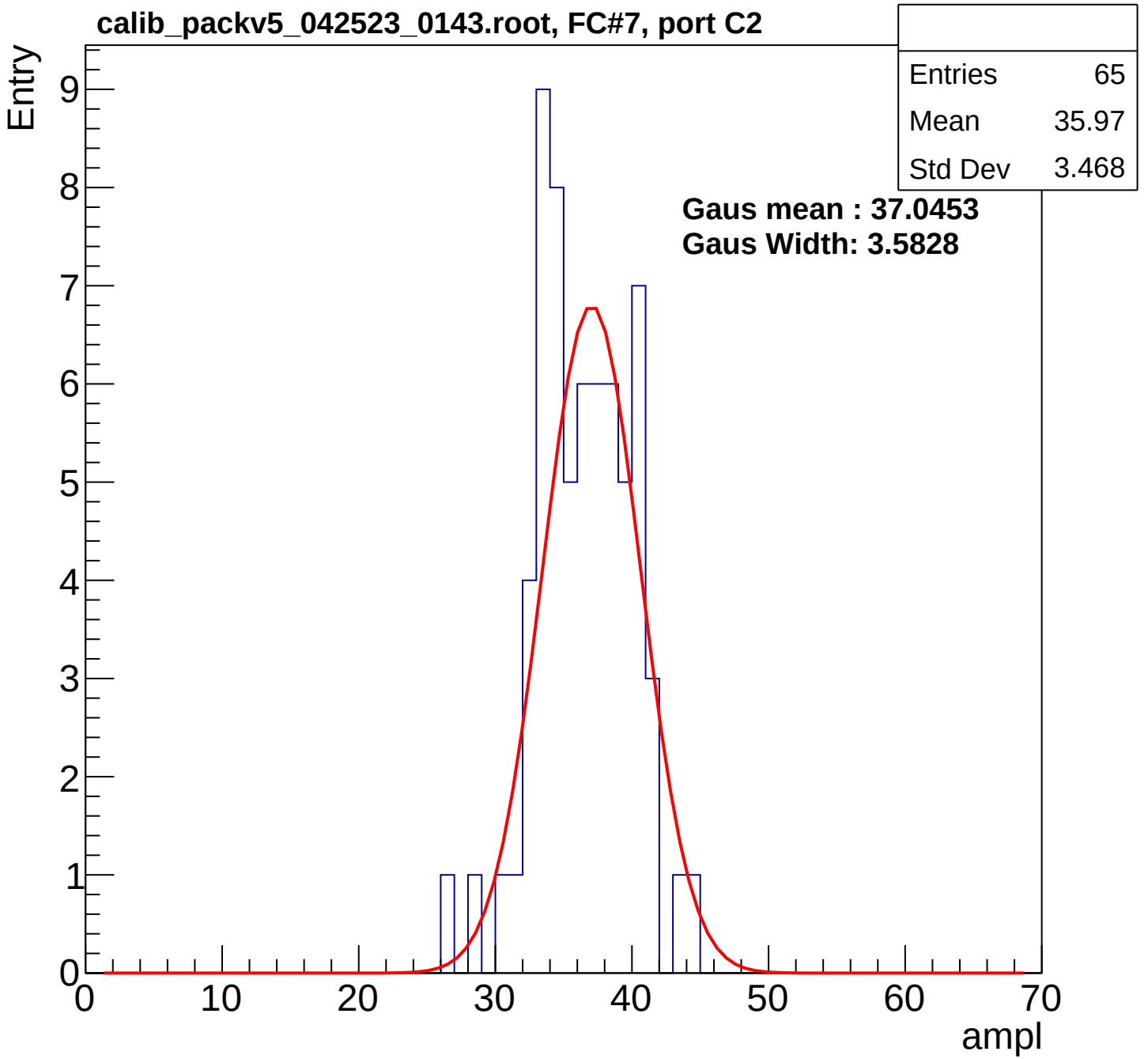
B1L103S, U13-ch84, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.97
Std Dev 3.468
Gaus mean : 37.0453
Gaus Width: 3.5828
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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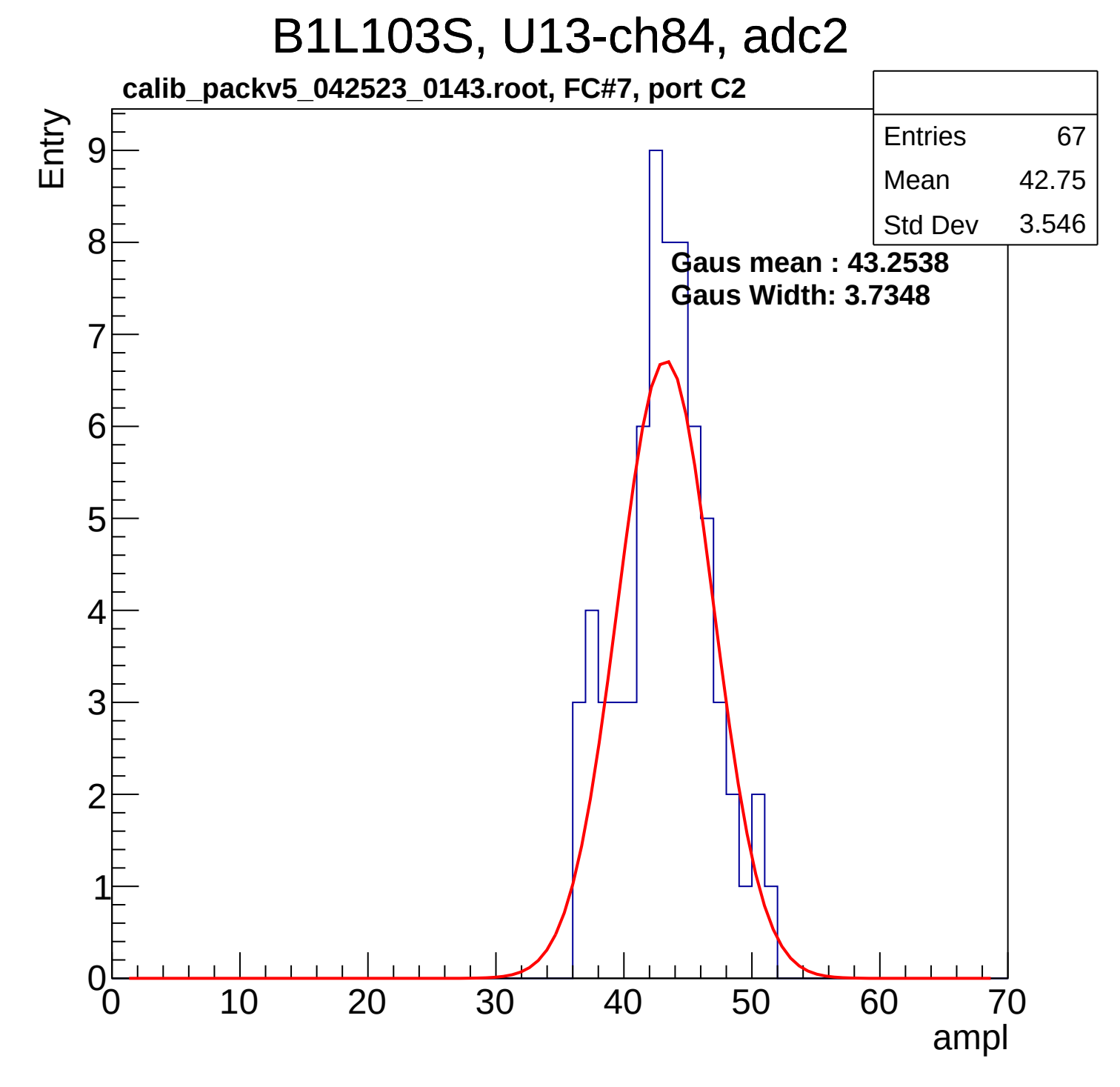
B1L103S, U13-ch84, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 42.75
Std Dev 3.546
Gaus mean : 43.2538
Gaus Width: 3.7348
Entry
ampl
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2
3
4
5
6
7
8
9
0
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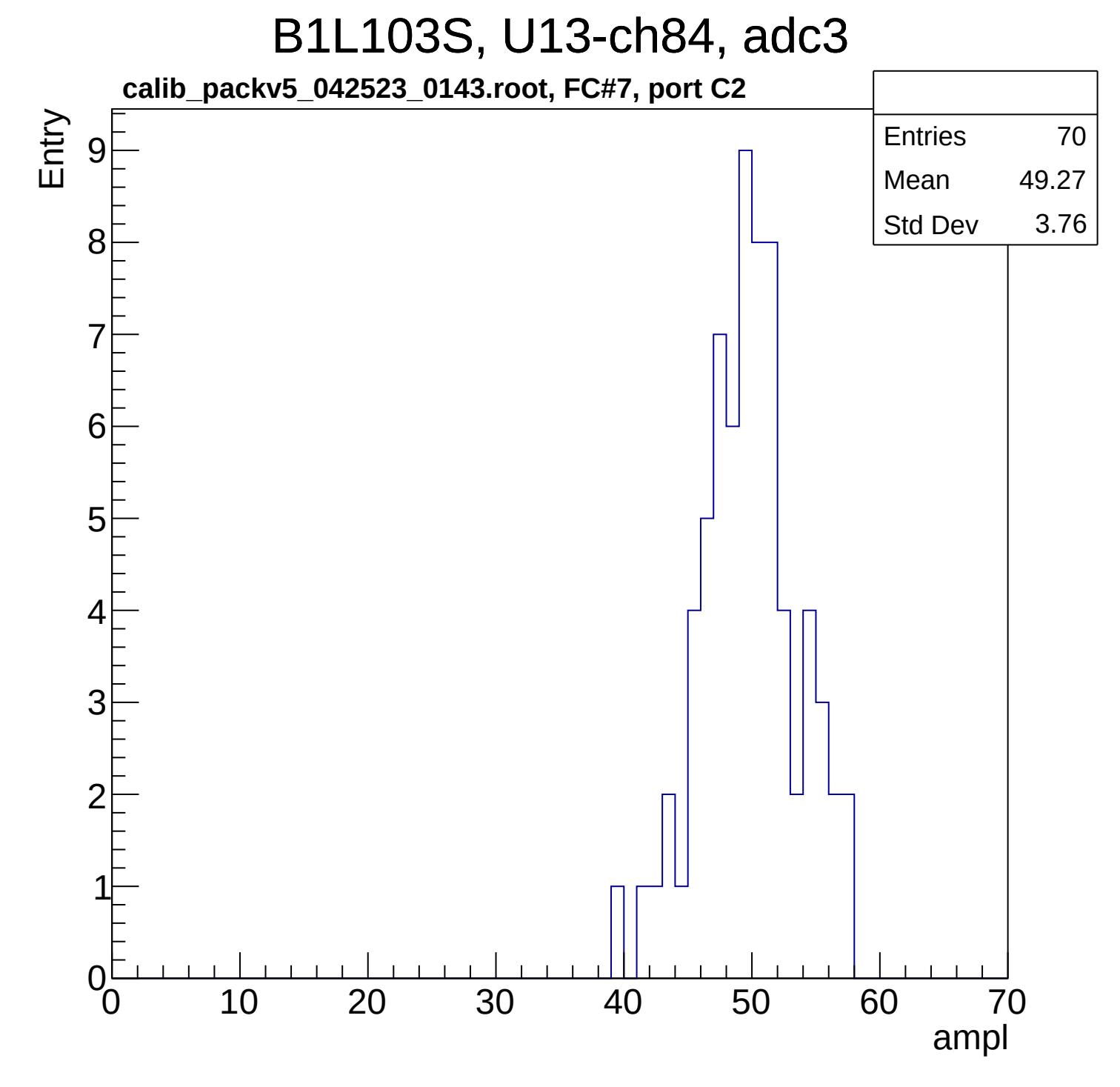
B1L103S, U13-ch84, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.27
Std Dev 3.76
Entry
ampl
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3
4
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6
7
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60
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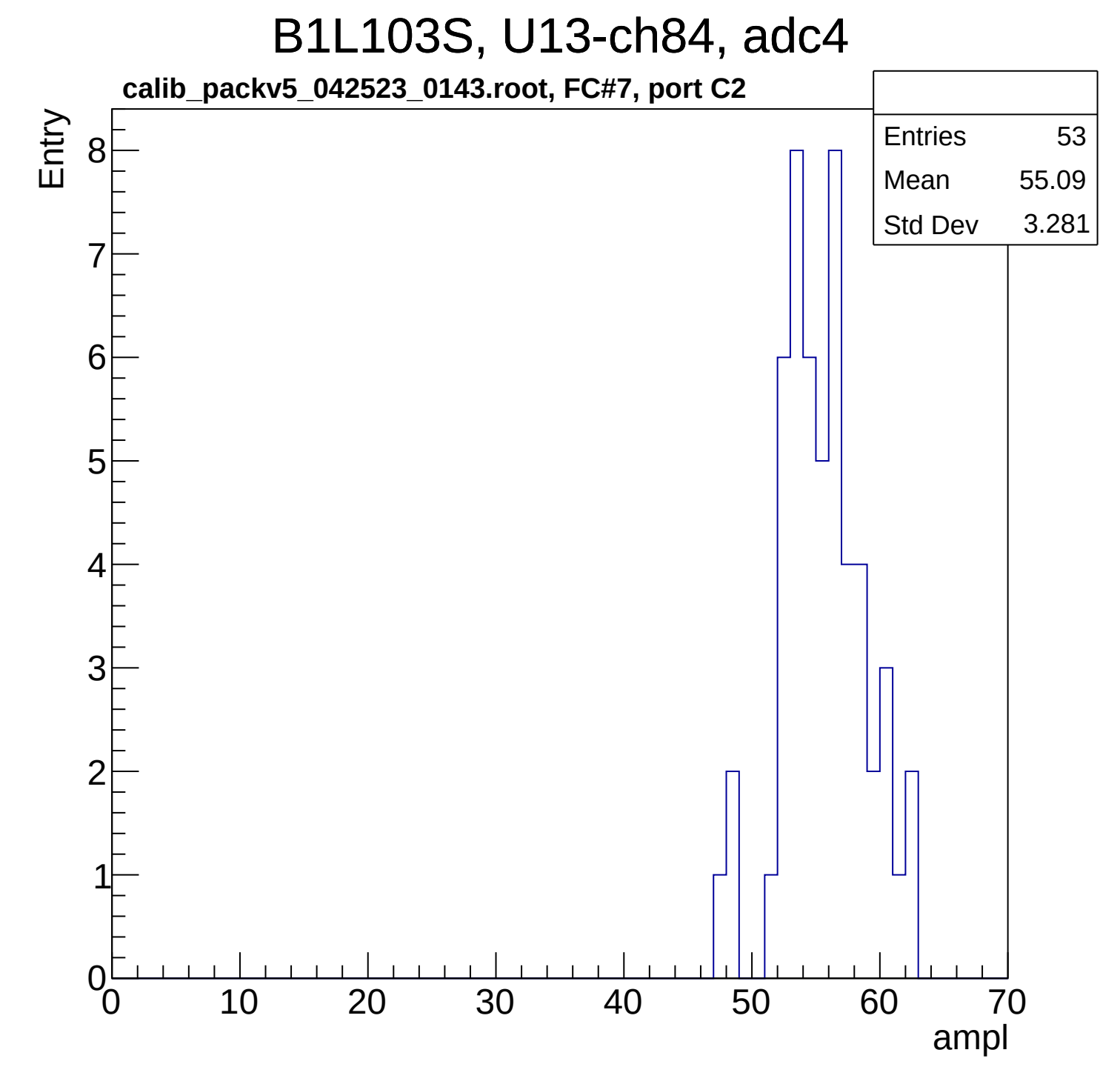
B1L103S, U13-ch84, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.09
Std Dev 3.281
Entry
ampl
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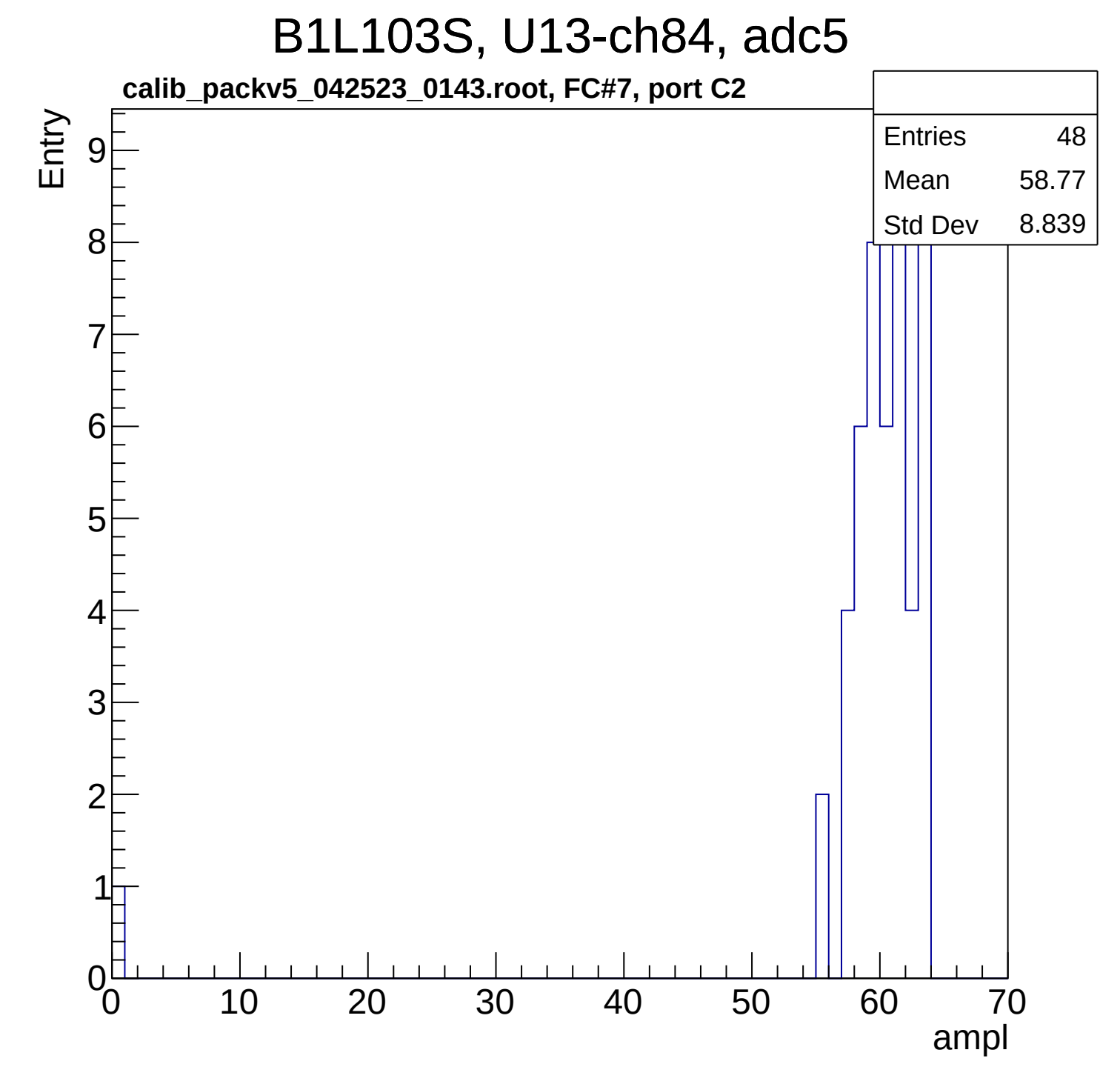
B1L103S, U13-ch84, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.77
Std Dev 8.839
Entry
ampl
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3
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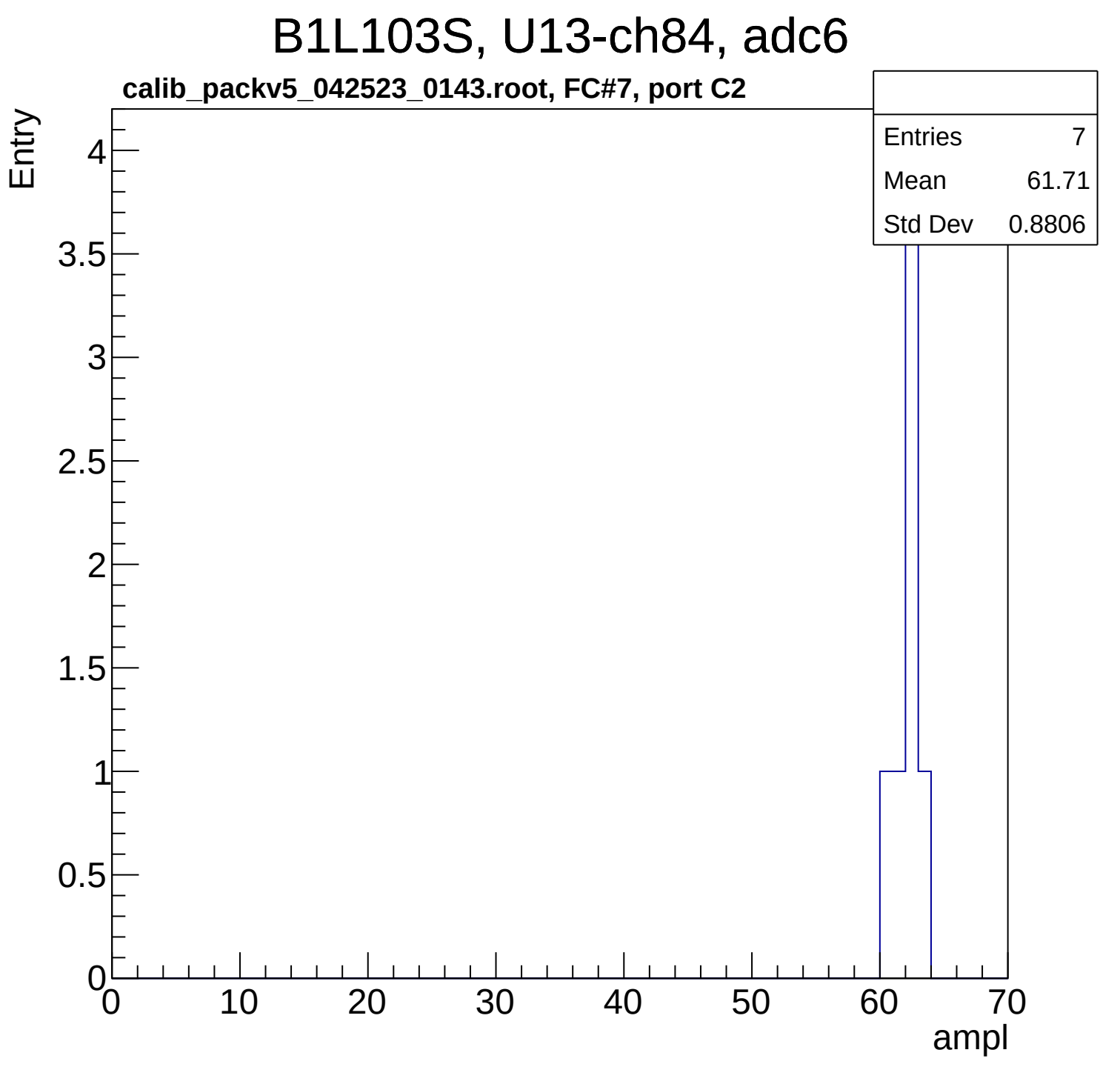
B1L103S, U13-ch84, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.71
Std Dev 0.8806
Entry
ampl
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1.5
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3.5
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60
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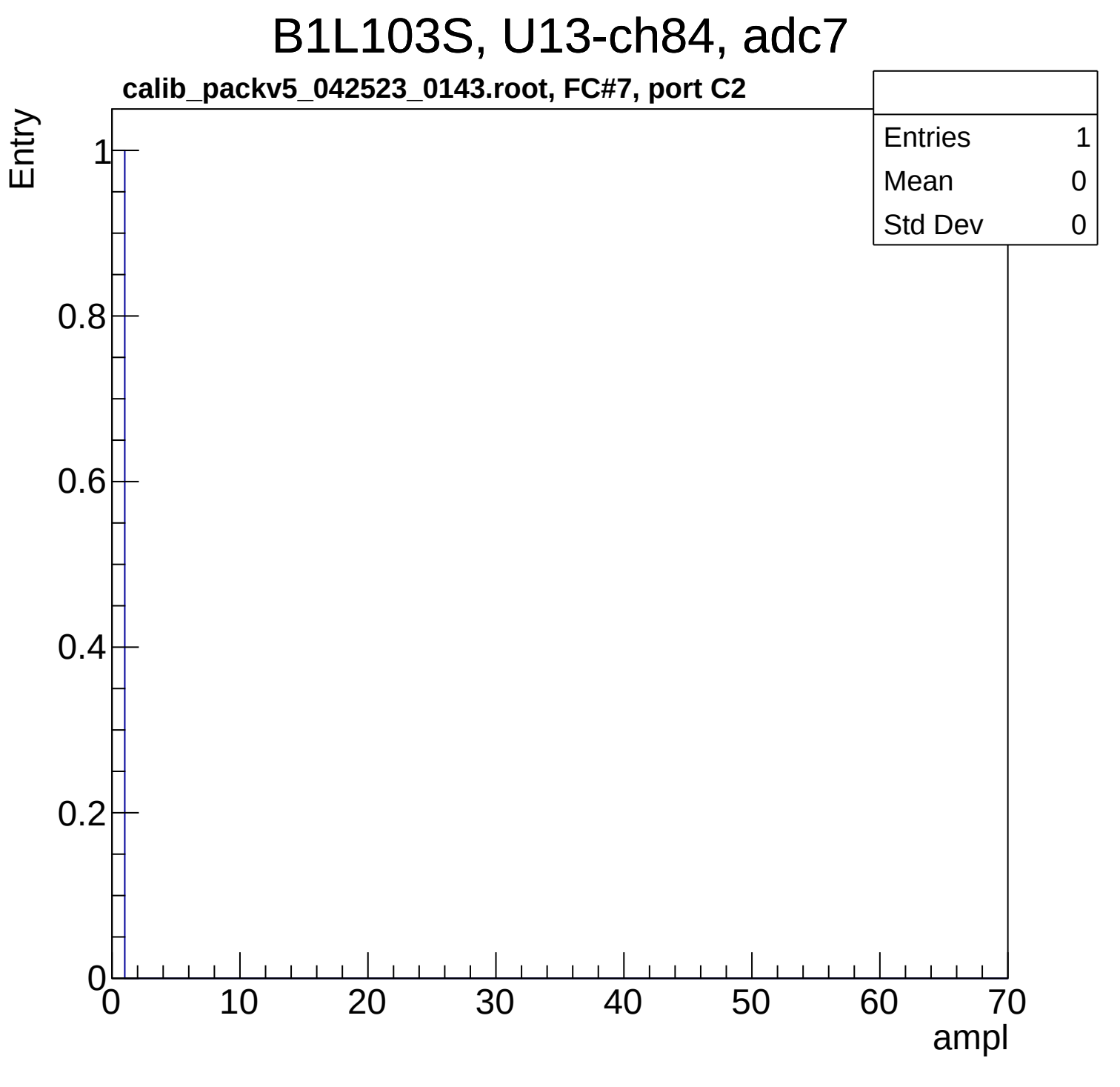
B1L103S, U13-ch84, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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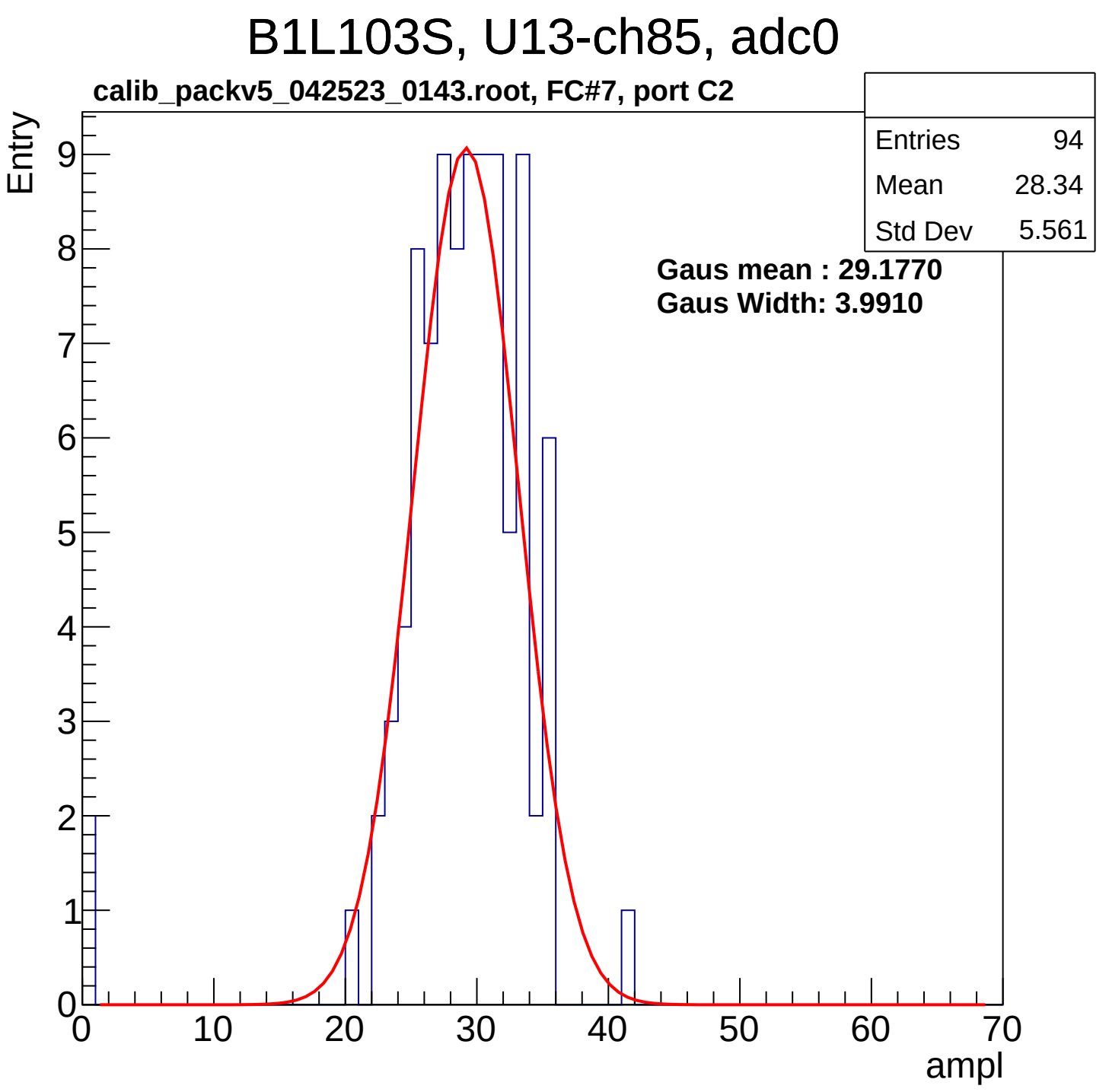
50
60
70
ampl


B1L103S, U13-ch85, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 94
Mean 28.34
Std Dev 5.561
Gaus mean : 29.1770
Gaus Width: 3.9910
Entry
ampl
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2
3
4
5
6
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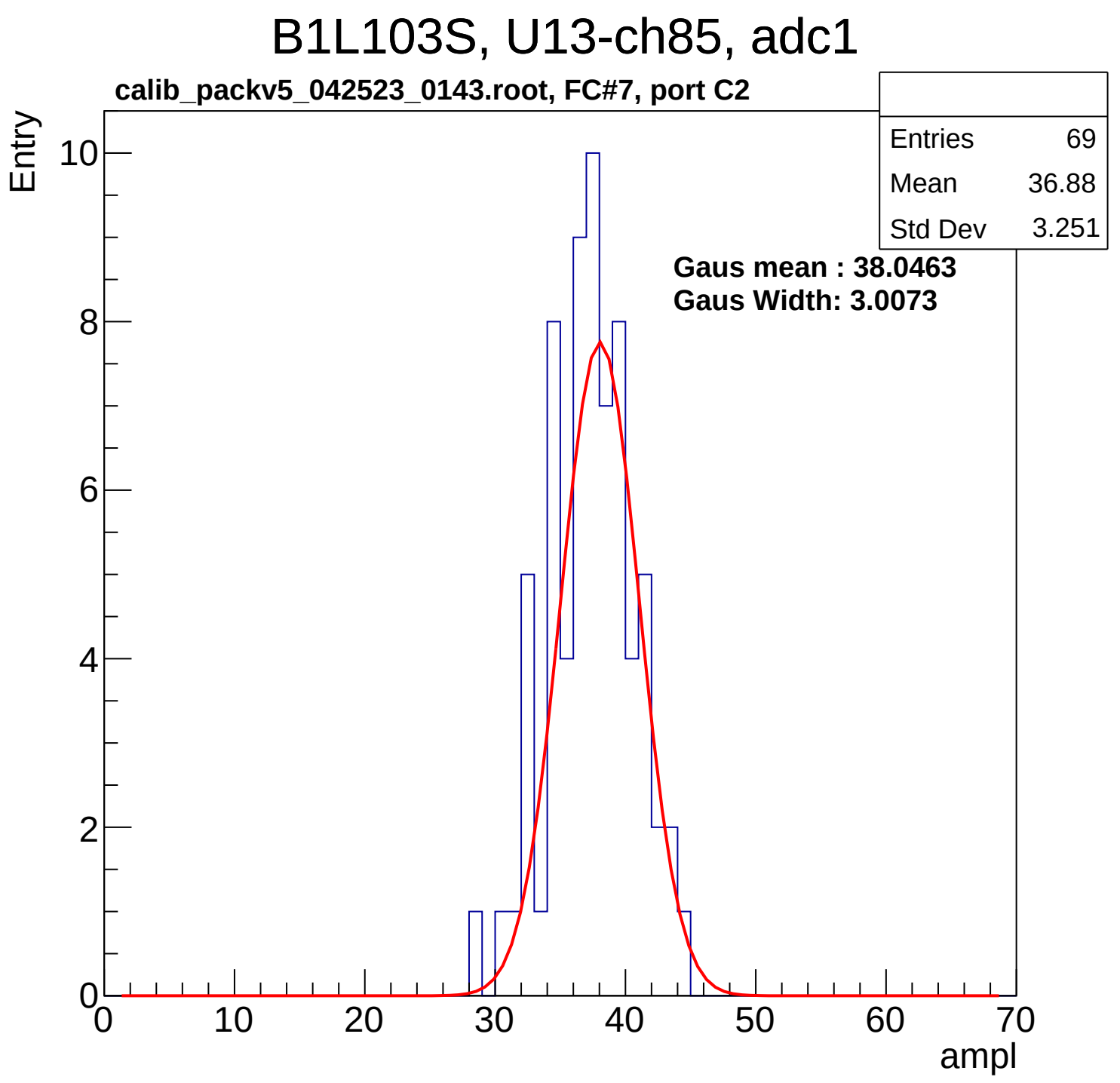
B1L103S, U13-ch85, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.88
Std Dev 3.251
Gaus mean : 38.0463
Gaus Width: 3.0073
Entry
ampl
0
2
4
6
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0
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60
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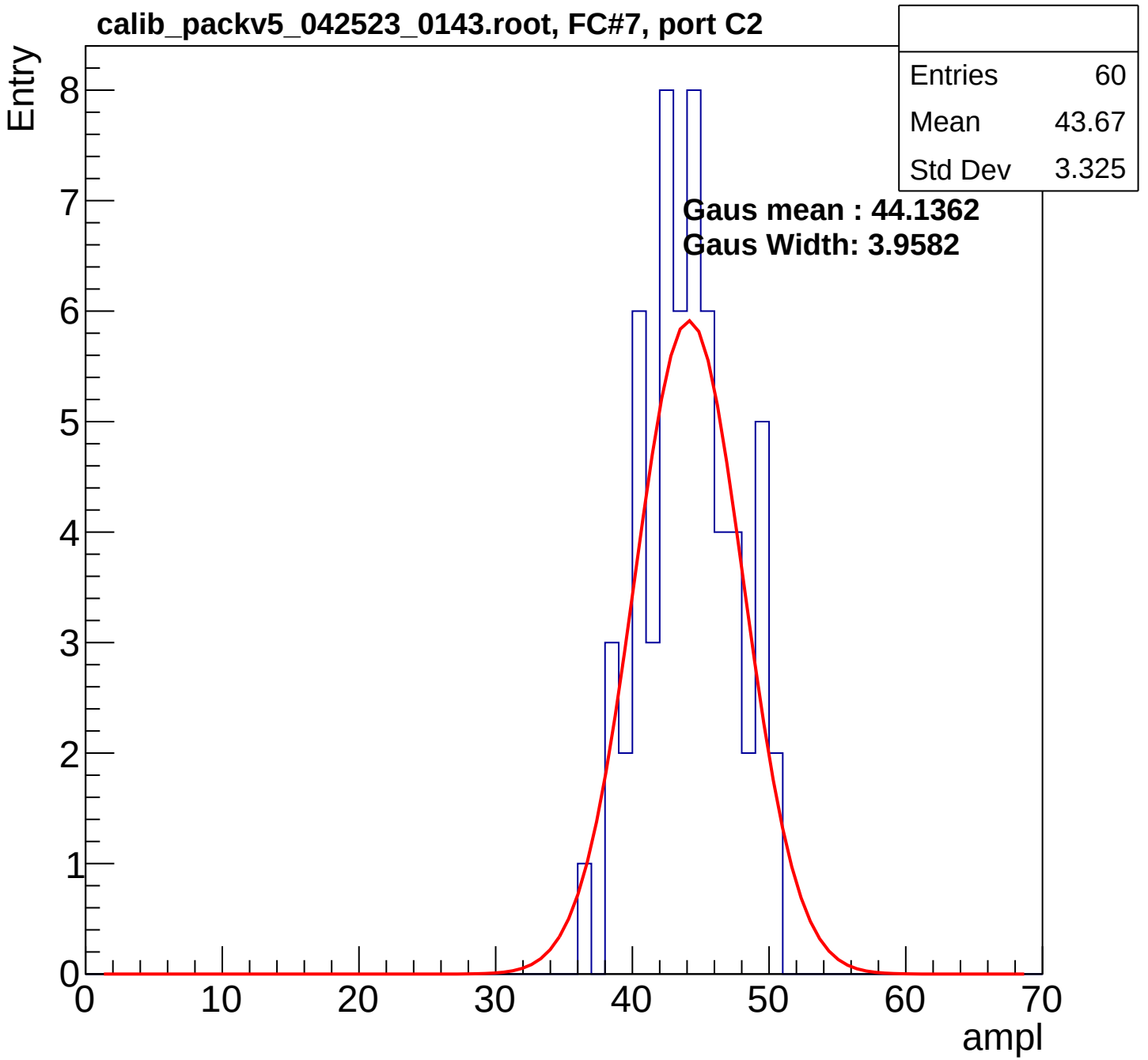
B1L103S, U13-ch85, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.67
Std Dev 3.325
Gaus mean : 44.1362
Gaus Width: 3.9582
Entry
ampl
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1
2
3
4
5
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0
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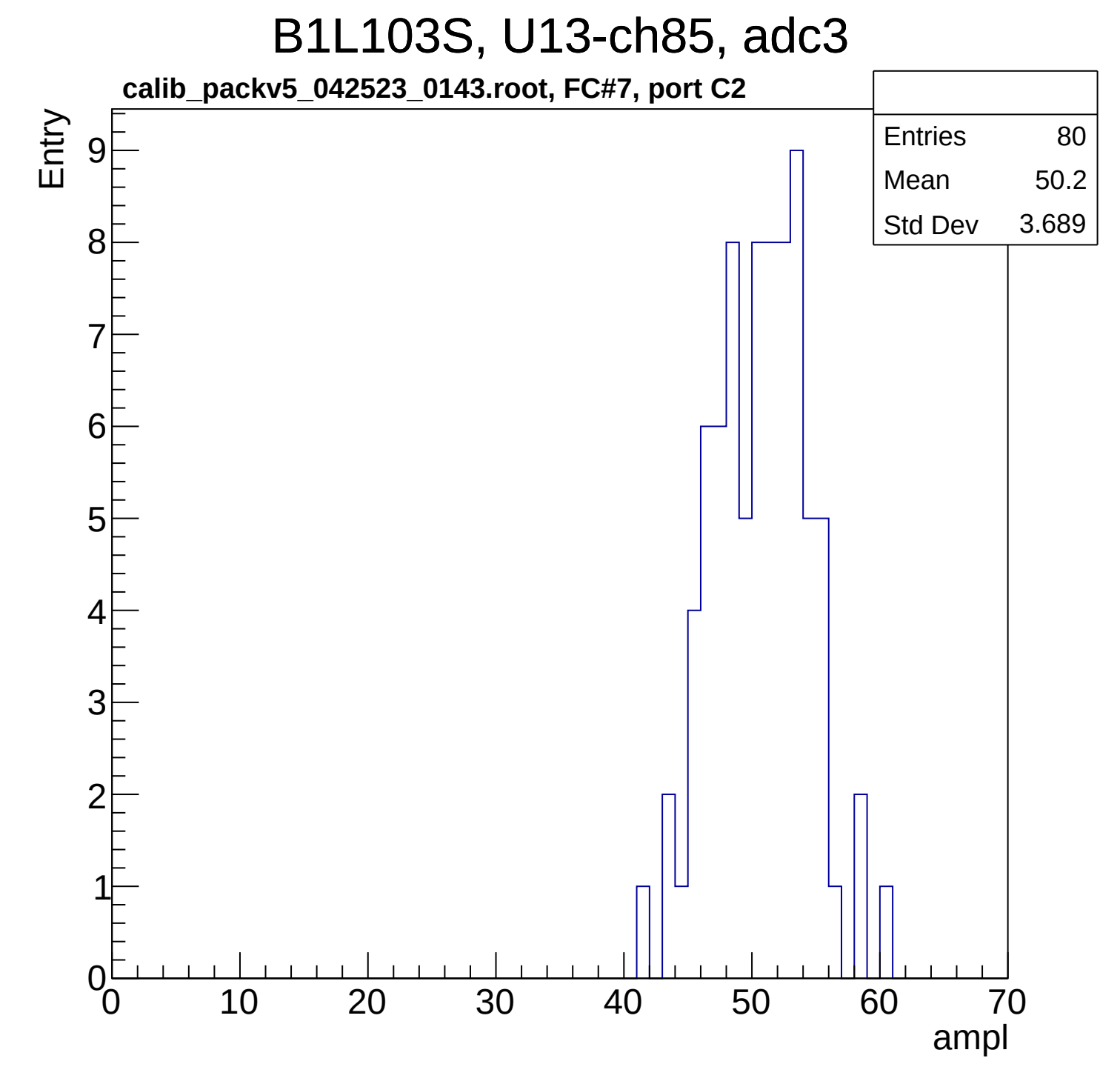
B1L103S, U13-ch85, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 50.2
Std Dev 3.689
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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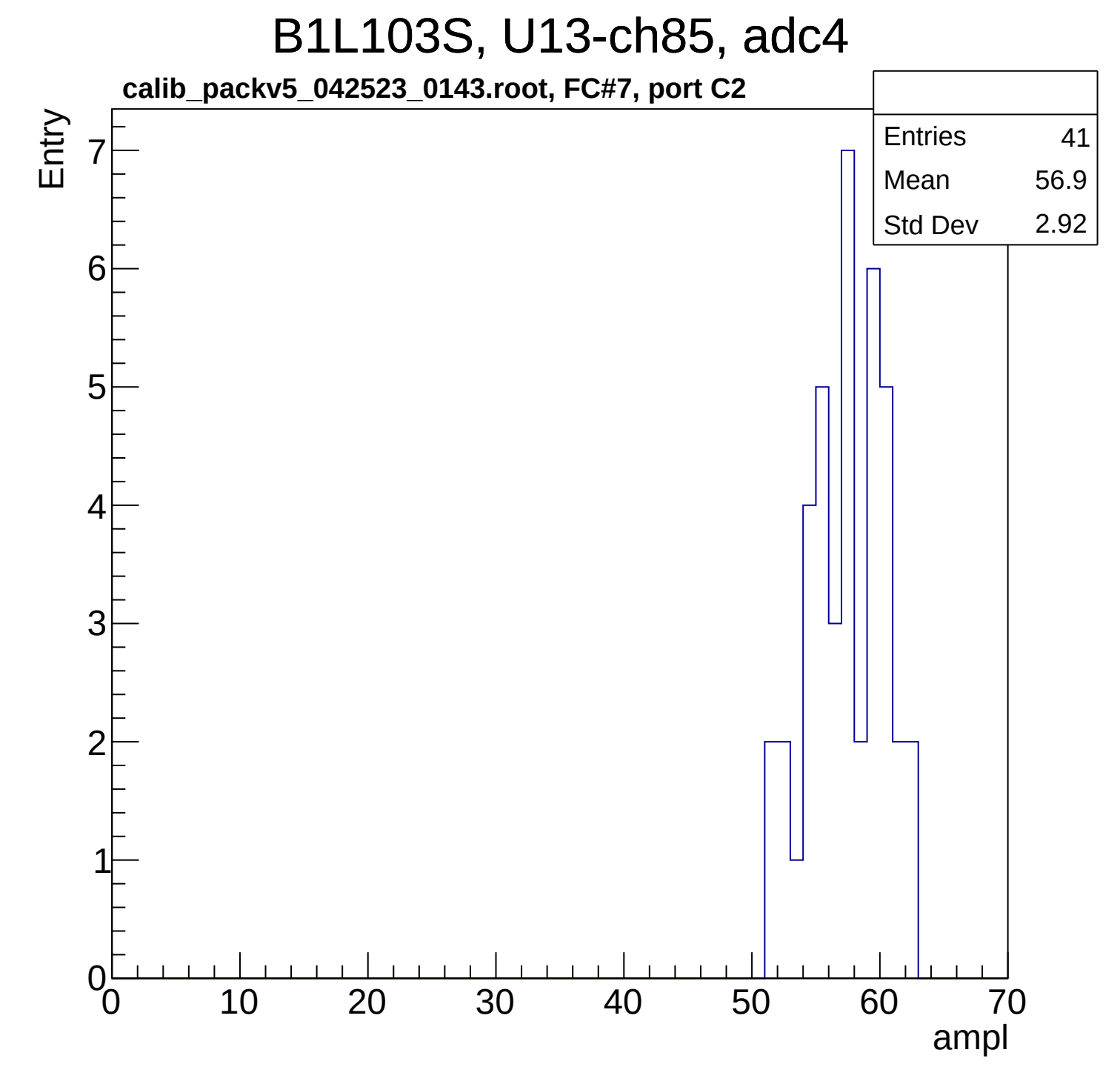
B1L103S, U13-ch85, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 56.9
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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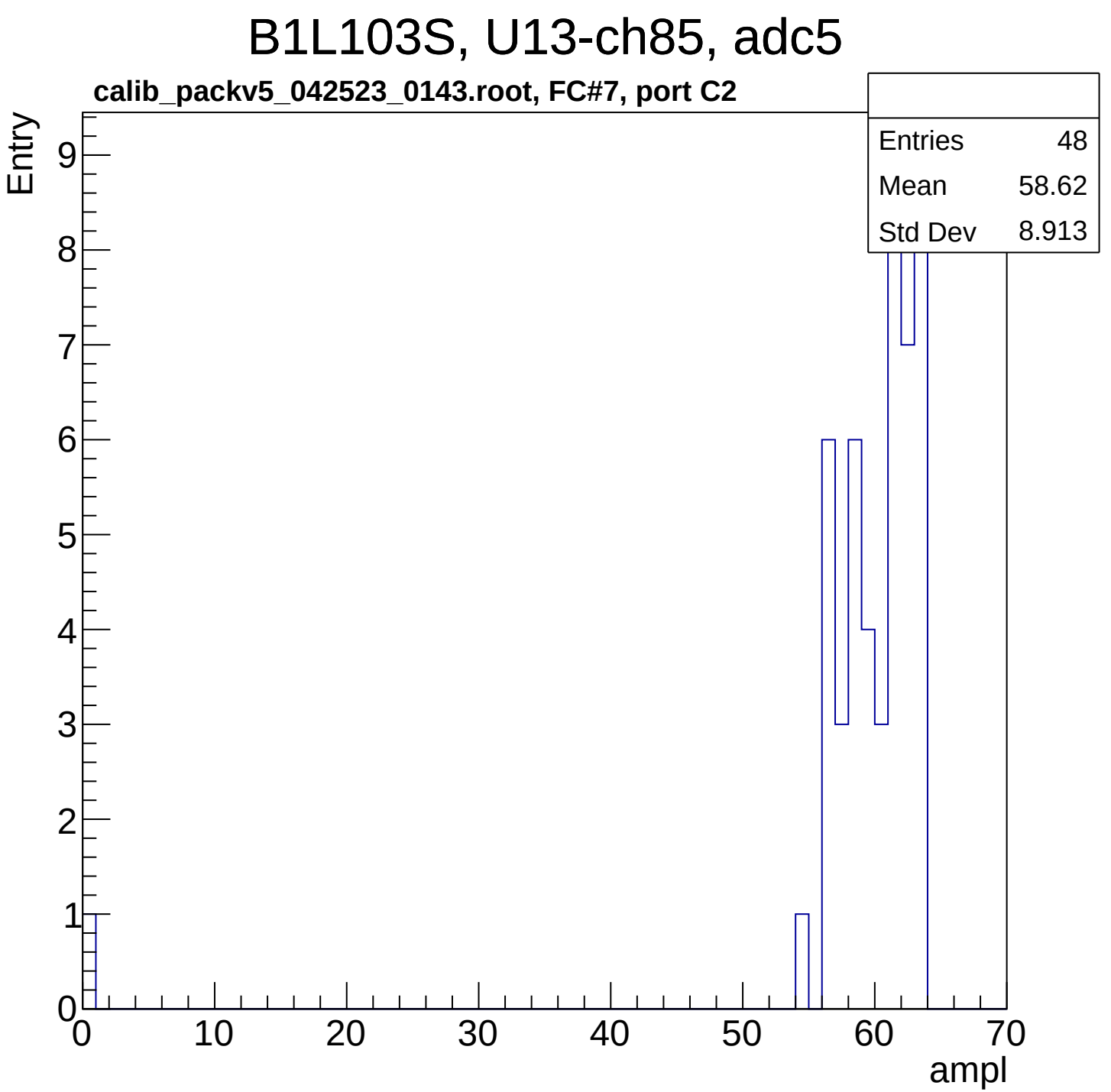
B1L103S, U13-ch85, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.62
Std Dev 8.913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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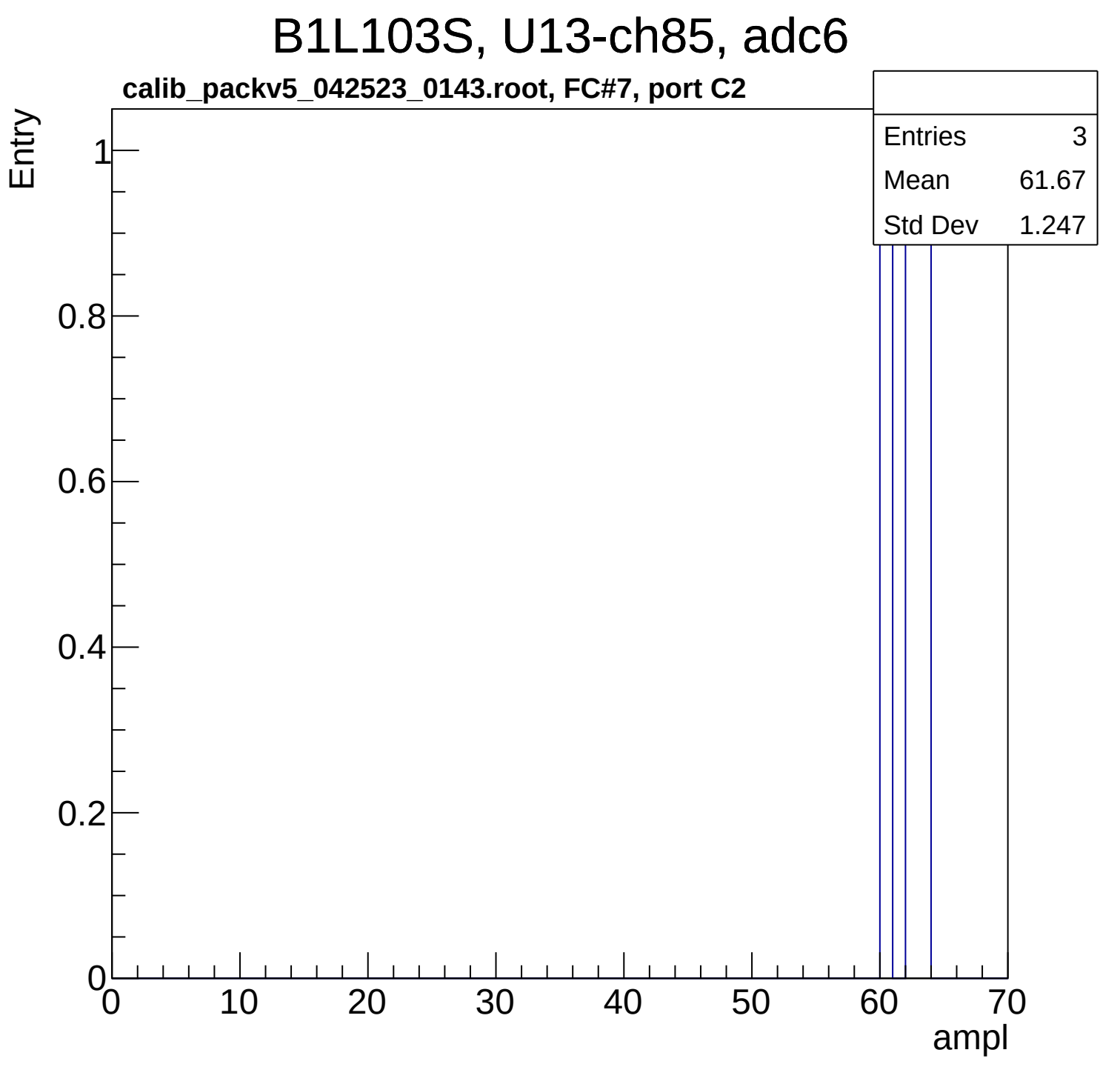
B1L103S, U13-ch85, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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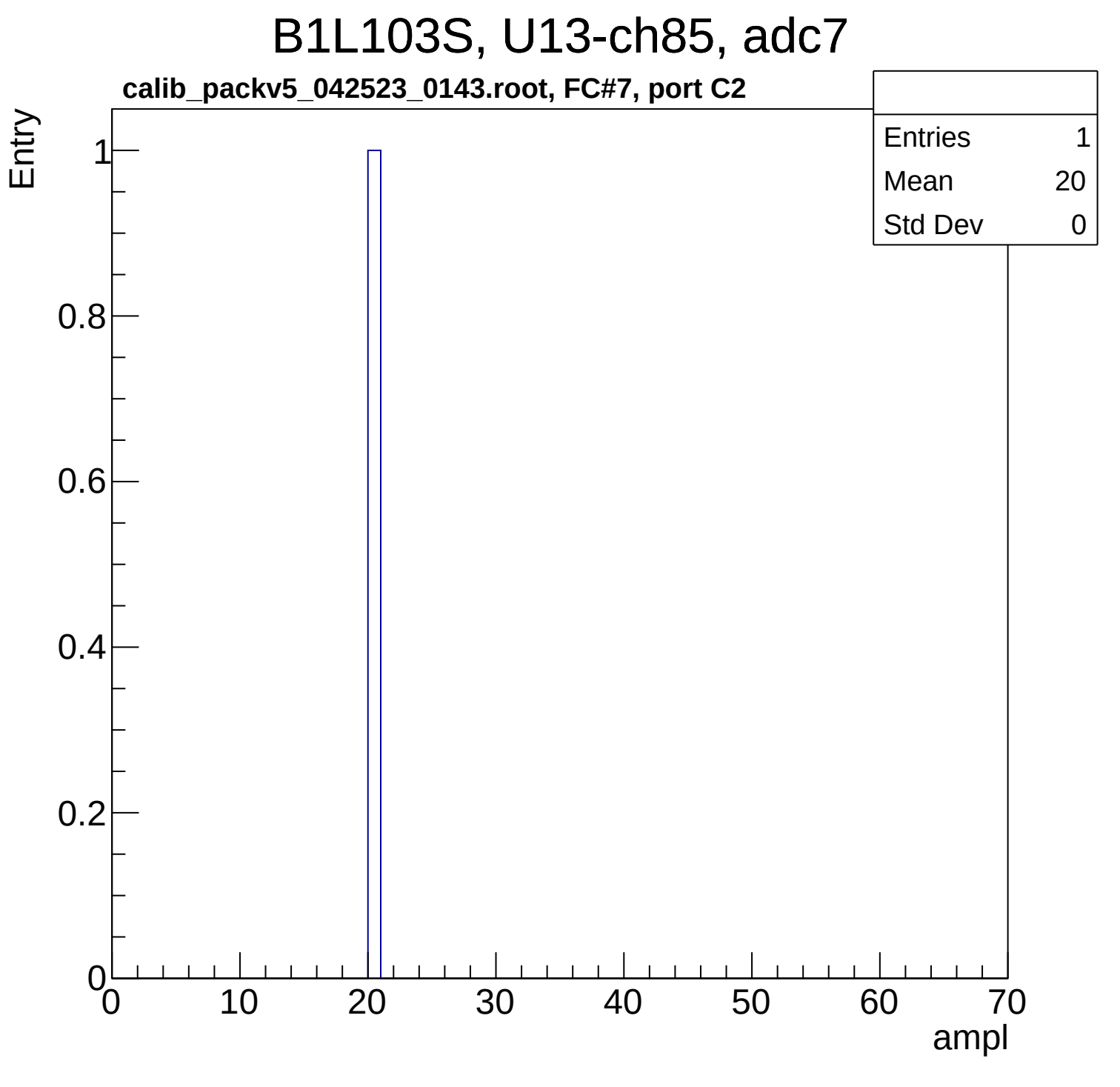
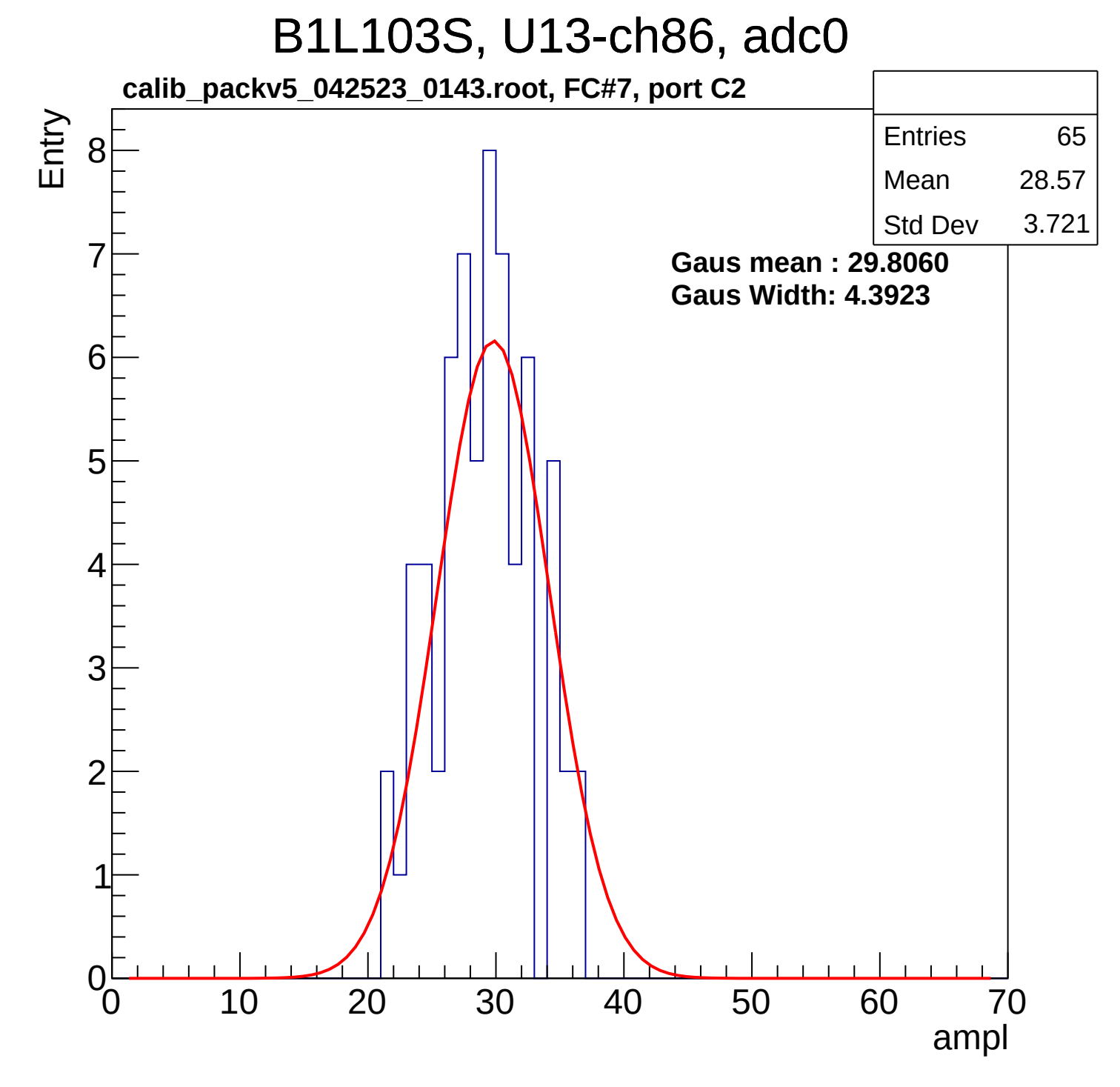
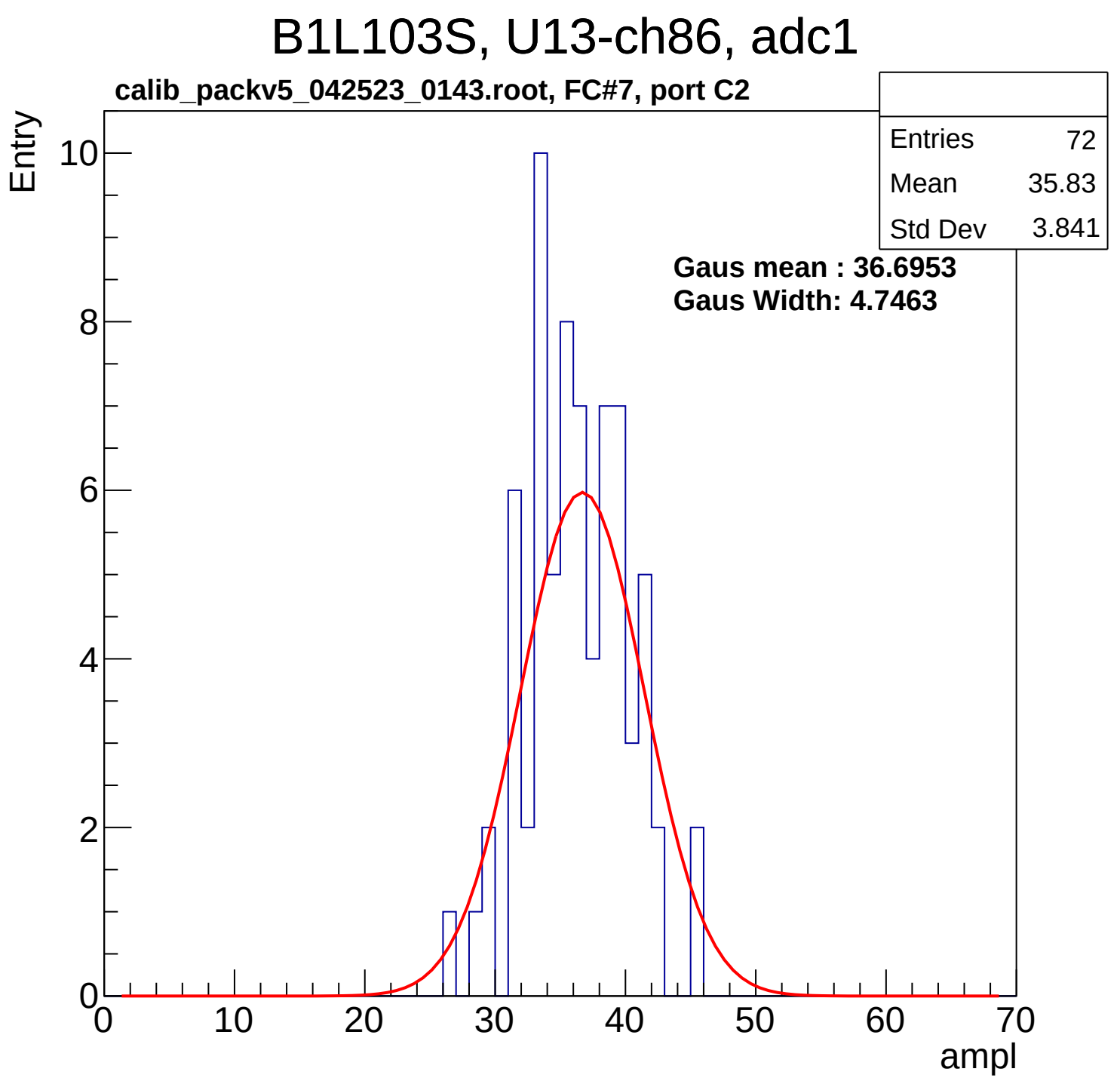
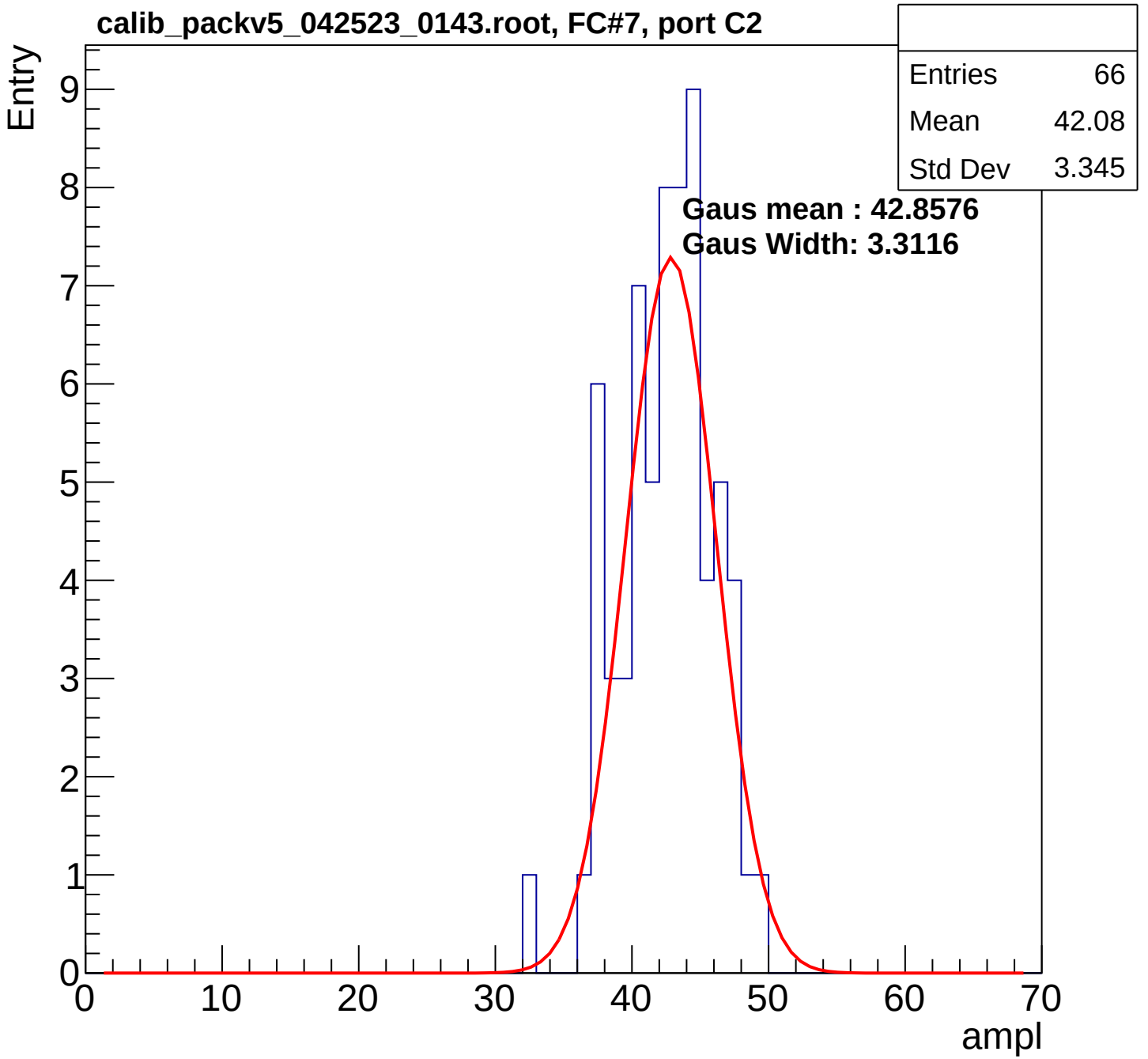
40
50
60
70


B1L103S, U13-ch85, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch86, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 28.57
Std Dev 3.721
Gaus mean : 29.8060
Gaus Width: 4.3923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch86, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.83
Std Dev 3.841
Gaus mean : 36.6953
Gaus Width: 4.7463
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch86, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.08
Std Dev 3.345
Gaus mean : 42.8576
Gaus Width: 3.3116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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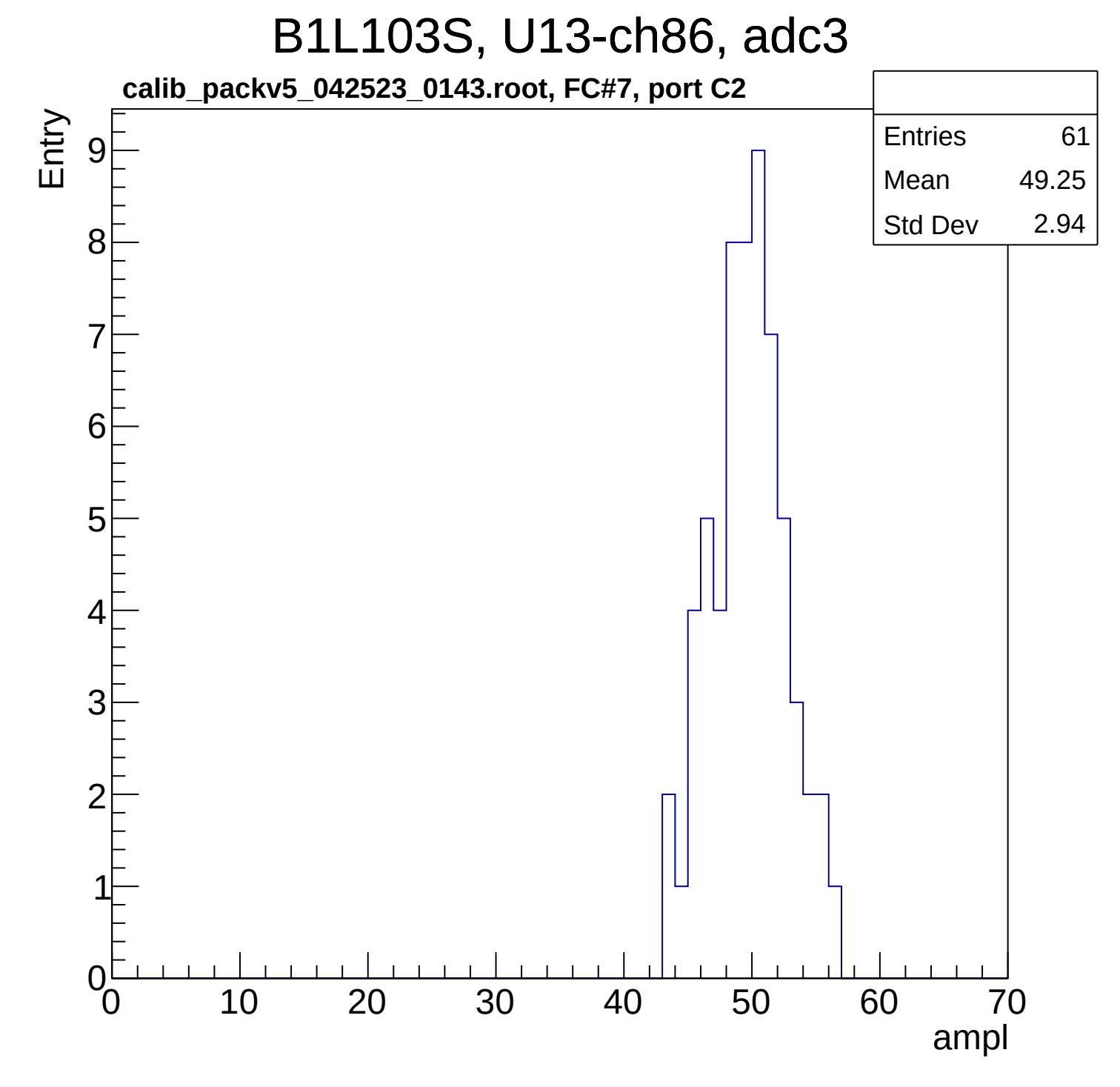
B1L103S, U13-ch86, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 49.25
Std Dev 2.94
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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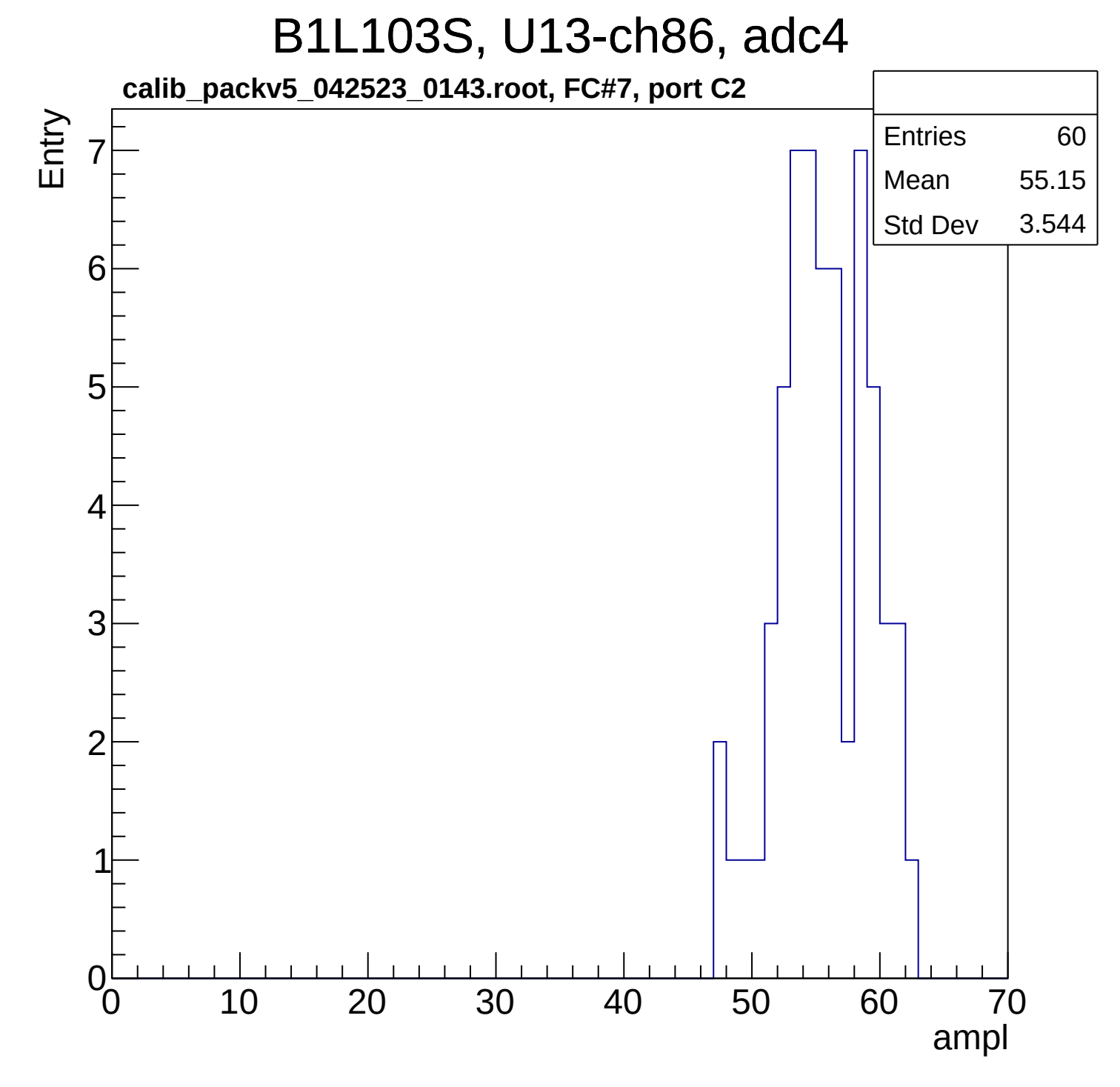
B1L103S, U13-ch86, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 55.15
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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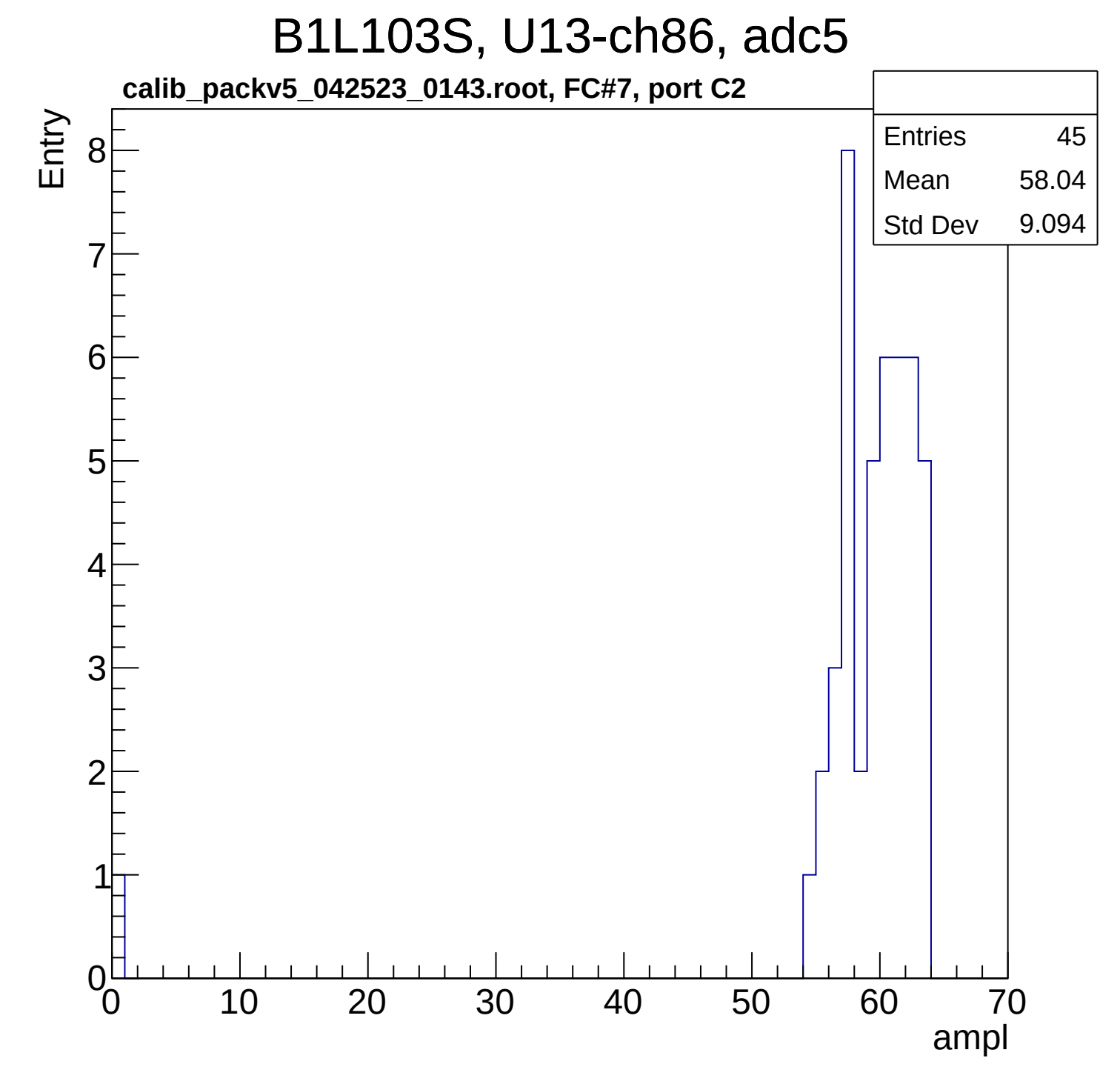
B1L103S, U13-ch86, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.04
Std Dev 9.094
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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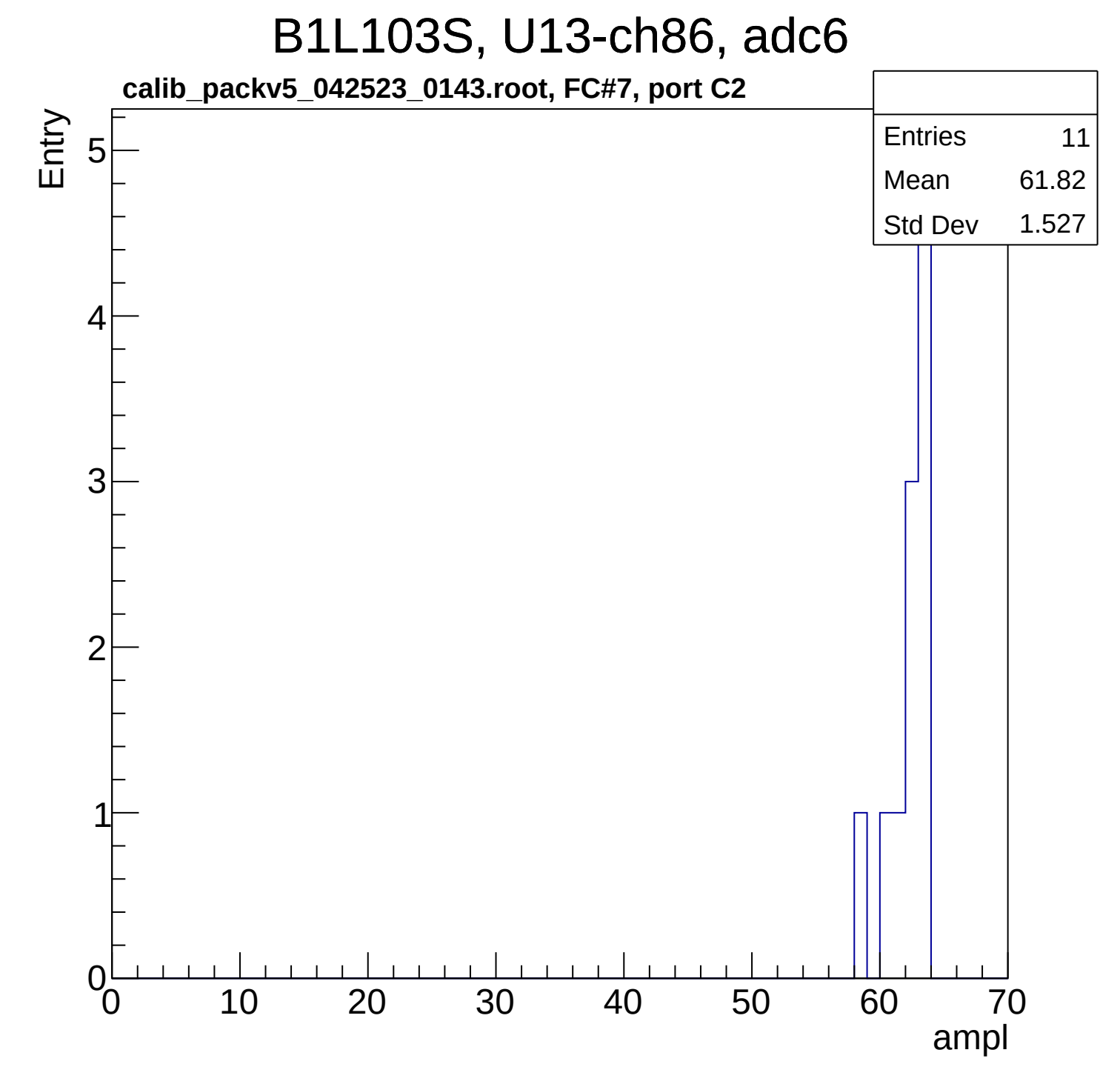
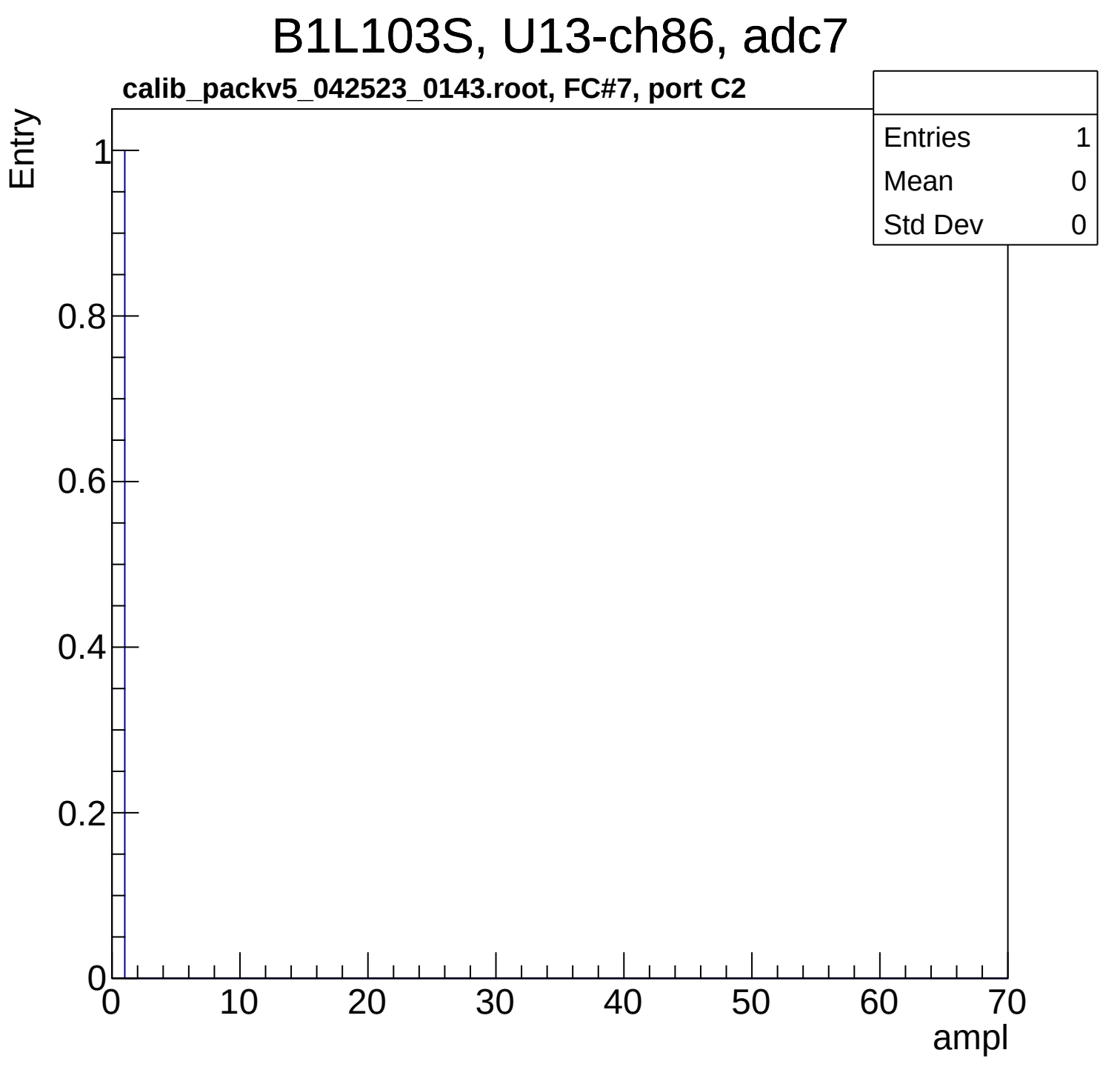
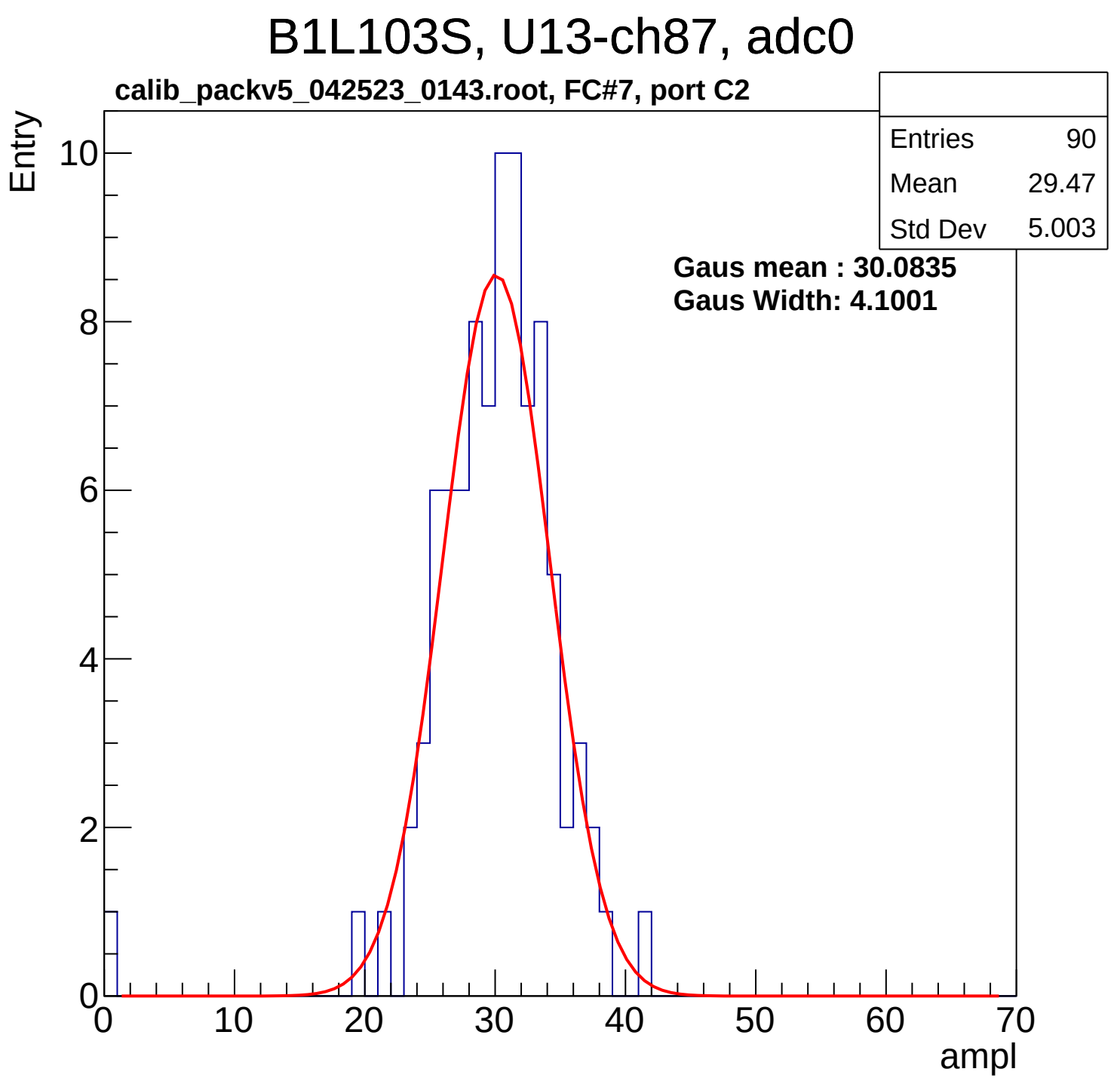
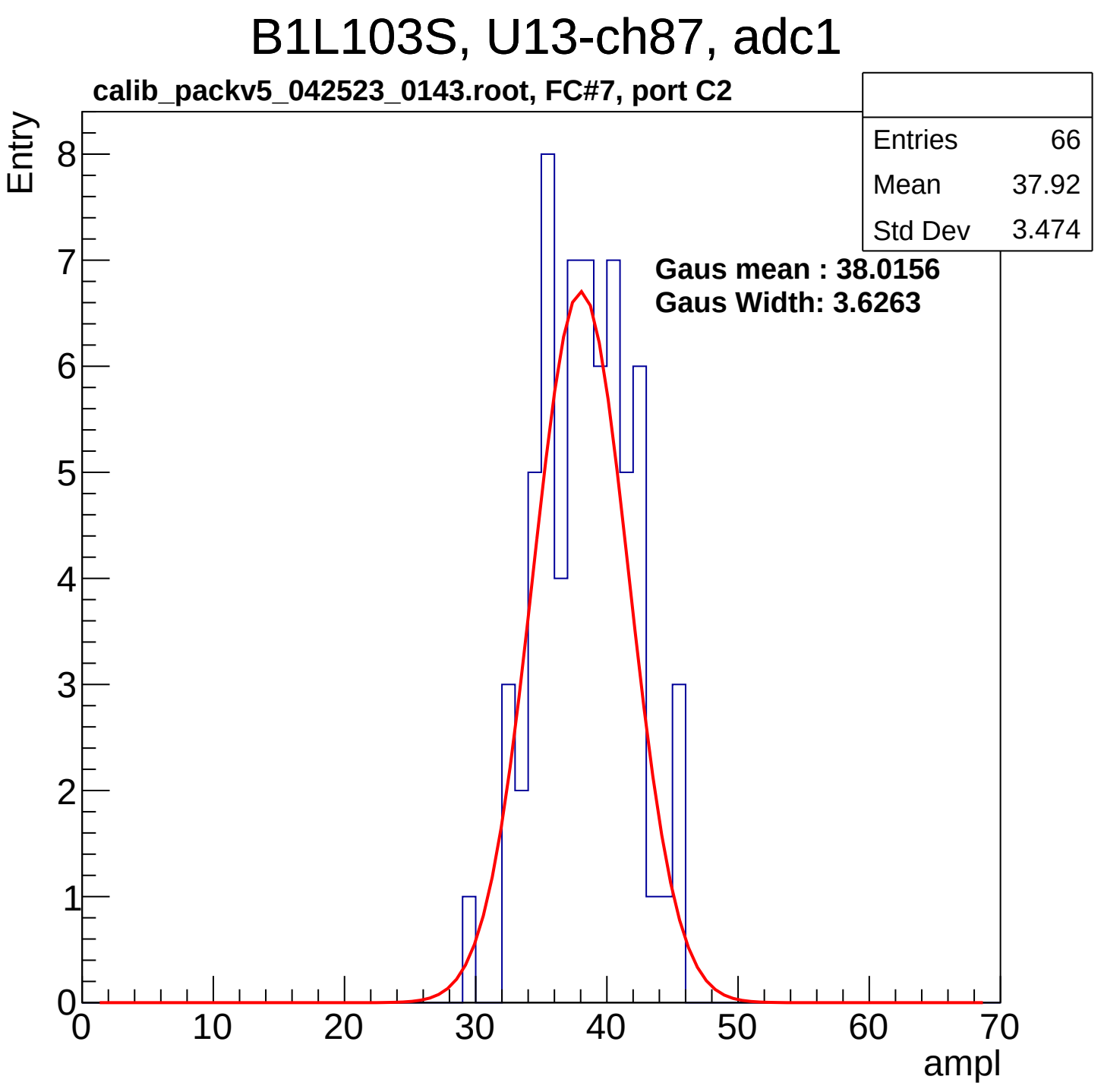

B1L103S, U13-ch86, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61.82
Std Dev 1.527
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U13-ch86, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch87, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 29.47
Std Dev 5.003
Gaus mean : 30.0835
Gaus Width: 4.1001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch87, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 37.92
Std Dev 3.474
Gaus mean : 38.0156
Gaus Width: 3.6263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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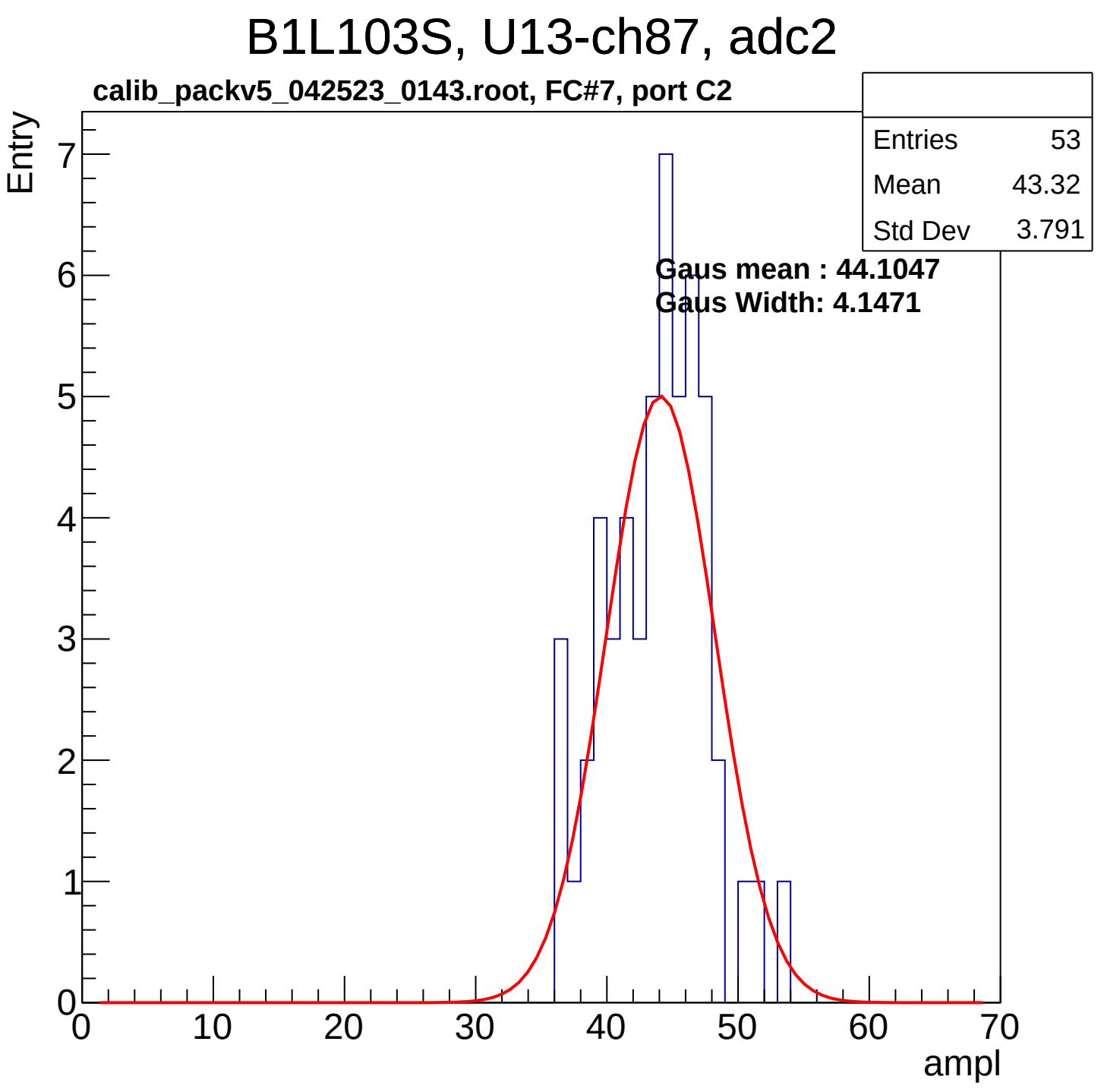
B1L103S, U13-ch87, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 43.32
Std Dev 3.791
Gaus mean : 44.1047
Gaus Width: 4.1471
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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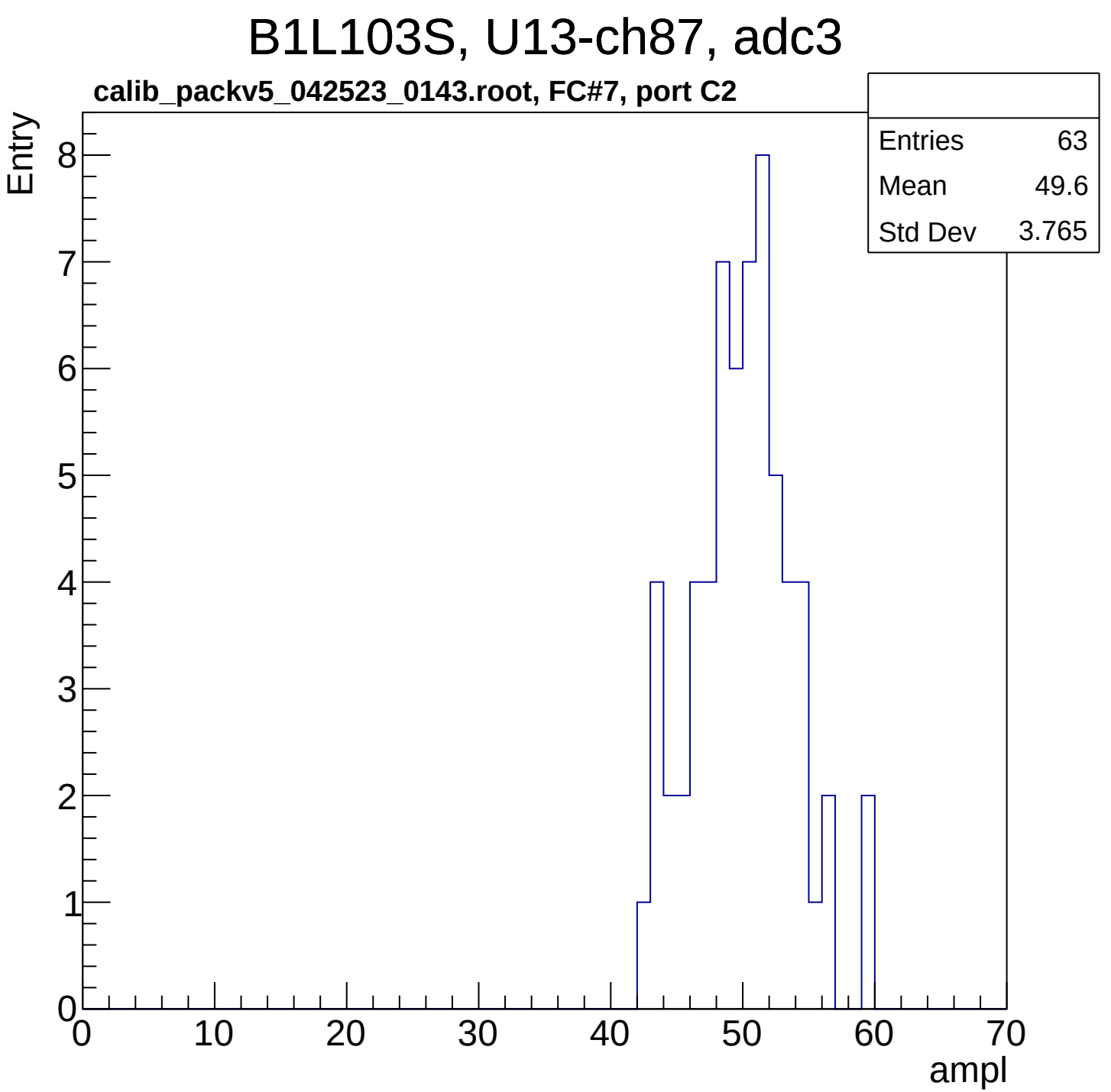
B1L103S, U13-ch87, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.6
Std Dev 3.765
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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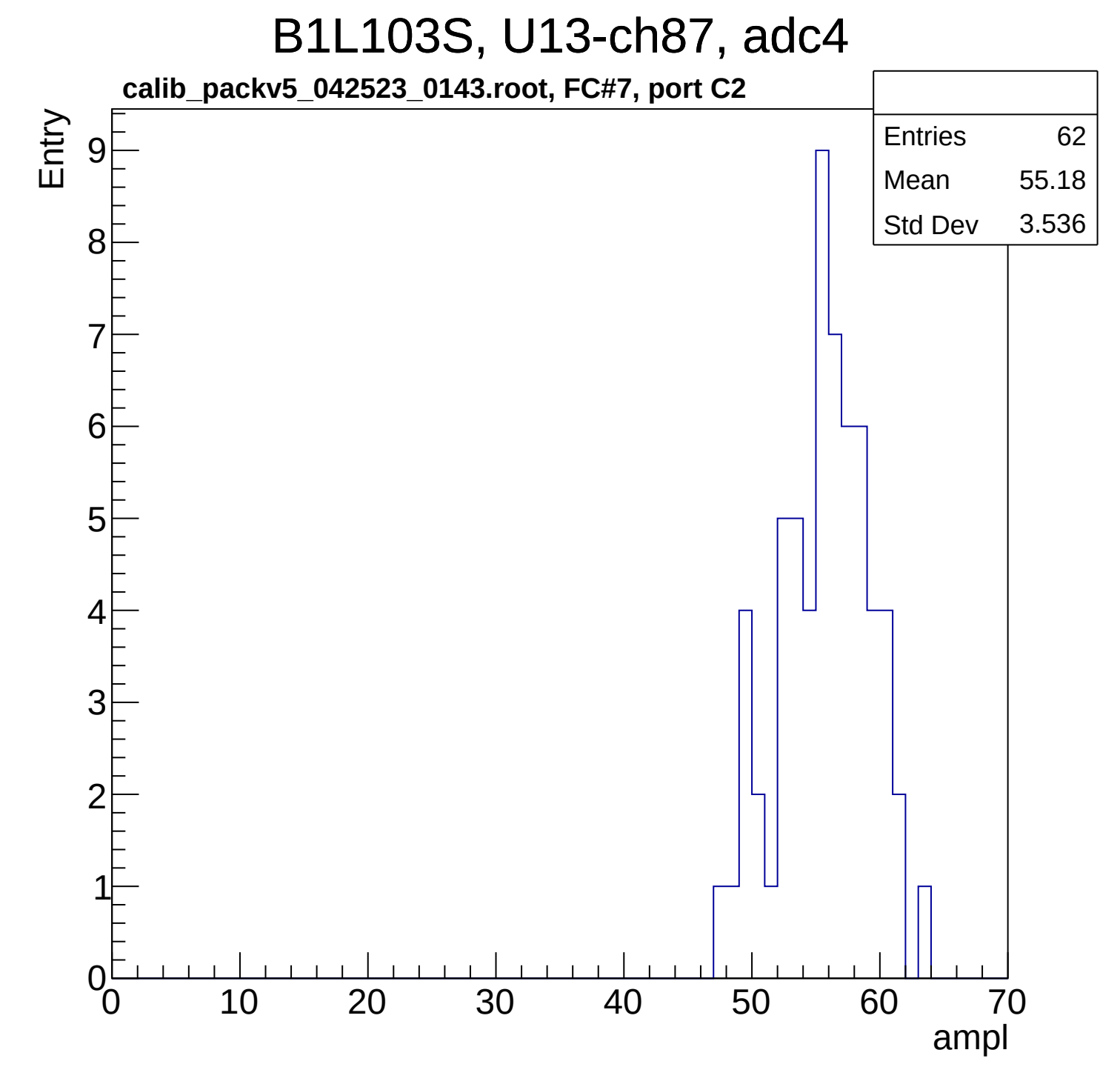
B1L103S, U13-ch87, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.18
Std Dev 3.536
Entry
ampl
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3
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5
6
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0
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60
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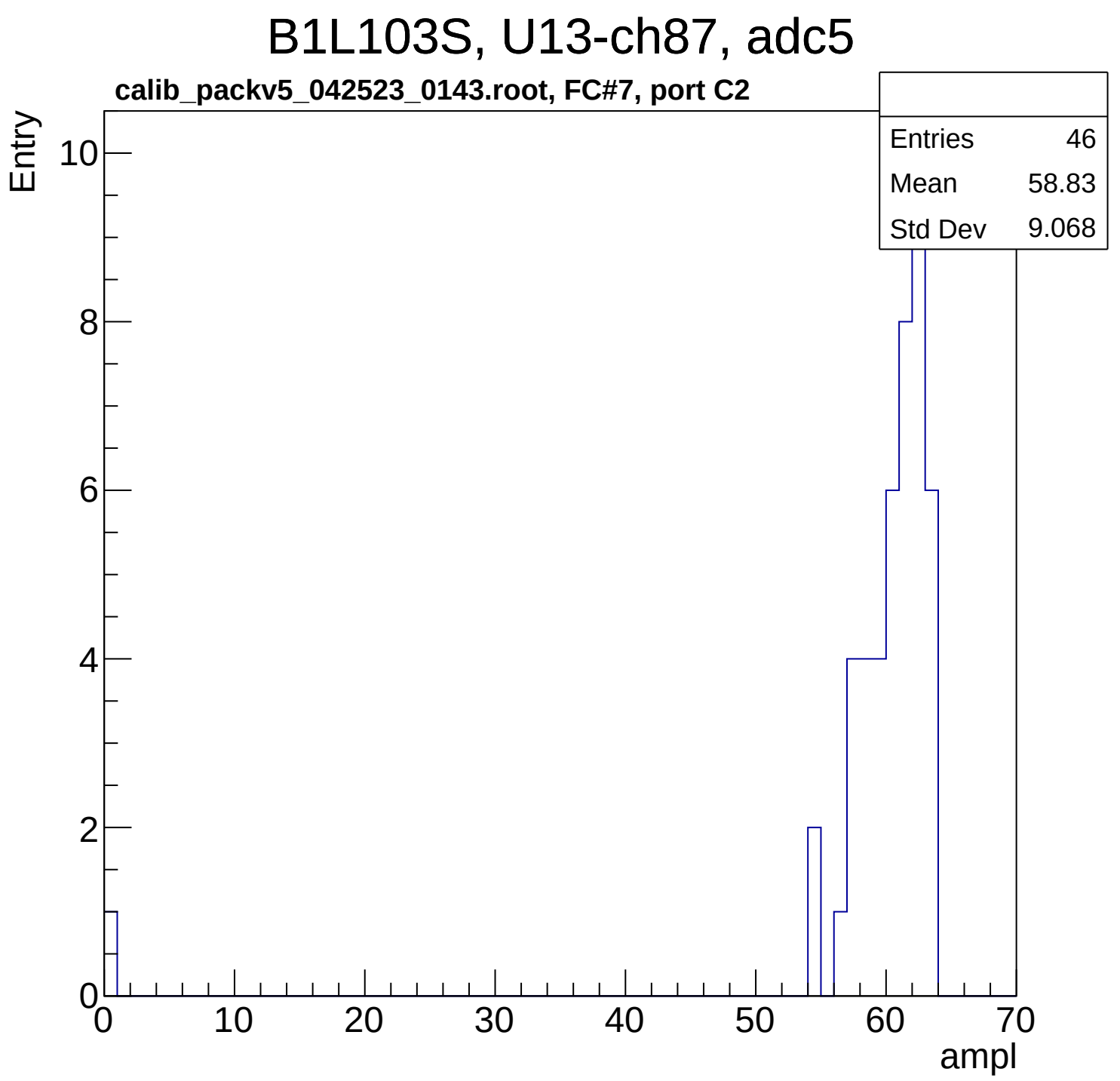
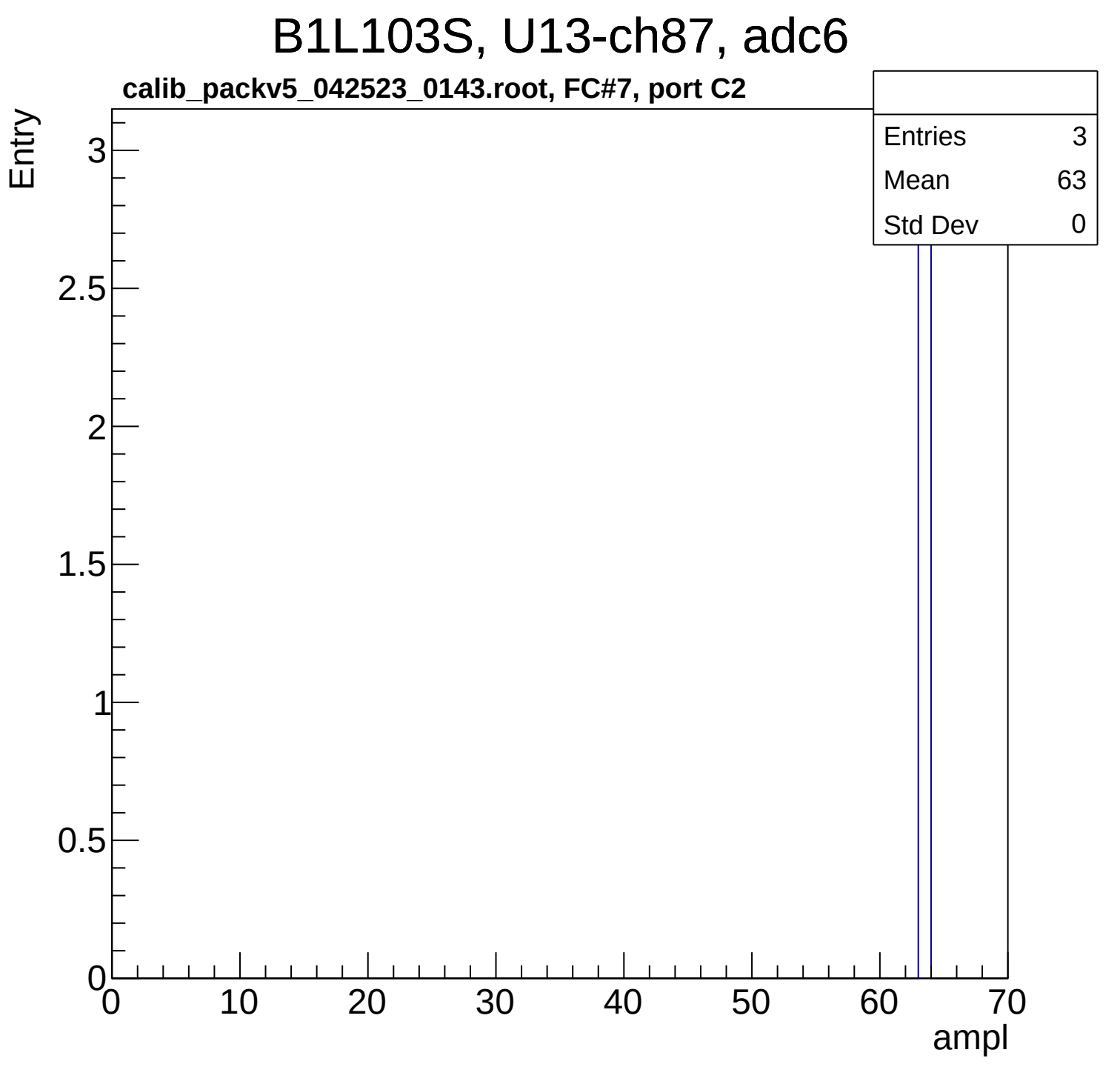

B1L103S, U13-ch87, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.83
Std Dev 9.068
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch87, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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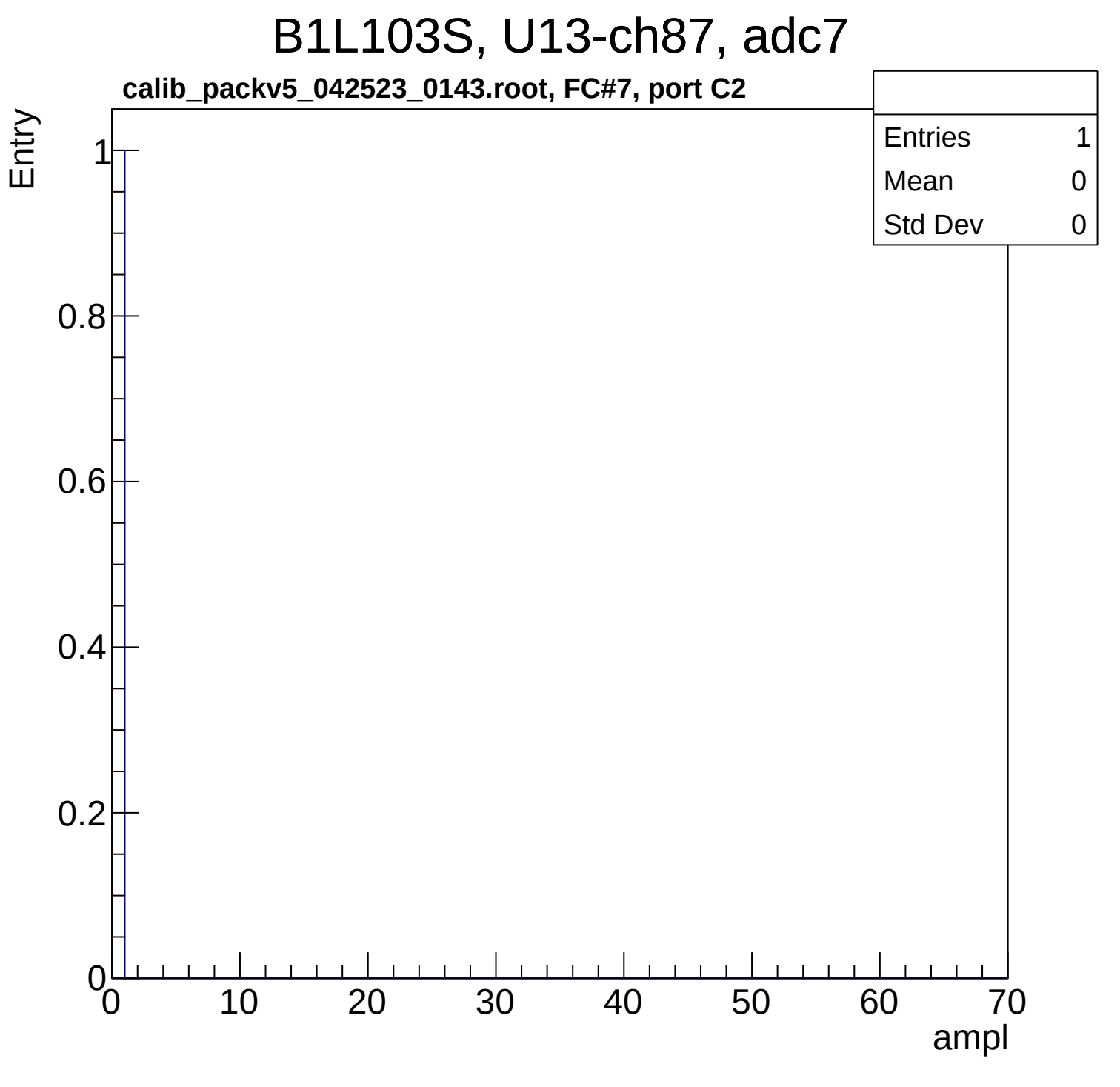
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch87, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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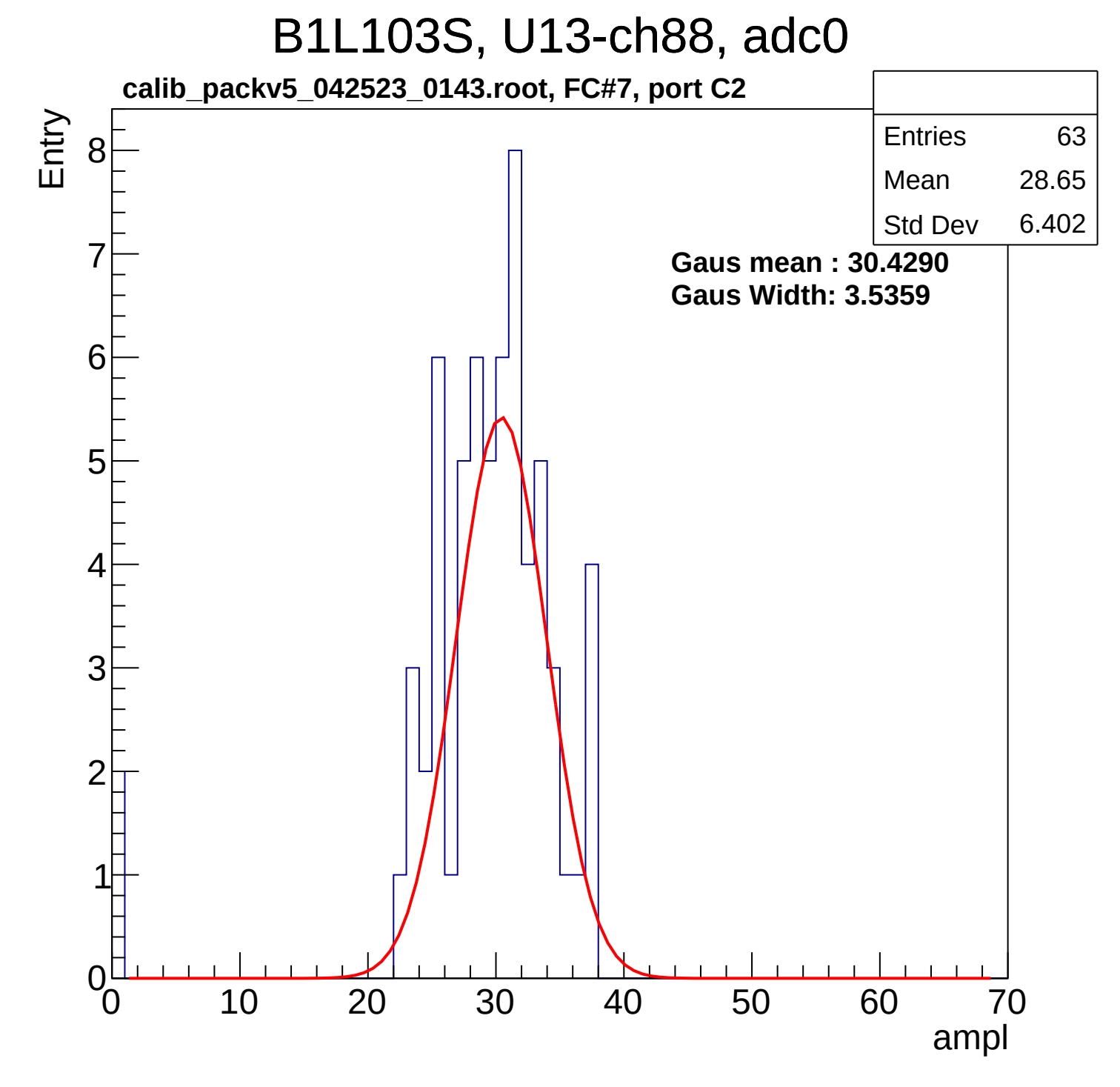
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch88, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 28.65
Std Dev 6.402
Gaus mean : 30.4290
Gaus Width: 3.5359
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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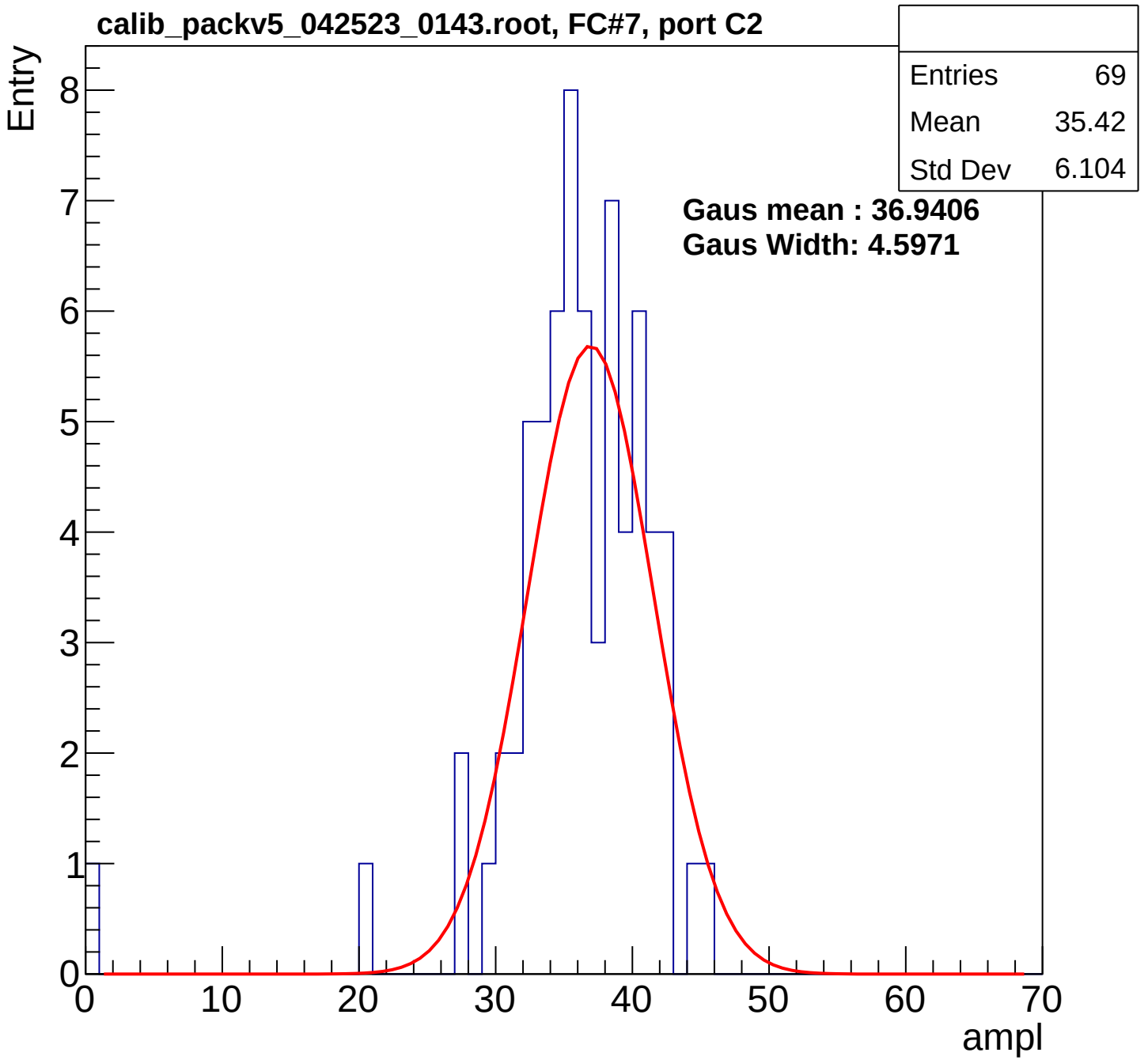
40
50
60
70


B1L103S, U13-ch88, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 35.42
Std Dev 6.104
Gaus mean : 36.9406
Gaus Width: 4.5971
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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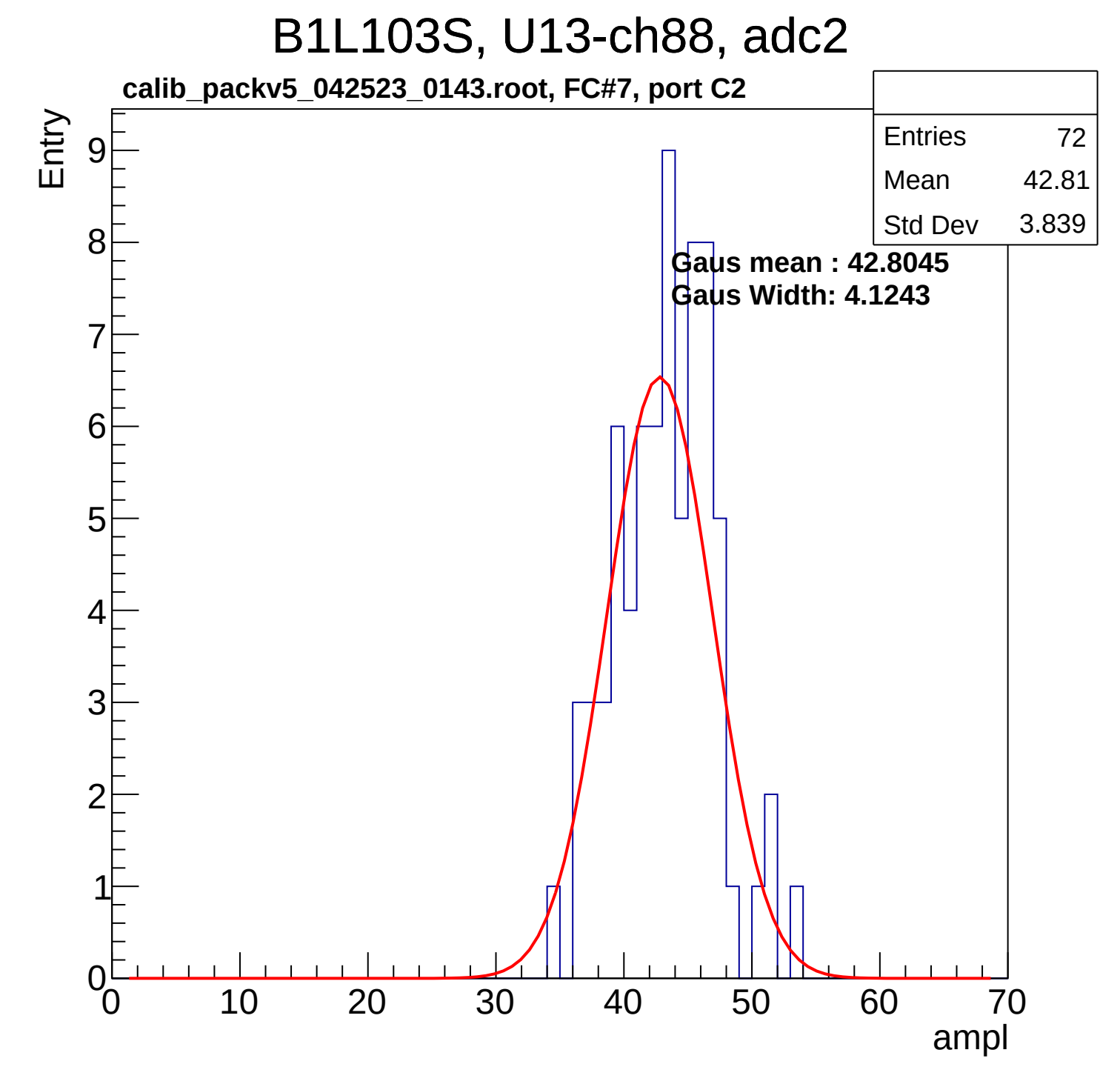
B1L103S, U13-ch88, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 42.81
Std Dev 3.839
Gaus mean : 42.8045
Gaus Width: 4.1243
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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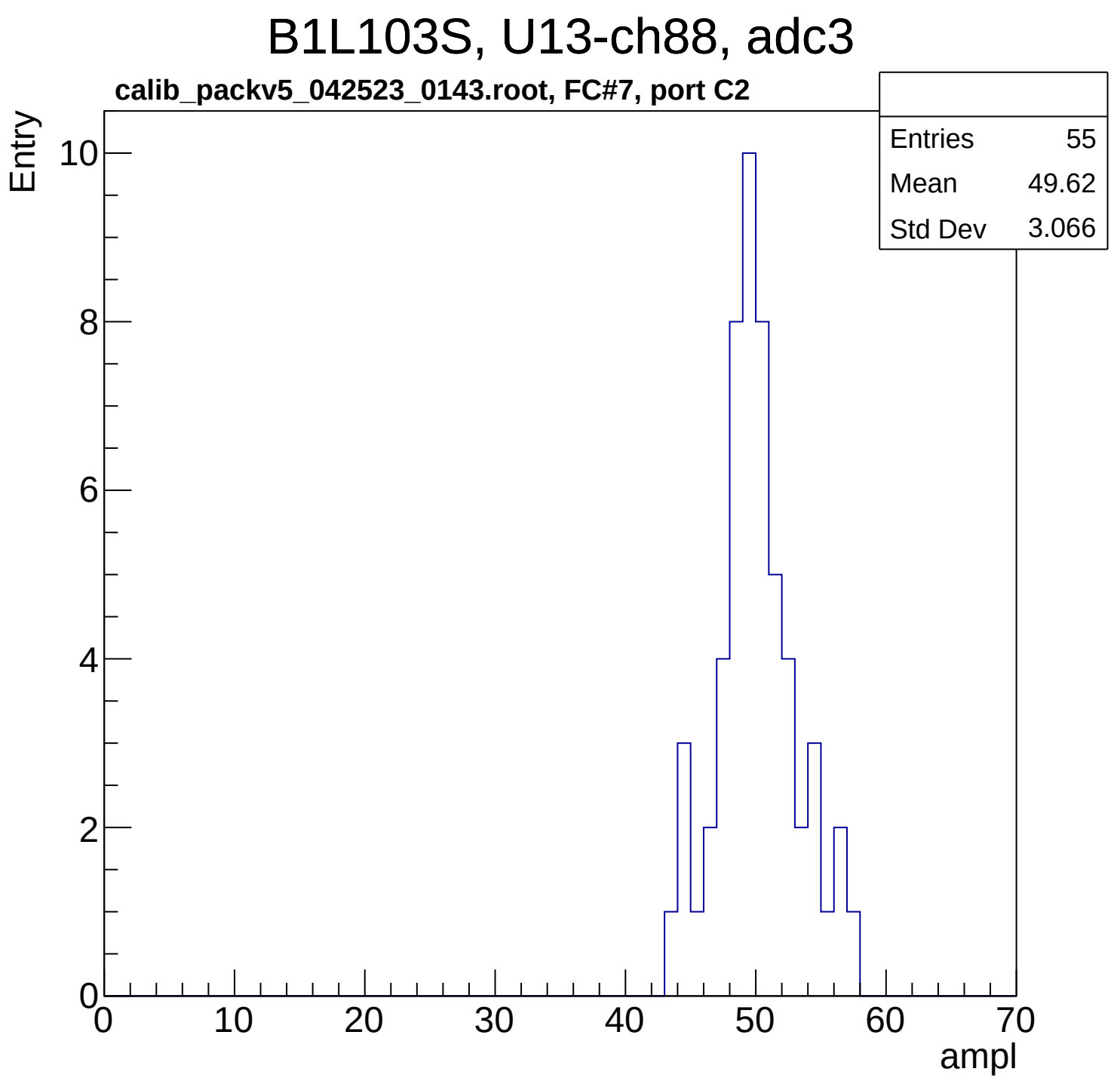
B1L103S, U13-ch88, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 49.62
Std Dev 3.066
Entry
0
2
4
6
8
10
0
10
20
30
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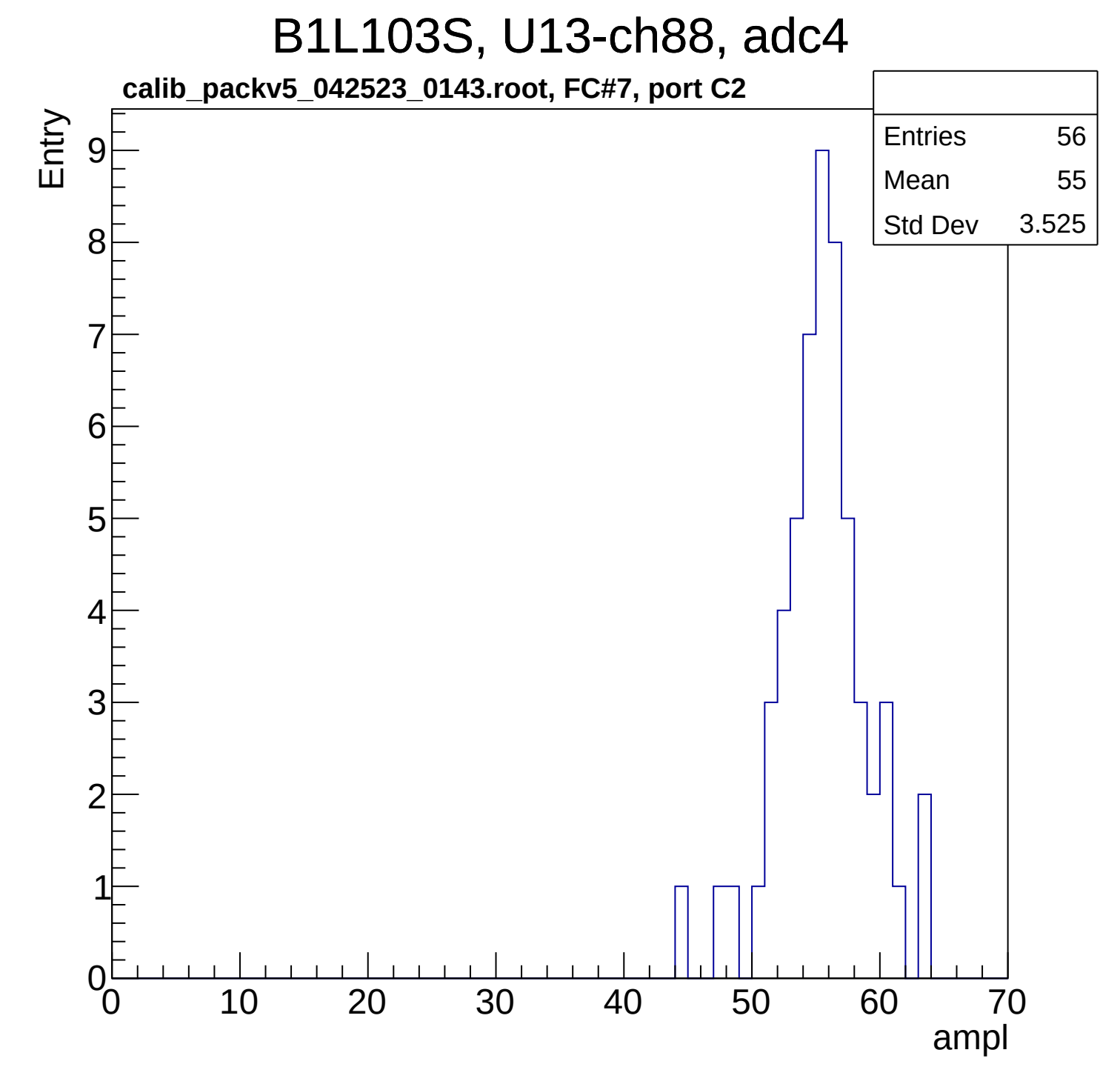
50
60
70
ampl


B1L103S, U13-ch88, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55
Std Dev 3.525
Entry
ampl
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60
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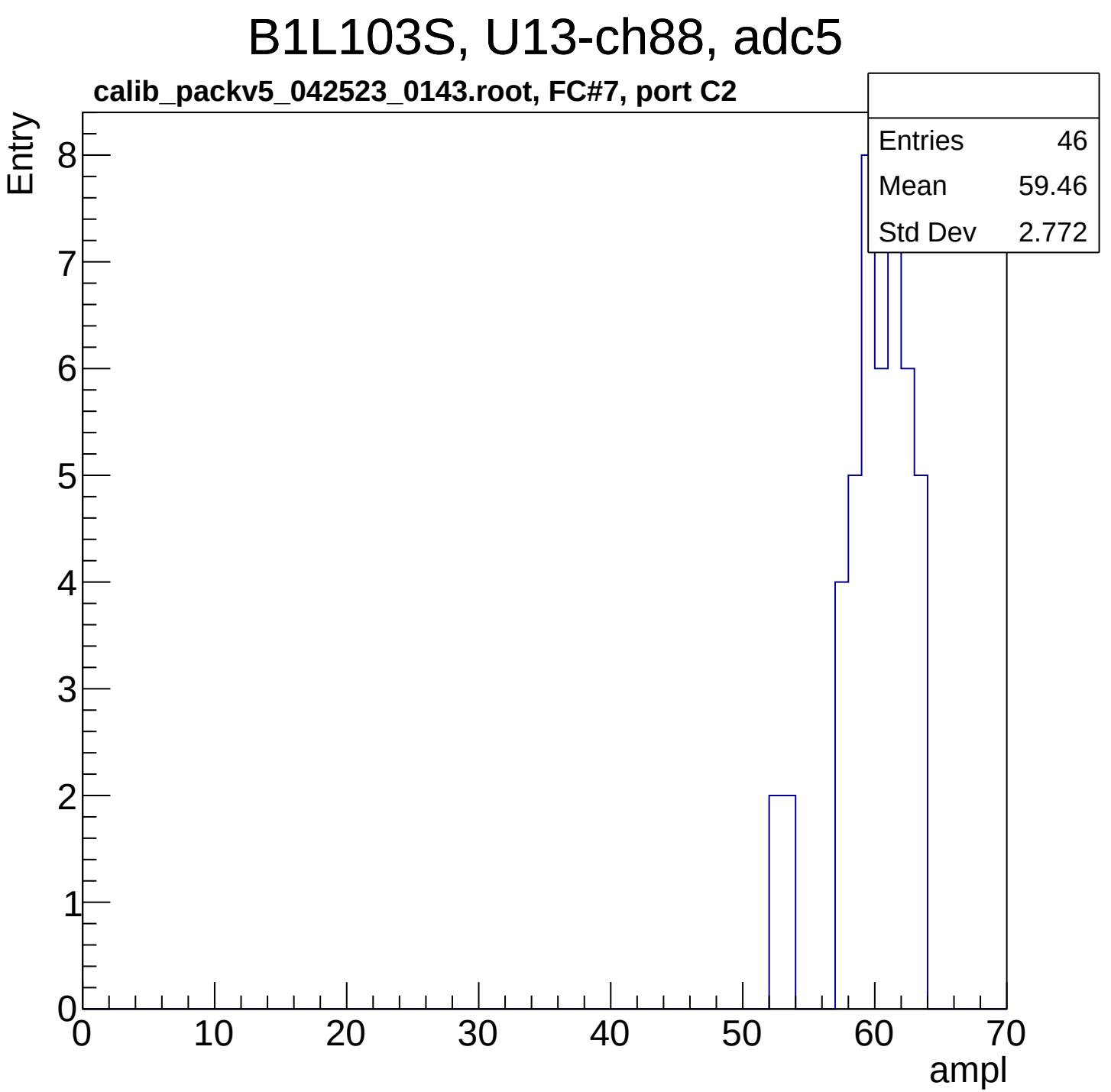
B1L103S, U13-ch88, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.46
Std Dev 2.772
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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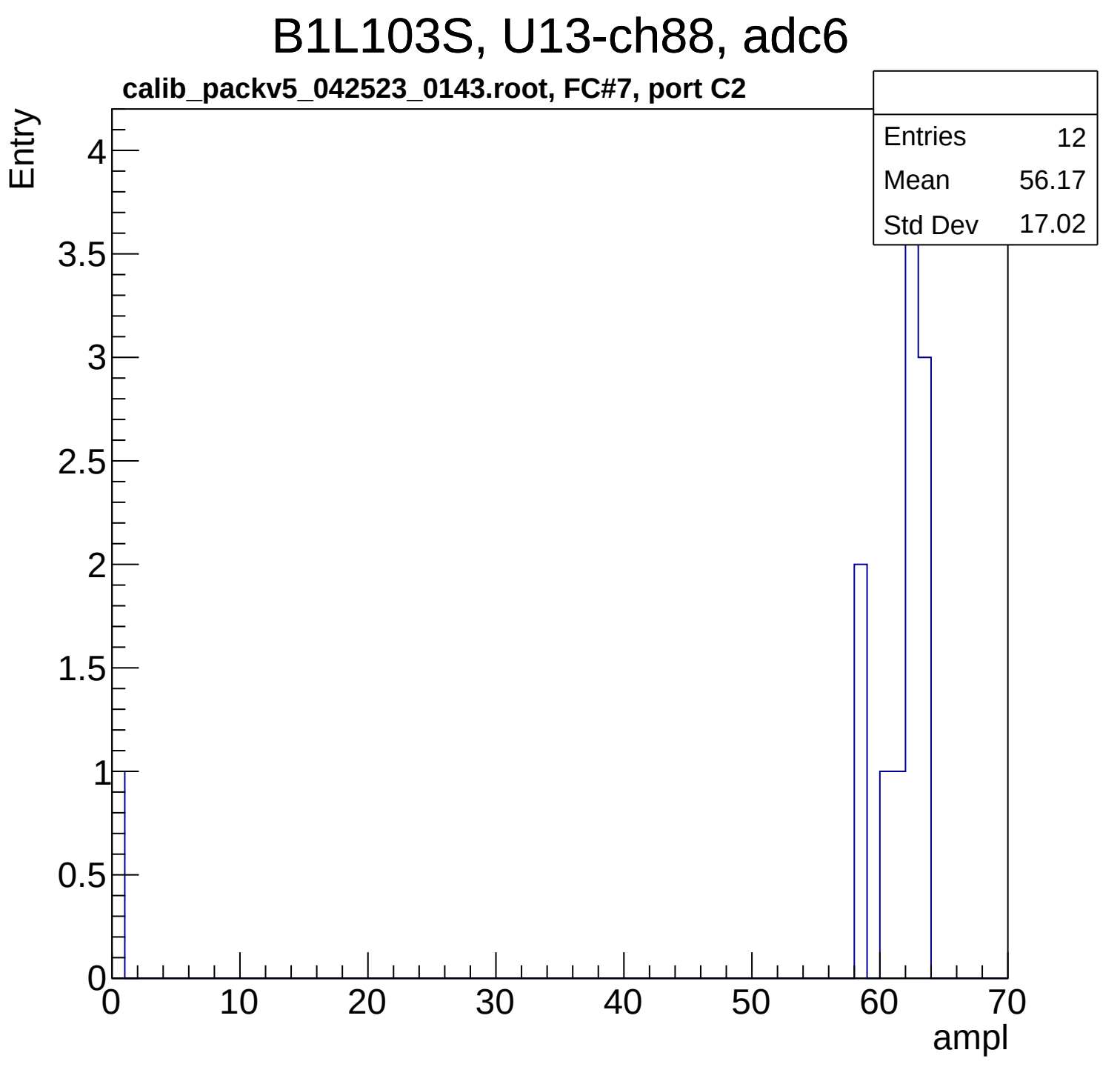
B1L103S, U13-ch88, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.17
Std Dev 17.02
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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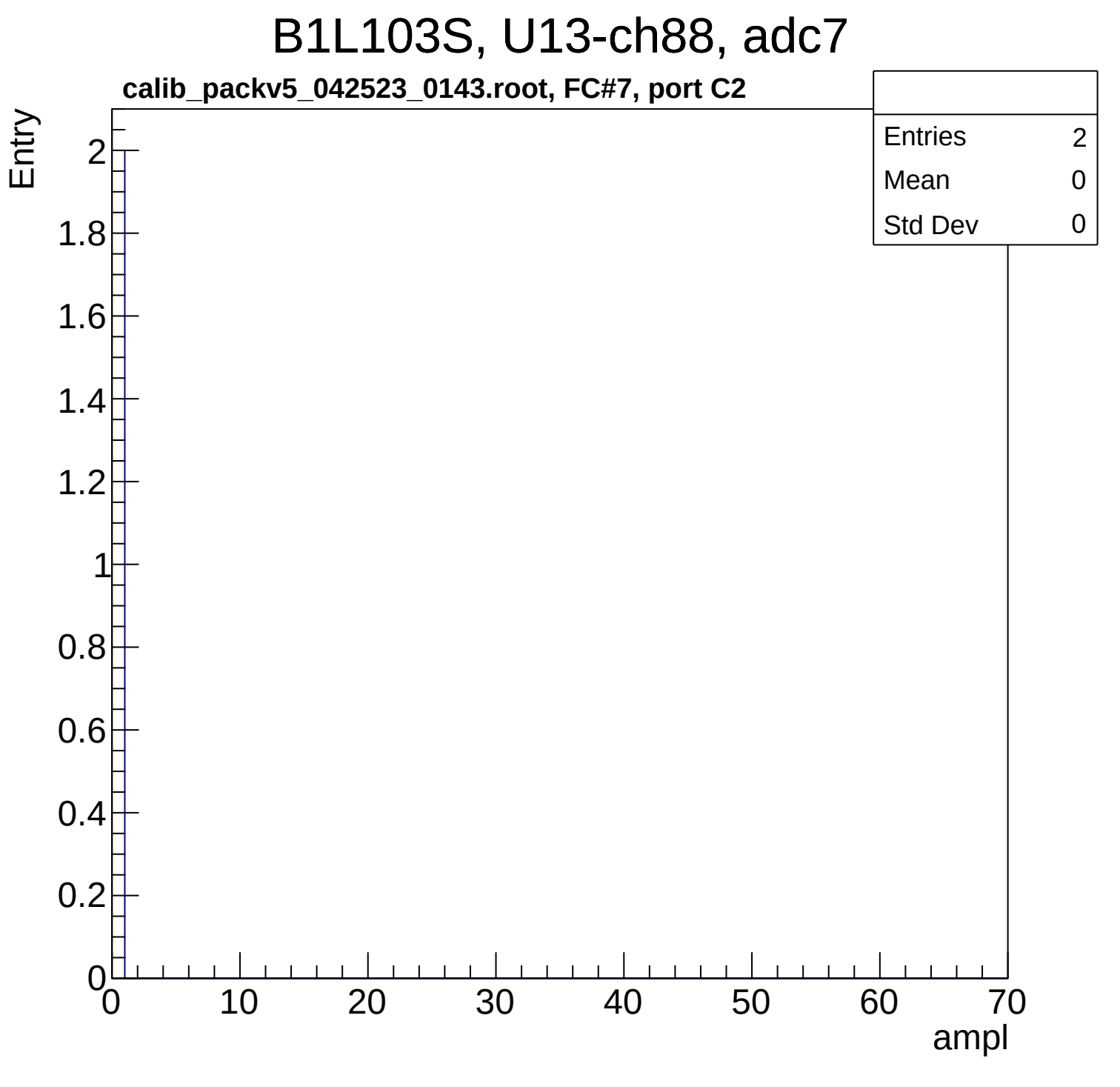
B1L103S, U13-ch88, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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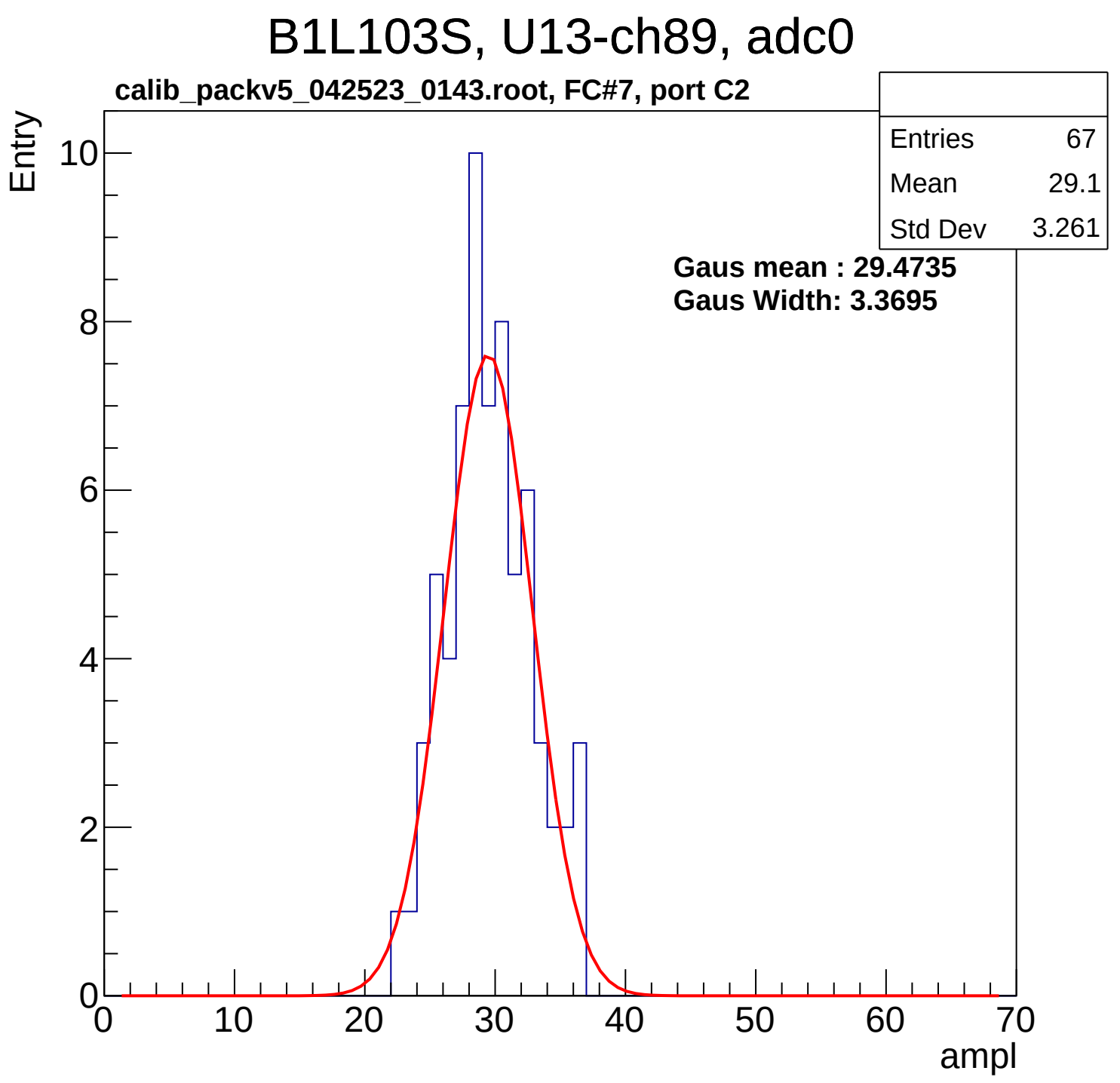
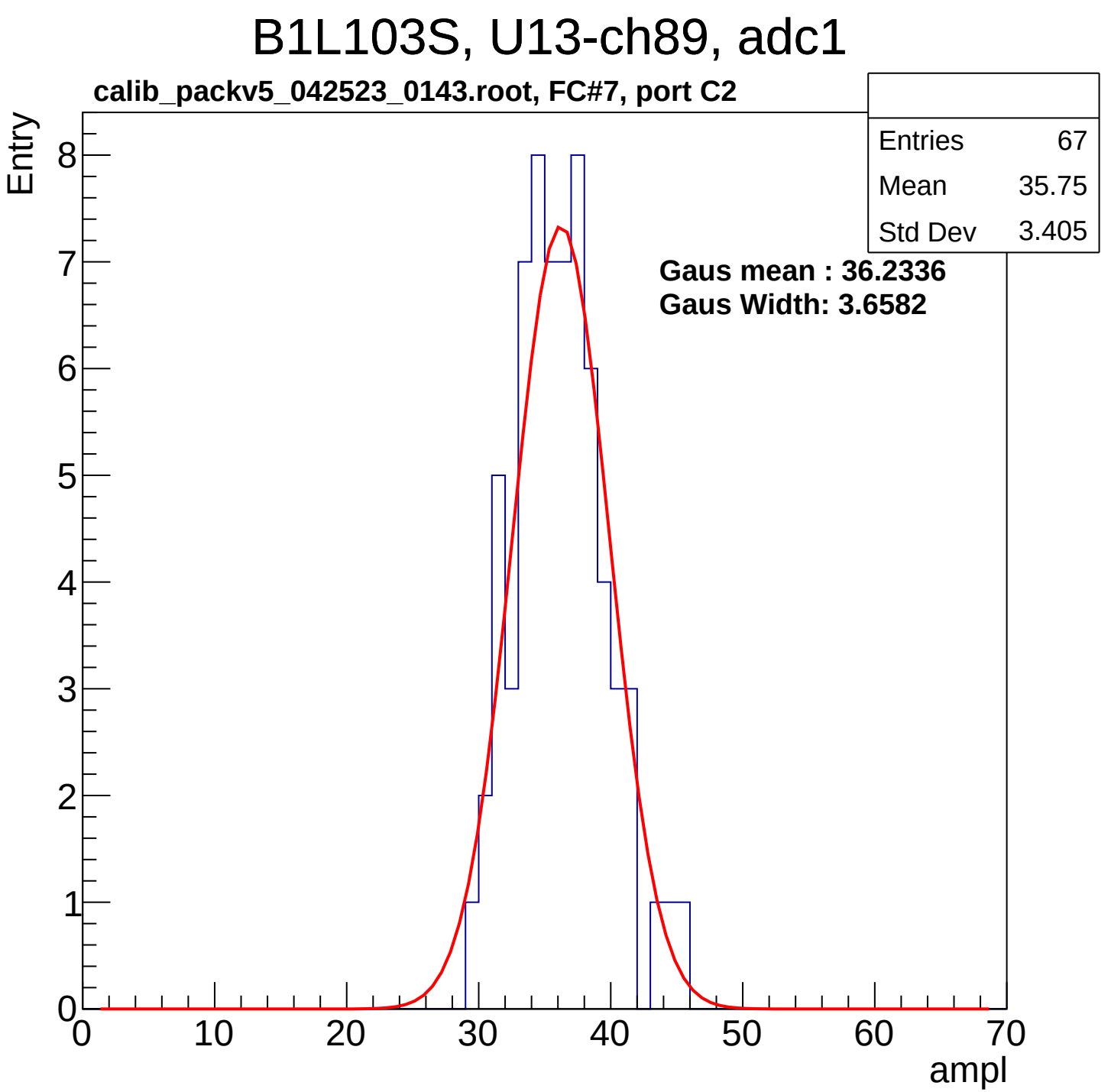
50
60
70
ampl


B1L103S, U13-ch89, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.1
Std Dev 3.261
Gaus mean : 29.4735
Gaus Width: 3.3695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch89, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.75
Std Dev 3.405
Gaus mean : 36.2336
Gaus Width: 3.6582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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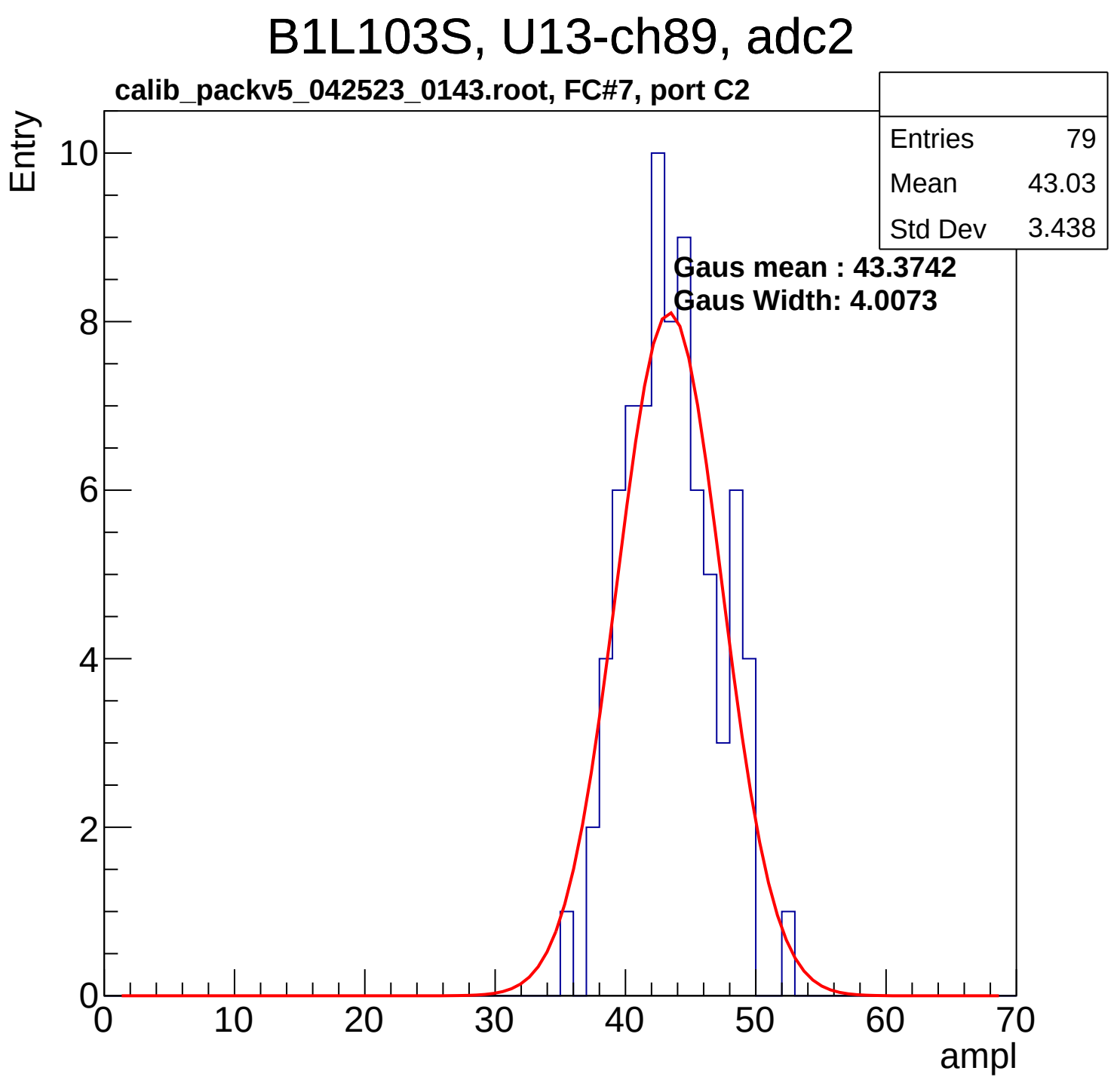
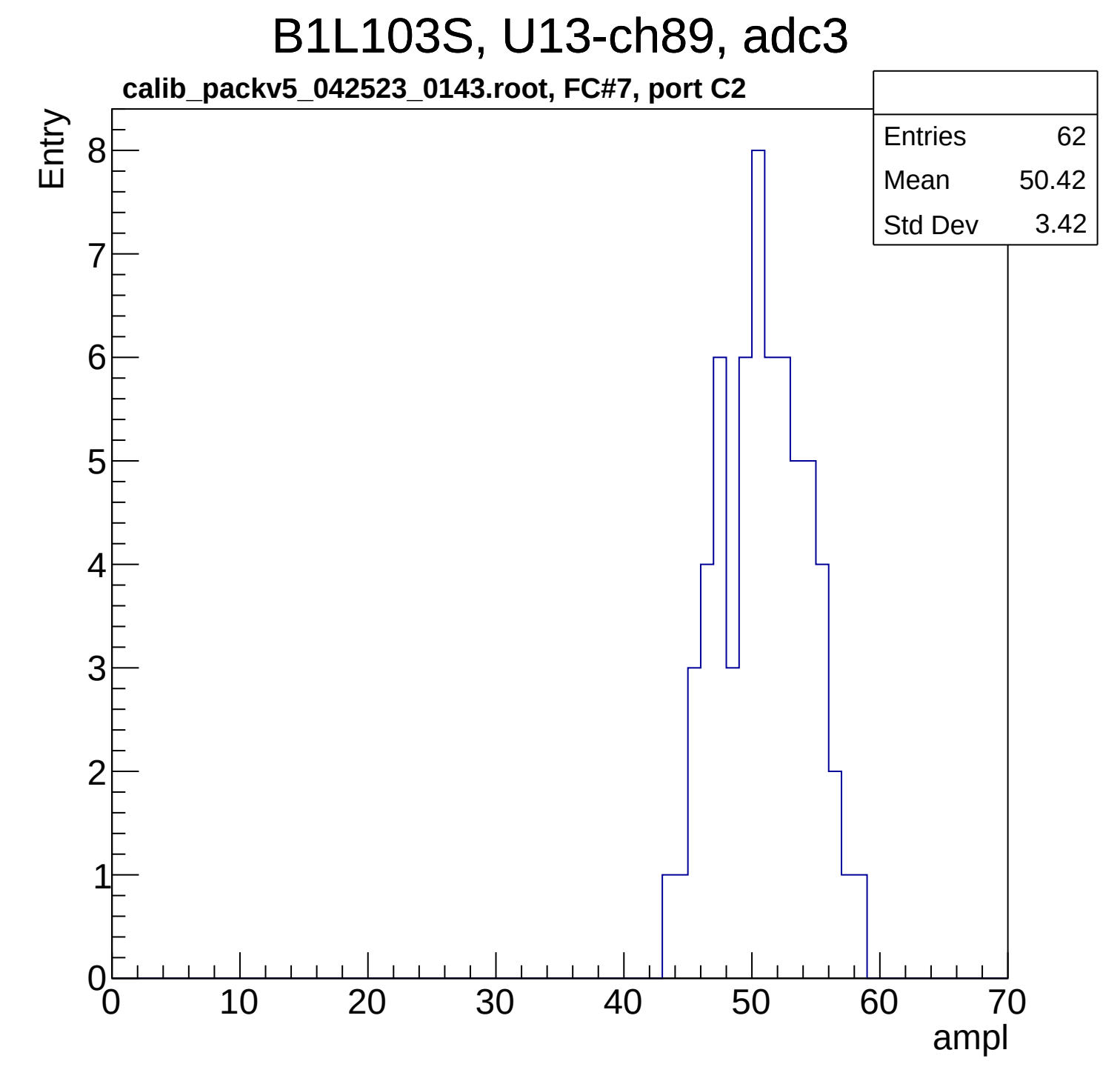

B1L103S, U13-ch89, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 43.03
Std Dev 3.438
Gaus mean : 43.3742
Gaus Width: 4.0073
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch89, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 50.42
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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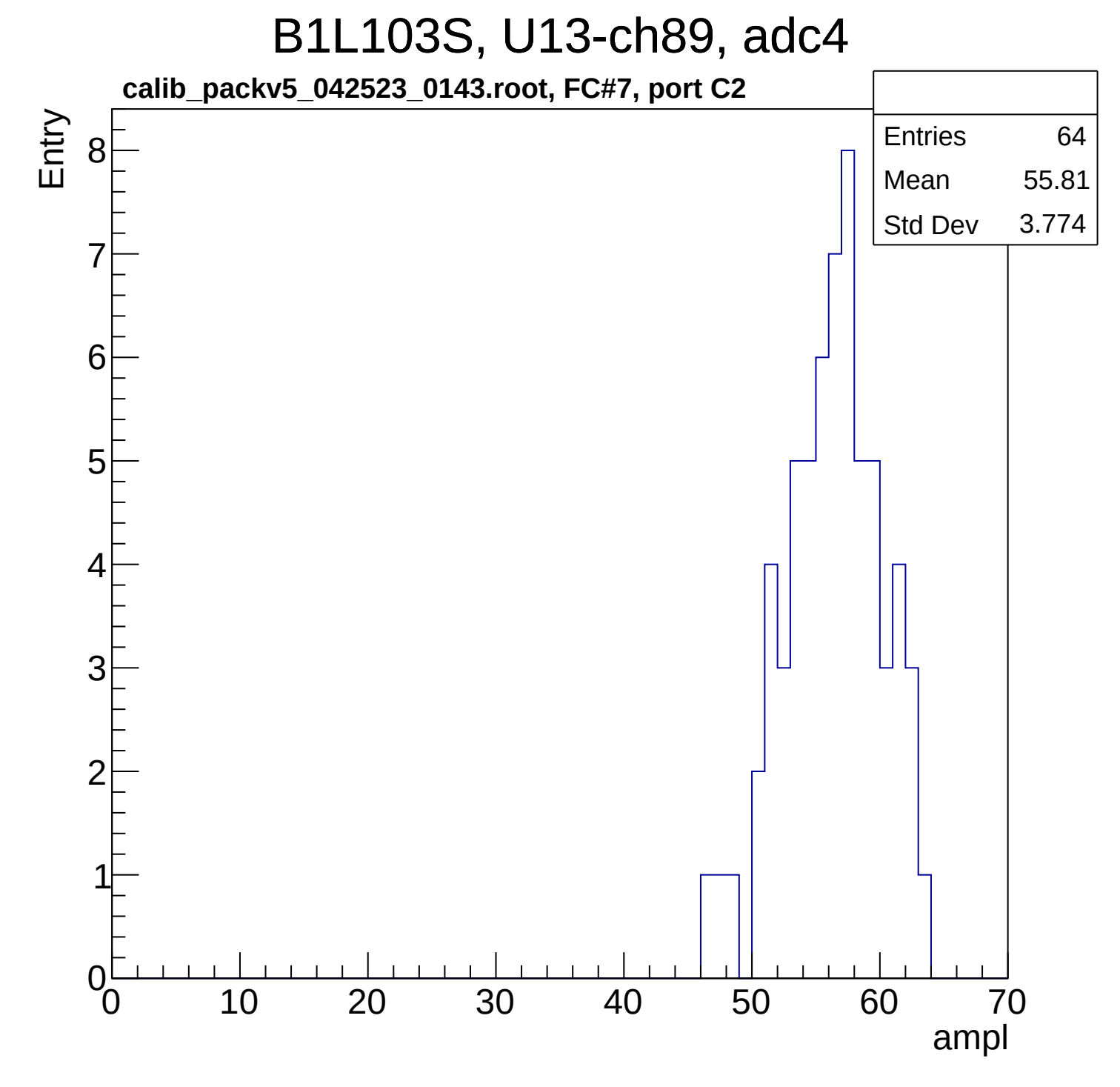
B1L103S, U13-ch89, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.81
Std Dev 3.774
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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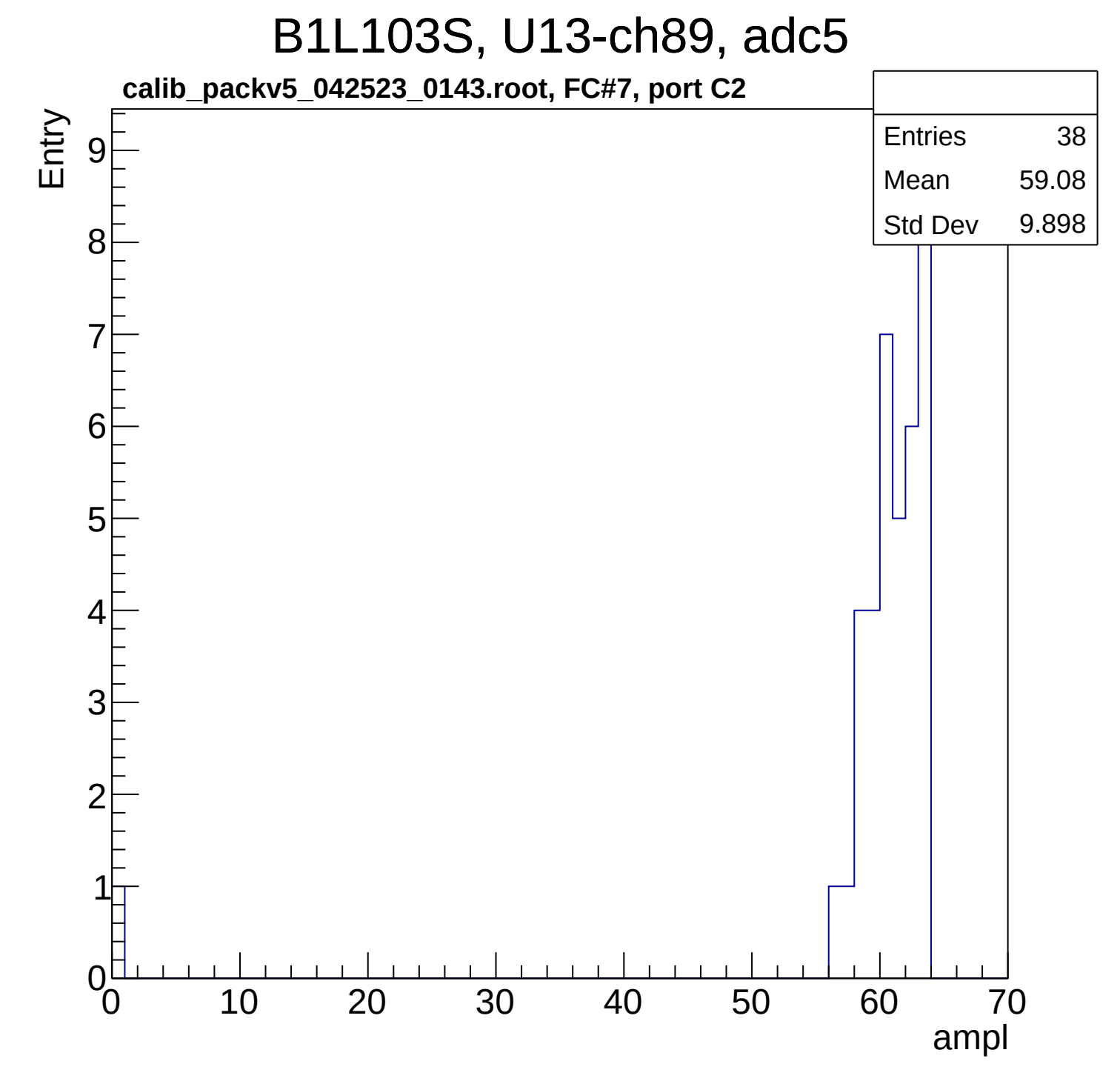
B1L103S, U13-ch89, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 59.08
Std Dev 9.898
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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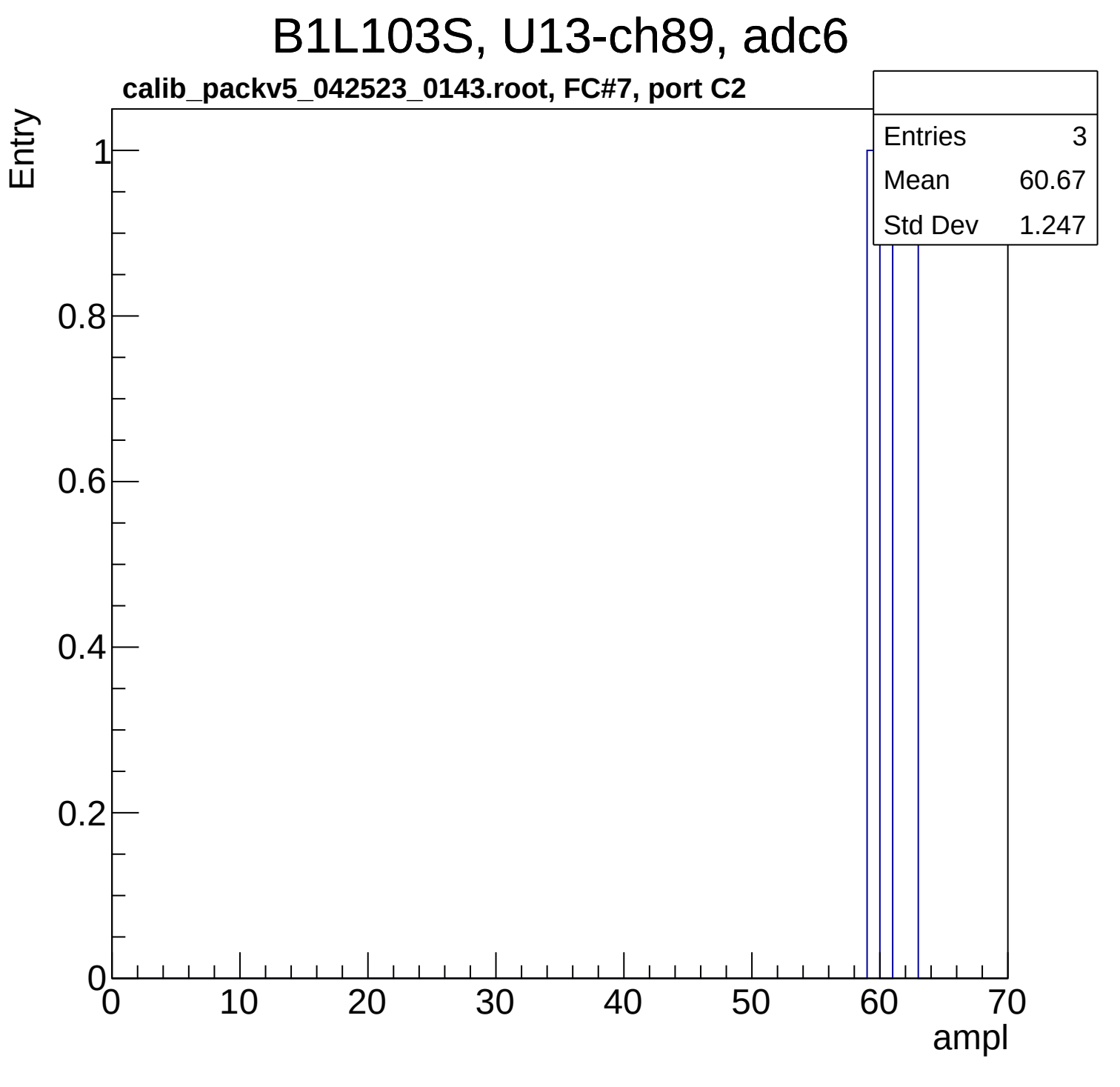
B1L103S, U13-ch89, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 60.67
Std Dev 1.247
Entry
ampl
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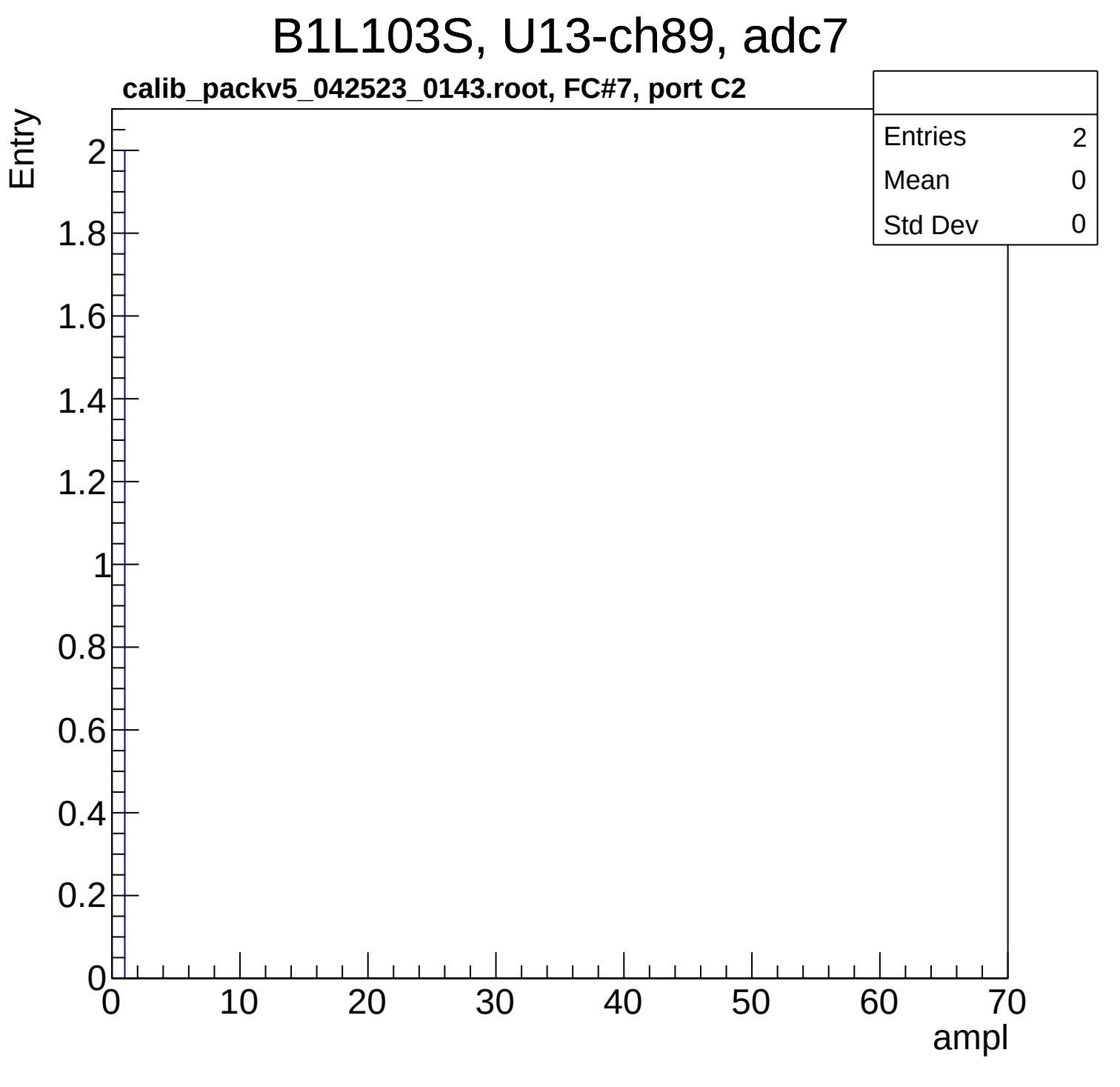
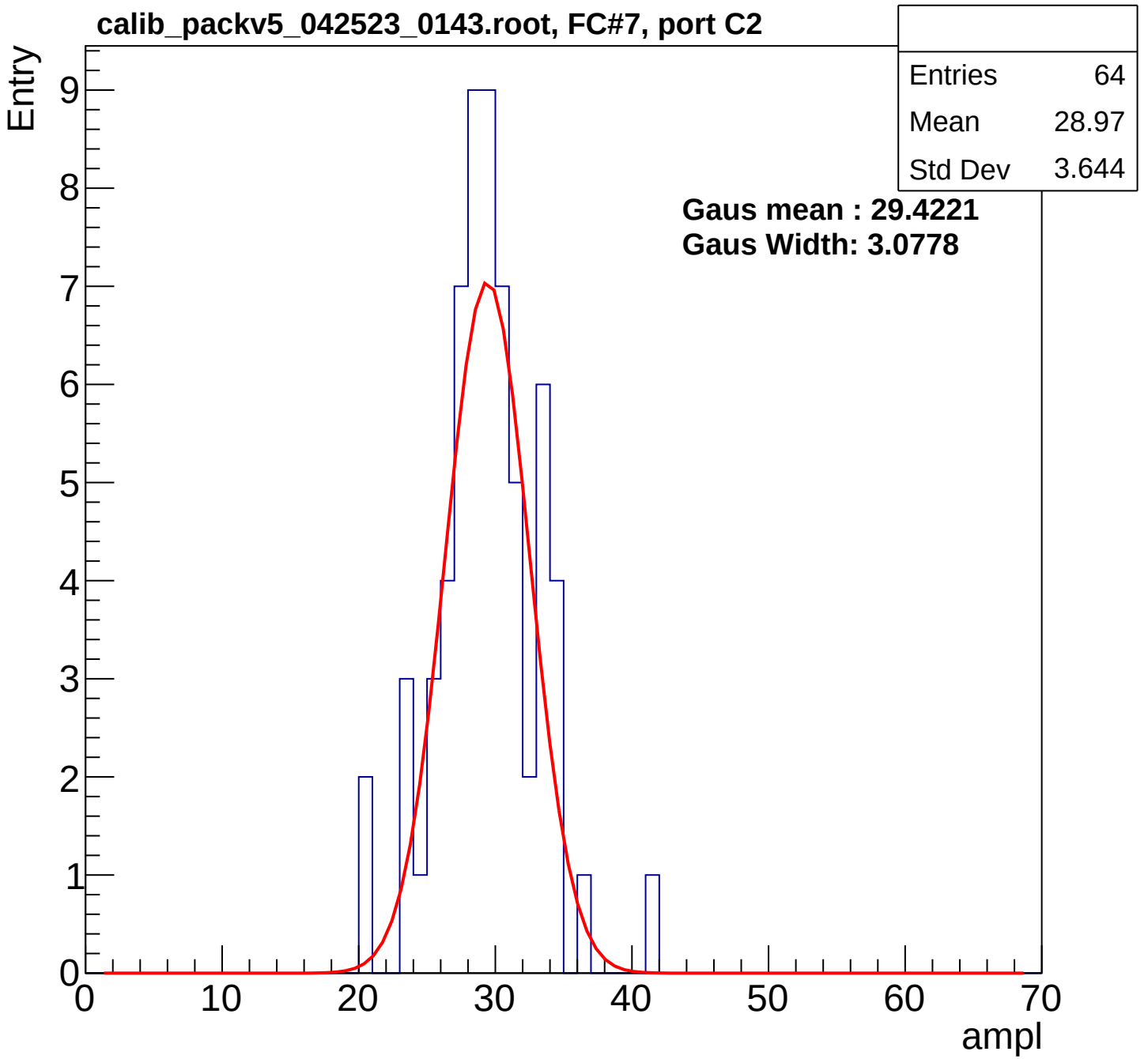
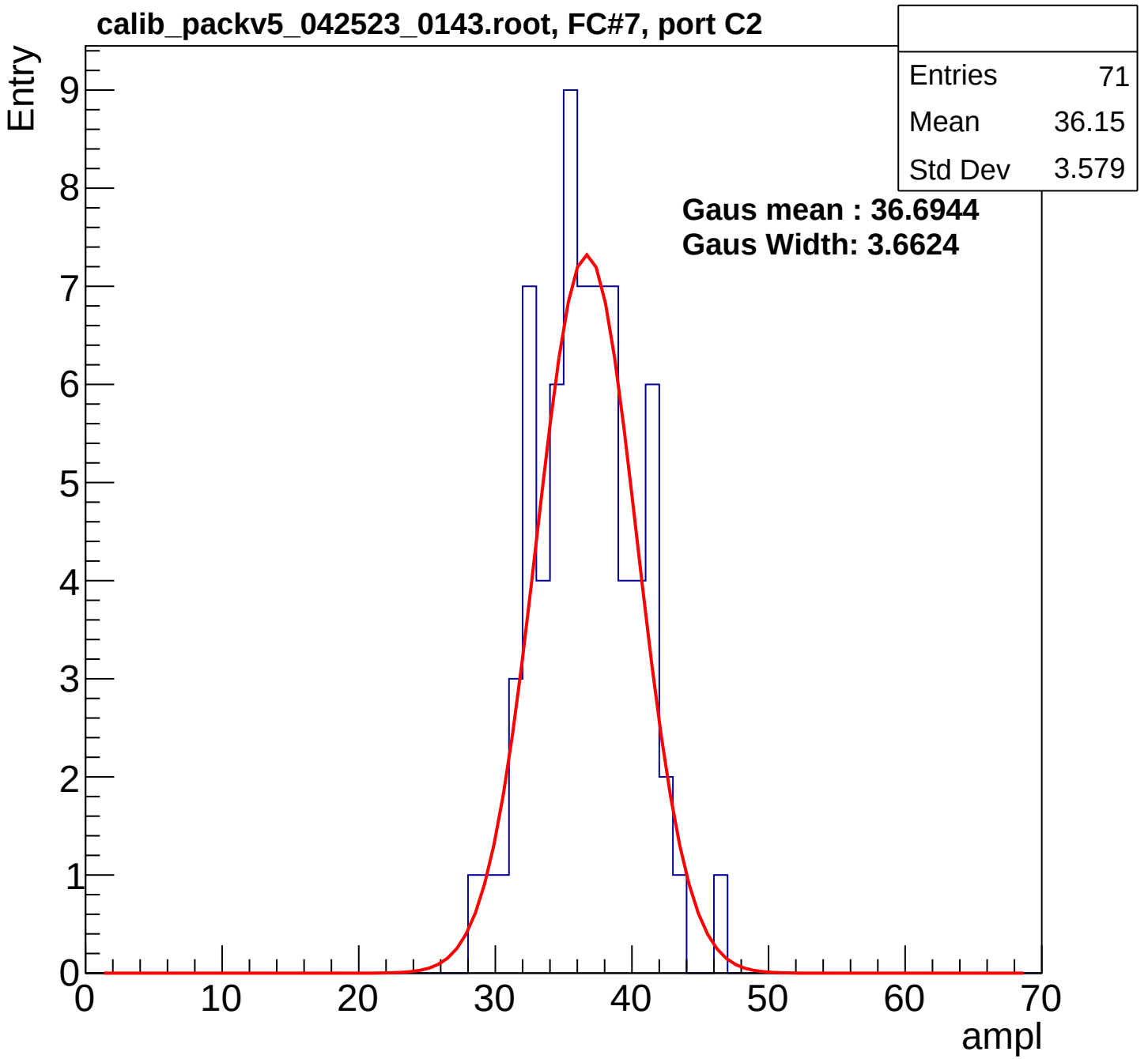
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch89, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch90, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 28.97
Std Dev 3.644
Gaus mean : 29.4221
Gaus Width: 3.0778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch90, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.15
Std Dev 3.579
Gaus mean : 36.6944
Gaus Width: 3.6624
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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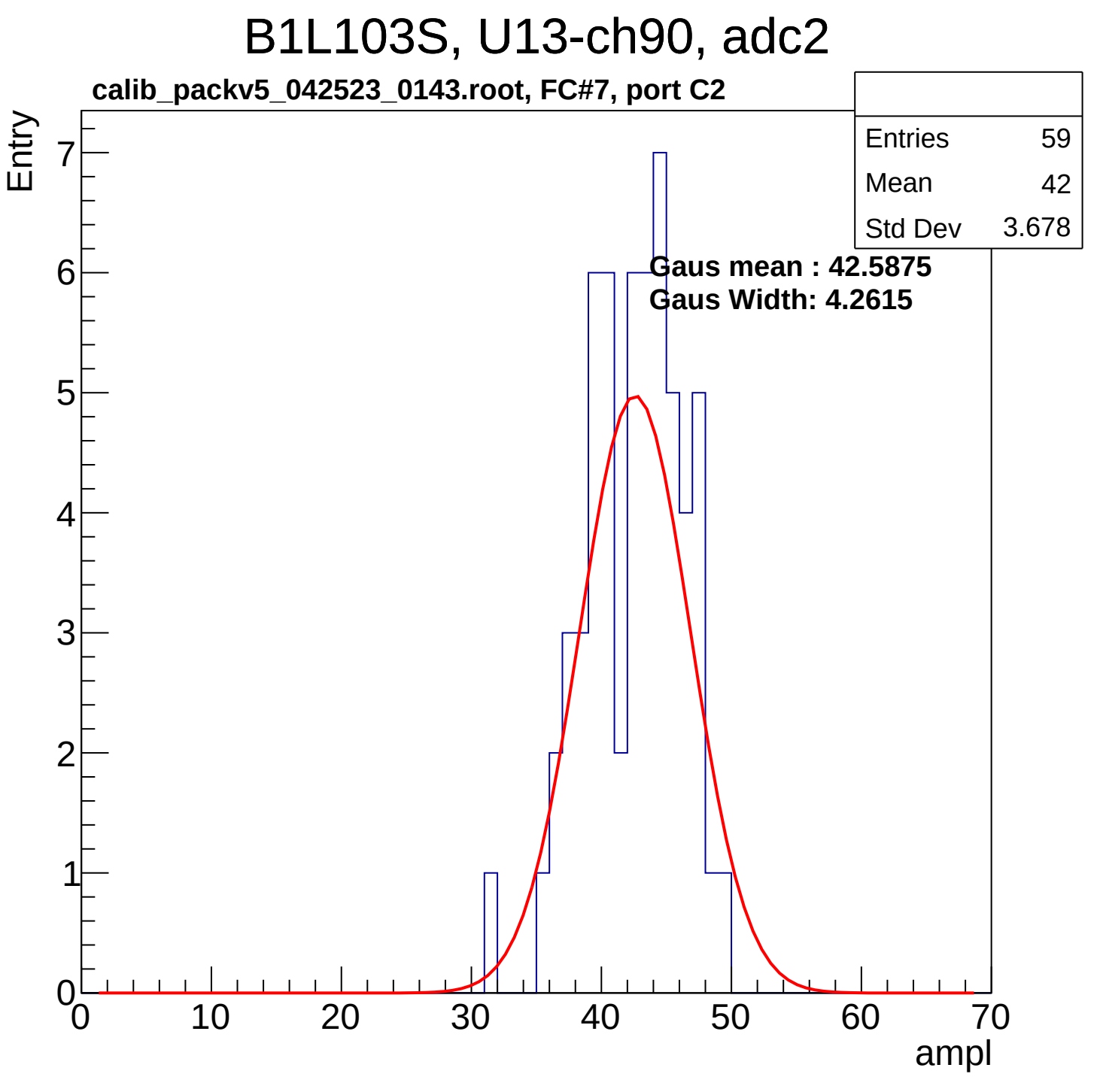
B1L103S, U13-ch90, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42
Std Dev 3.678
Gaus mean : 42.5875
Gaus Width: 4.2615
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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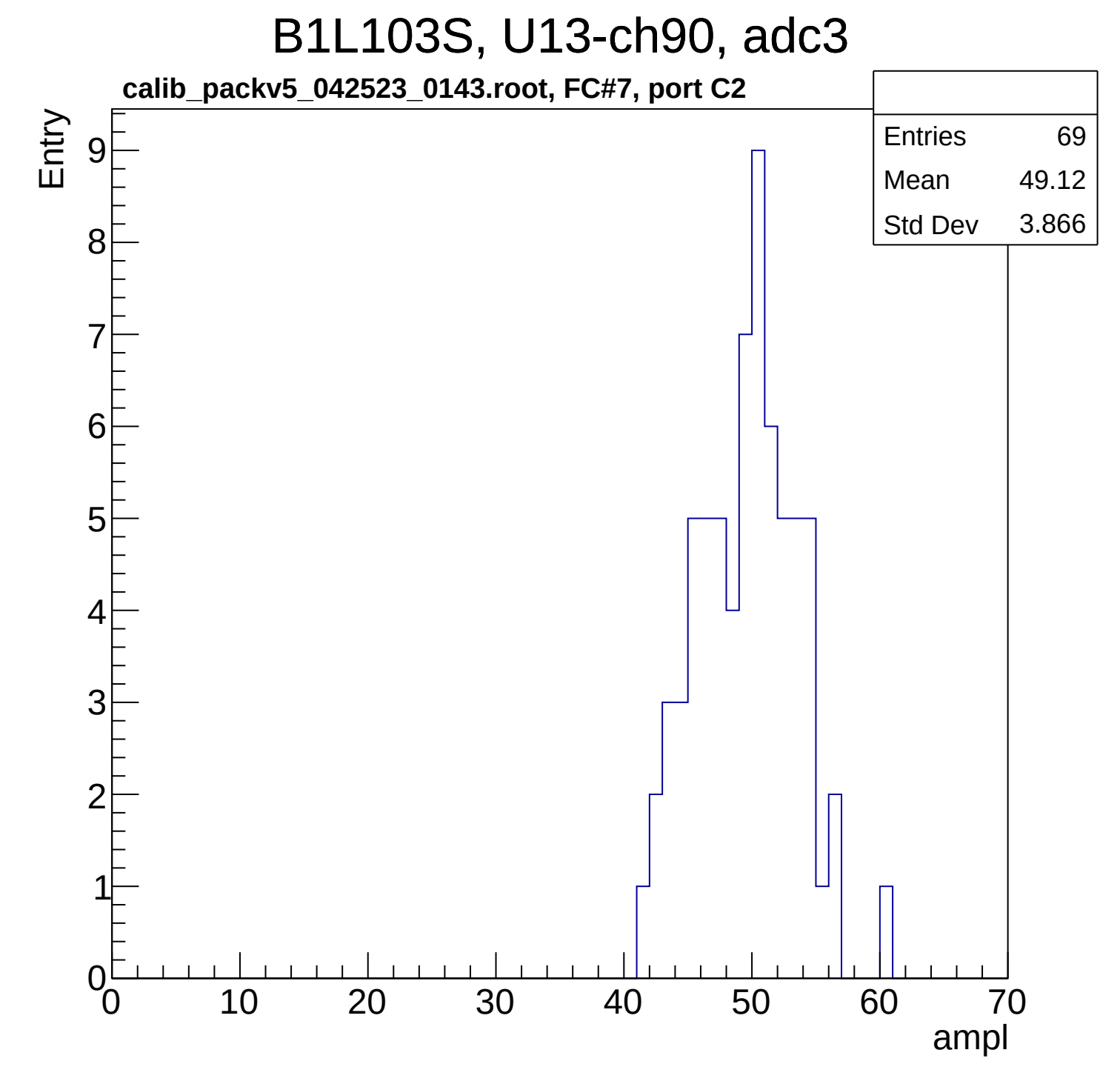
B1L103S, U13-ch90, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.12
Std Dev 3.866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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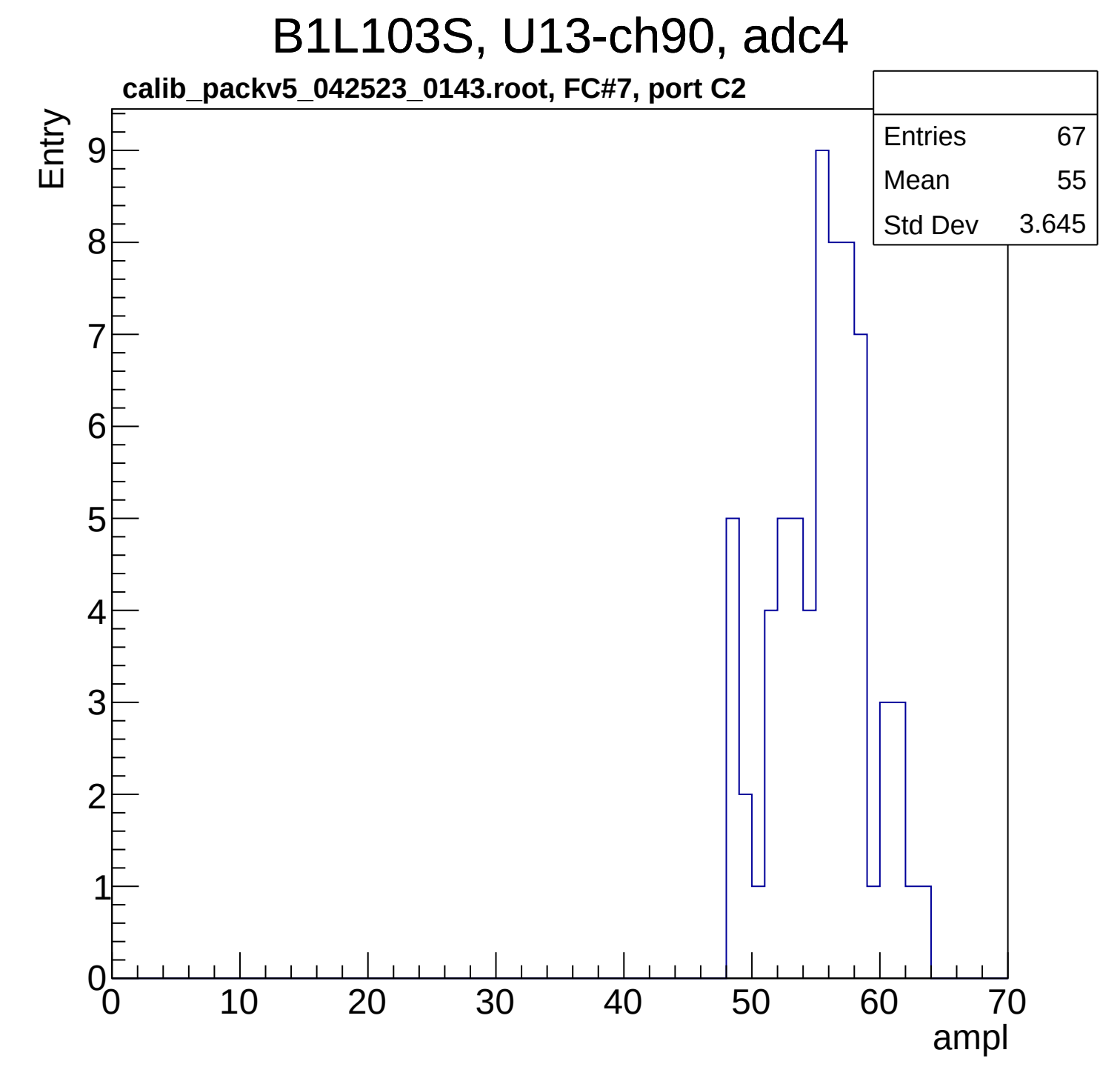
B1L103S, U13-ch90, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 55
Std Dev 3.645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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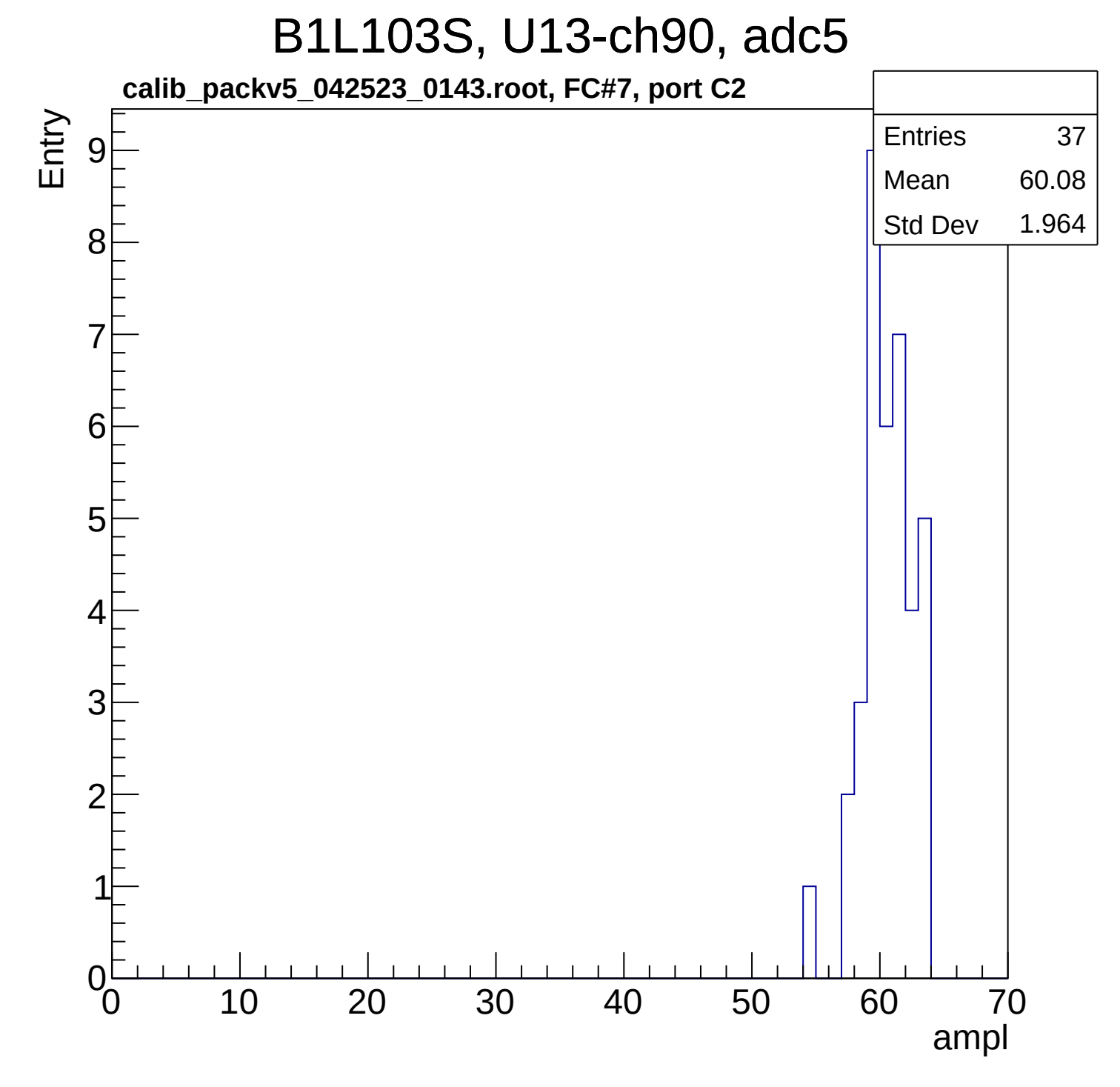
B1L103S, U13-ch90, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.08
Std Dev 1.964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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50
60
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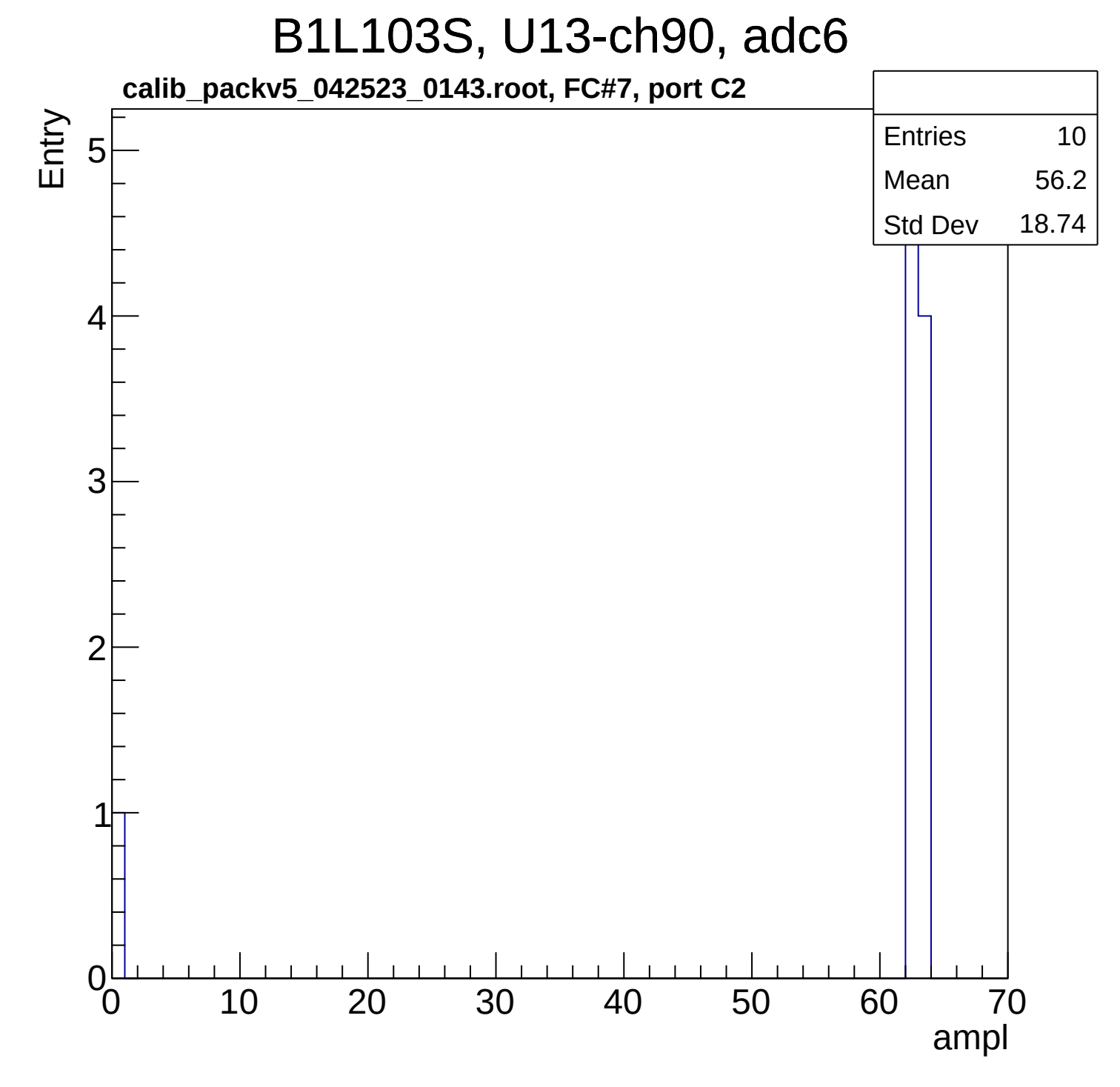
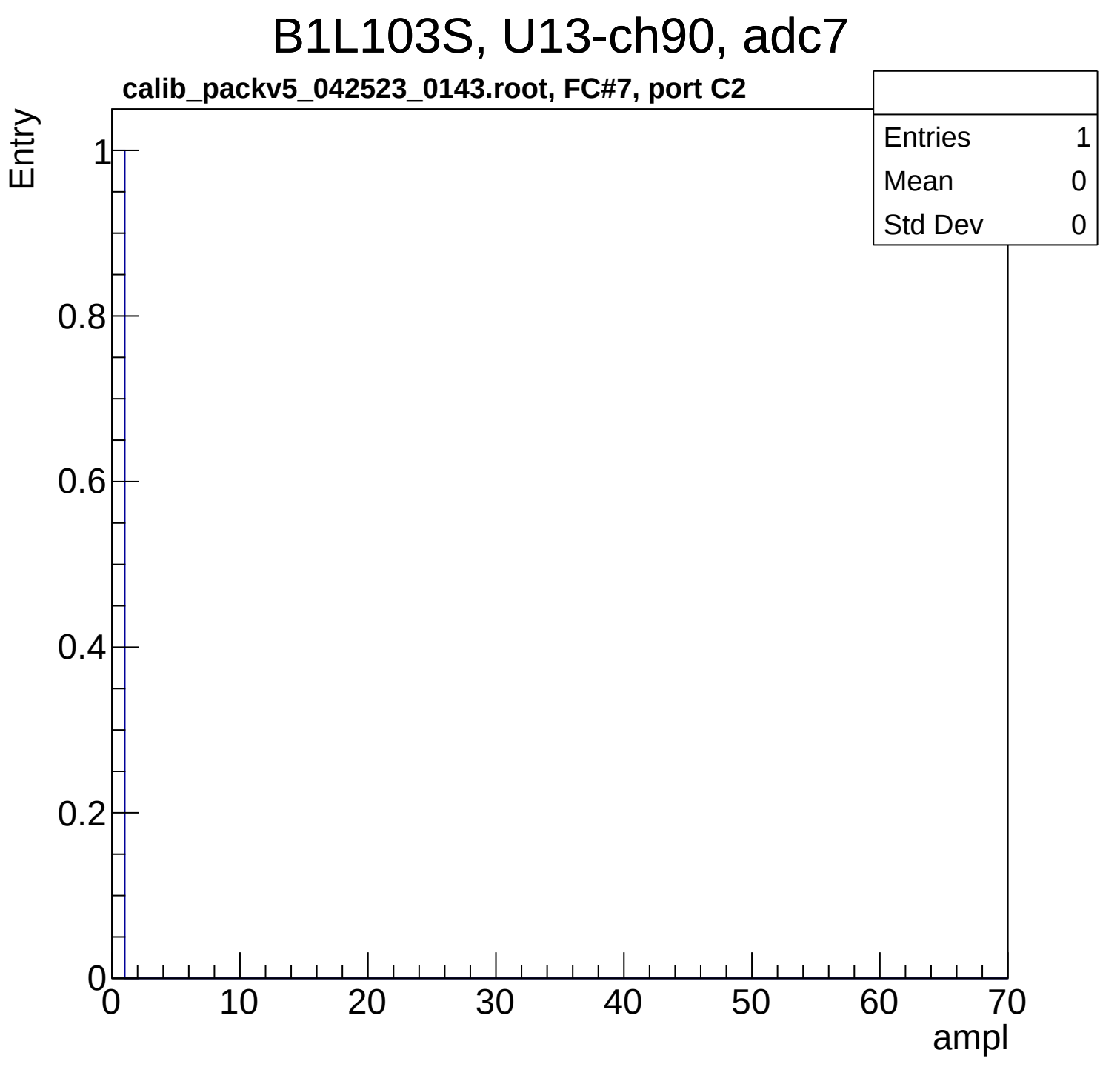
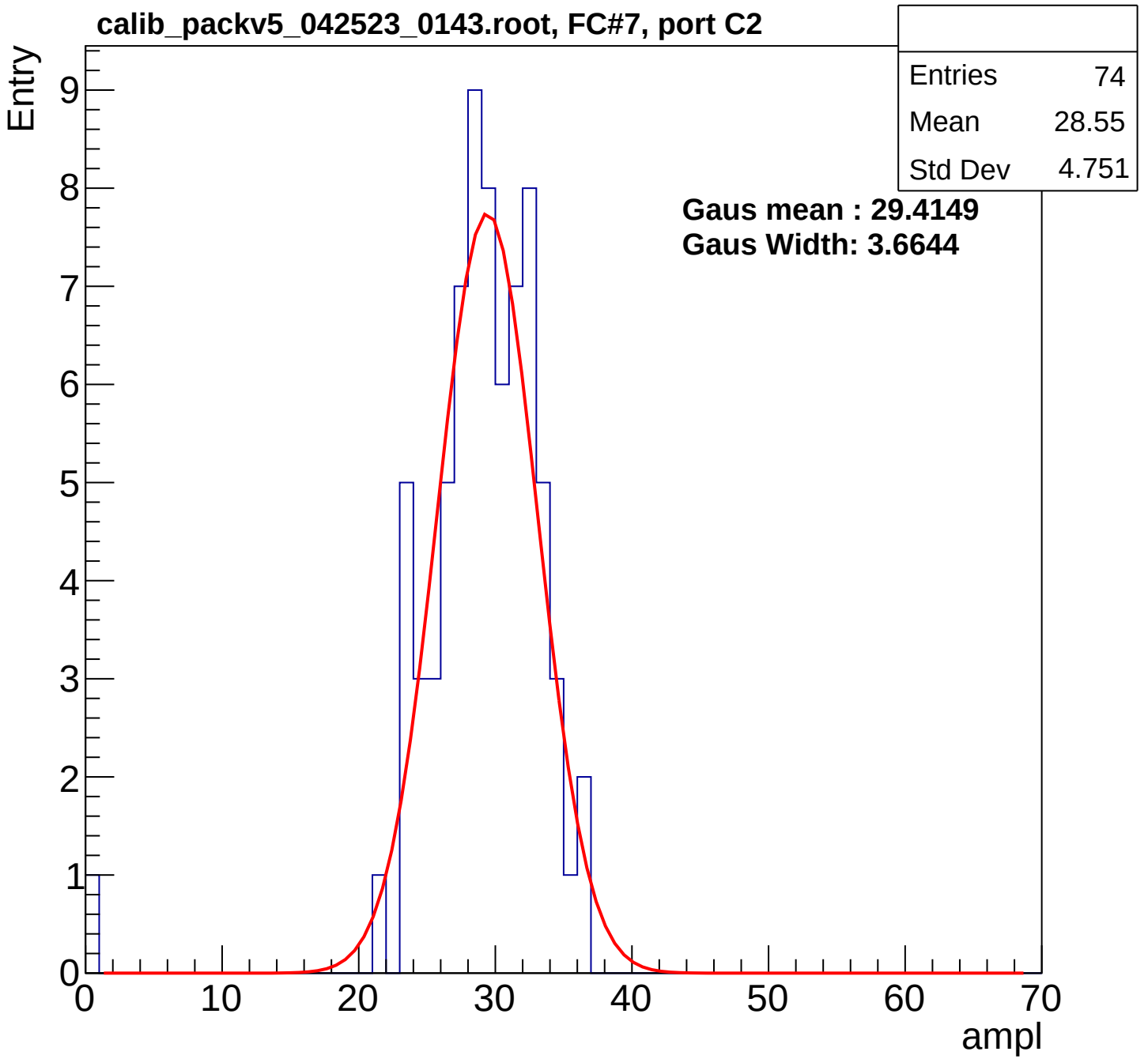

B1L103S, U13-ch90, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56.2
Std Dev 18.74
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch90, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch91, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 28.55
Std Dev 4.751
Gaus mean : 29.4149
Gaus Width: 3.6644
Entry
ampl
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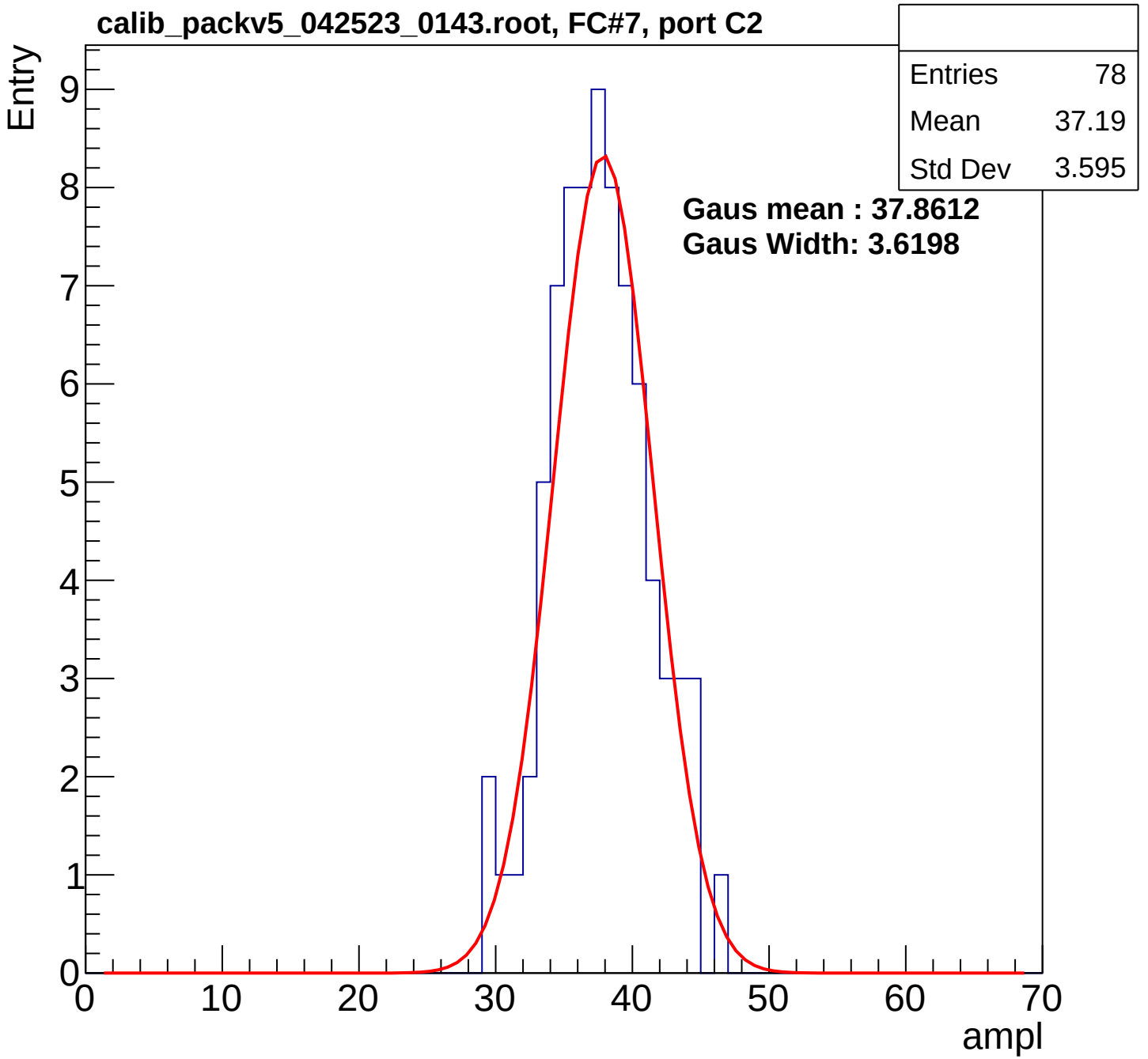
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch91, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 37.19
Std Dev 3.595
Gaus mean : 37.8612
Gaus Width: 3.6198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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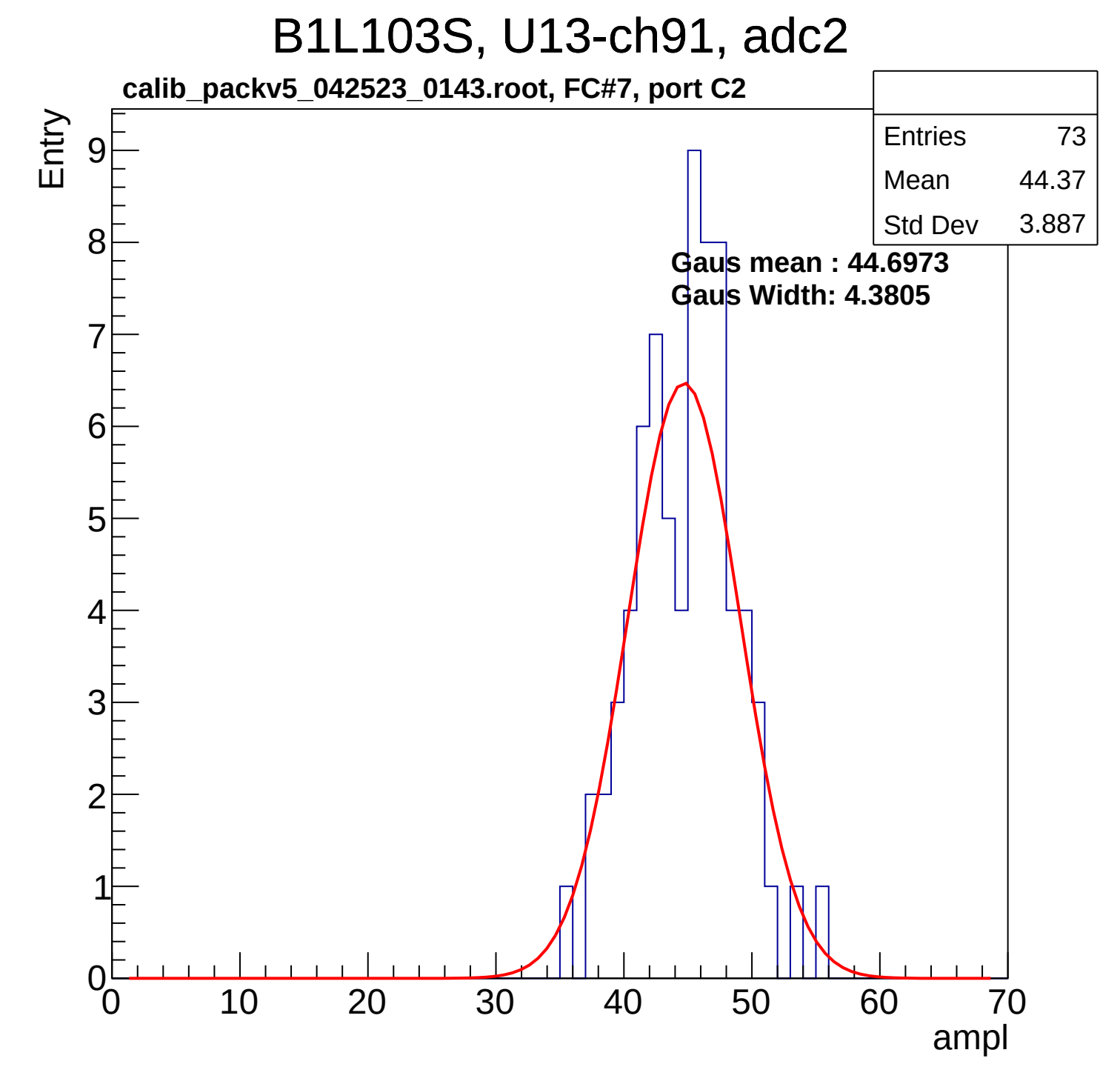
60
70


B1L103S, U13-ch91, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 44.37
Std Dev 3.887
Gaus mean : 44.6973
Gaus Width: 4.3805
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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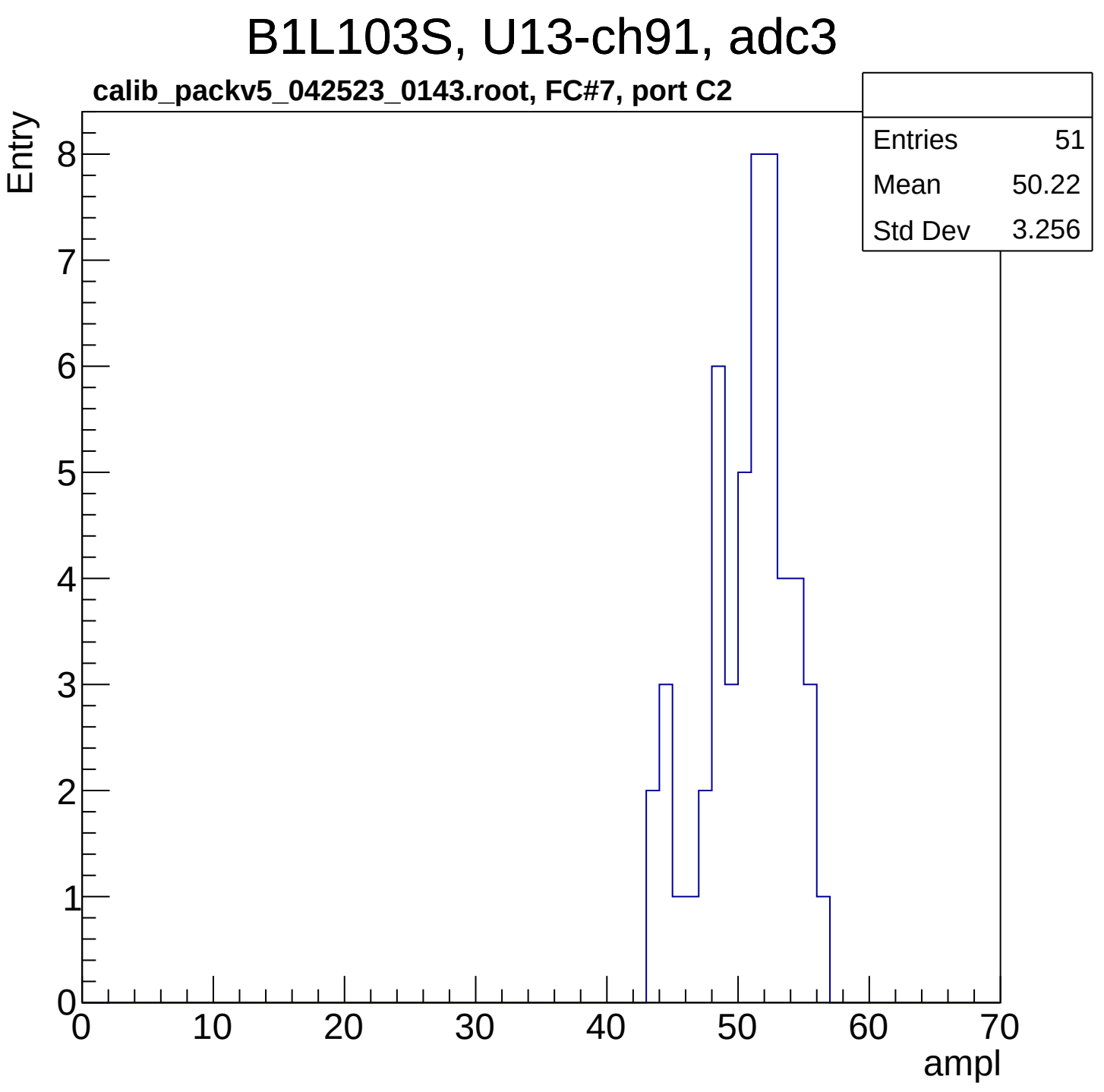
B1L103S, U13-ch91, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 50.22
Std Dev 3.256
Entry
ampl
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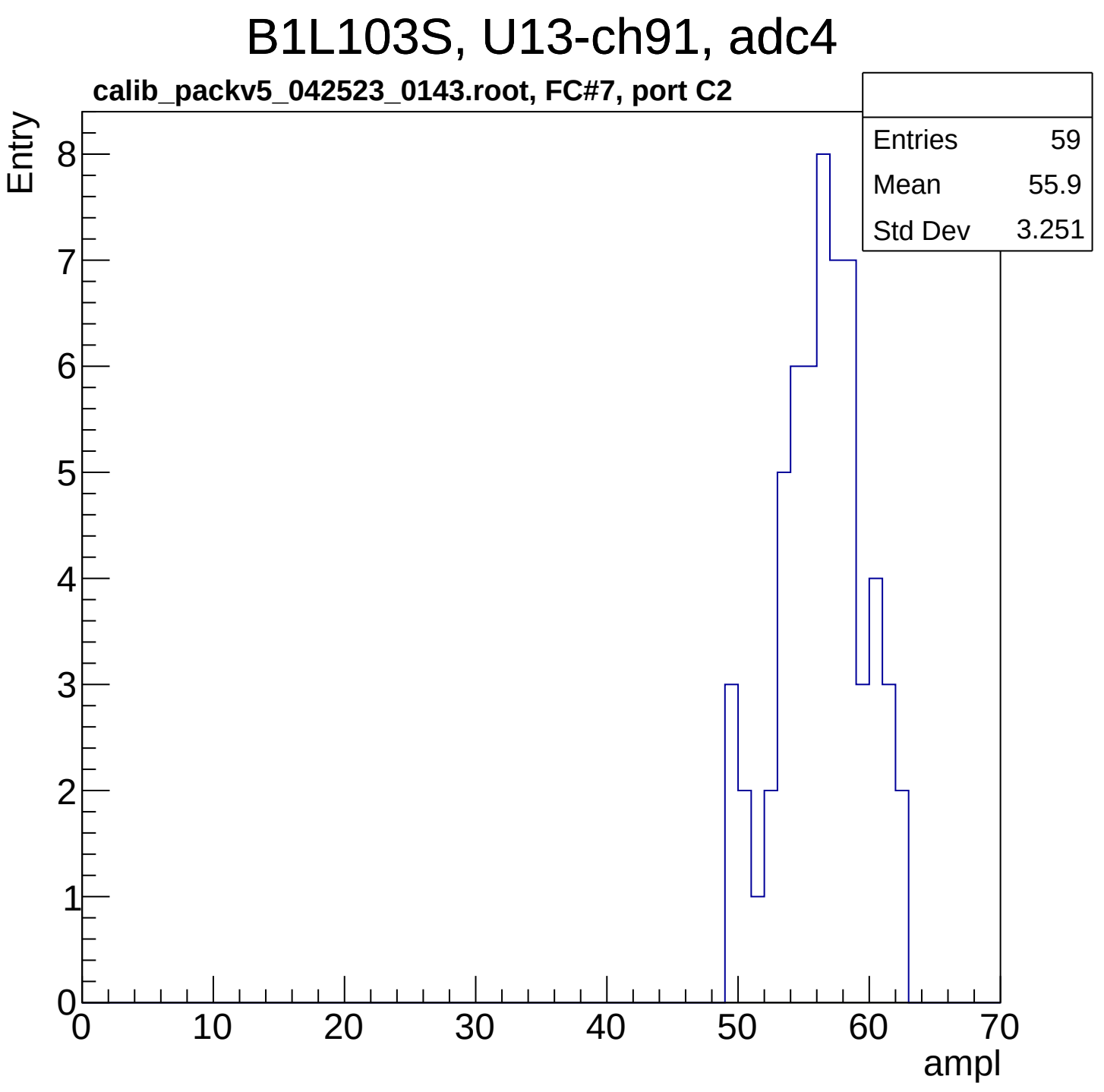
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch91, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.9
Std Dev 3.251
Entry
0
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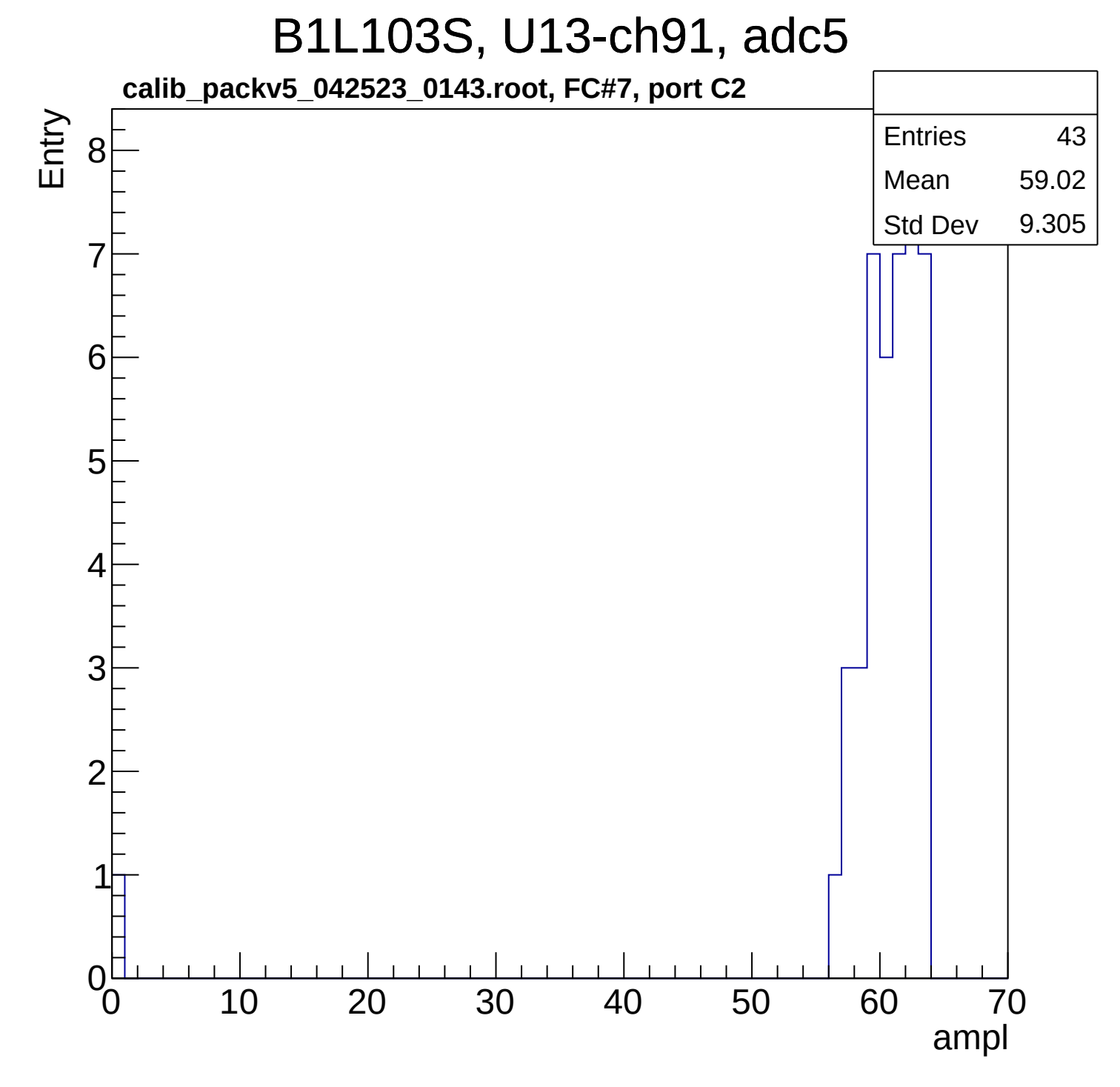
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch91, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.02
Std Dev 9.305
Entry
ampl
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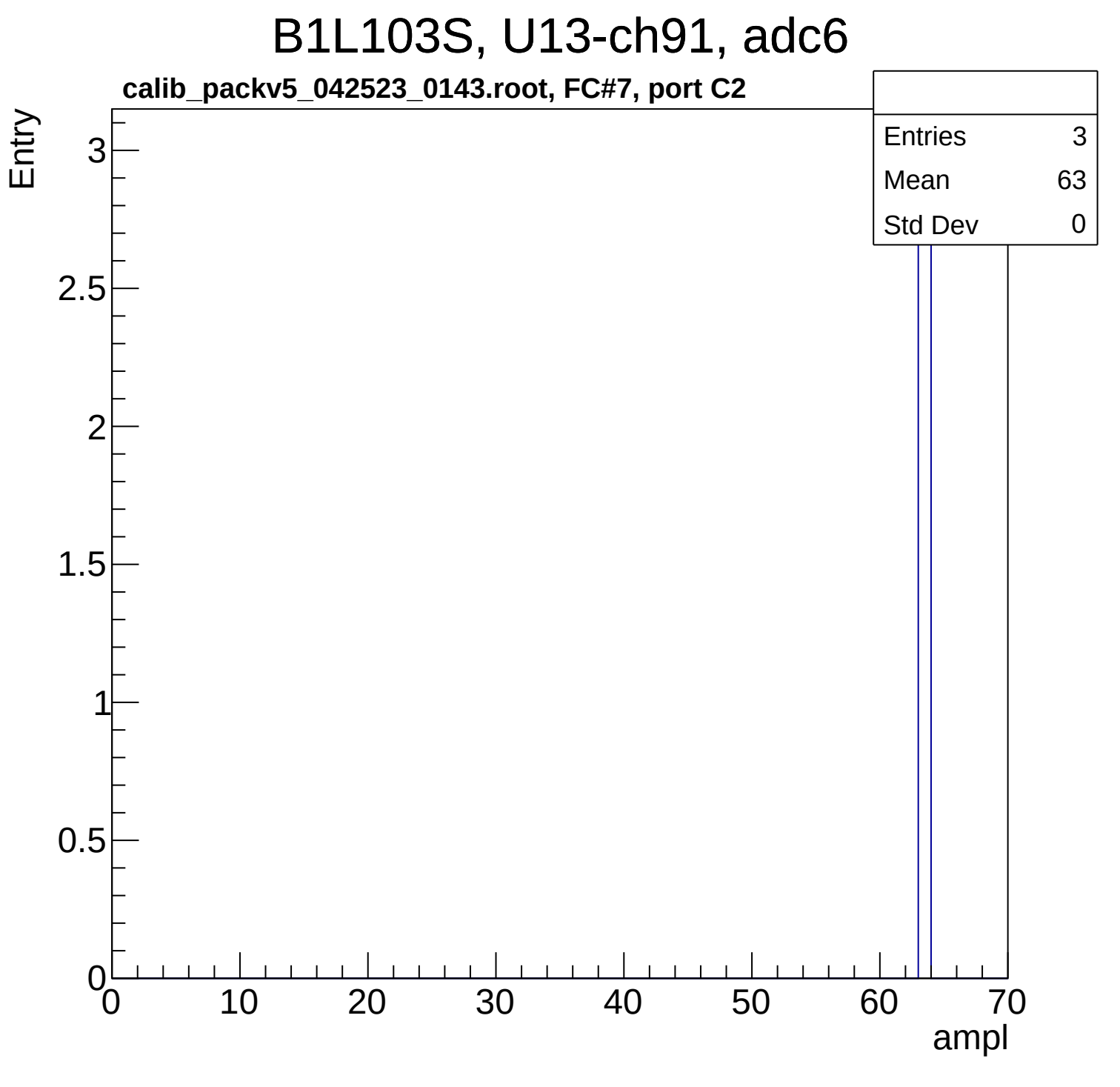
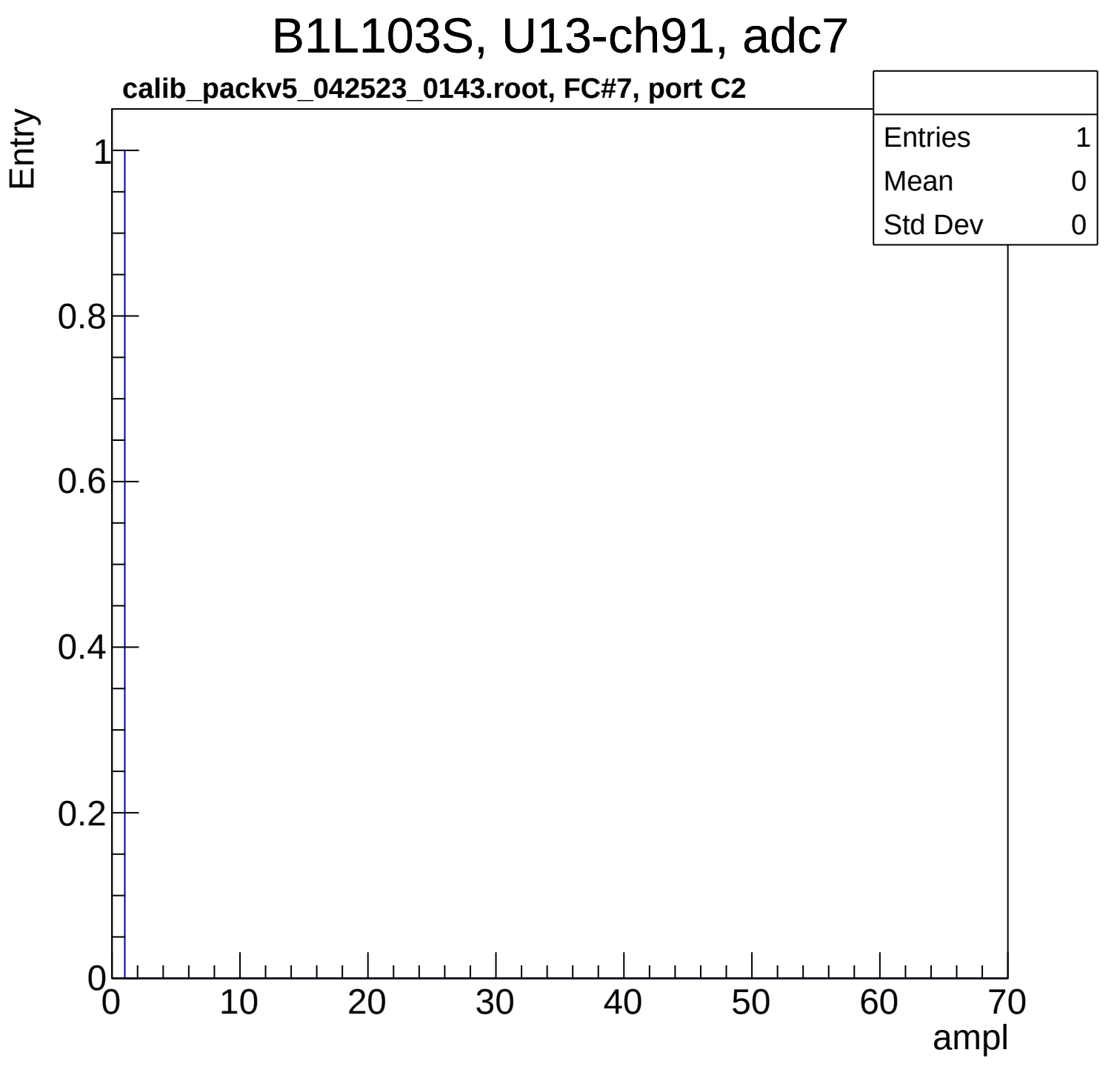
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch91, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch91, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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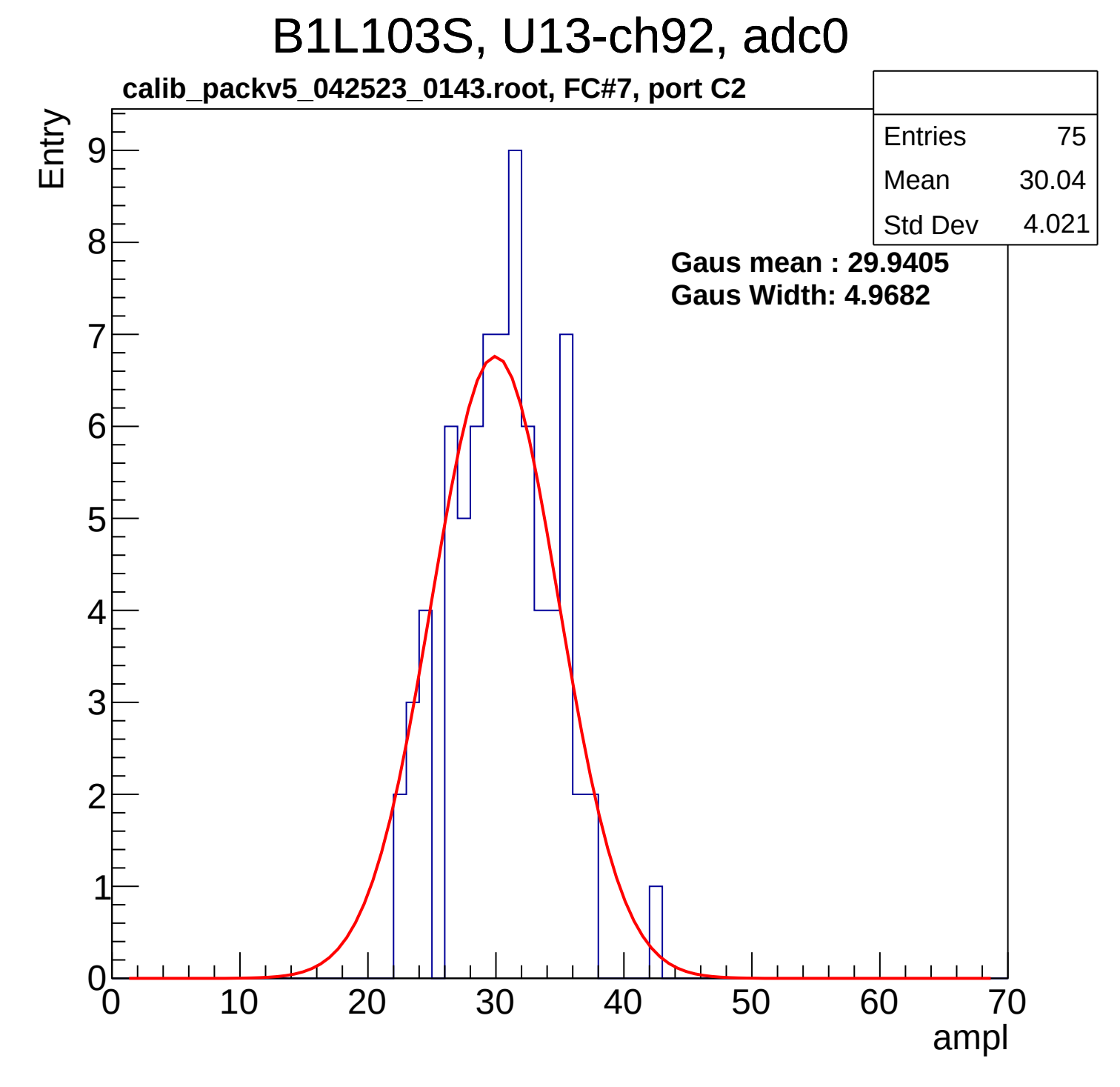
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch92, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.04
Std Dev 4.021
Gaus mean : 29.9405
Gaus Width: 4.9682
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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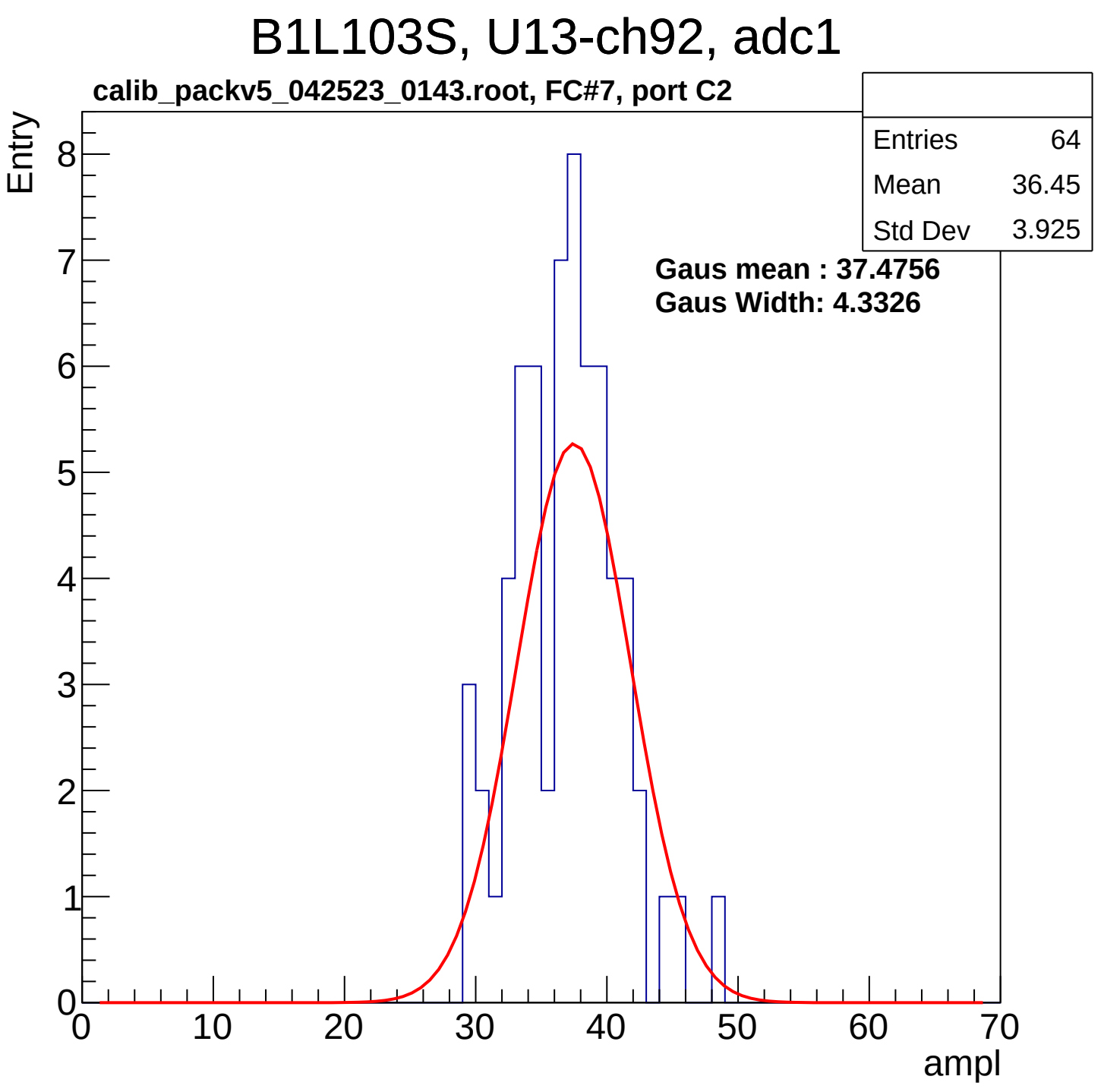
B1L103S, U13-ch92, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 36.45
Std Dev 3.925
Gaus mean : 37.4756
Gaus Width: 4.3326
Entry
ampl
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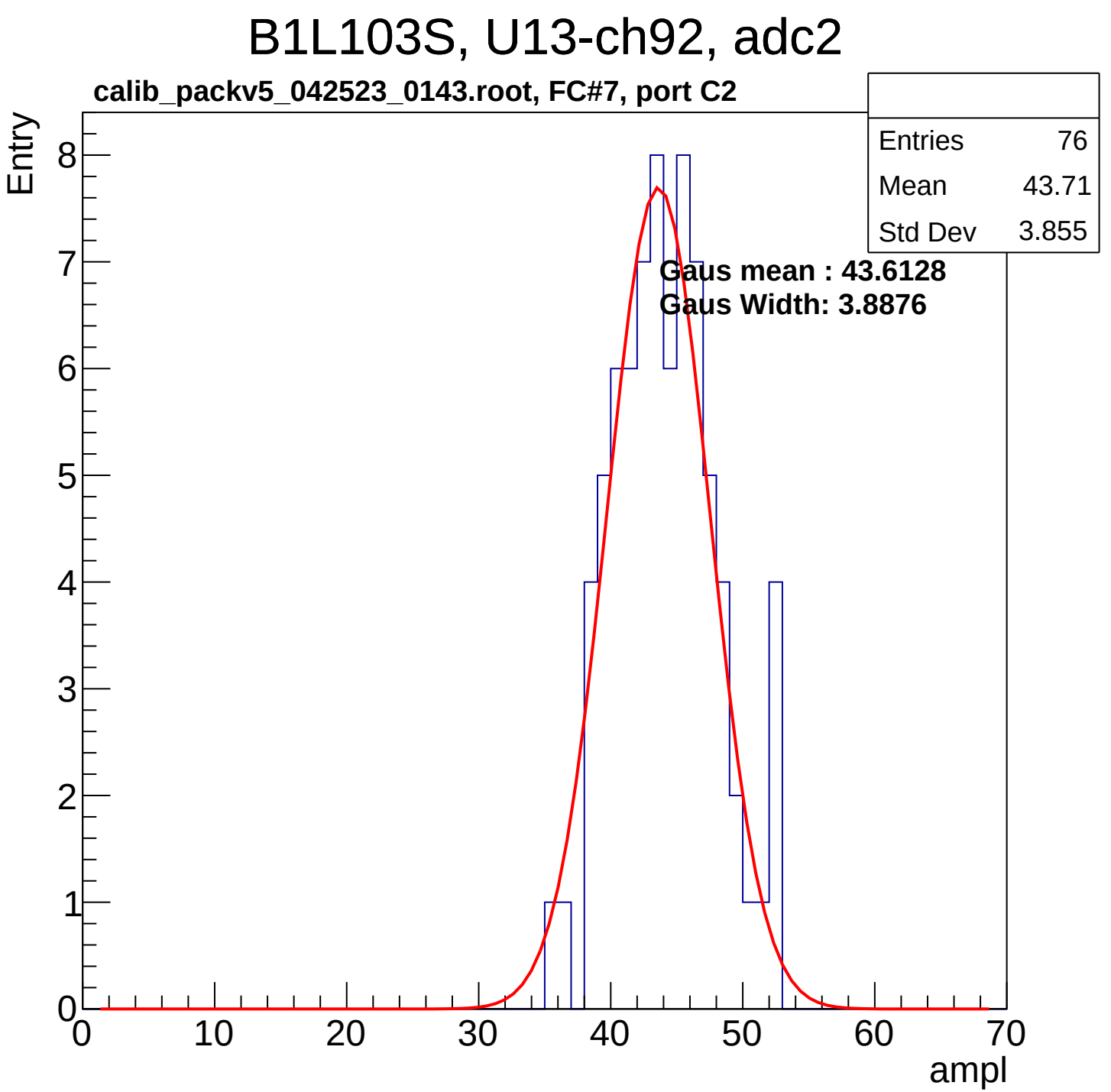
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch92, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 43.71
Std Dev 3.855
Gaus mean : 43.6128
Gaus Width: 3.8876
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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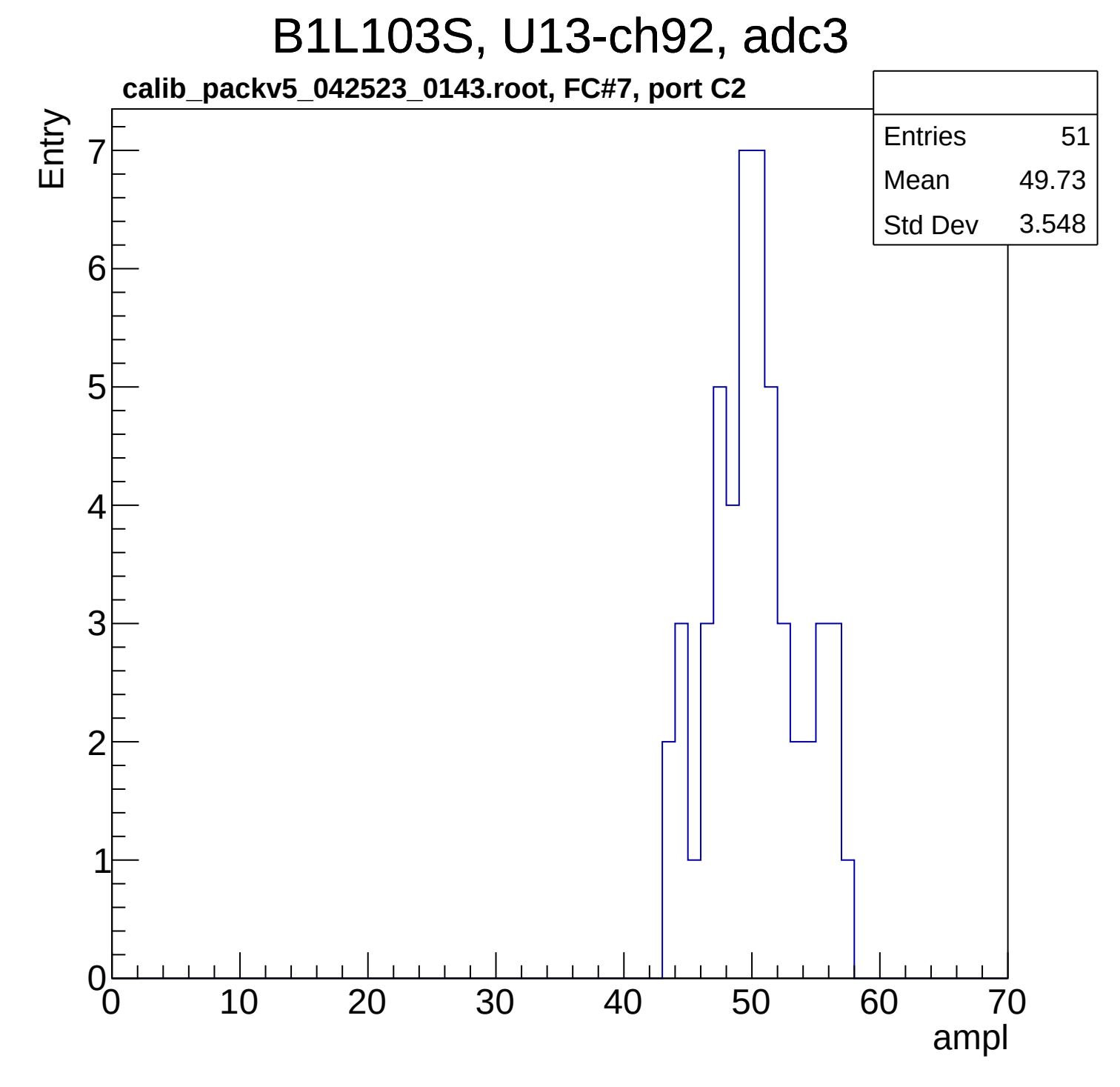
B1L103S, U13-ch92, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 49.73
Std Dev 3.548
Entry
ampl
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3
4
5
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7
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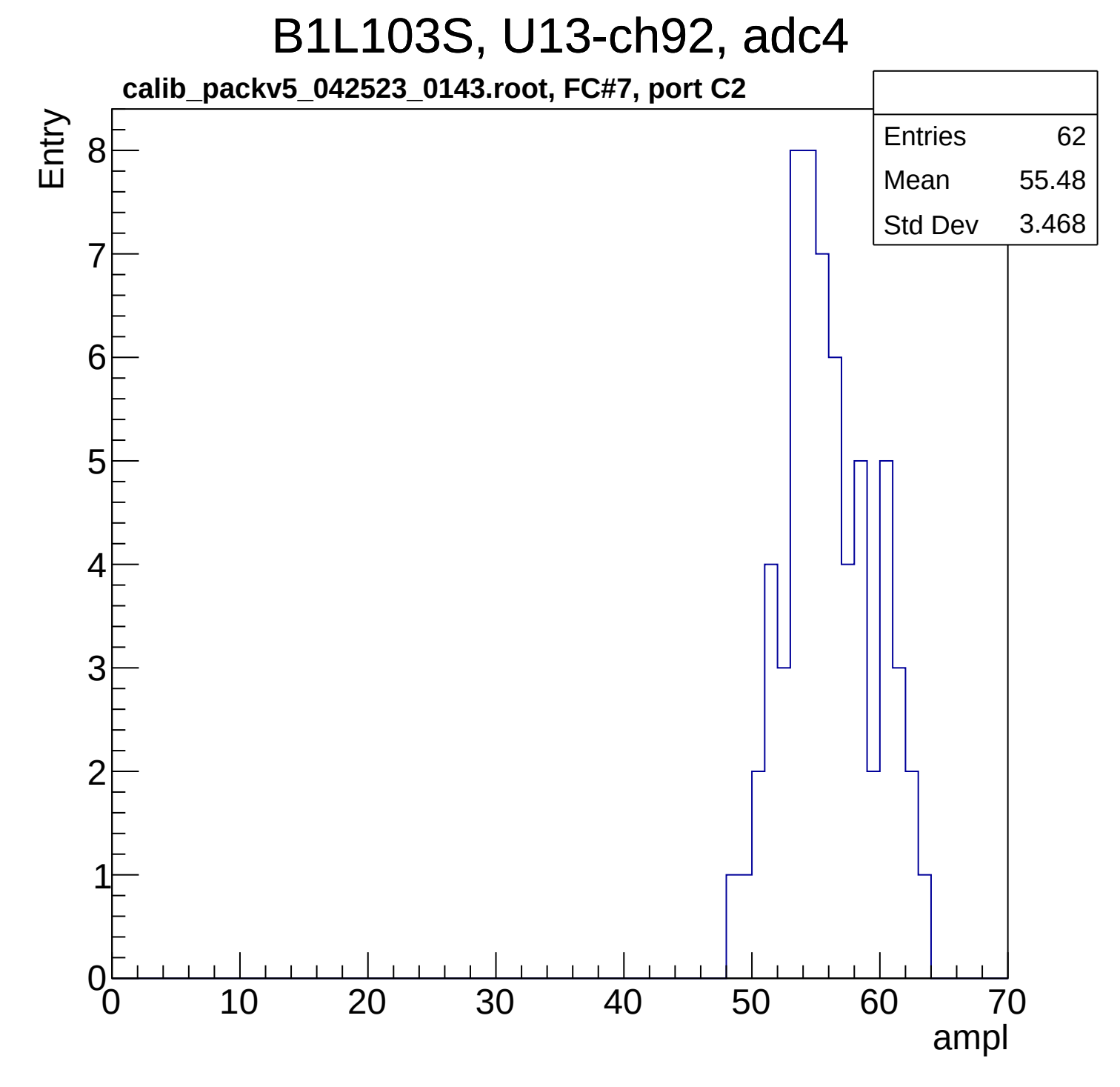
B1L103S, U13-ch92, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.48
Std Dev 3.468
Entry
ampl
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3
4
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6
7
8
0
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60
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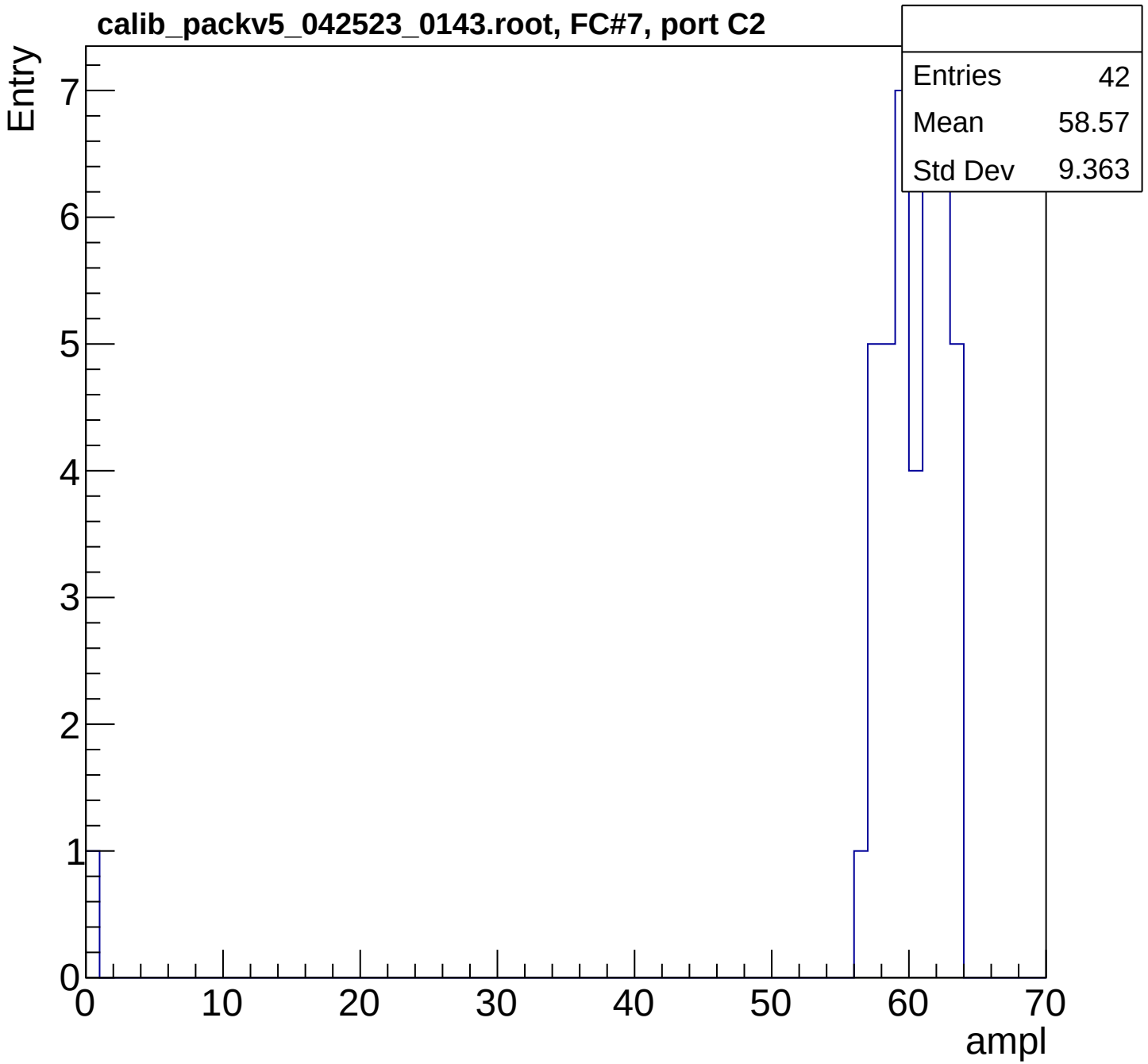
B1L103S, U13-ch92, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.57
Std Dev 9.363
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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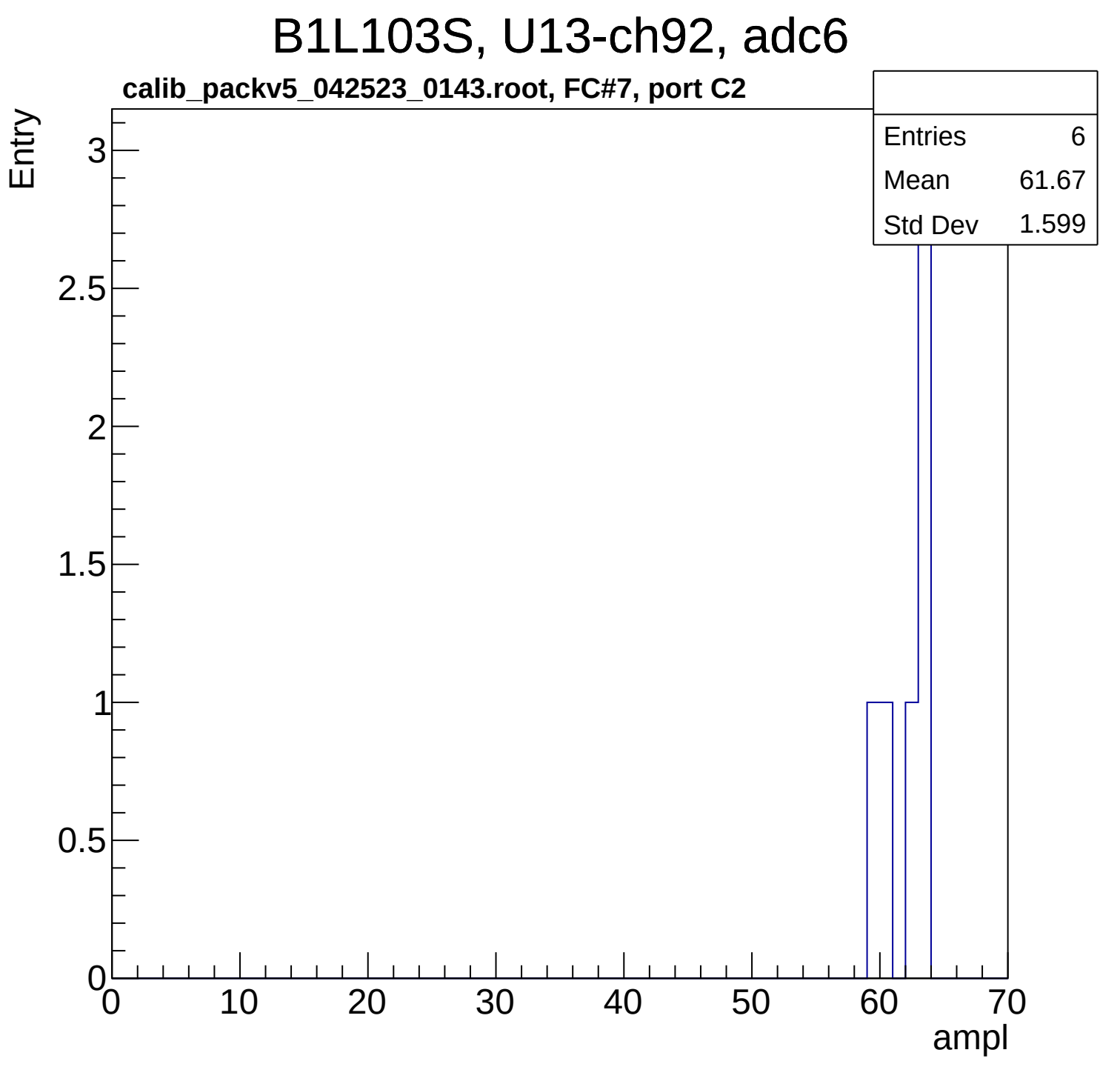
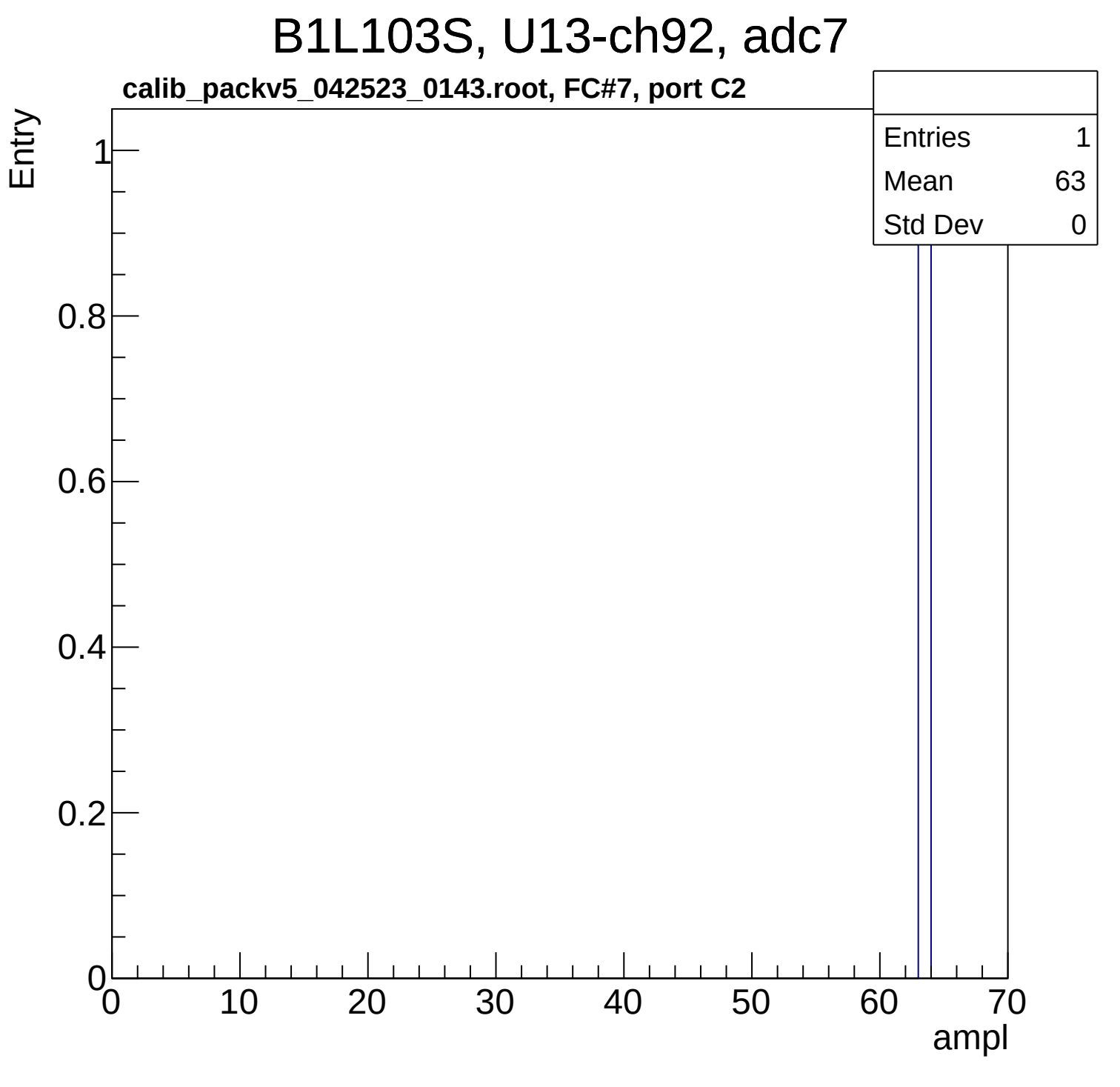
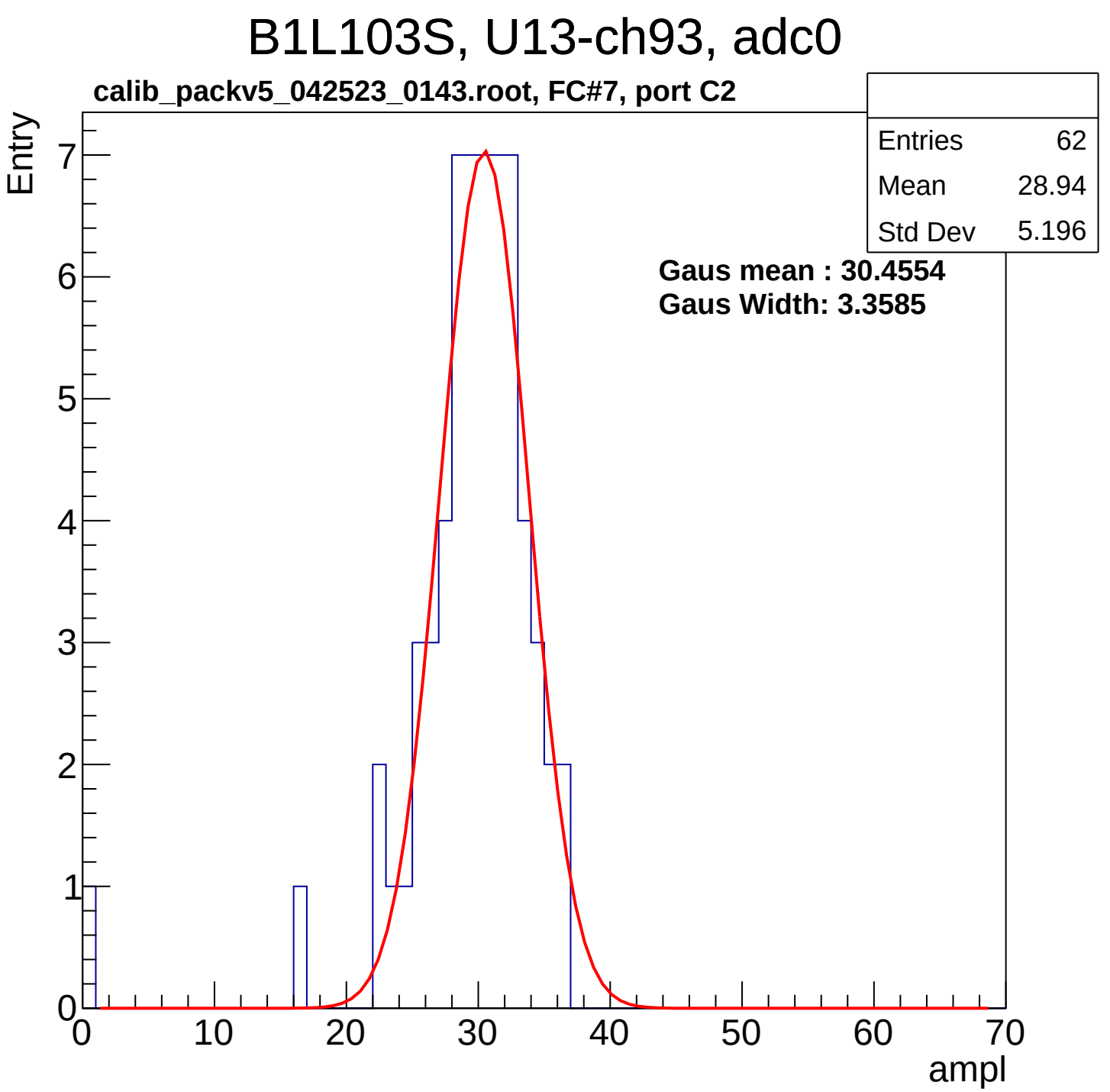
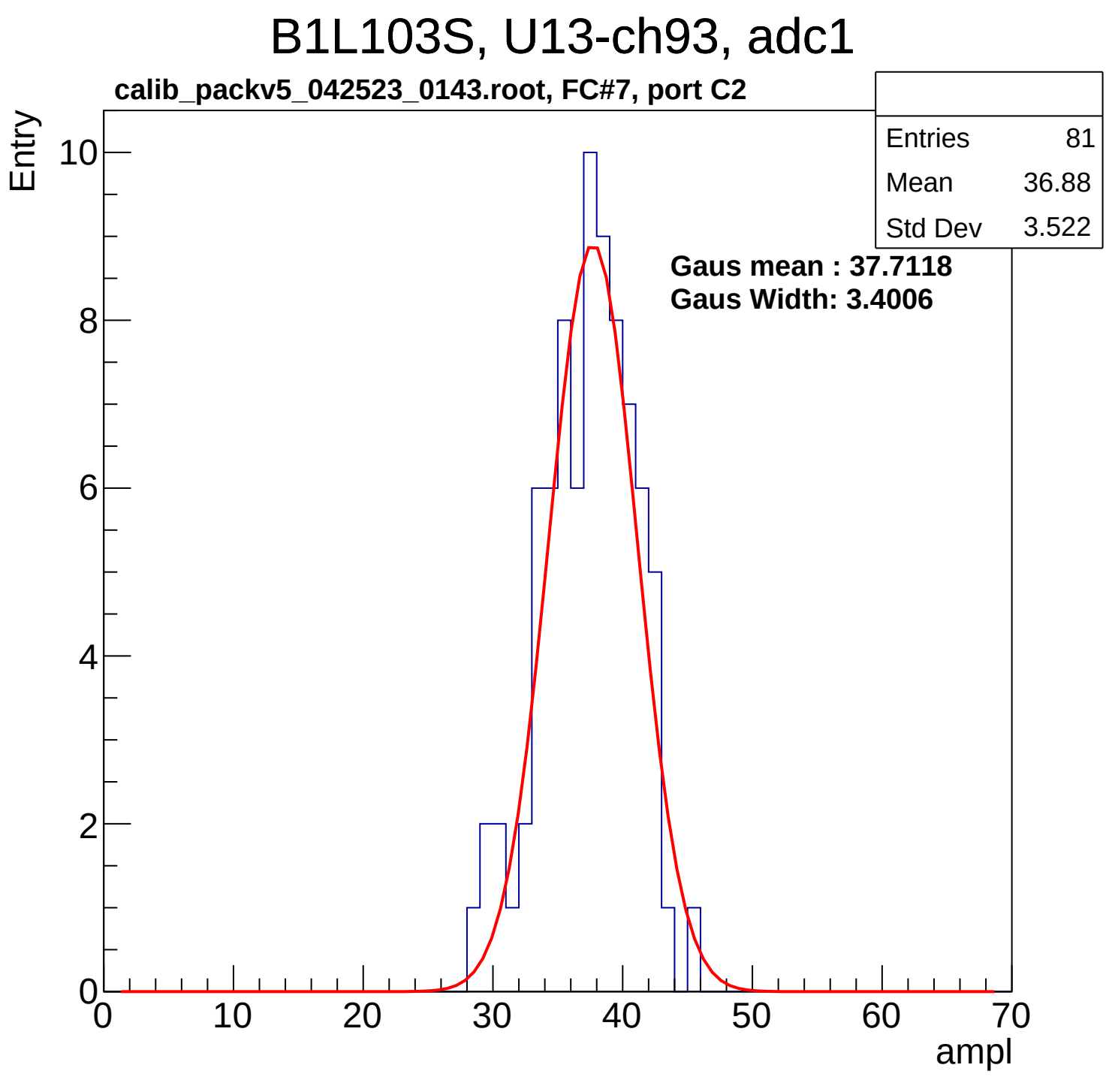
40
50
60
70


B1L103S, U13-ch92, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.67
Std Dev 1.599
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch92, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch93, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28.94
Std Dev 5.196
Gaus mean : 30.4554
Gaus Width: 3.3585
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch93, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 36.88
Std Dev 3.522
Gaus mean : 37.7118
Gaus Width: 3.4006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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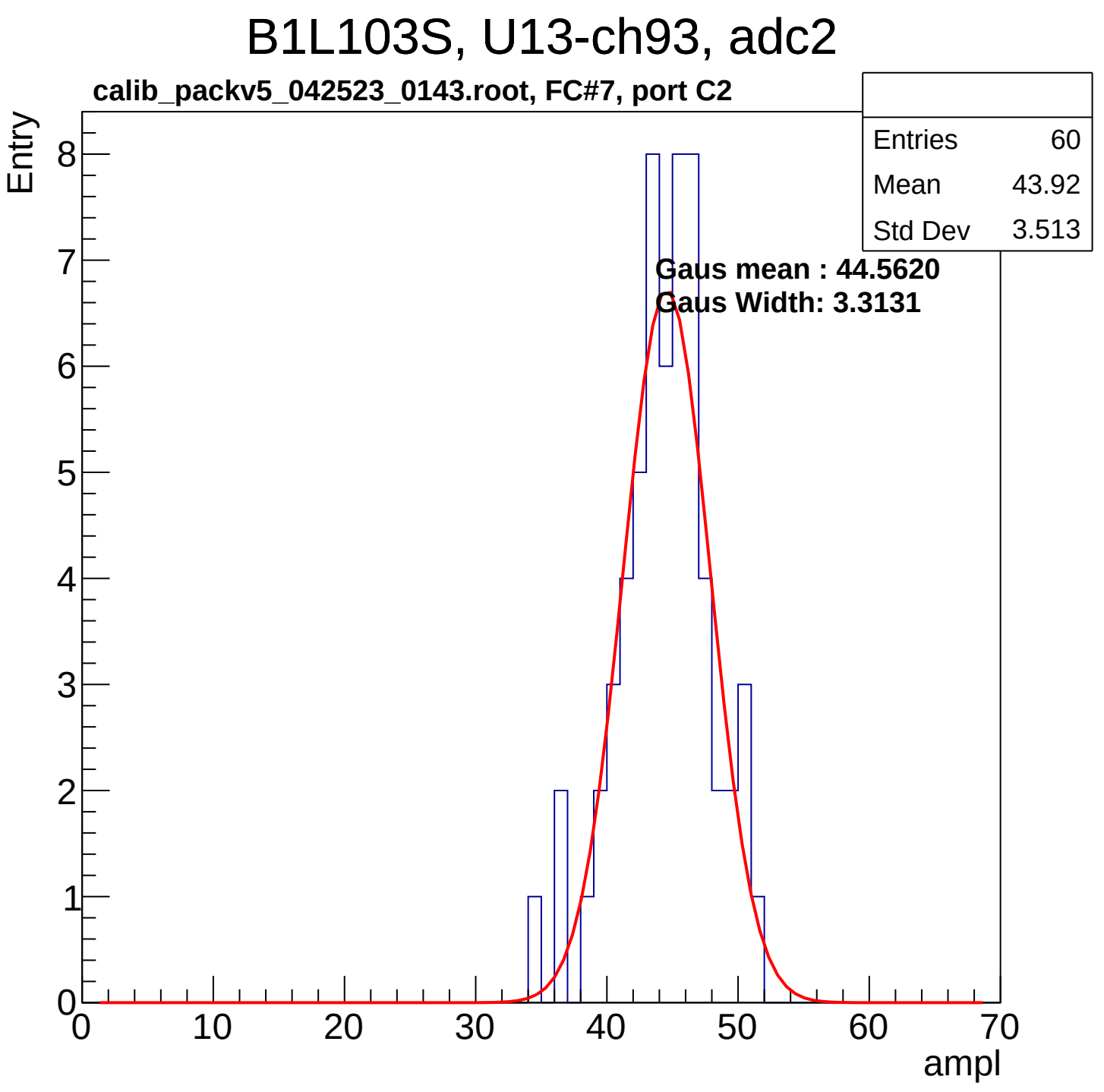
B1L103S, U13-ch93, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.92
Std Dev 3.513
Gaus mean : 44.5620
Gaus Width: 3.3131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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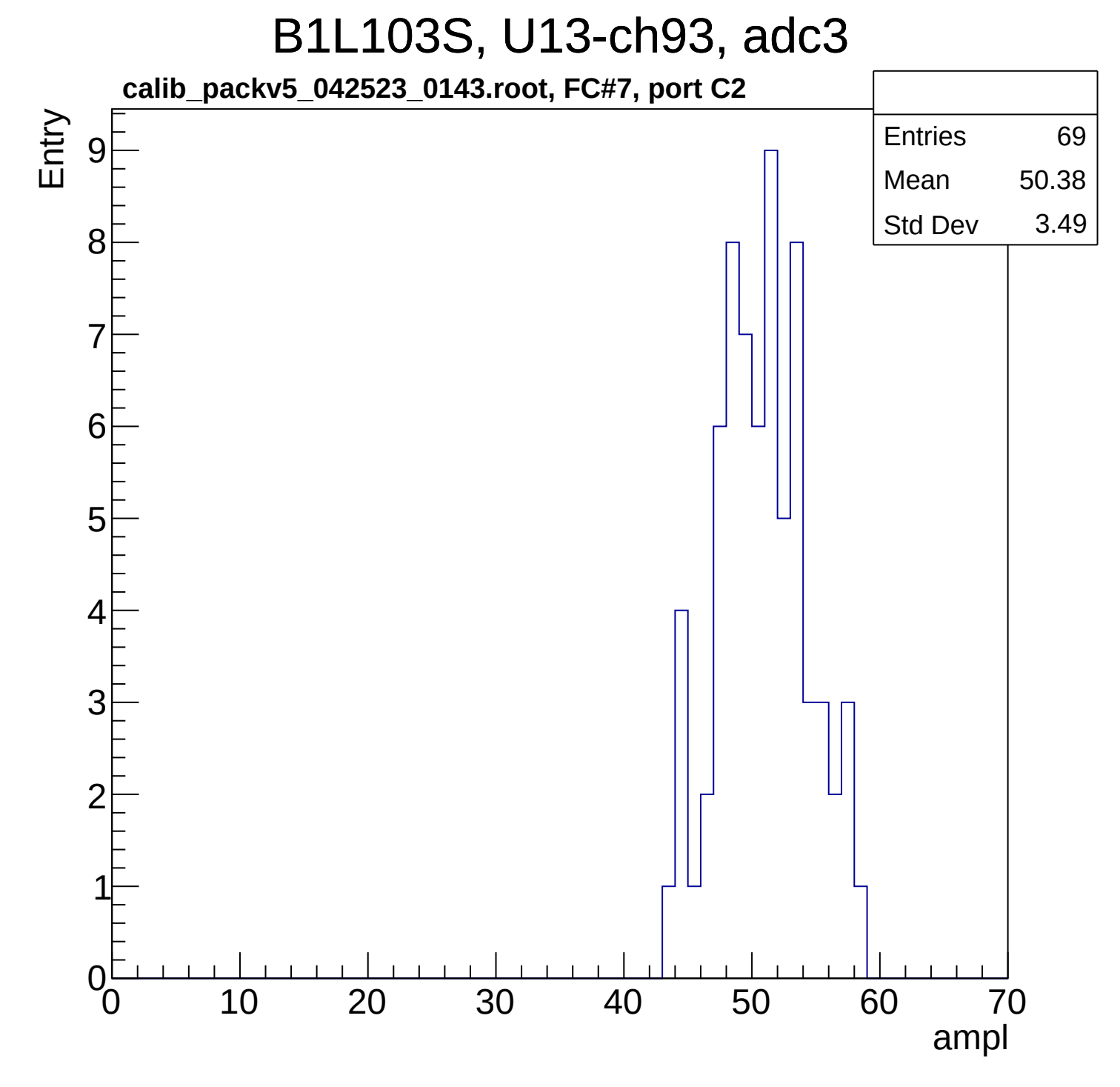
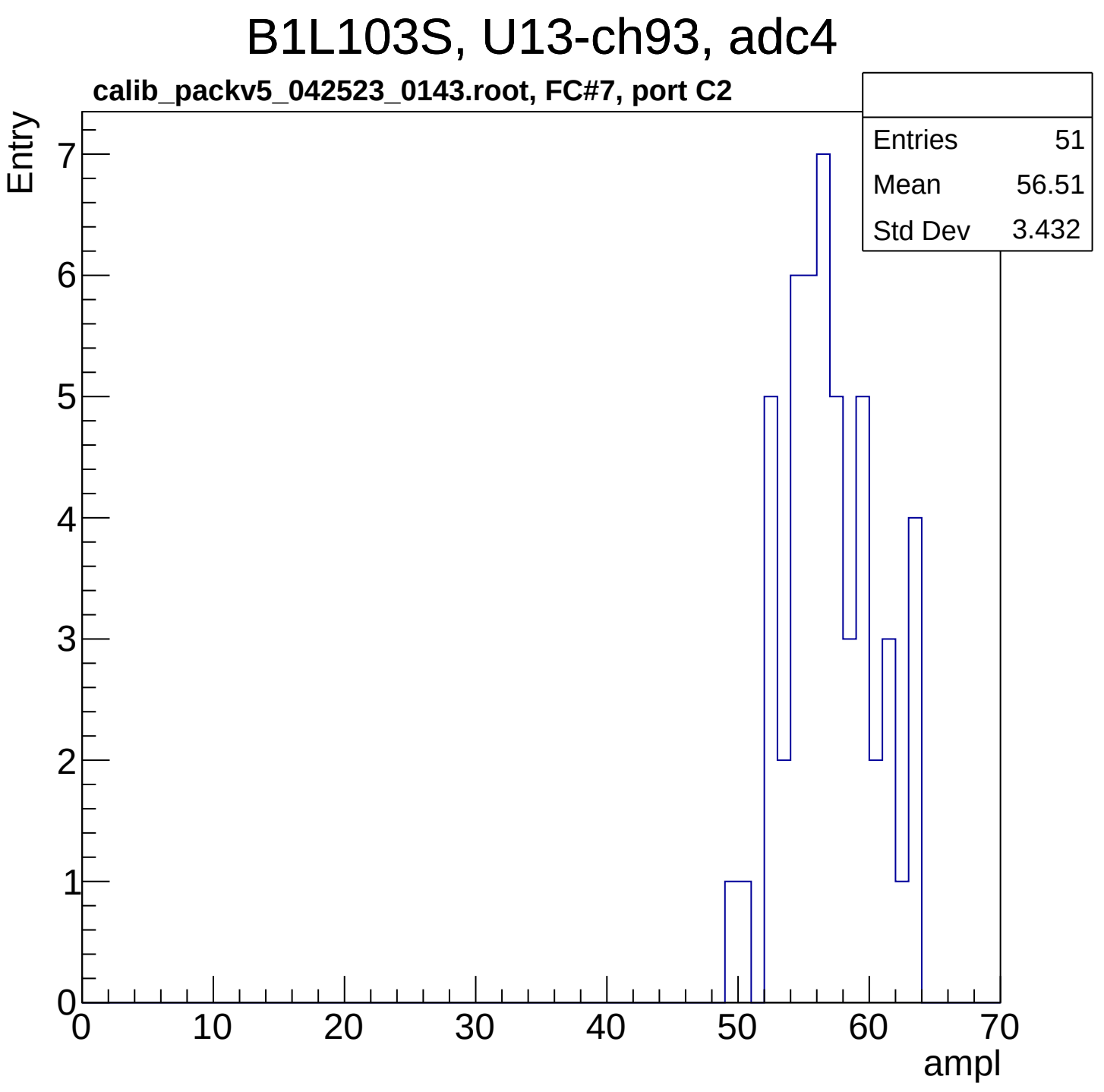
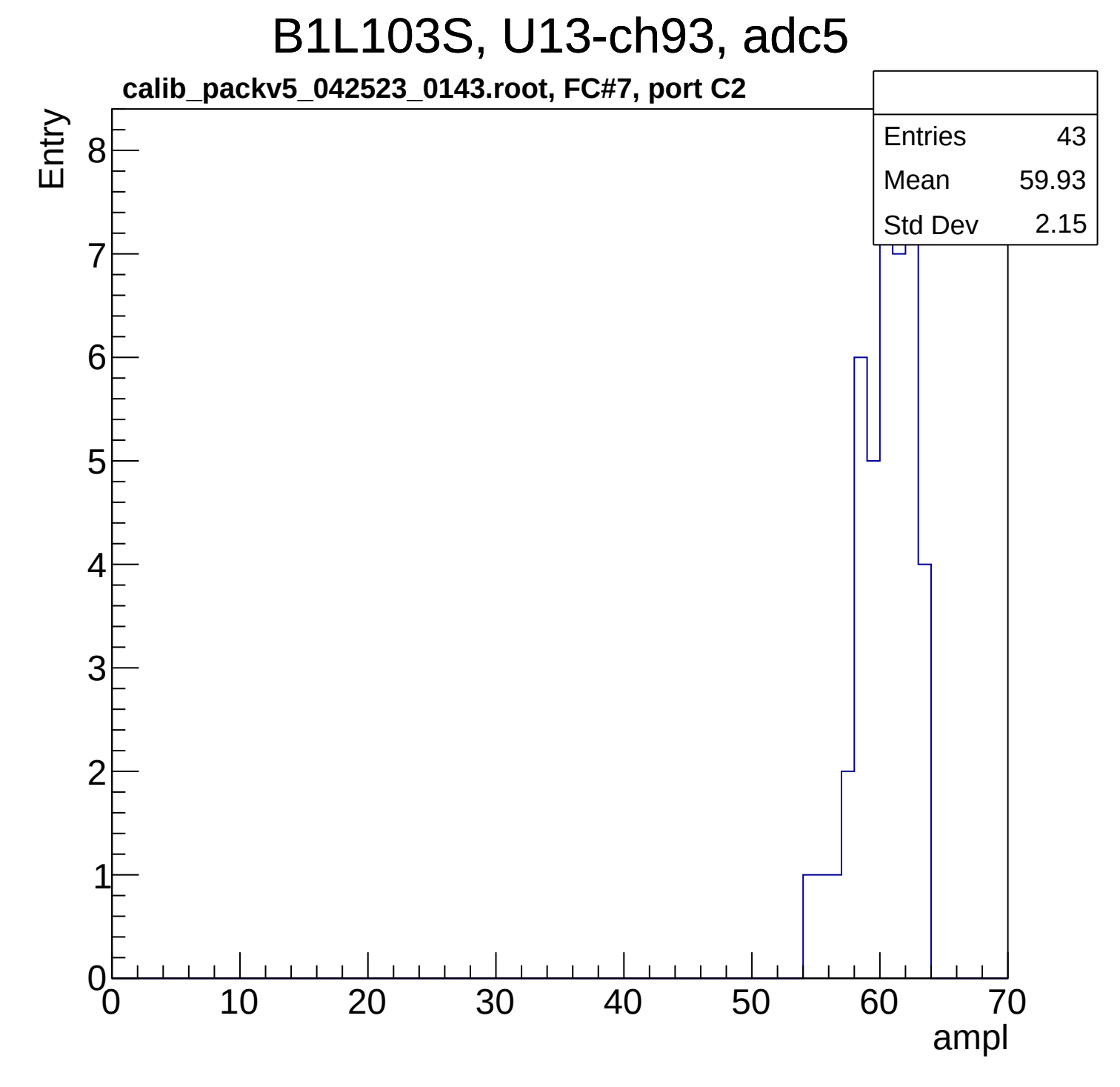

B1L103S, U13-ch93, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.38
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch93, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 56.51
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch93, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.93
Std Dev 2.15
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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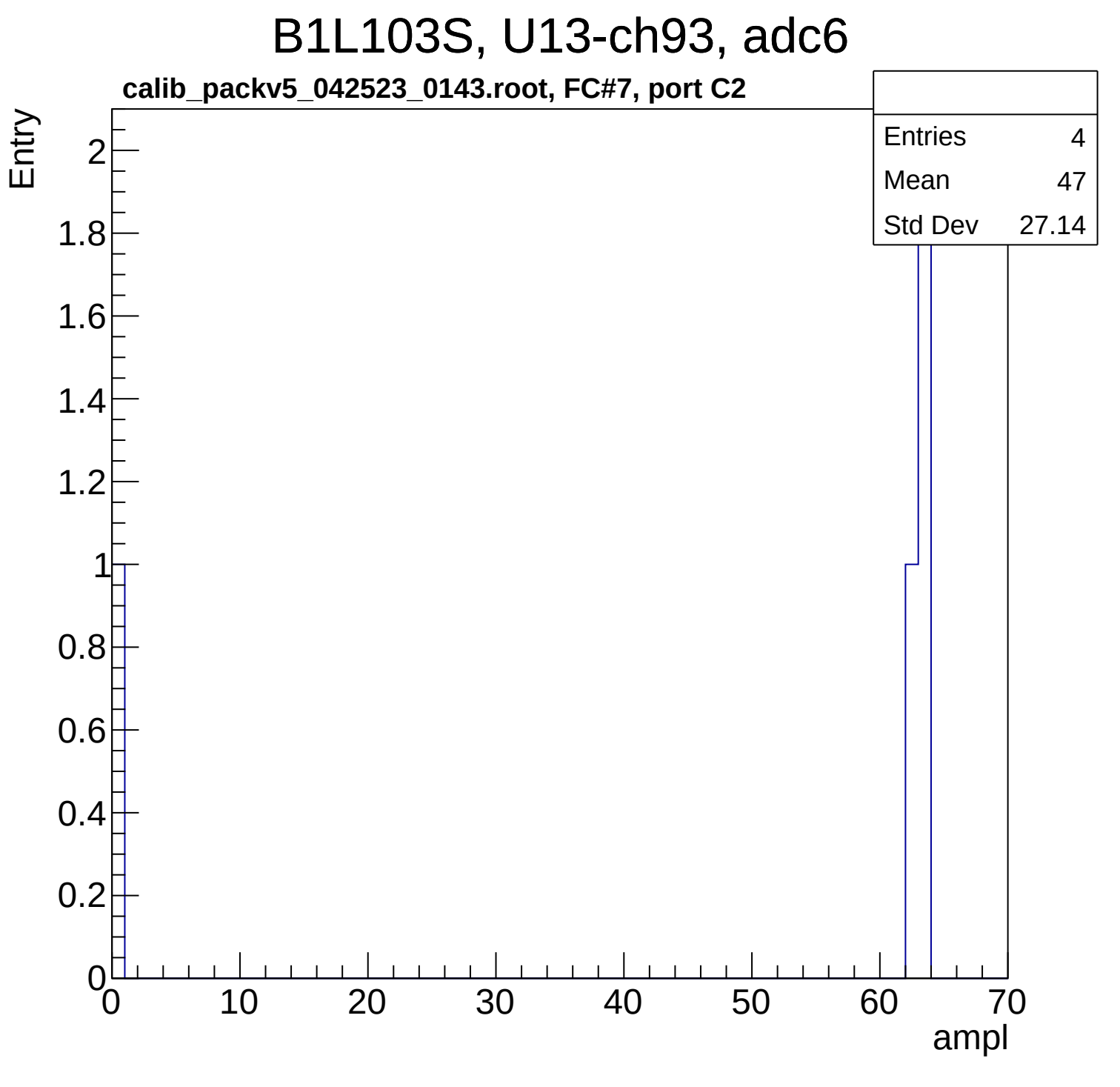
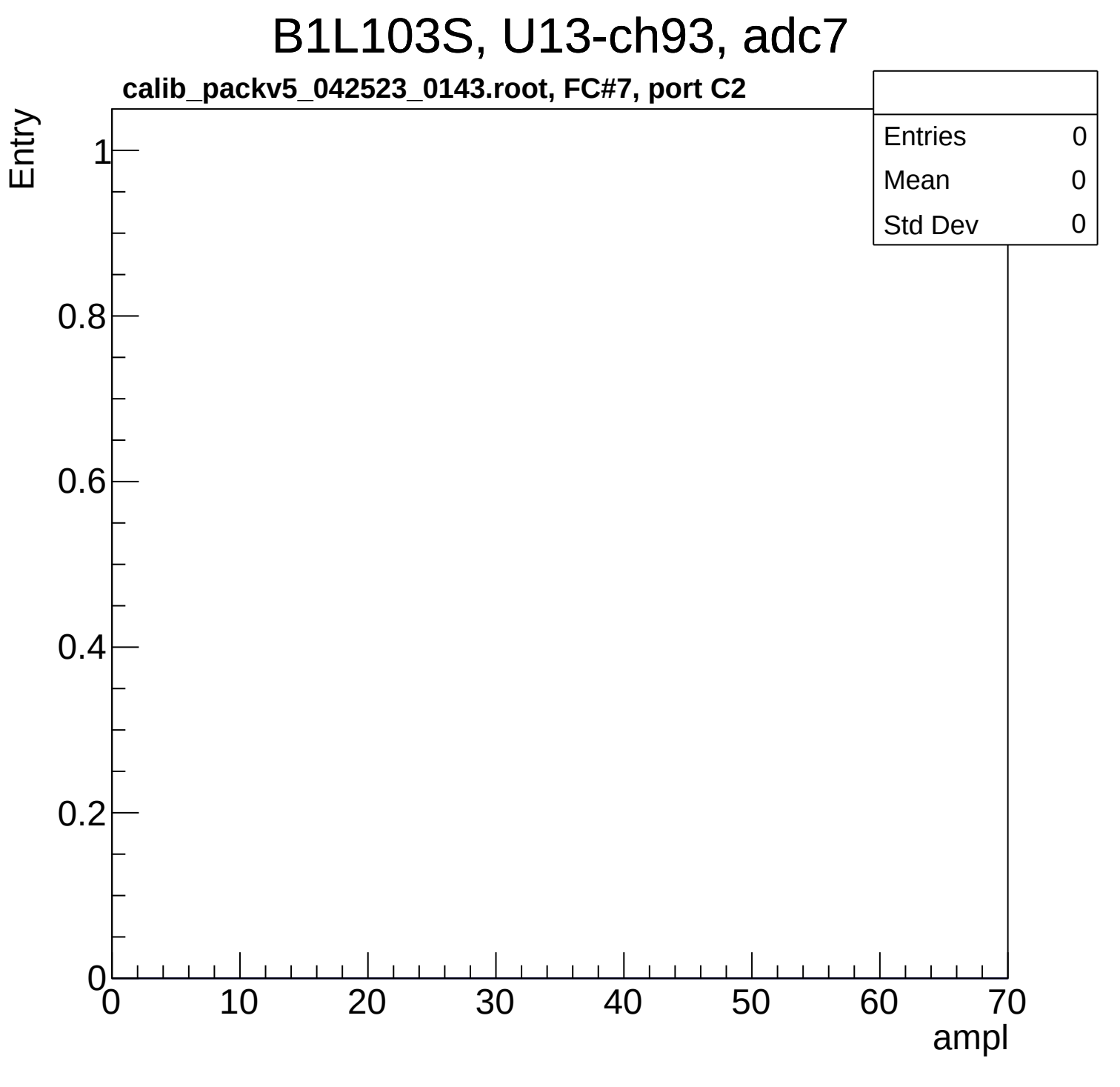
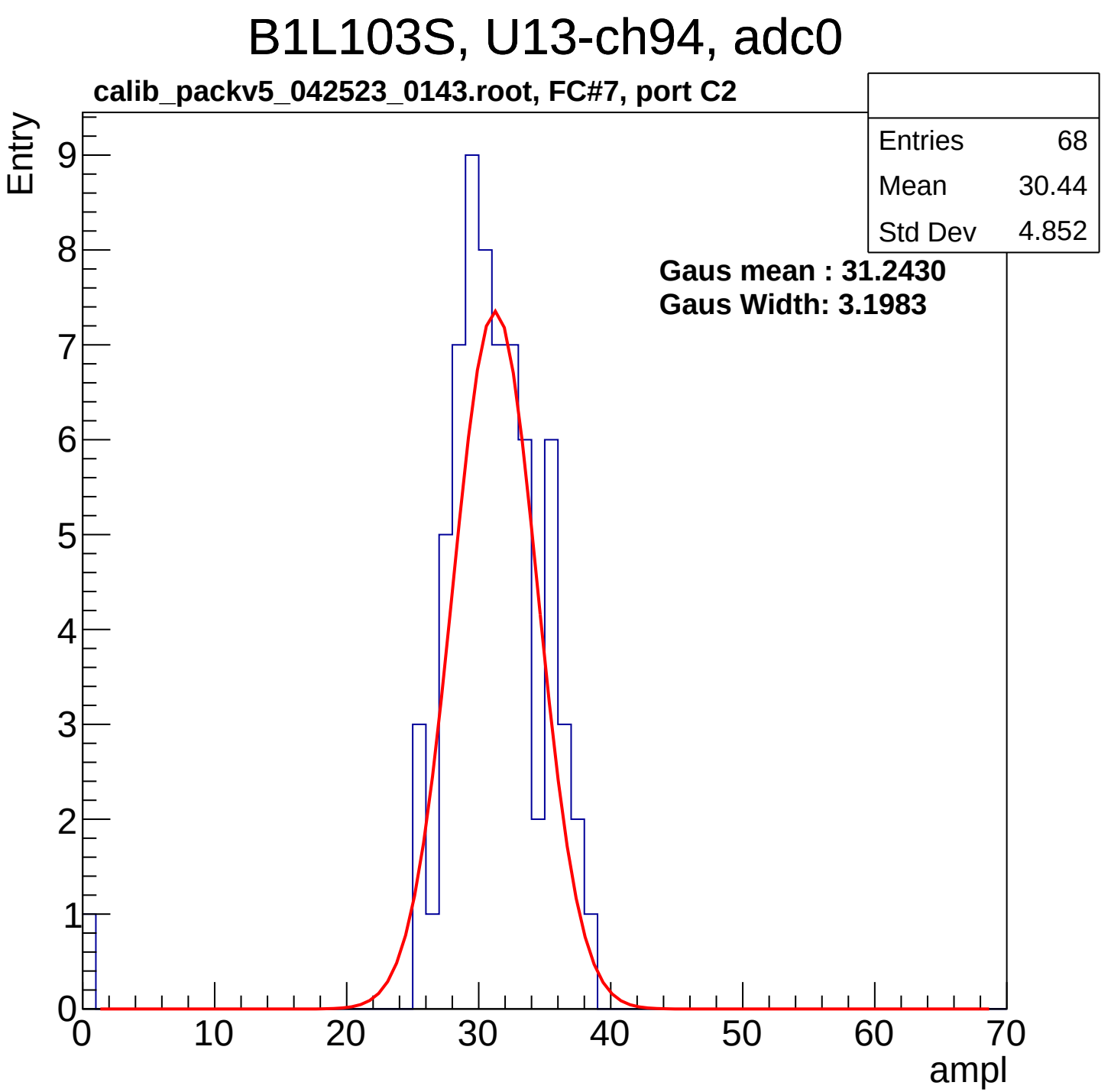
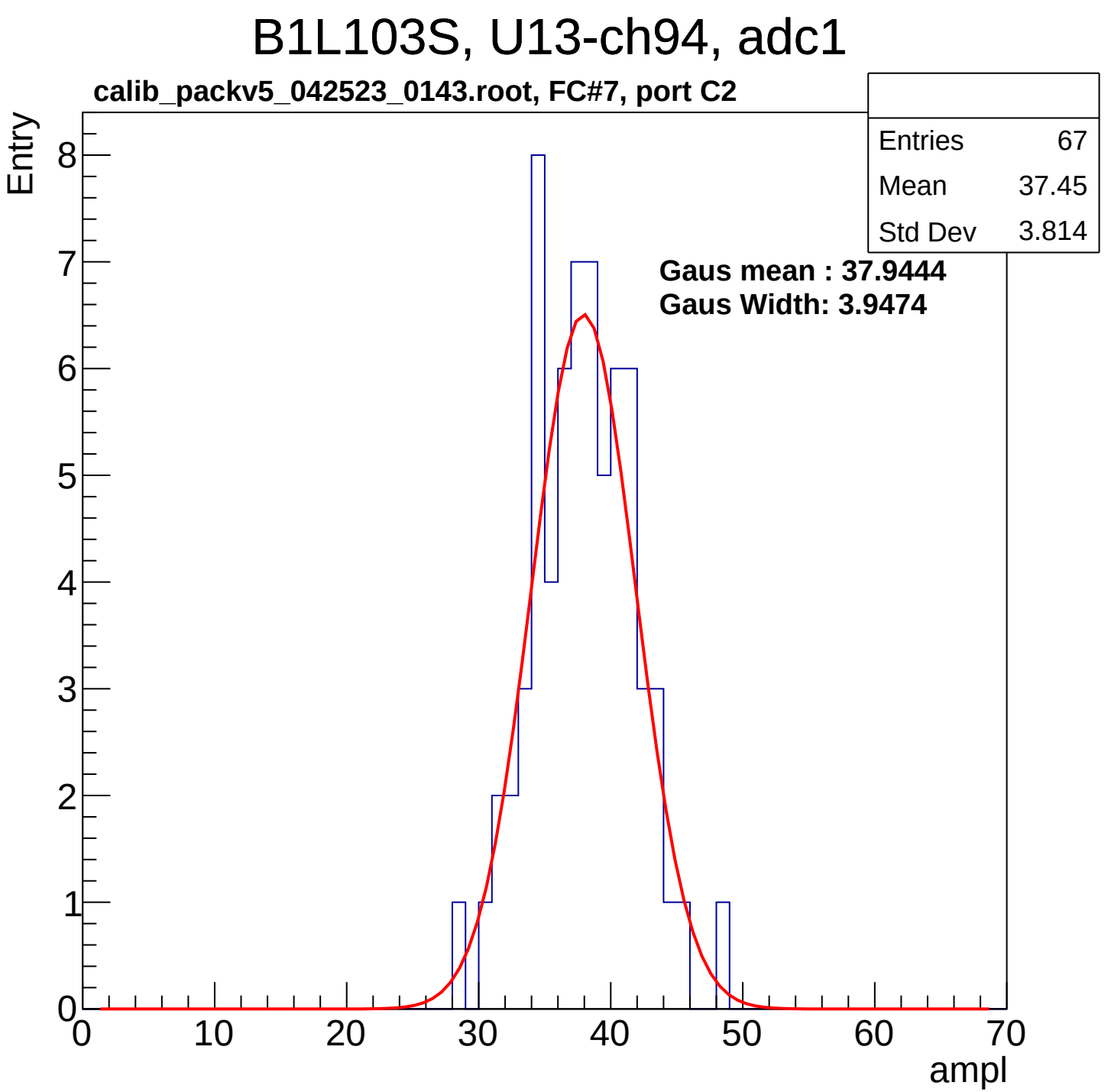
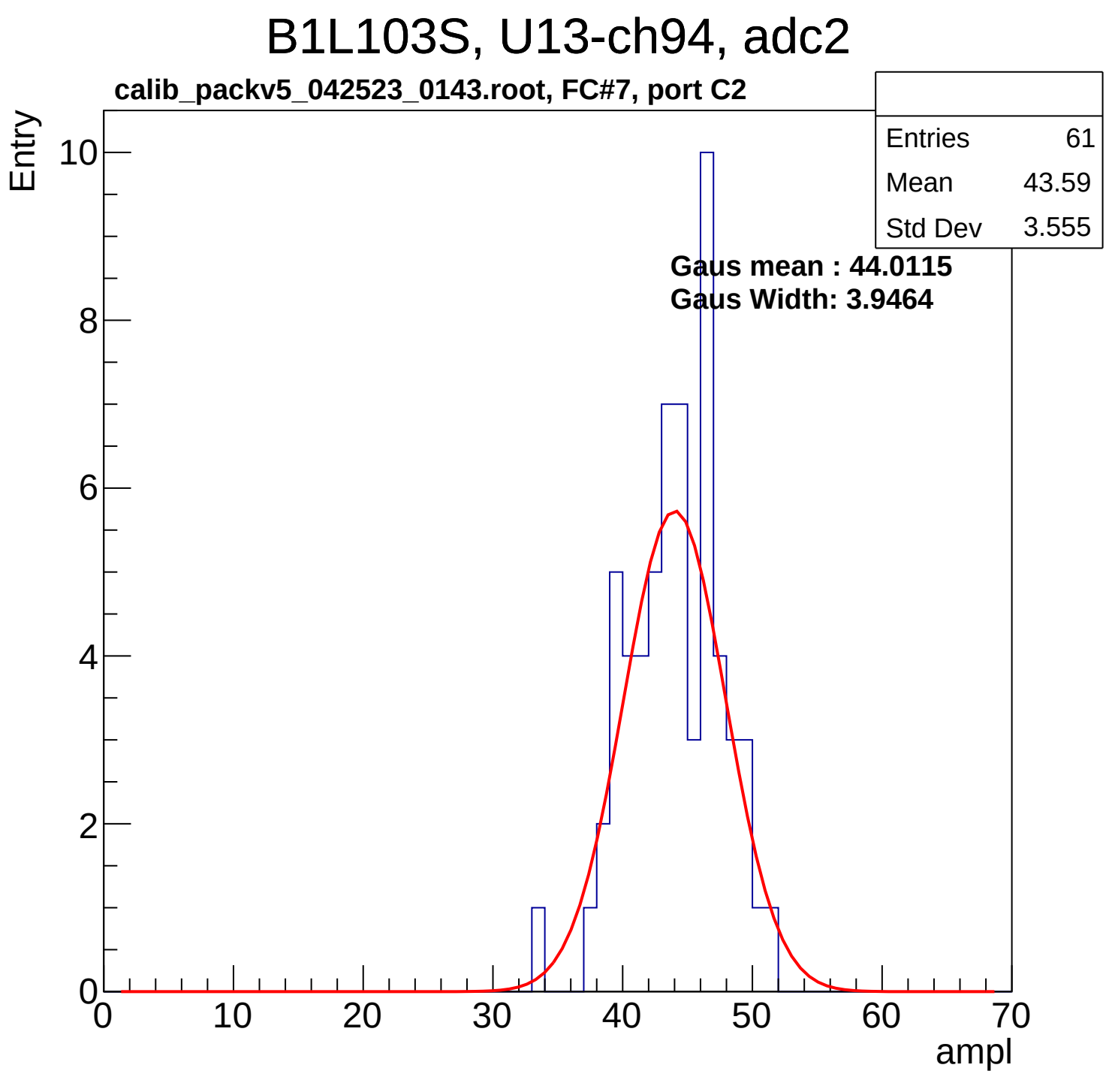
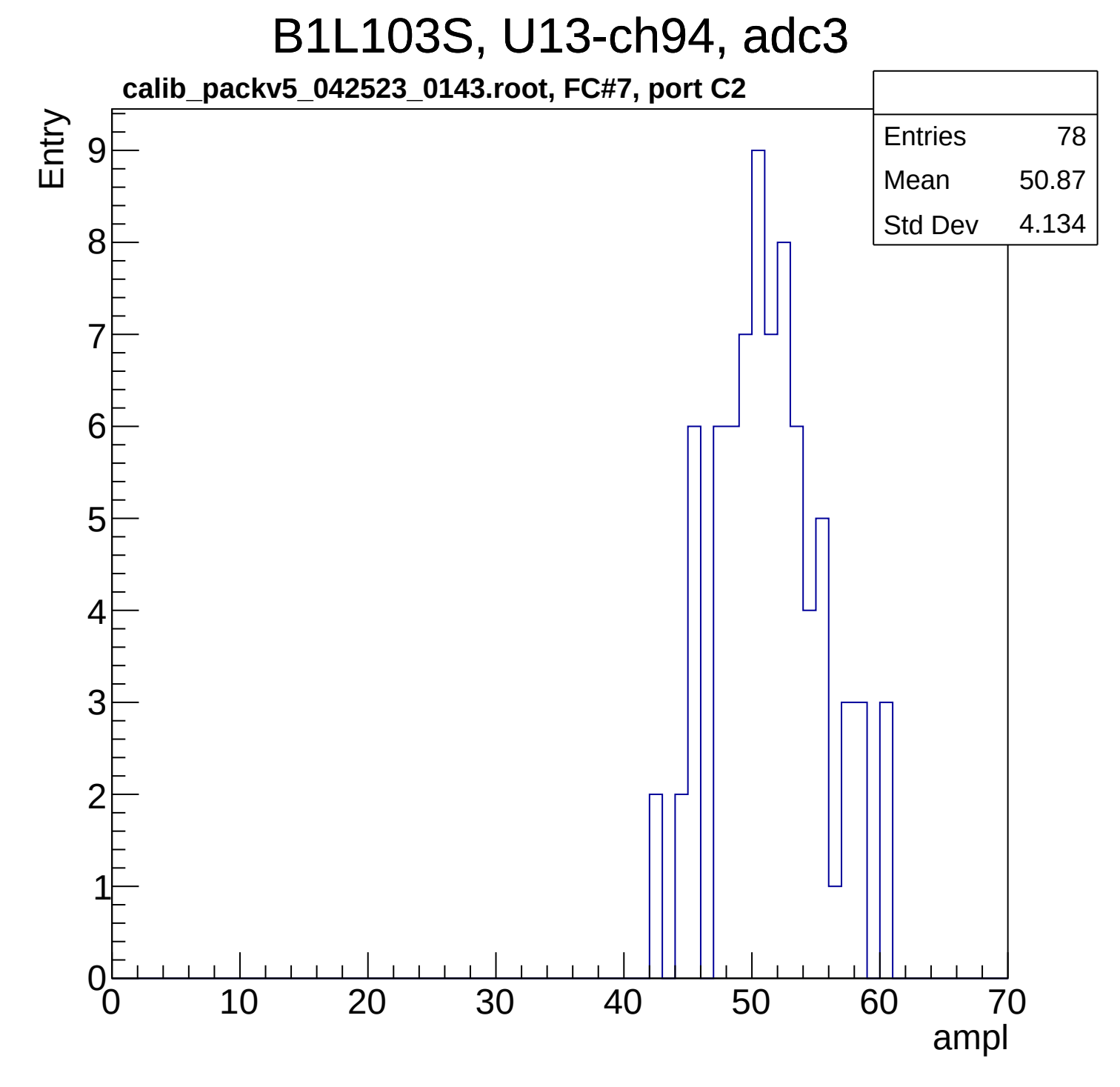

B1L103S, U13-ch93, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

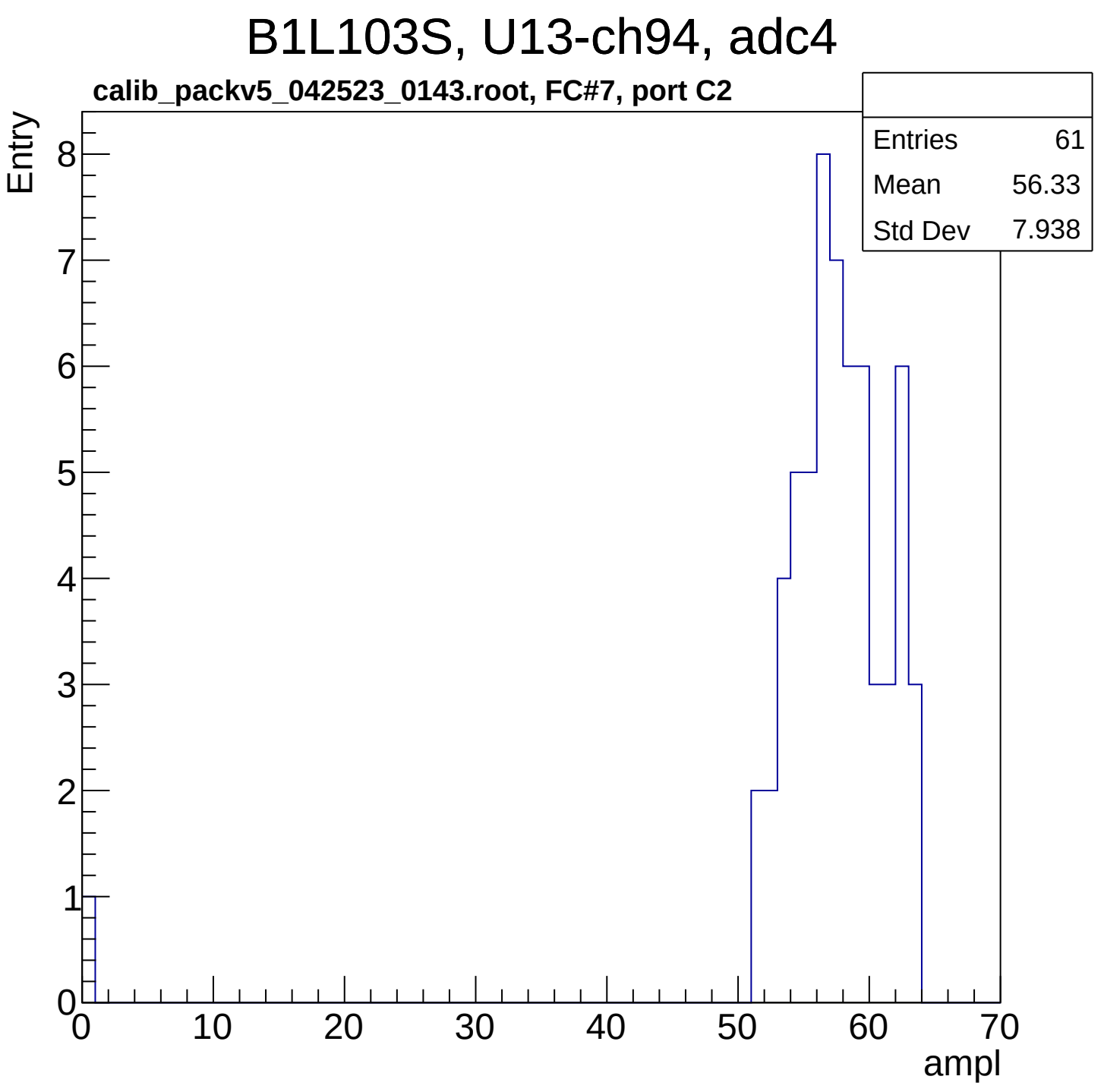

B1L103S, U13-ch93, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

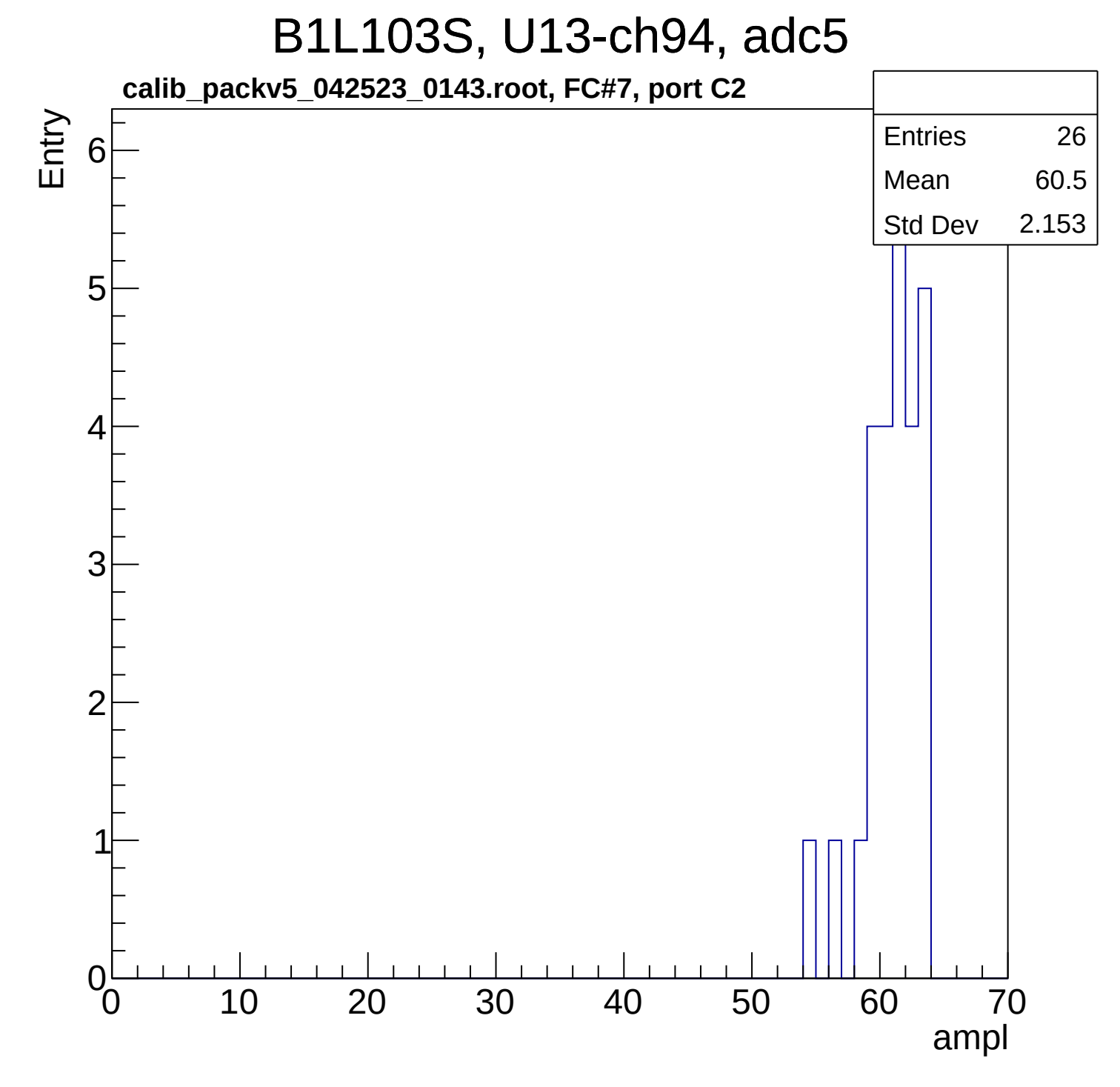

B1L103S, U13-ch94, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.44
Std Dev 4.852
Gaus mean : 31.2430
Gaus Width: 3.1983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

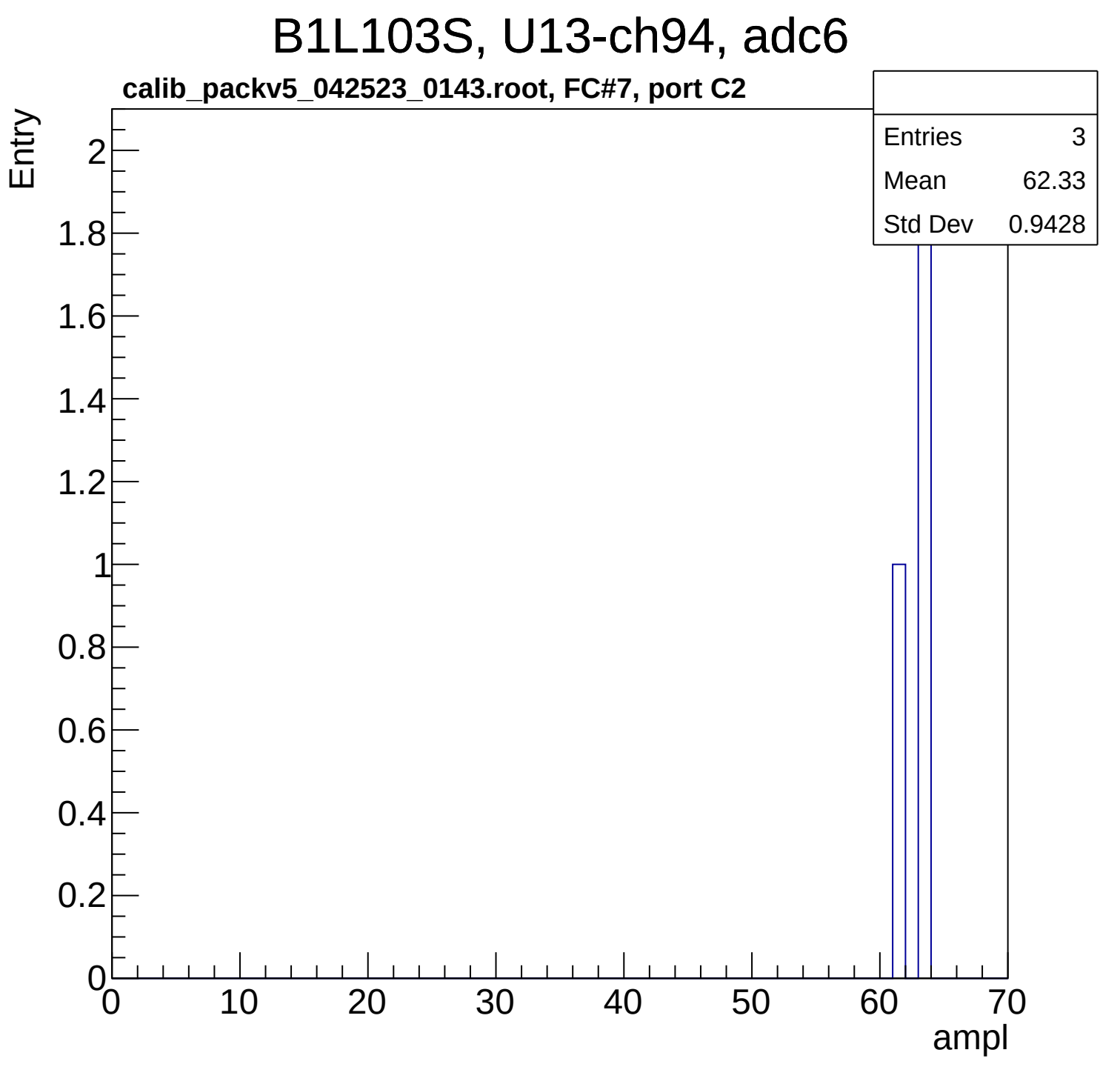

B1L103S, U13-ch94, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.45
Std Dev 3.814
Gaus mean : 37.9444
Gaus Width: 3.9474
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

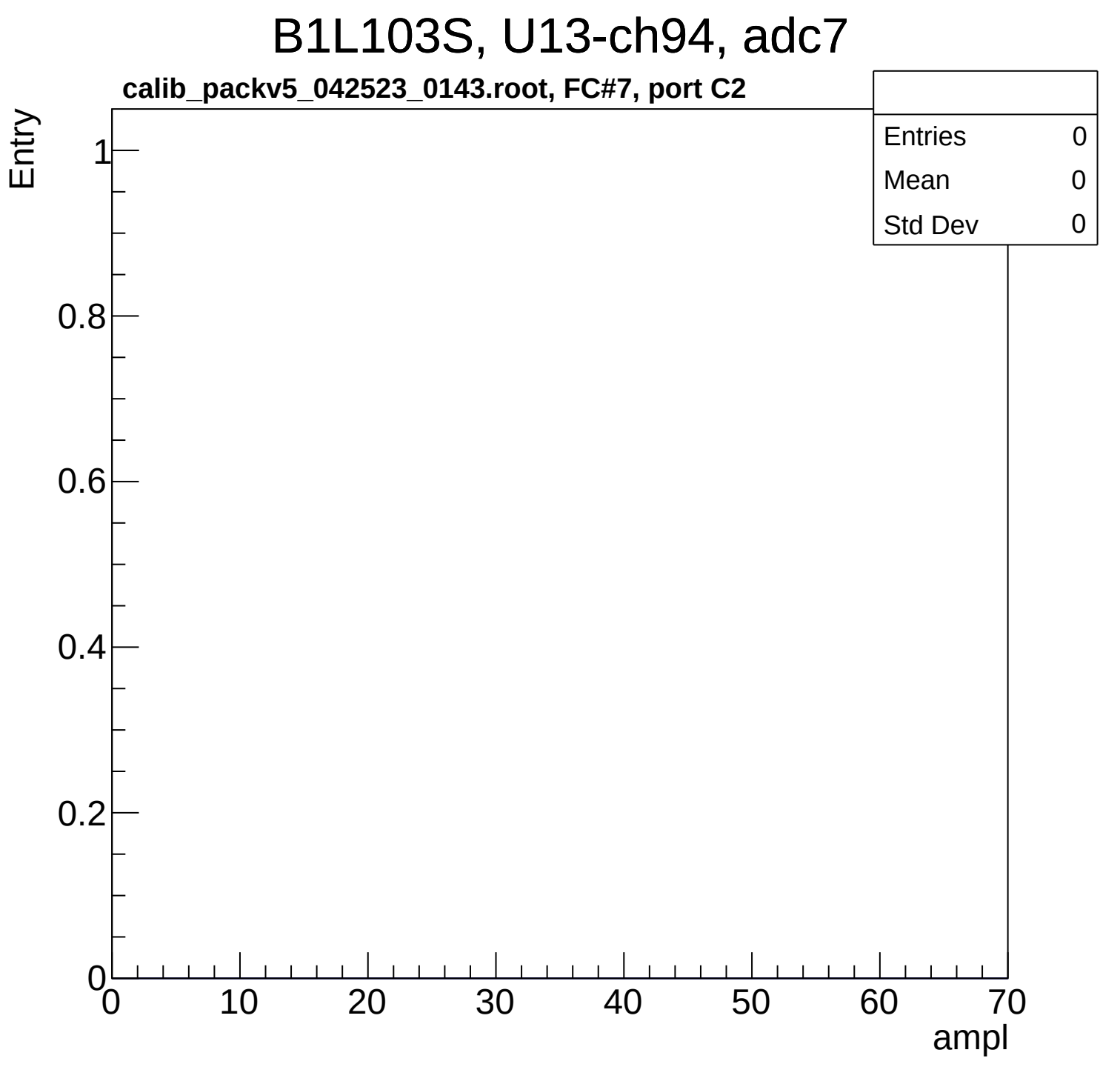

B1L103S, U13-ch94, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.59
Std Dev 3.555
Gaus mean : 44.0115
Gaus Width: 3.9464
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

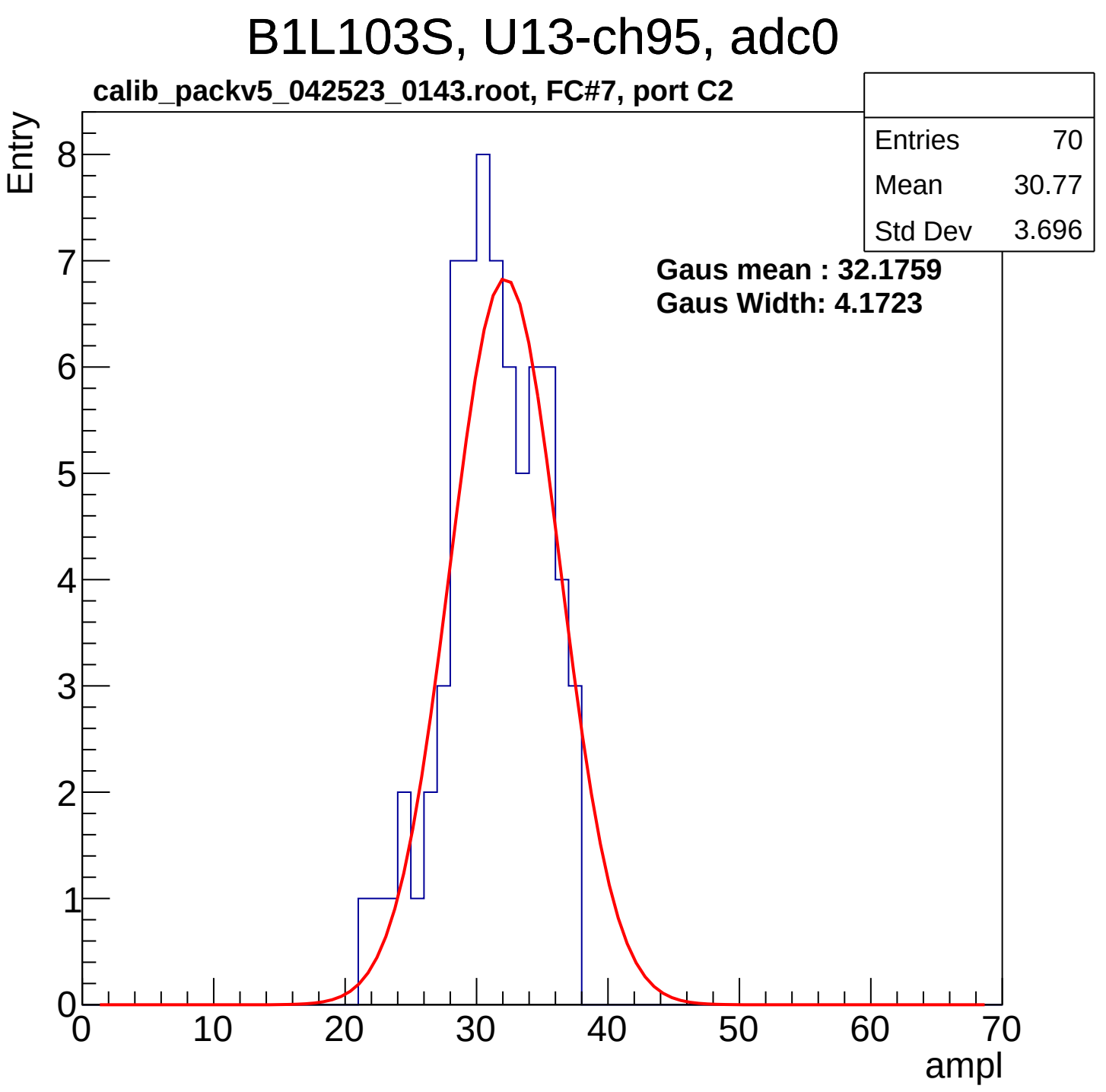

B1L103S, U13-ch94, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 50.87
Std Dev 4.134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

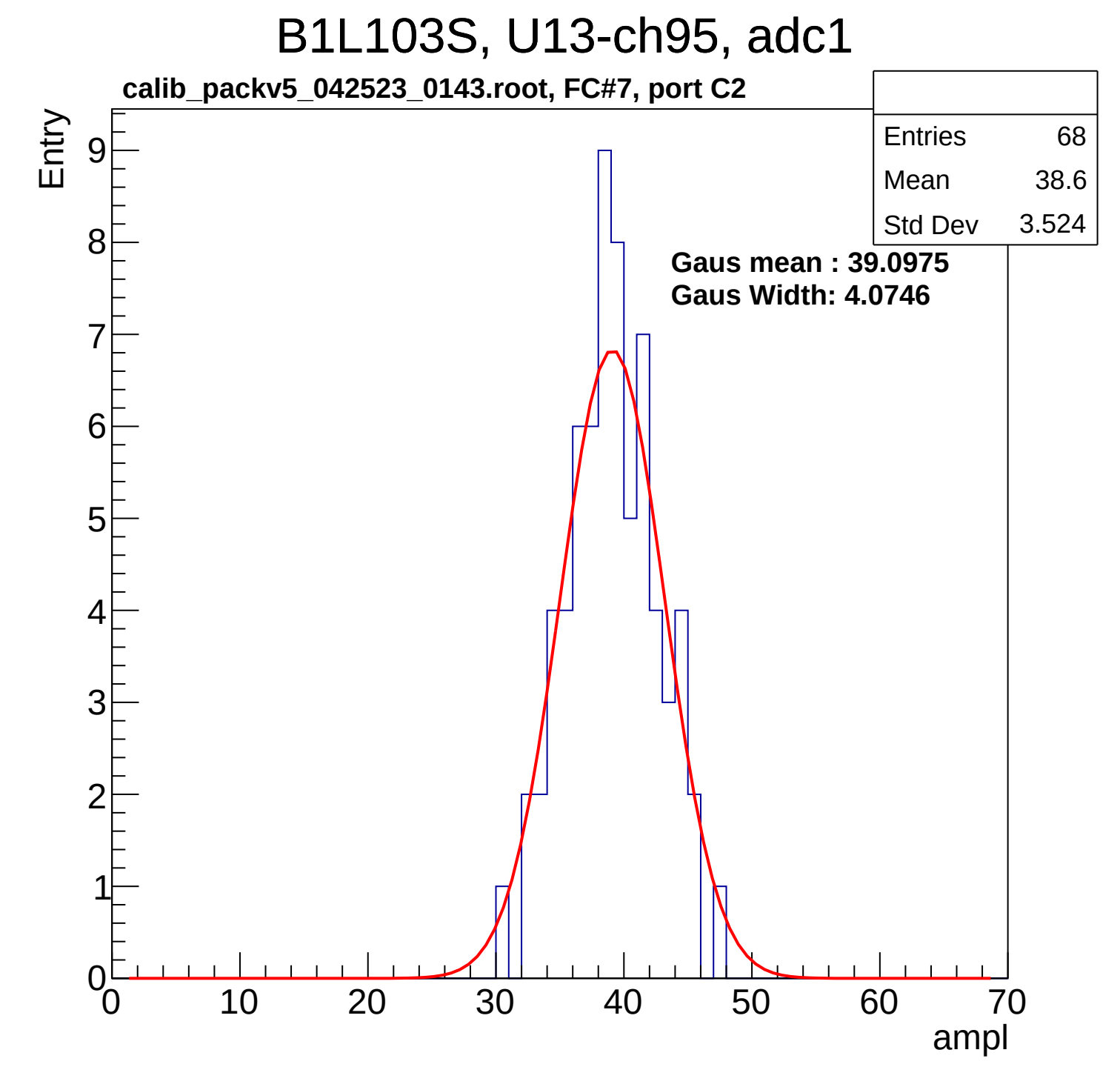

B1L103S, U13-ch94, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.33
Std Dev 7.938
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch94, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.5
Std Dev 2.153
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch94, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch94, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch95, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 30.77
Std Dev 3.696
Gaus mean : 32.1759
Gaus Width: 4.1723
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch95, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 38.6
Std Dev 3.524
Gaus mean : 39.0975
Gaus Width: 4.0746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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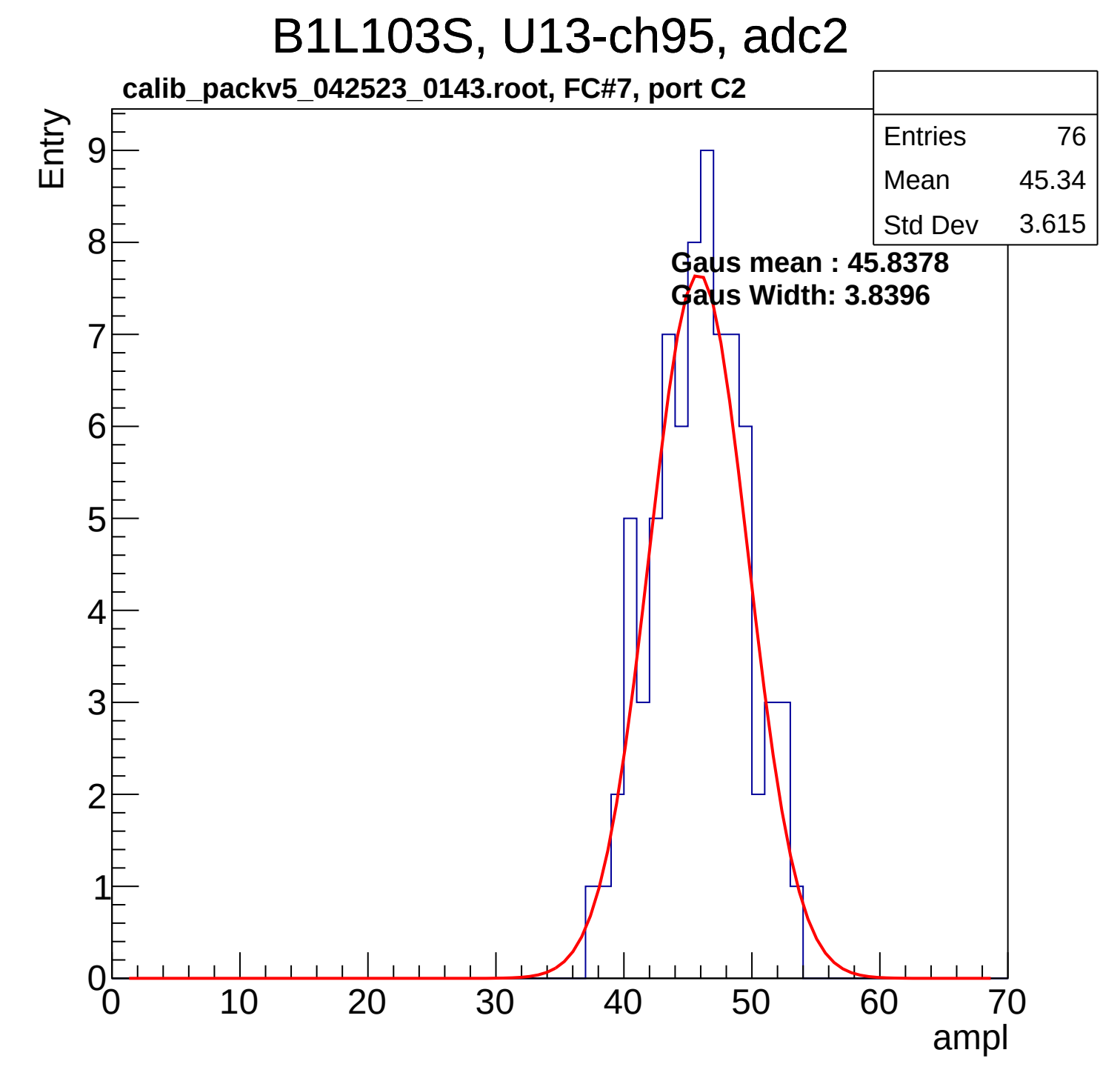
B1L103S, U13-ch95, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 45.34
Std Dev 3.615
Gaus mean : 45.8378
Gaus Width: 3.8396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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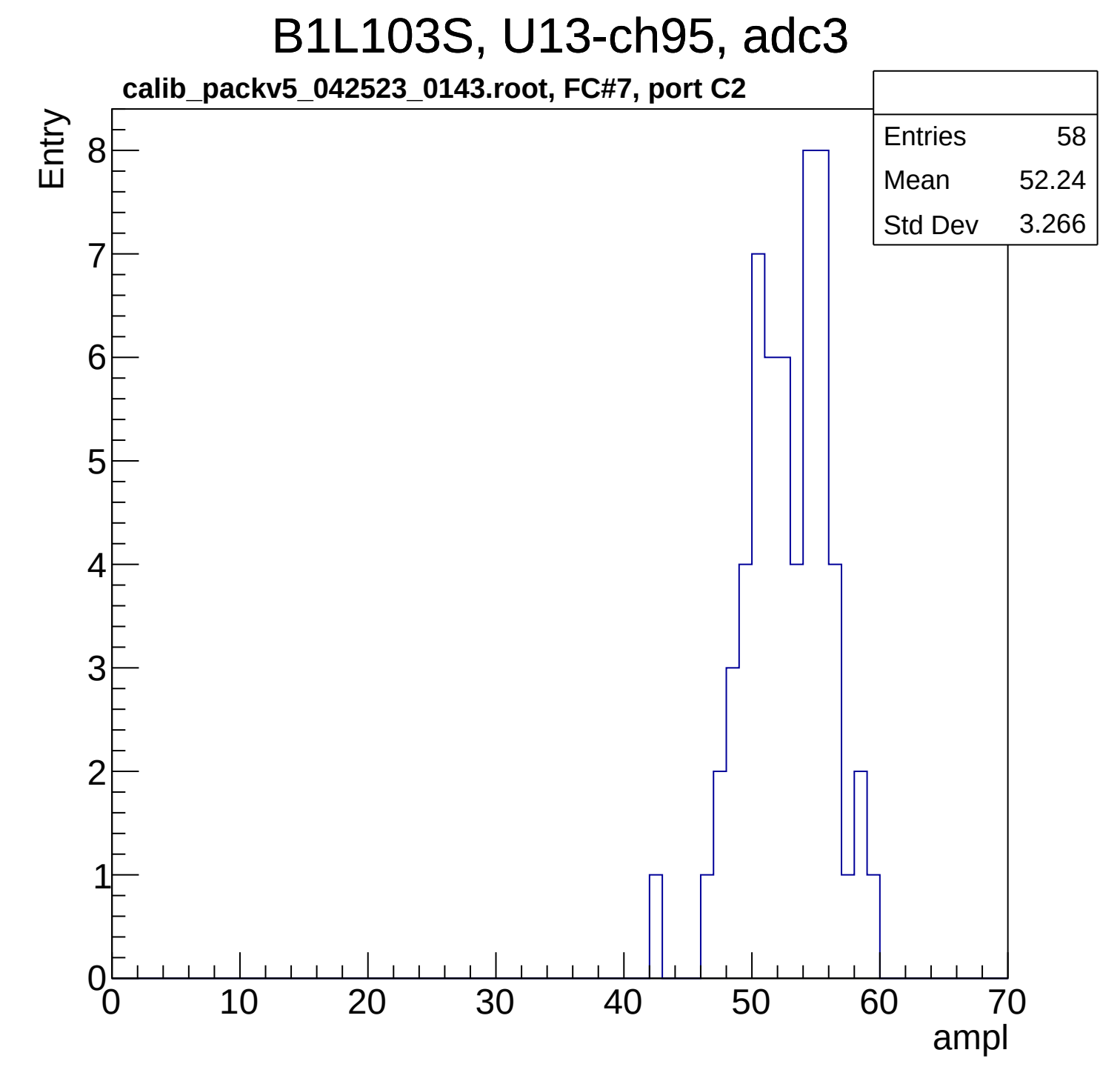
B1L103S, U13-ch95, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 52.24
Std Dev 3.266
Entry
ampl
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2
3
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5
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7
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0
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20
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40
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60
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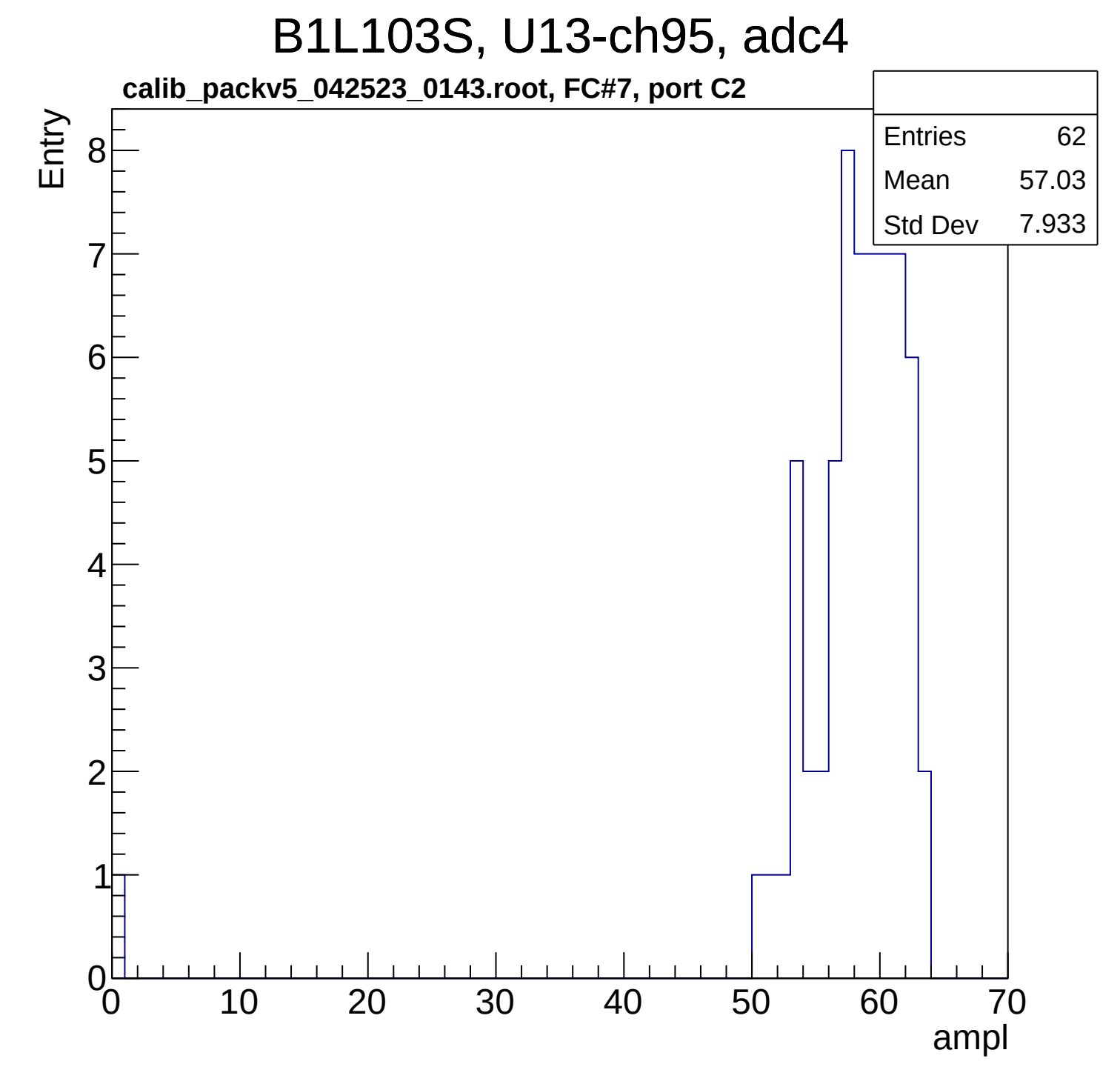
B1L103S, U13-ch95, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 57.03
Std Dev 7.933
Entry
ampl
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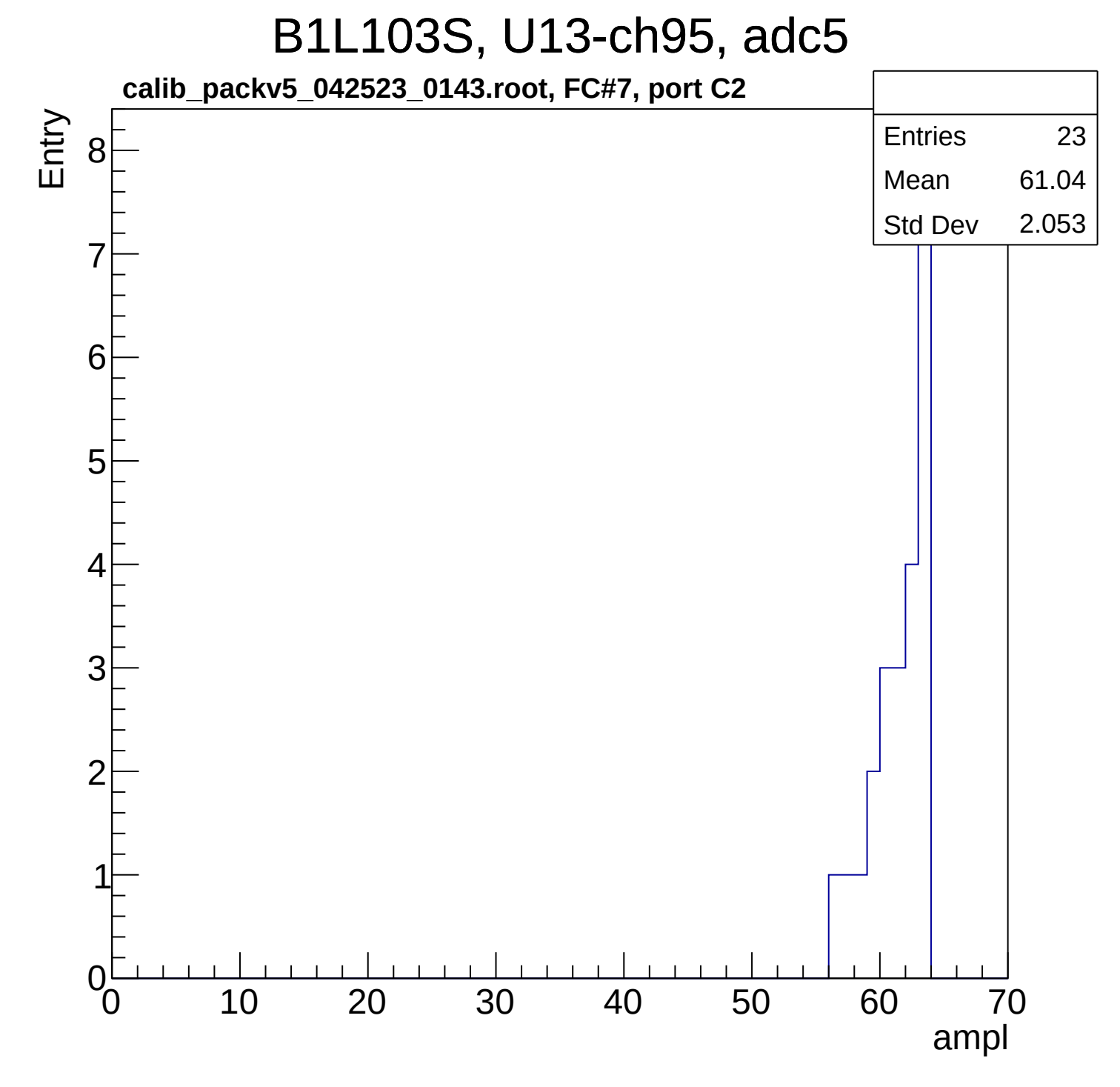
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch95, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 23
Mean 61.04
Std Dev 2.053
Entry
ampl
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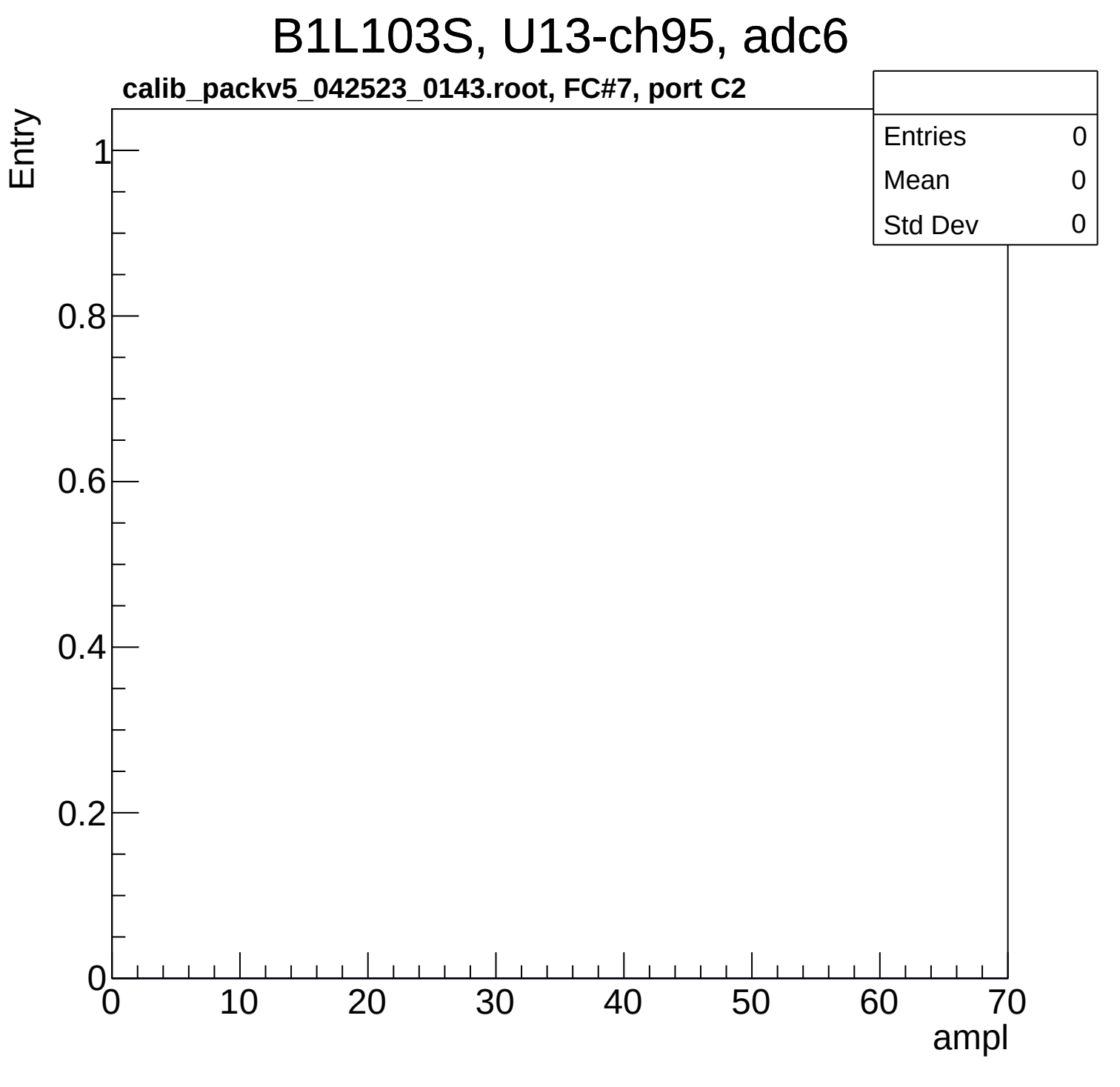
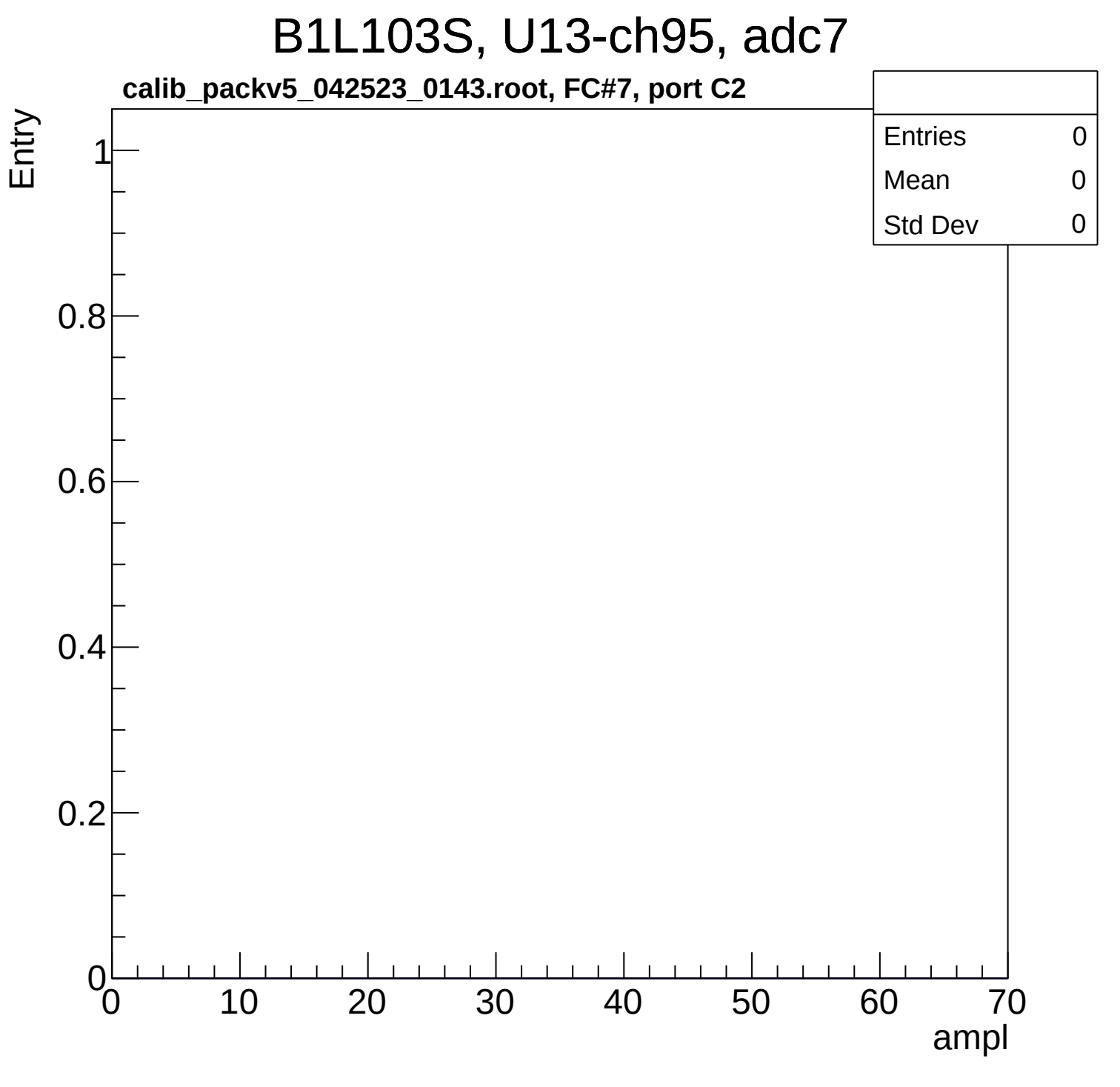
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch95, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch95, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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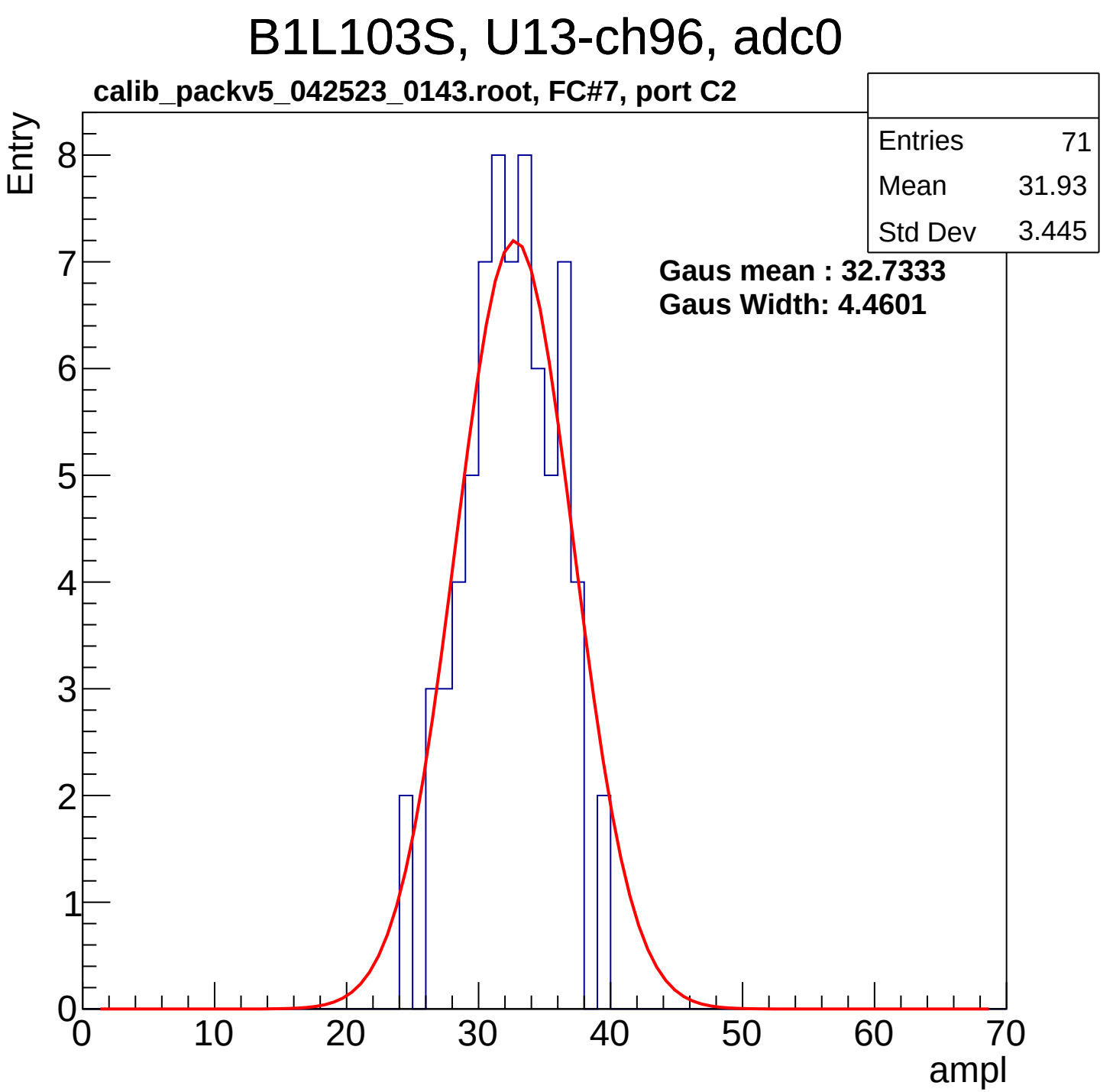
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch96, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 31.93
Std Dev 3.445
Gaus mean : 32.7333
Gaus Width: 4.4601
Entry
ampl
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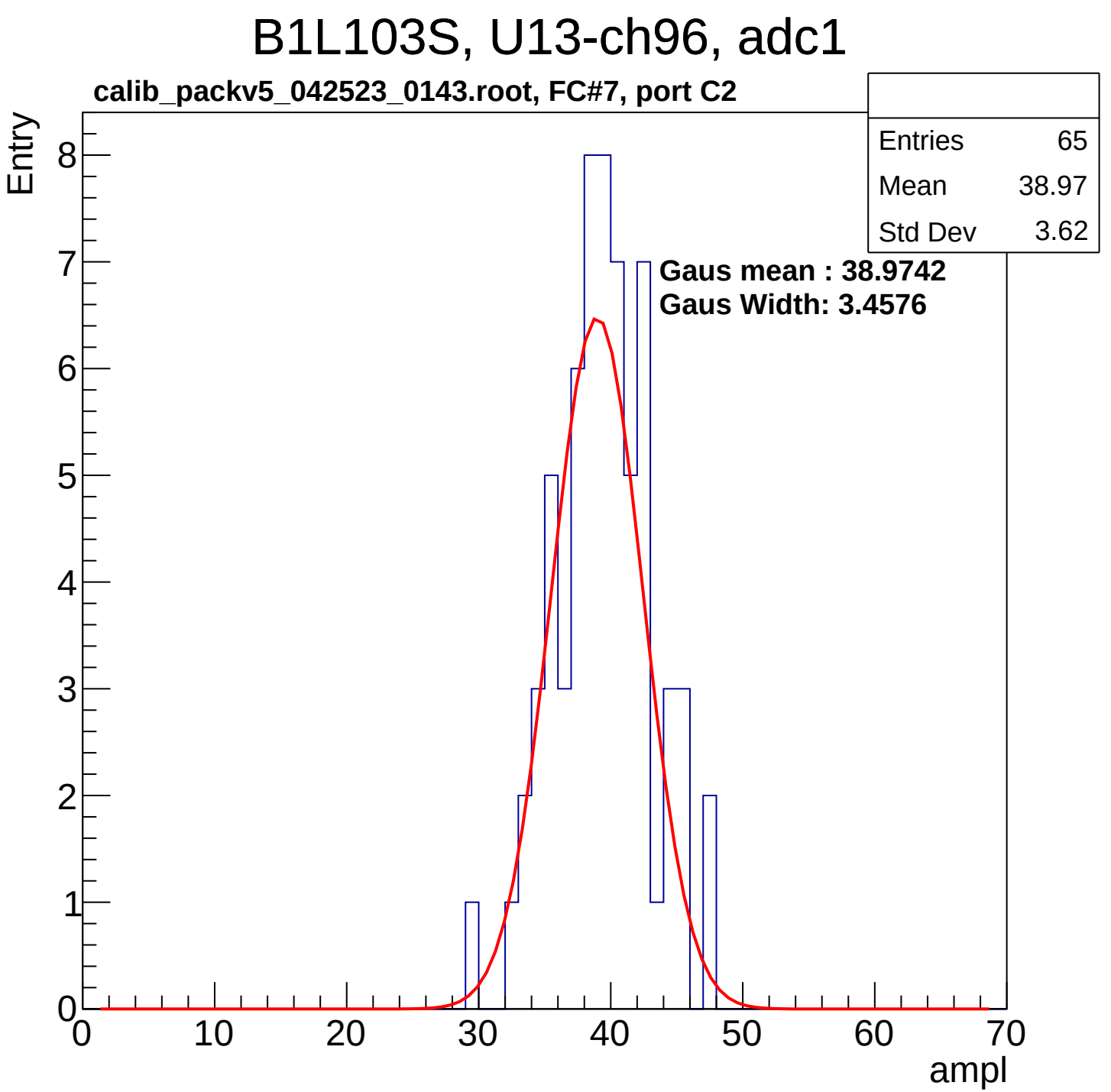
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch96, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 38.97
Std Dev 3.62
Gaus mean : 38.9742
Gaus Width: 3.4576
Entry
ampl
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1
2
3
4
5
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0
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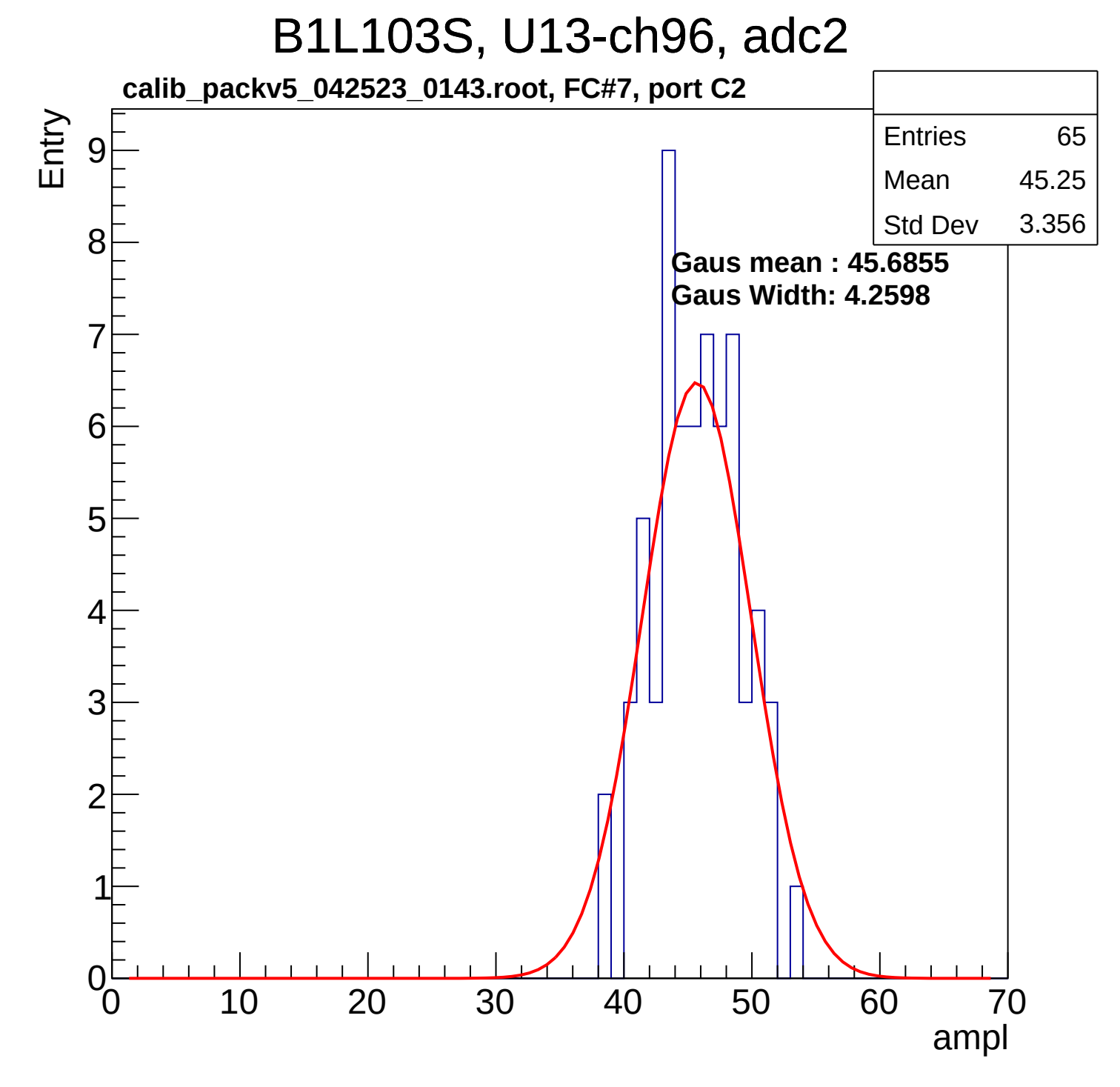
B1L103S, U13-ch96, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 45.25
Std Dev 3.356
Gaus mean : 45.6855
Gaus Width: 4.2598
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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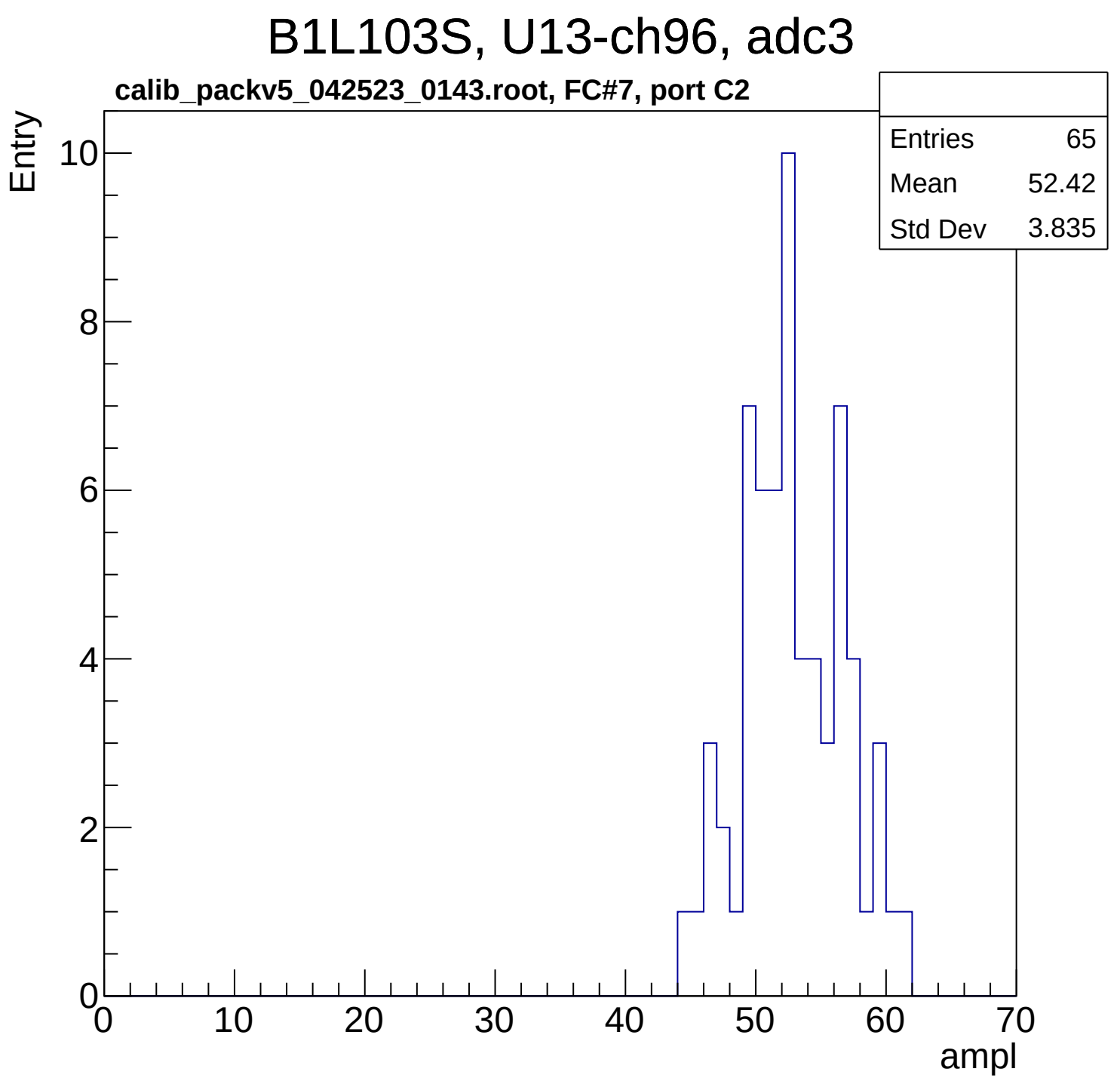
B1L103S, U13-ch96, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 52.42
Std Dev 3.835
Entry
ampl
0
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4
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0
10
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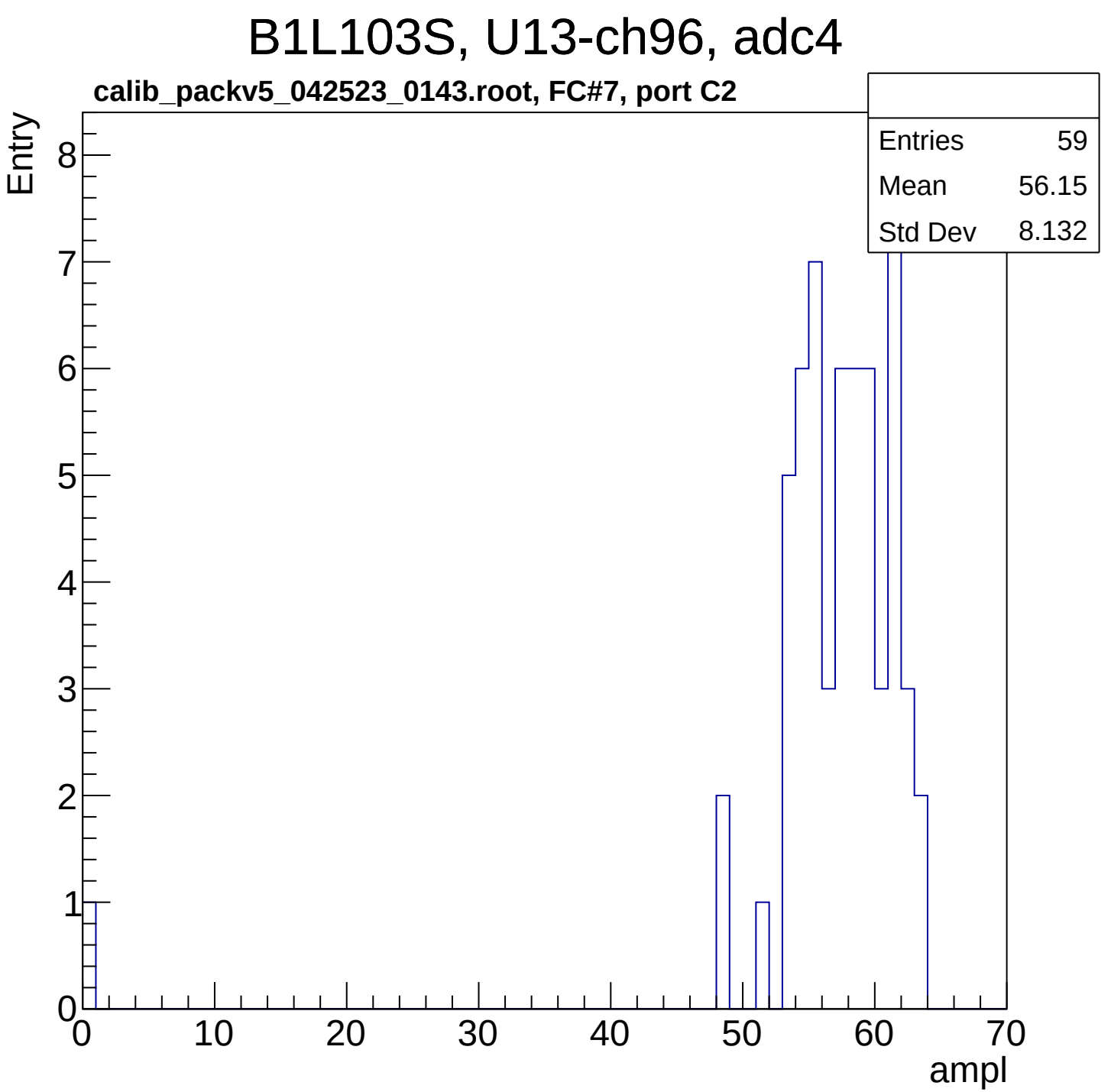
40
50
60
70


B1L103S, U13-ch96, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 56.15
Std Dev 8.132
Entry
ampl
0
1
2
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60
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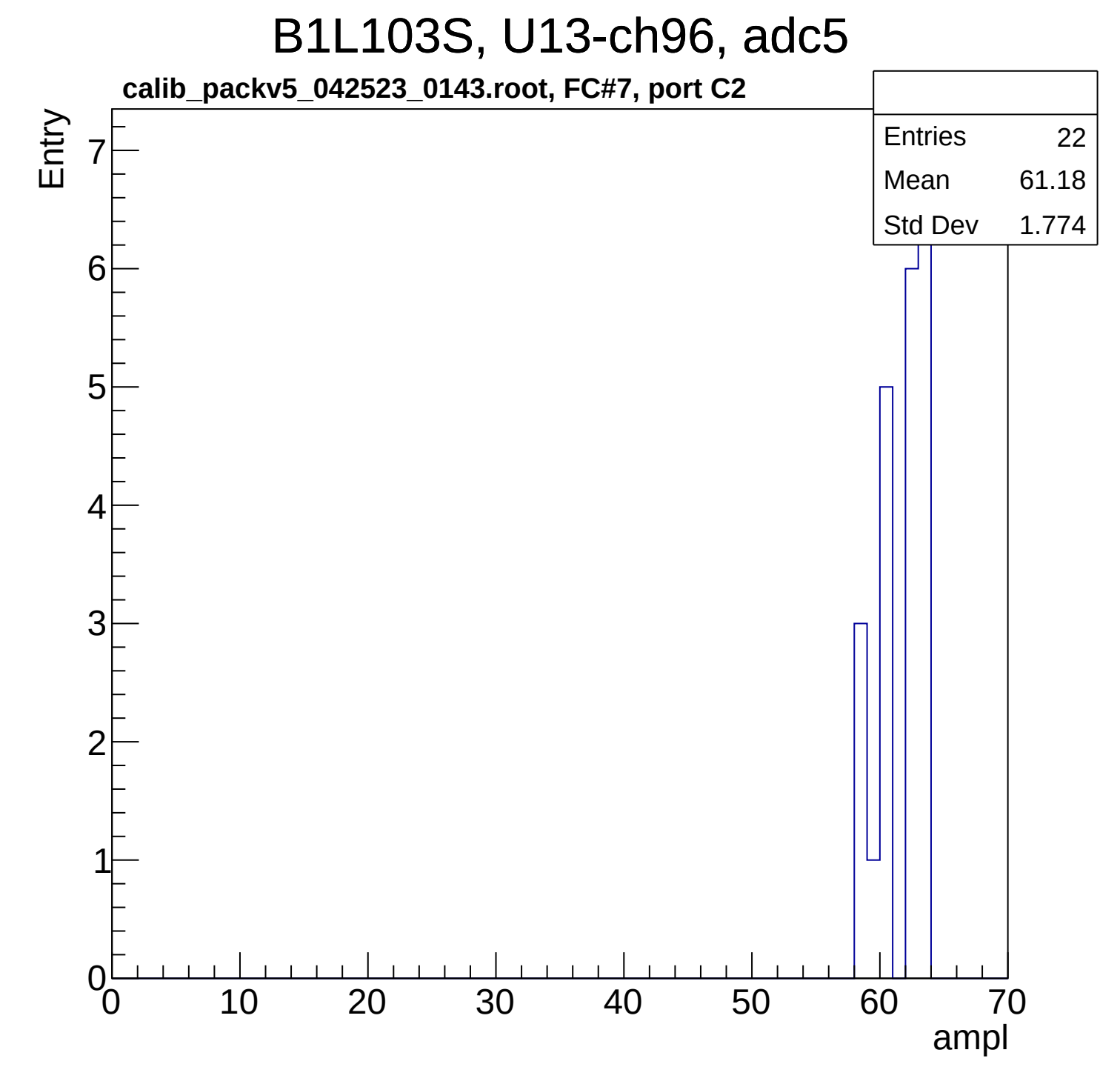
B1L103S, U13-ch96, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 61.18
Std Dev 1.774
Entry
ampl
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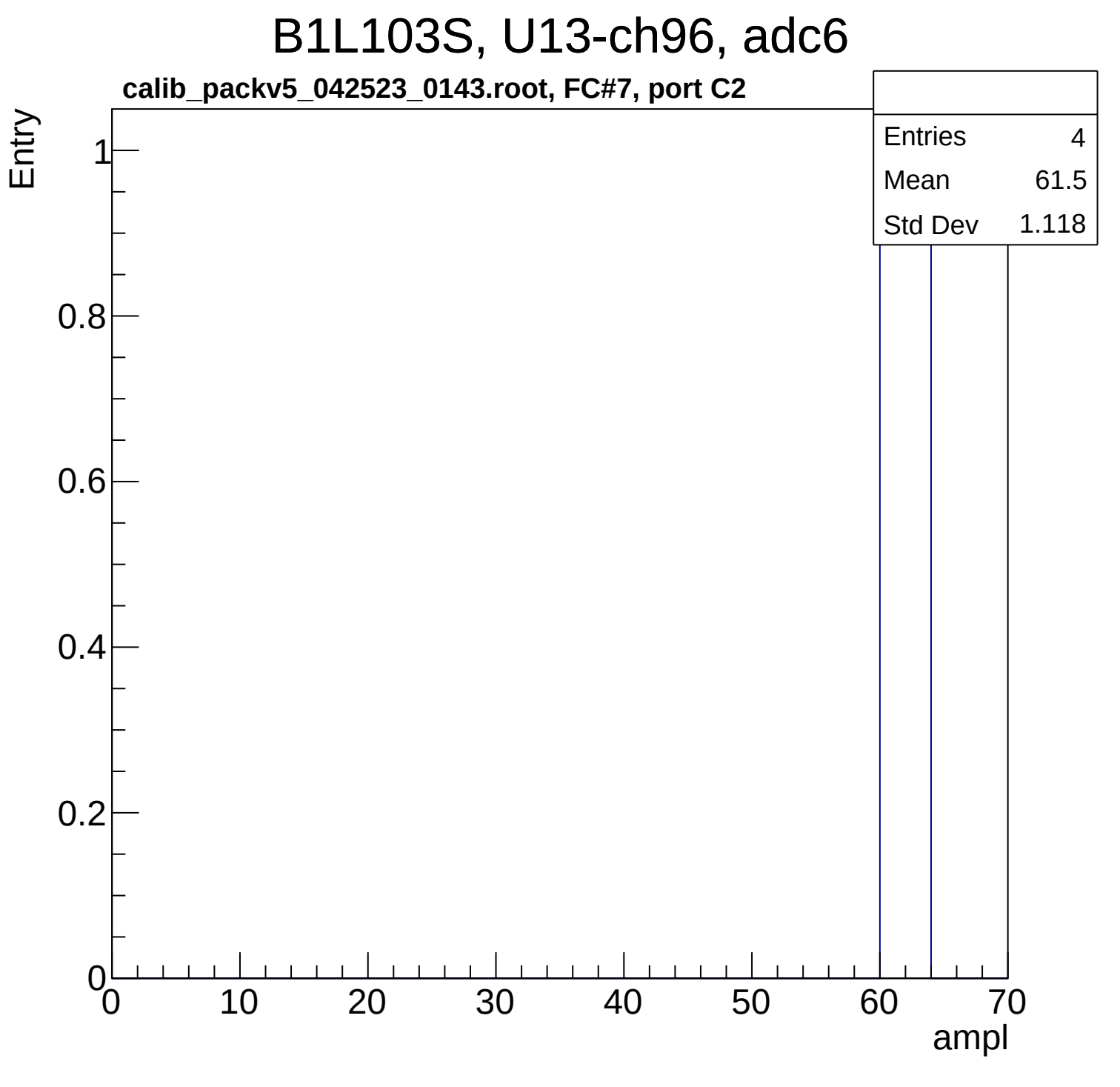
B1L103S, U13-ch96, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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0.8
0.6
0.4
0.2
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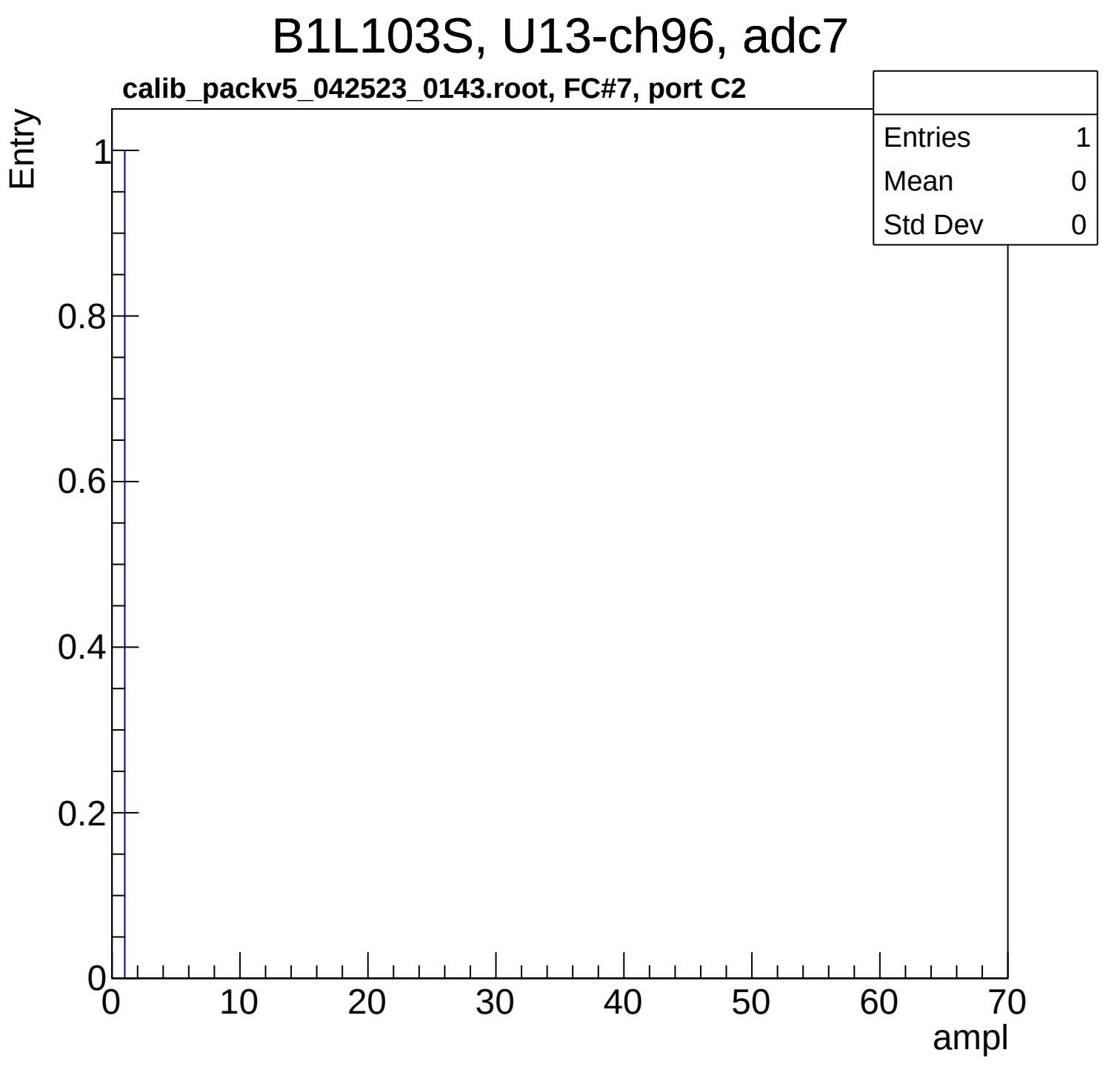
70
ampl


B1L103S, U13-ch96, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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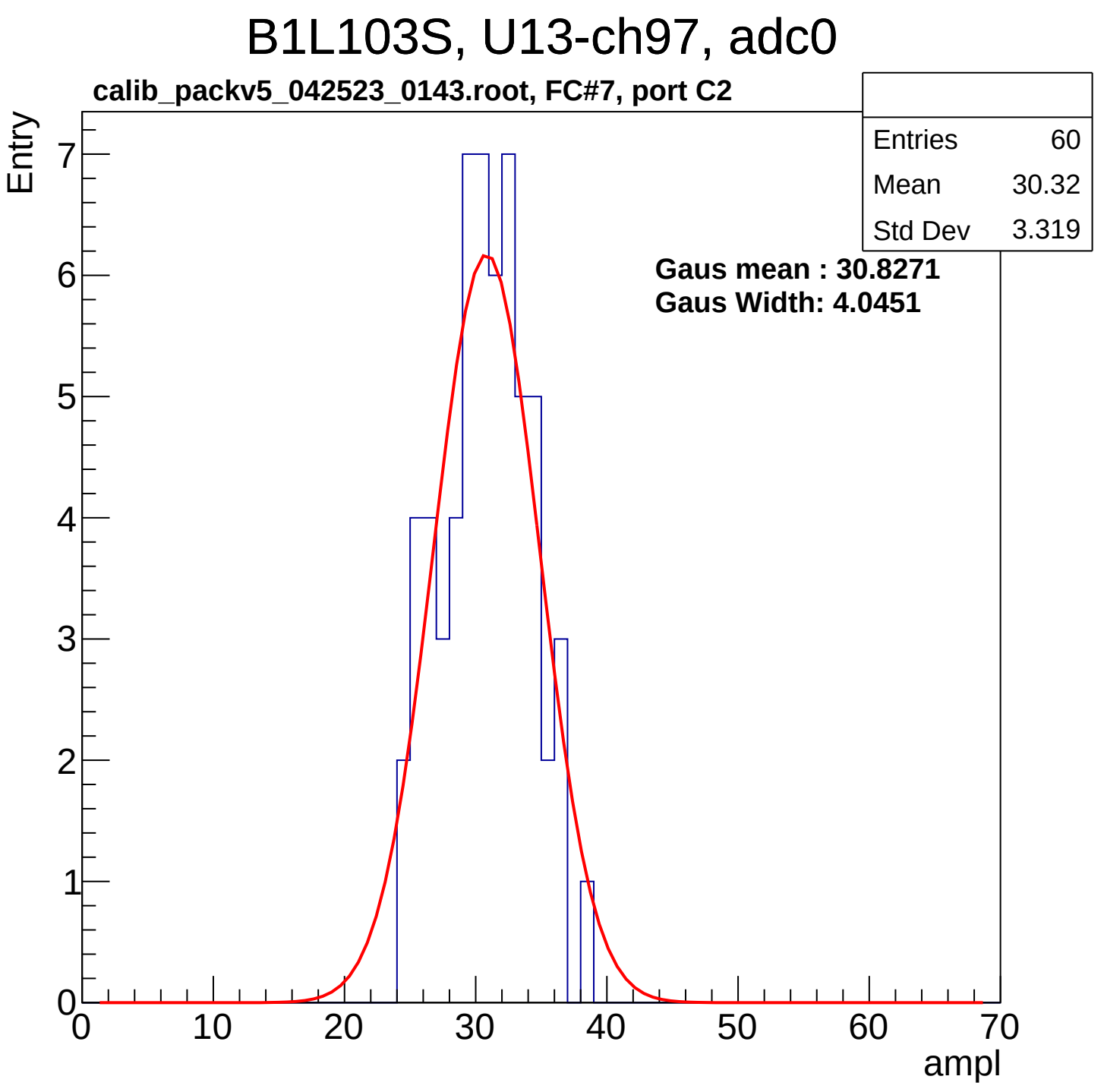
50
60
70
ampl


B1L103S, U13-ch97, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 30.32
Std Dev 3.319
Gaus mean : 30.8271
Gaus Width: 4.0451
Entry
ampl
0
1
2
3
4
5
6
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0
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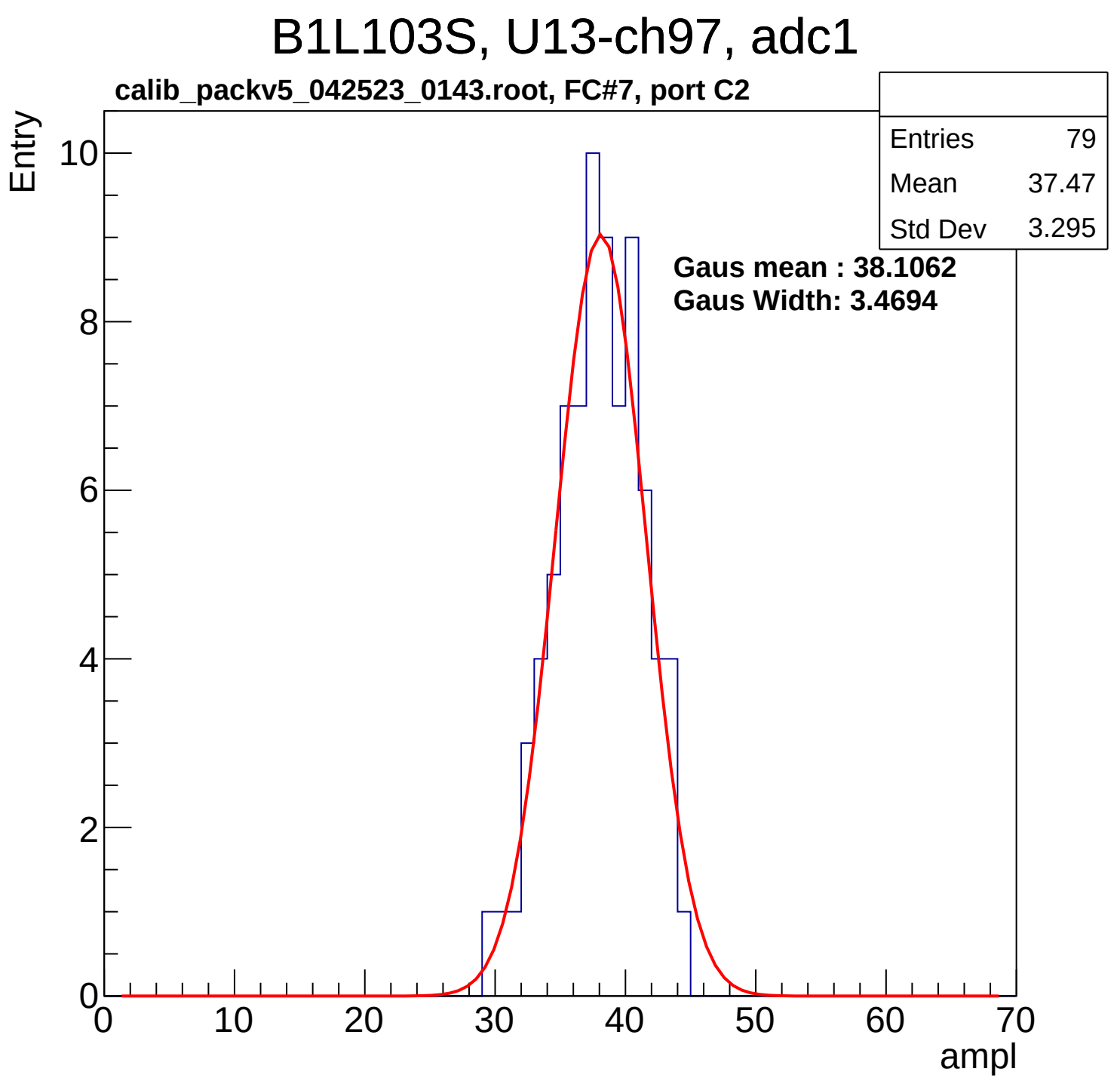
B1L103S, U13-ch97, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 37.47
Std Dev 3.295
Gaus mean : 38.1062
Gaus Width: 3.4694
Entry
ampl
0
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0
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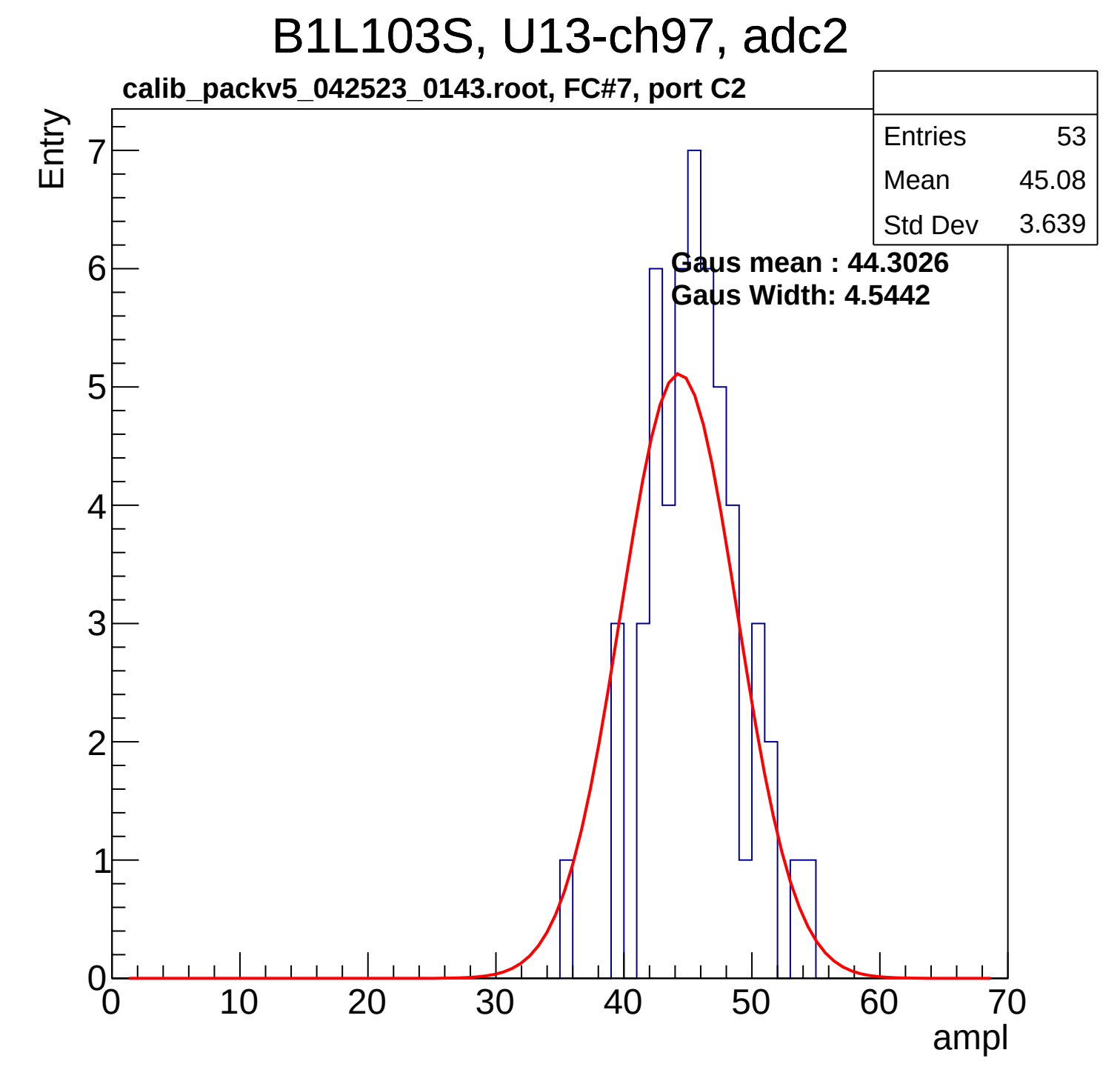
B1L103S, U13-ch97, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 45.08
Std Dev 3.639
Gaus mean : 44.3026
Gaus Width: 4.5442
Entry
ampl
0
1
2
3
4
5
6
7
0
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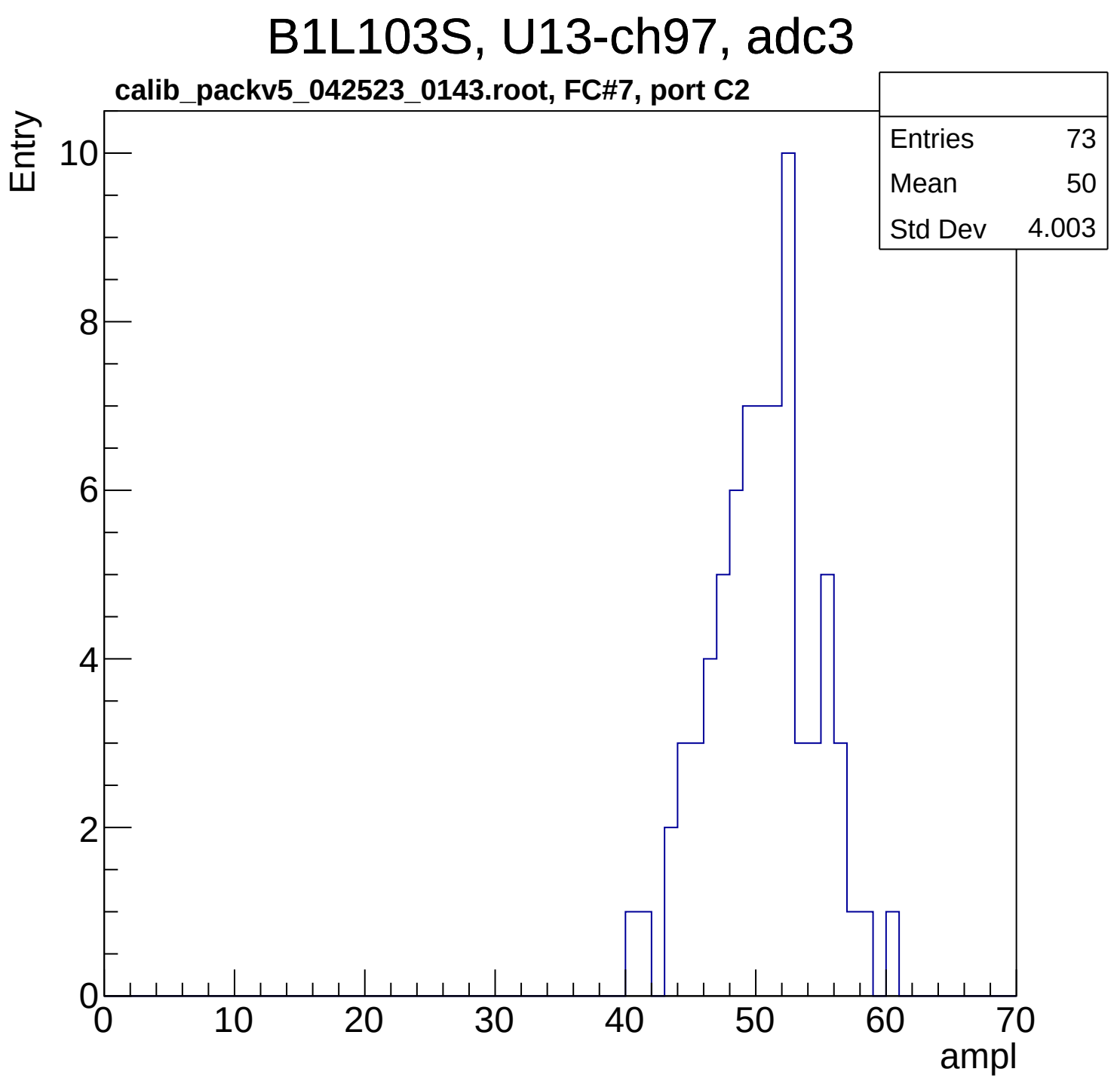
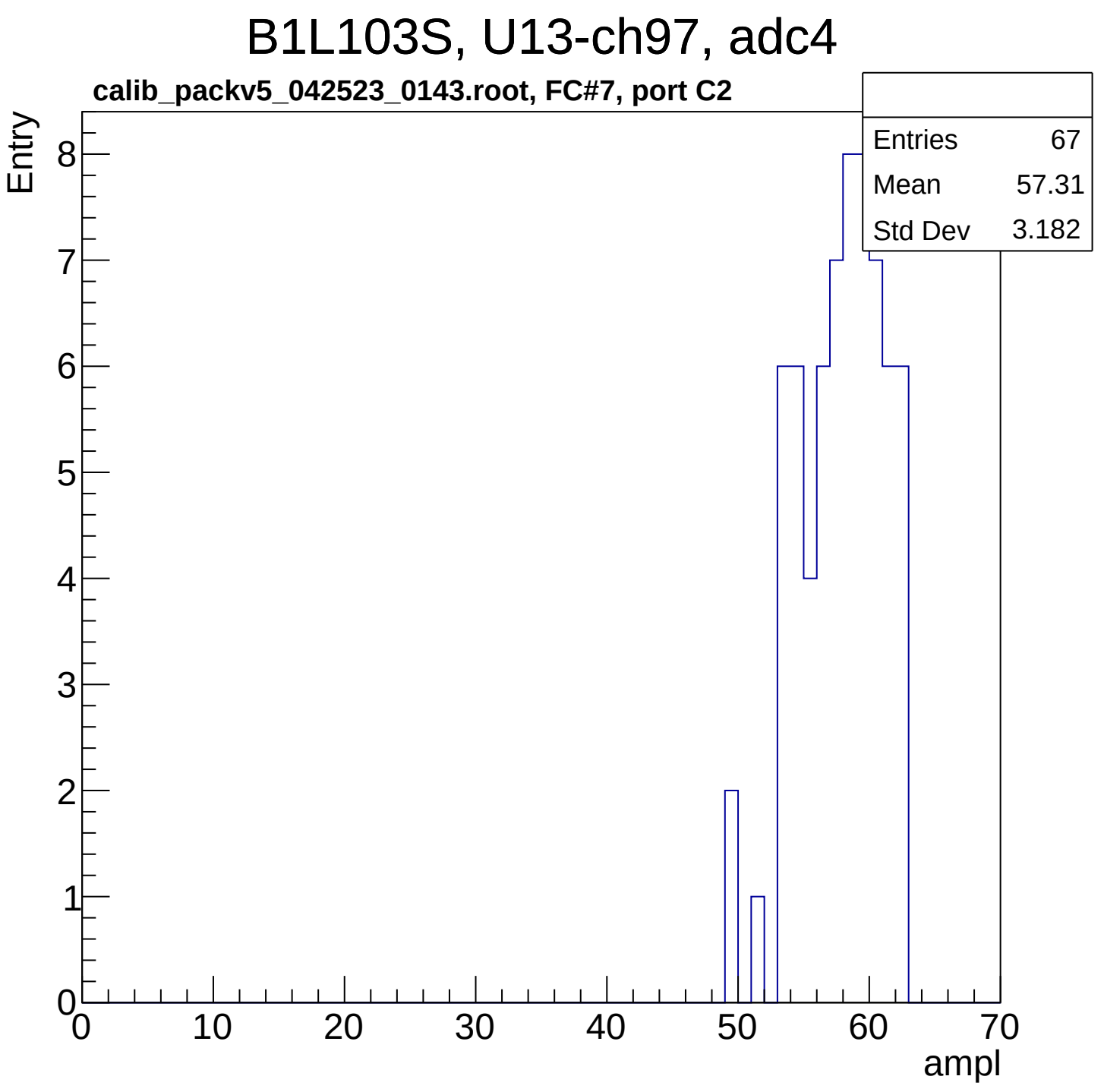

B1L103S, U13-ch97, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50
Std Dev 4.003
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch97, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 57.31
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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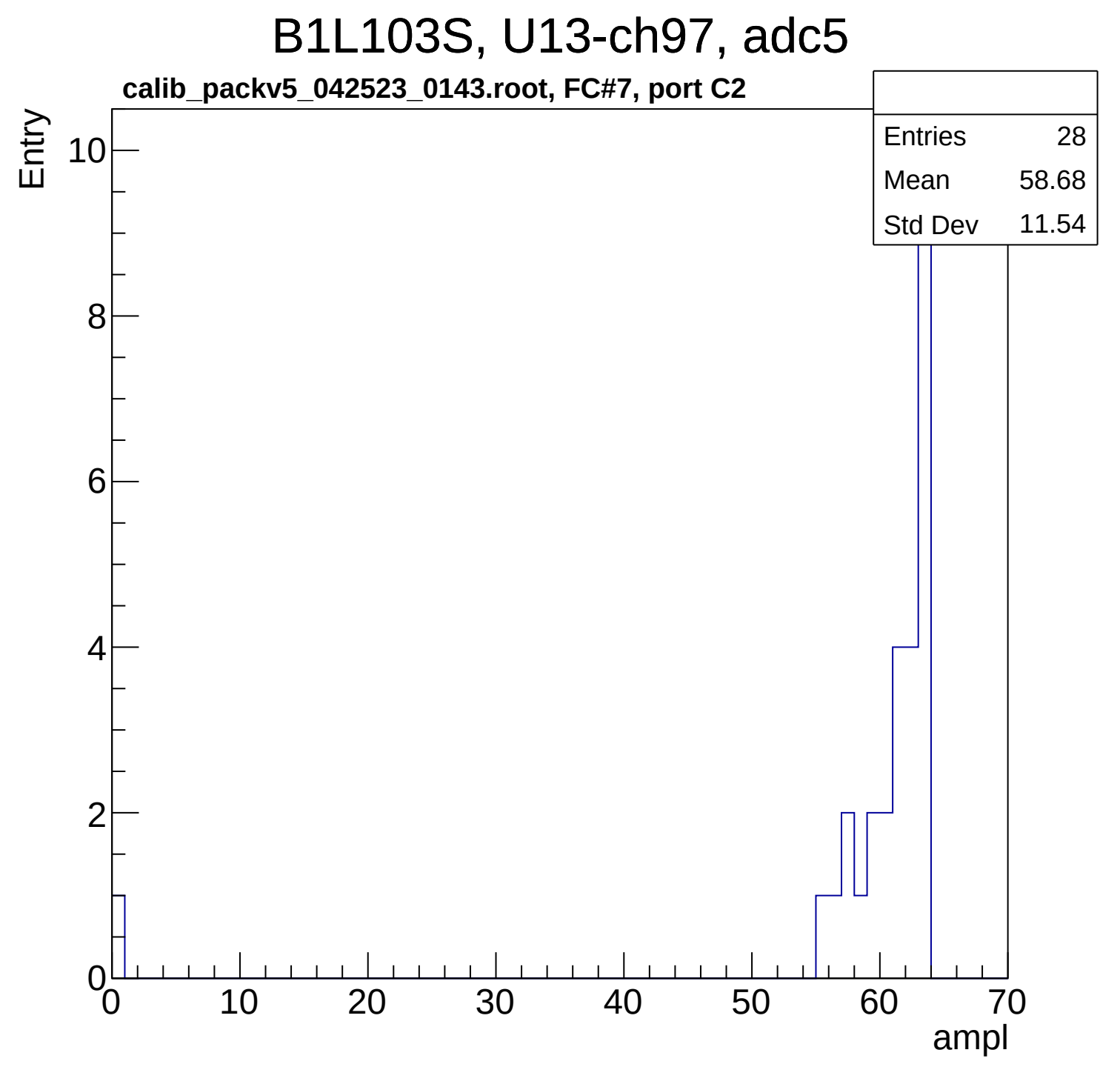
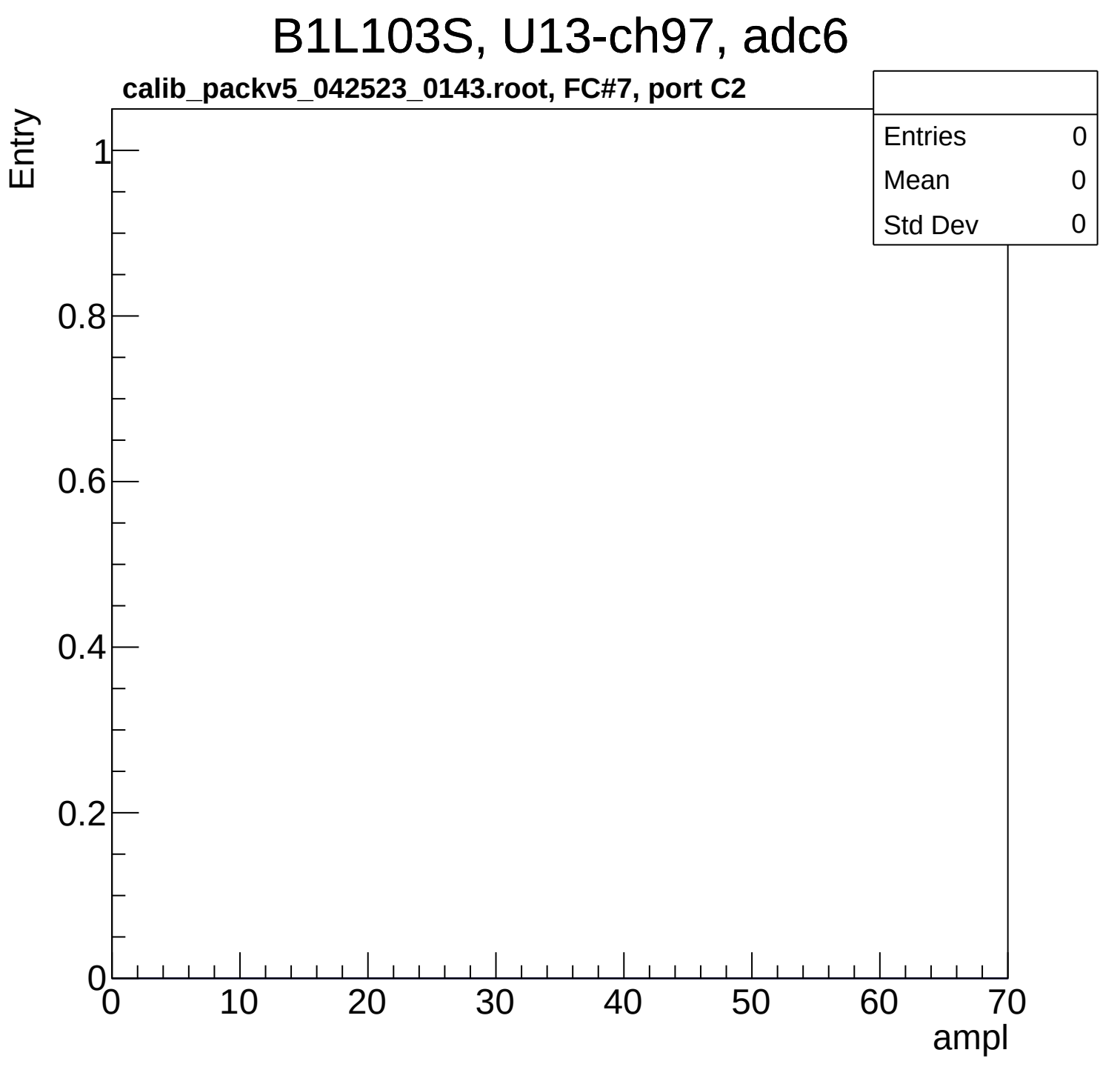
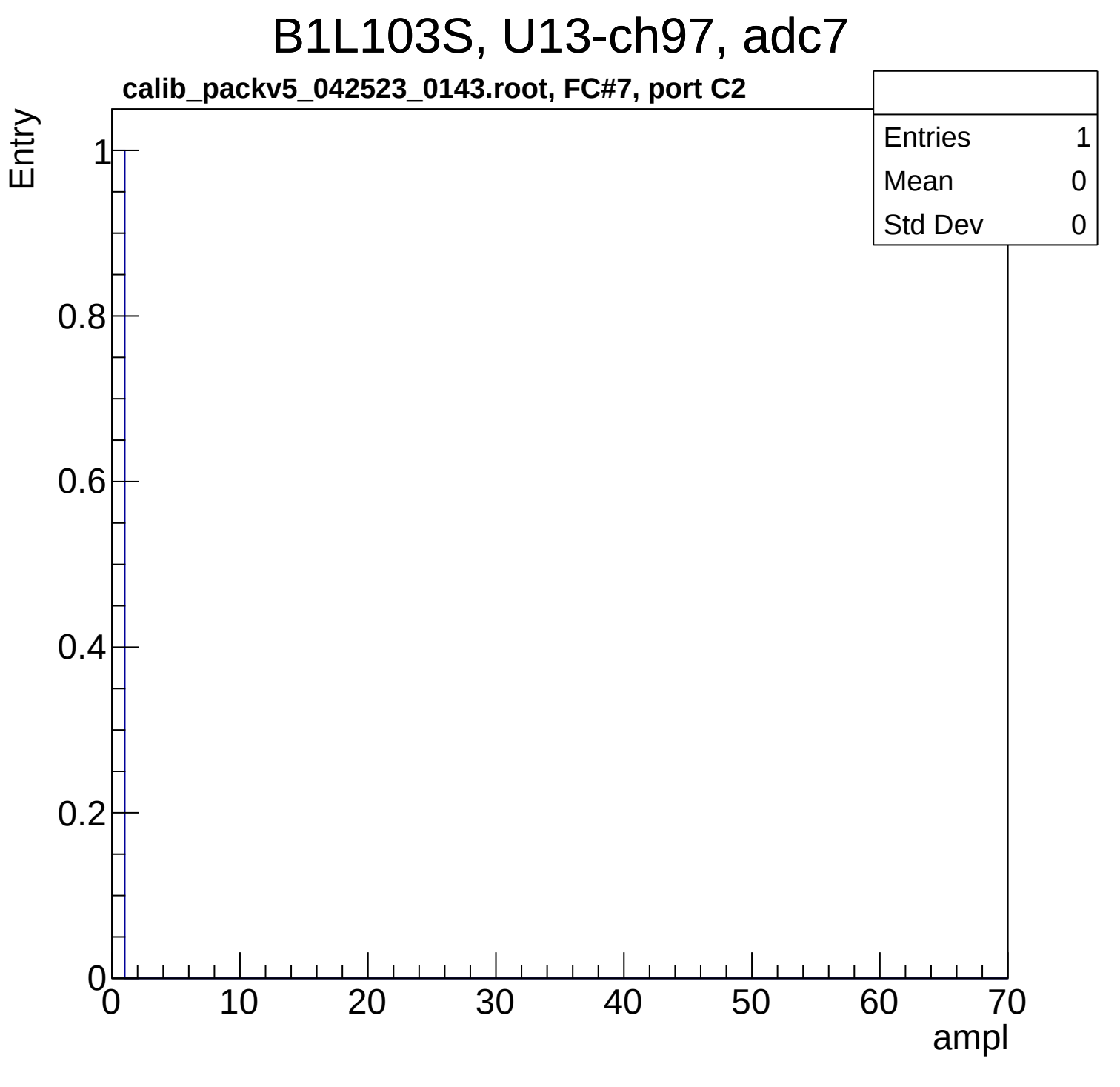

B1L103S, U13-ch97, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 28
Mean 58.68
Std Dev 11.54
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch97, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch97, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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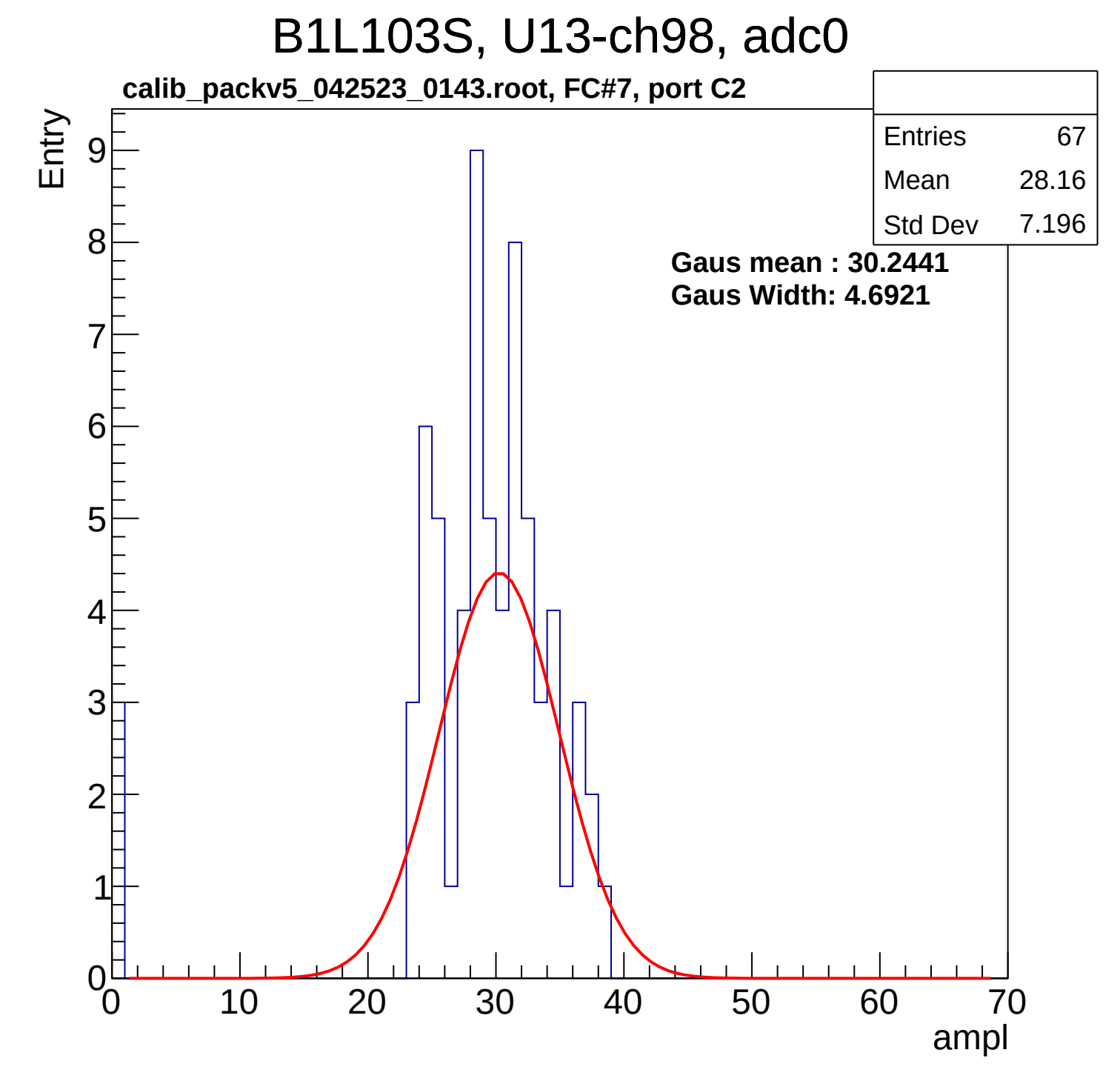
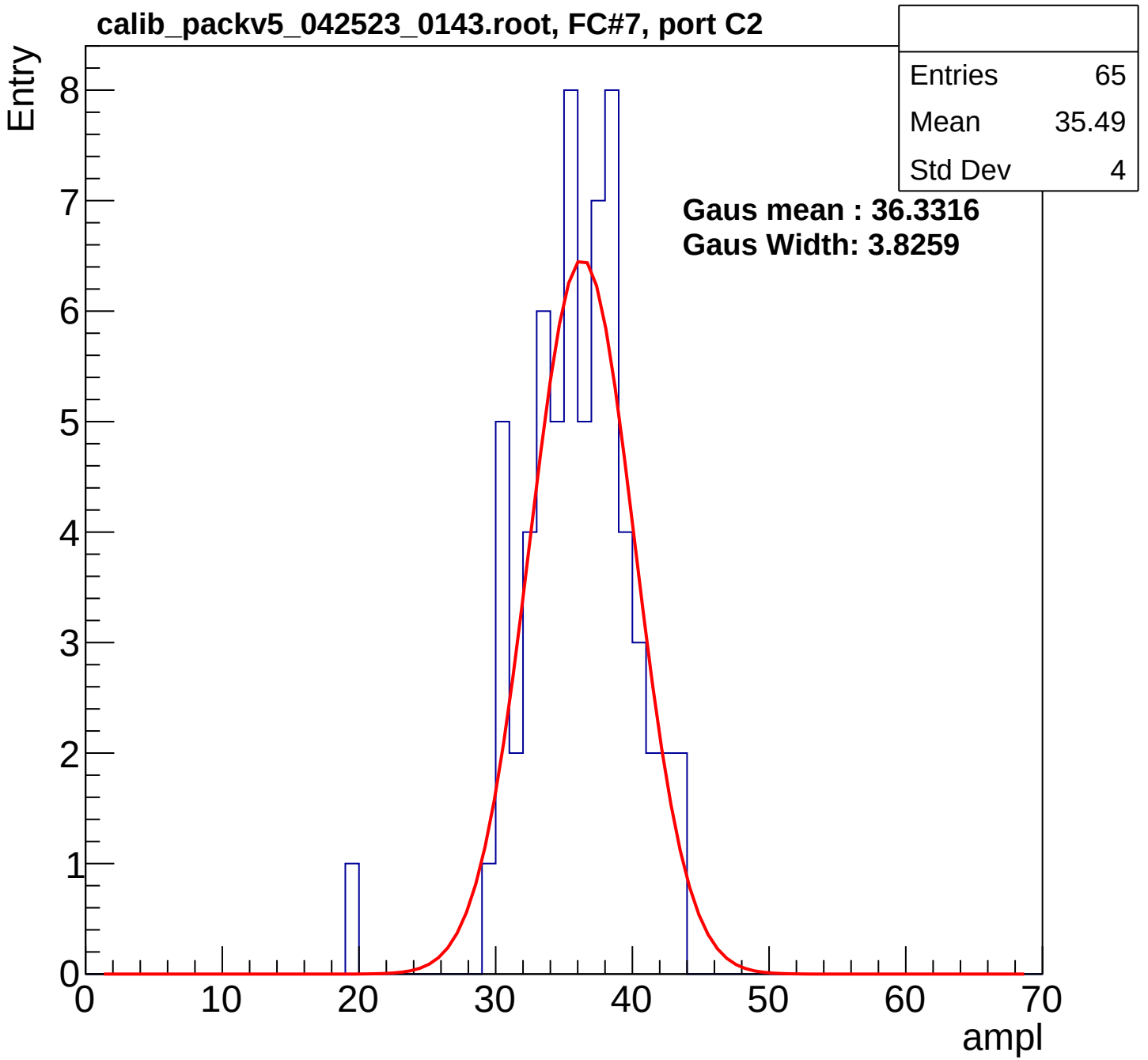
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch98, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.16
Std Dev 7.196
Gaus mean : 30.2441
Gaus Width: 4.6921
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch98, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.49
Std Dev 4
Gaus mean : 36.3316
Gaus Width: 3.8259
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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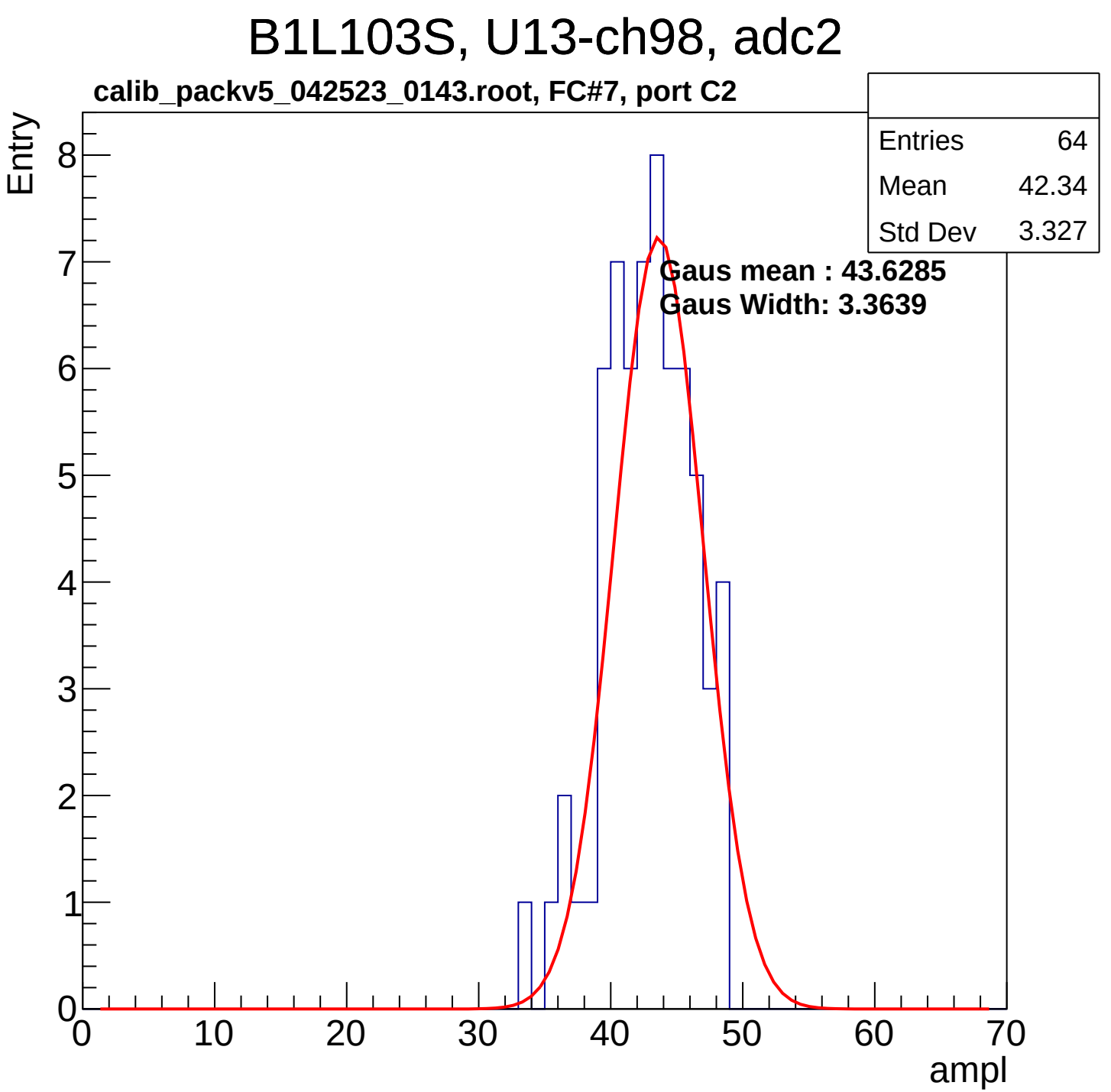
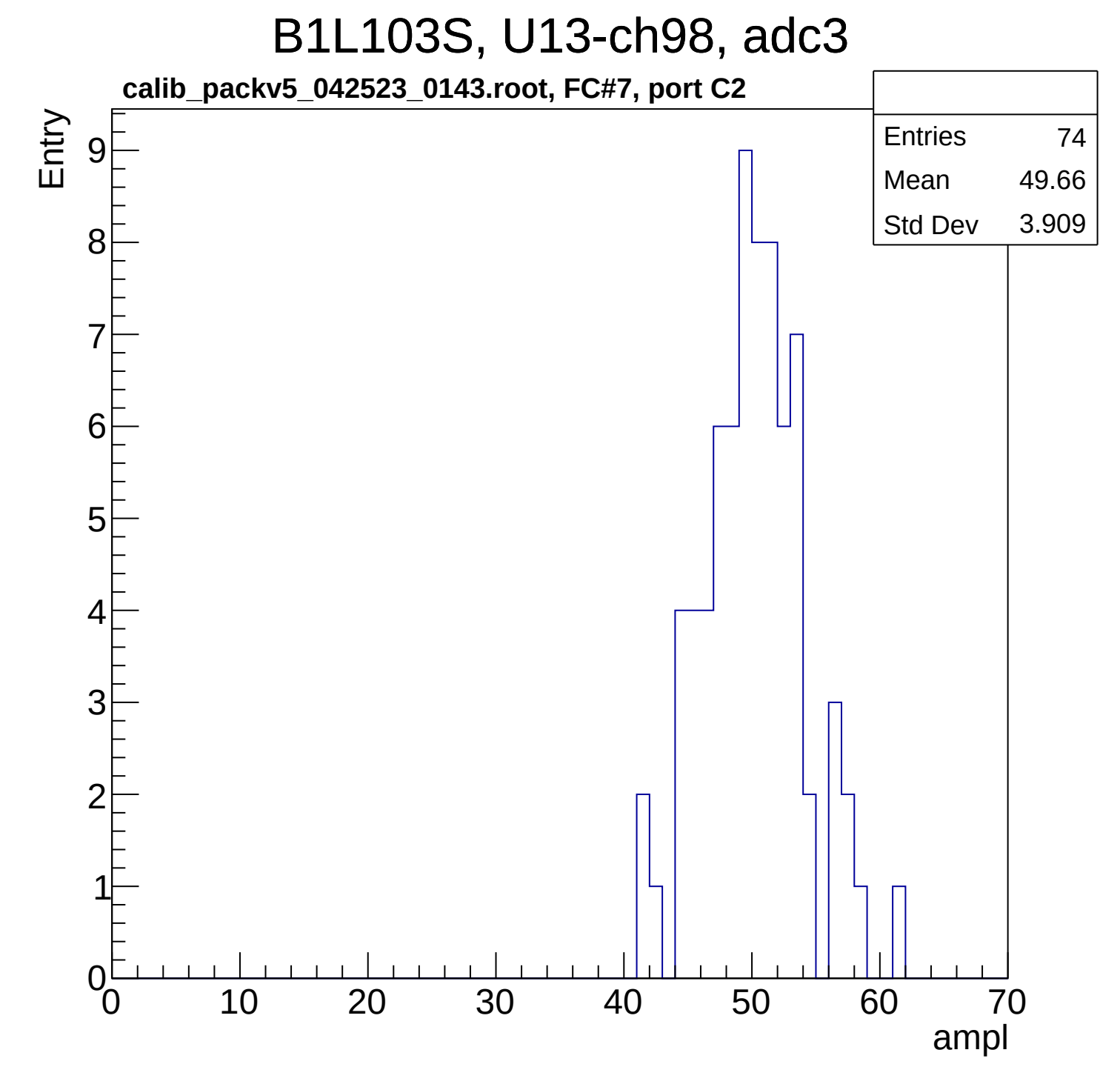

B1L103S, U13-ch98, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 42.34
Std Dev 3.327
Gaus mean : 43.6285
Gaus Width: 3.3639
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch98, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 49.66
Std Dev 3.909
Entry
ampl
0
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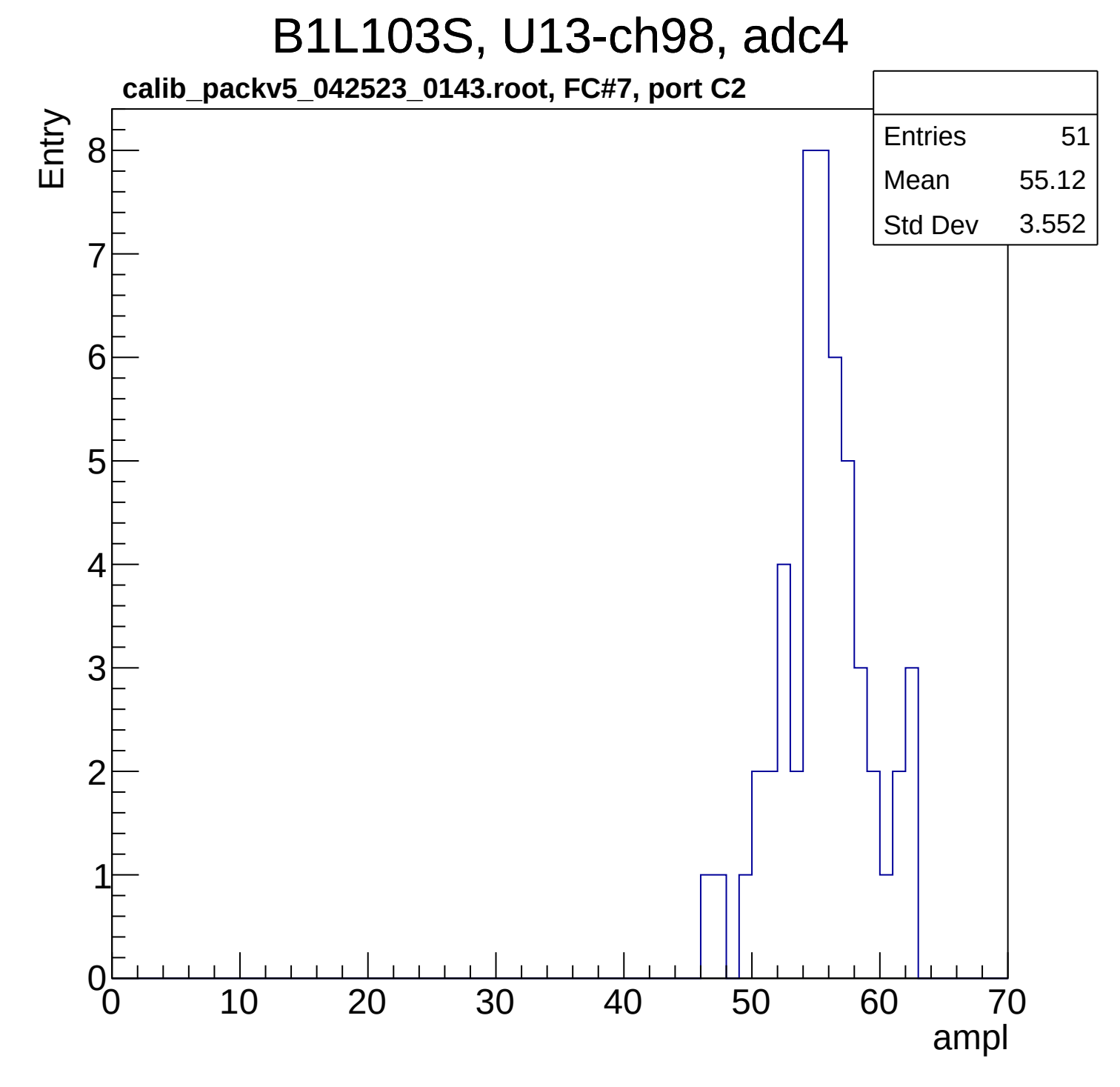
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch98, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.12
Std Dev 3.552
Entry
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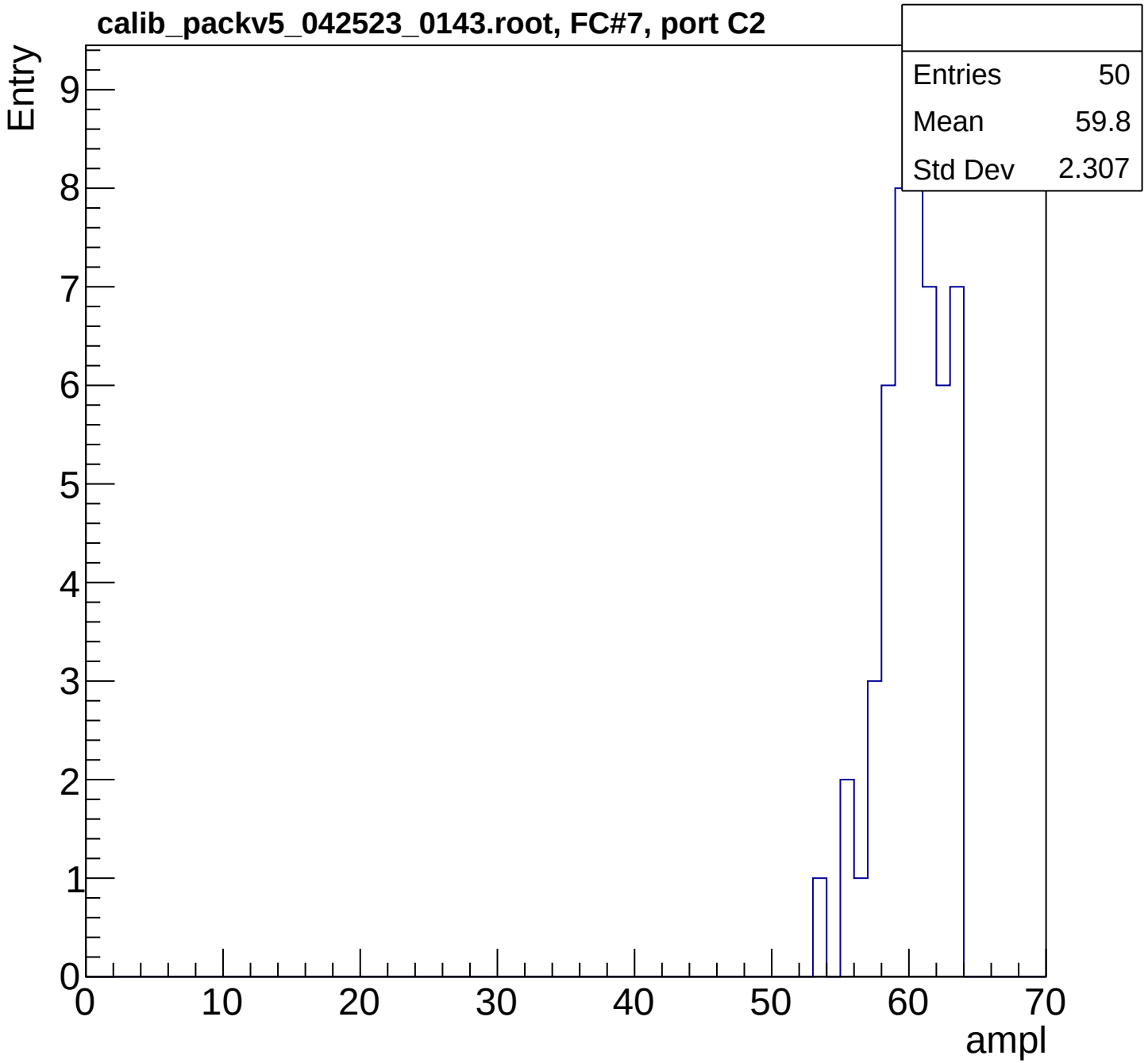
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch98, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.8
Std Dev 2.307
Entry
ampl
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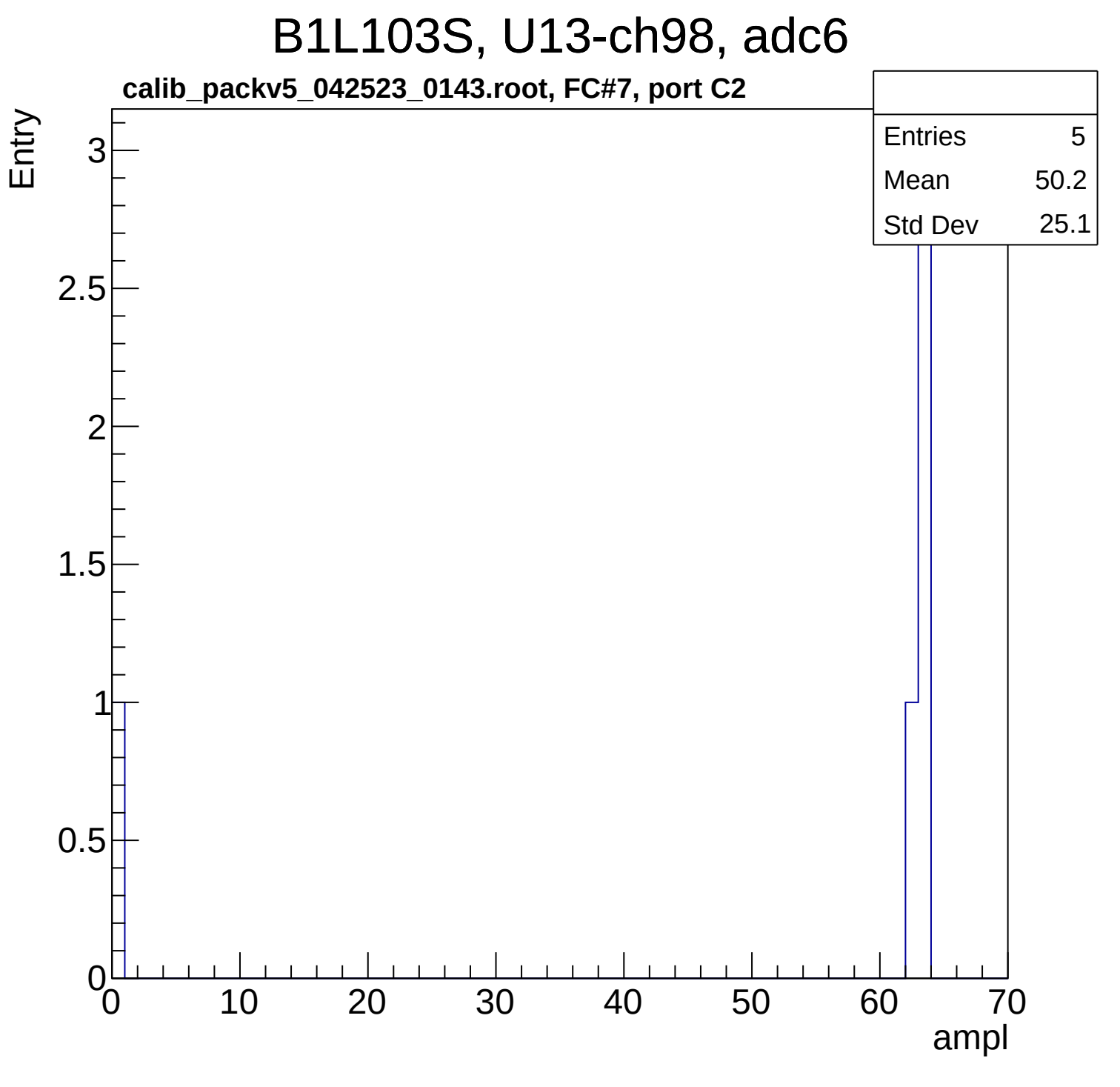
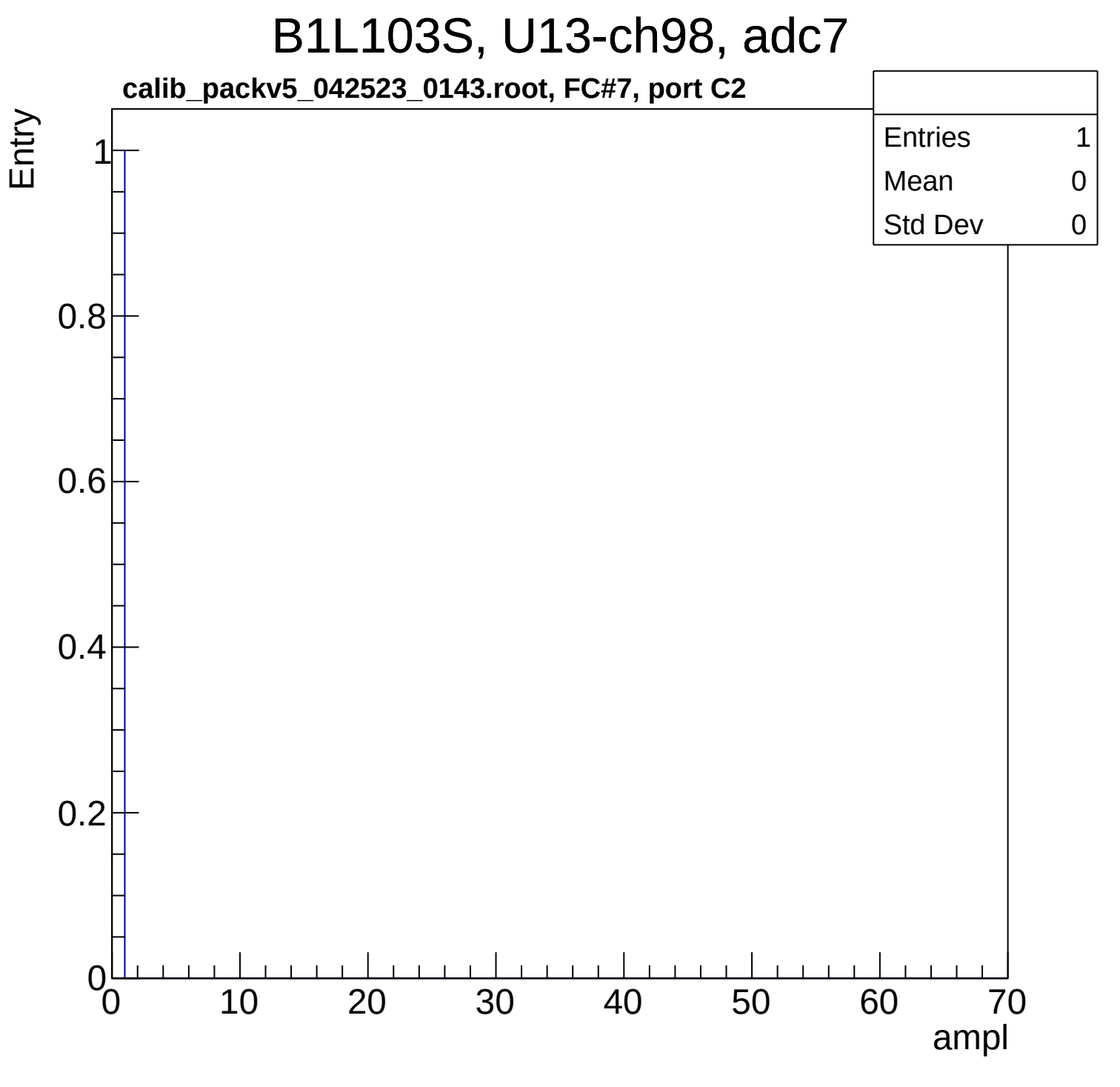
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch98, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch98, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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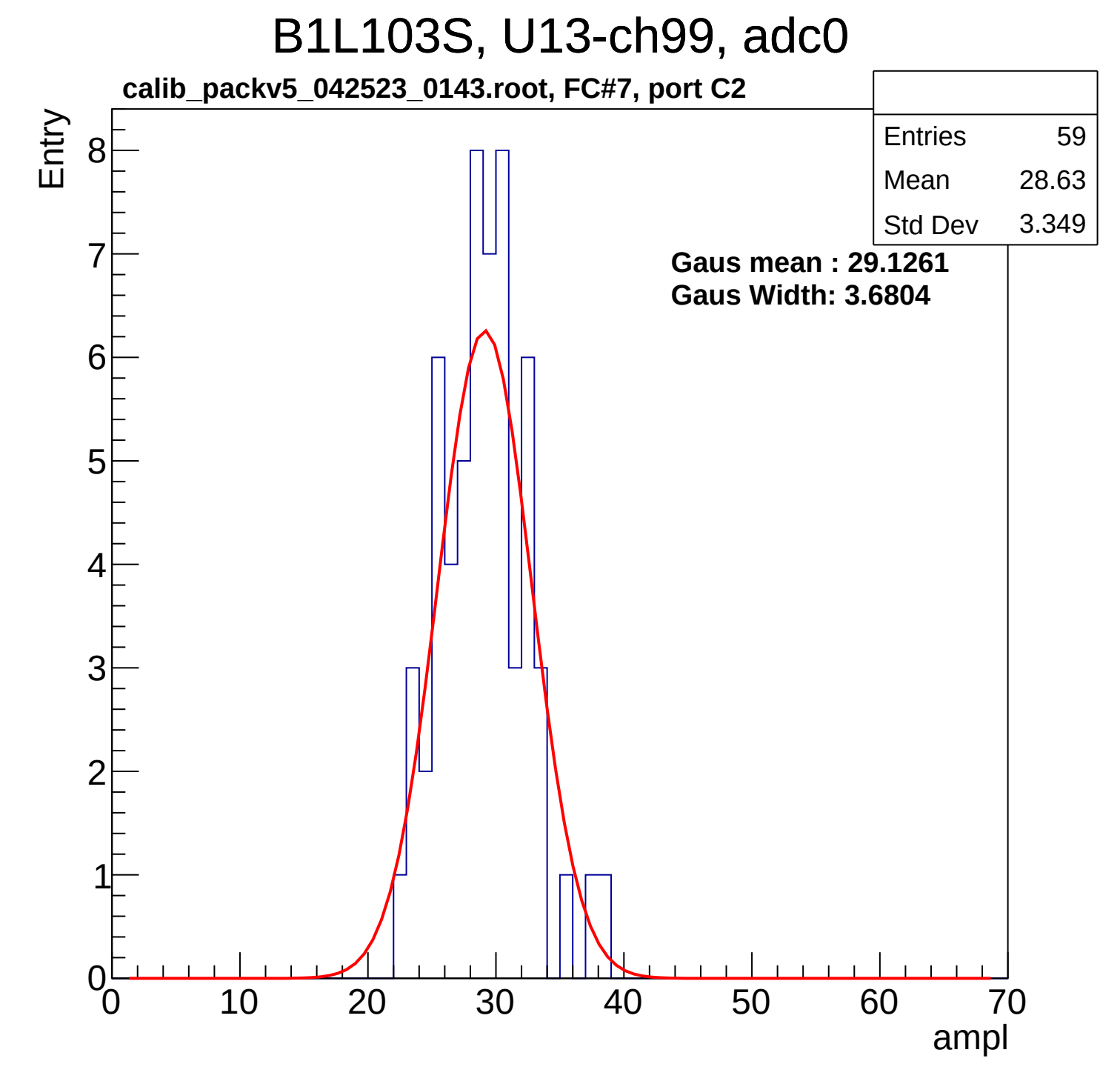
30
40
50
60
70
ampl


B1L103S, U13-ch99, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 28.63
Std Dev 3.349
Gaus mean : 29.1261
Gaus Width: 3.6804
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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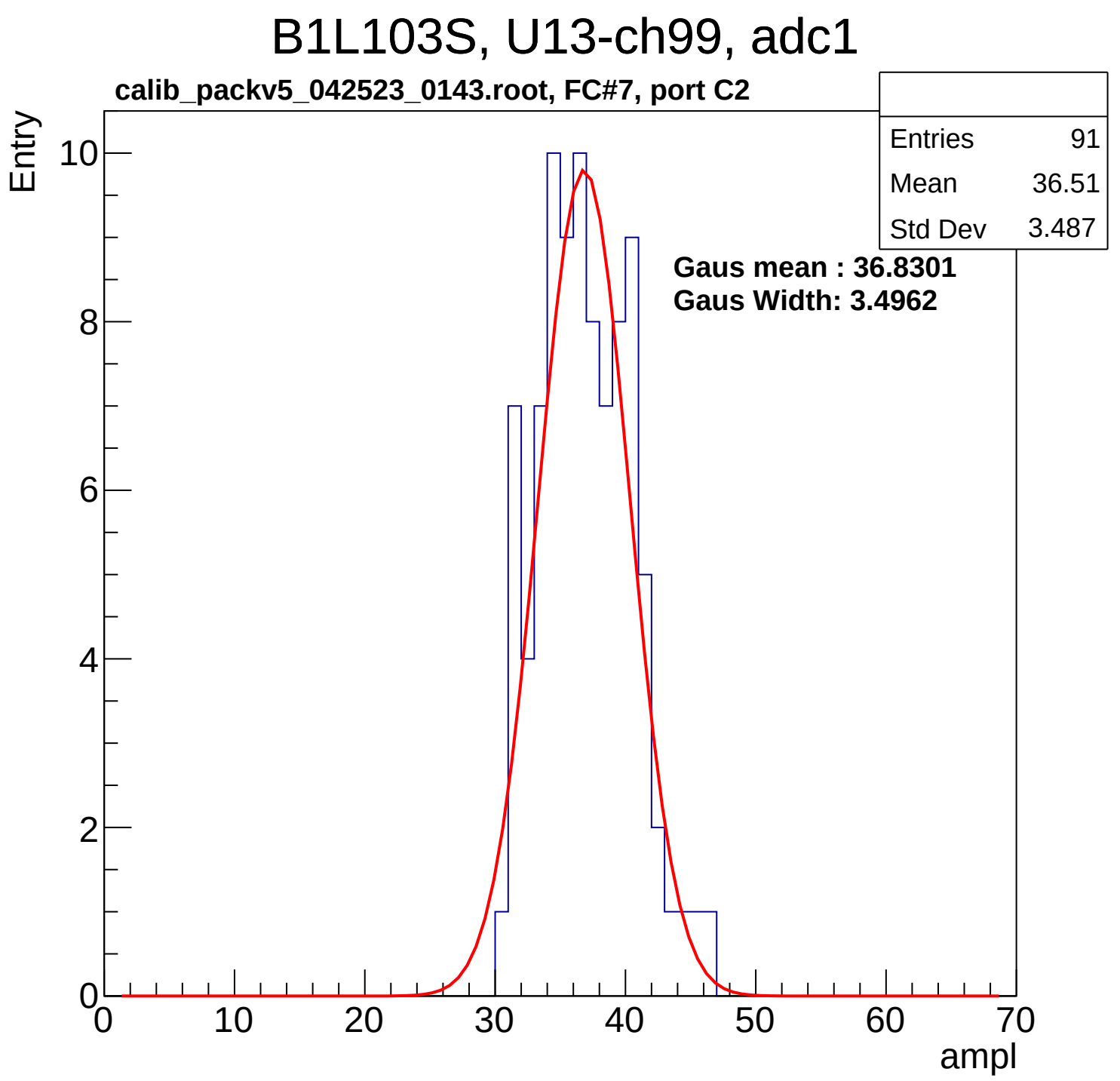
B1L103S, U13-ch99, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 36.51
Std Dev 3.487
Gaus mean : 36.8301
Gaus Width: 3.4962
Entry
ampl
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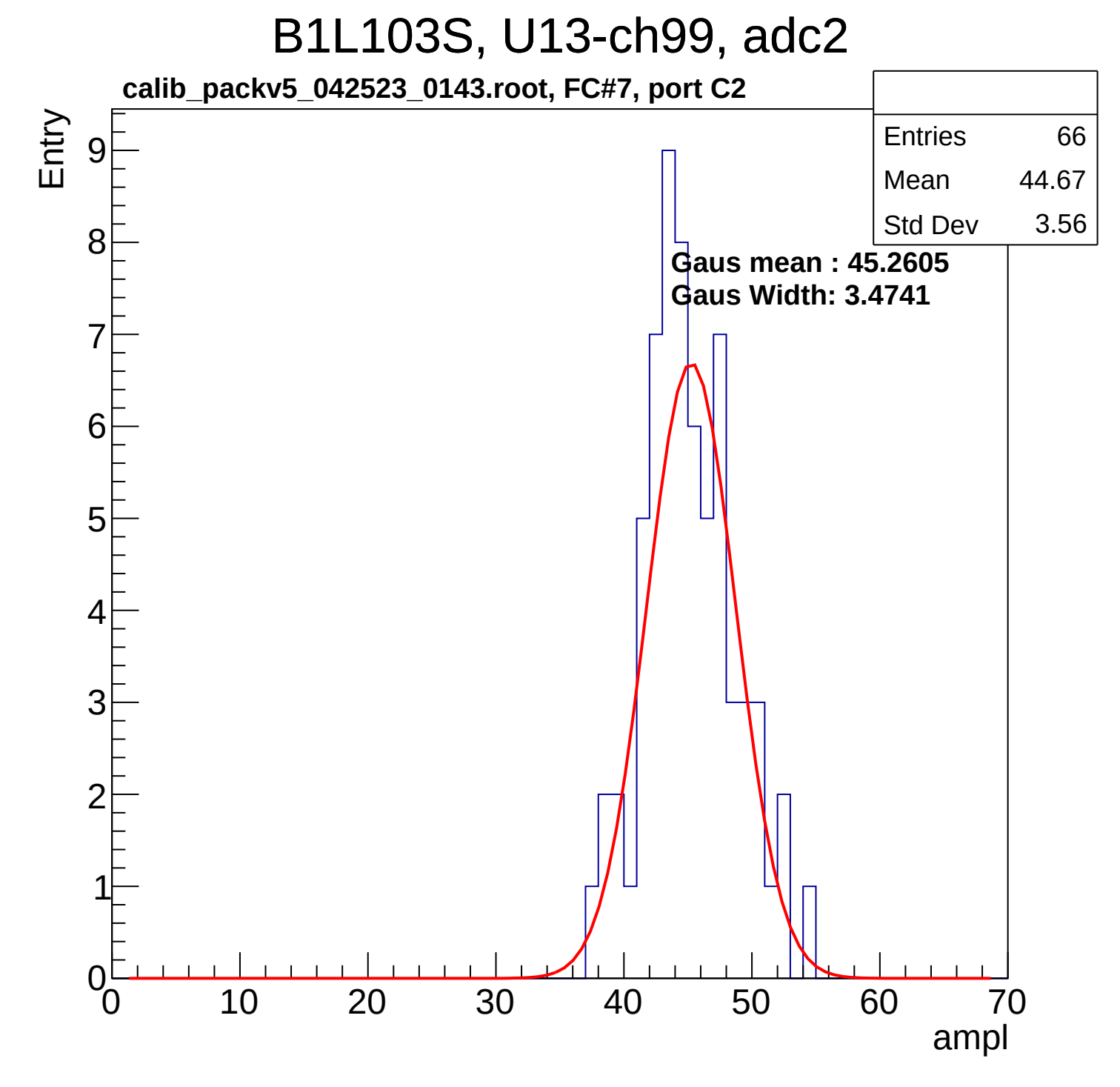
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch99, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 44.67
Std Dev 3.56
Gaus mean : 45.2605
Gaus Width: 3.4741
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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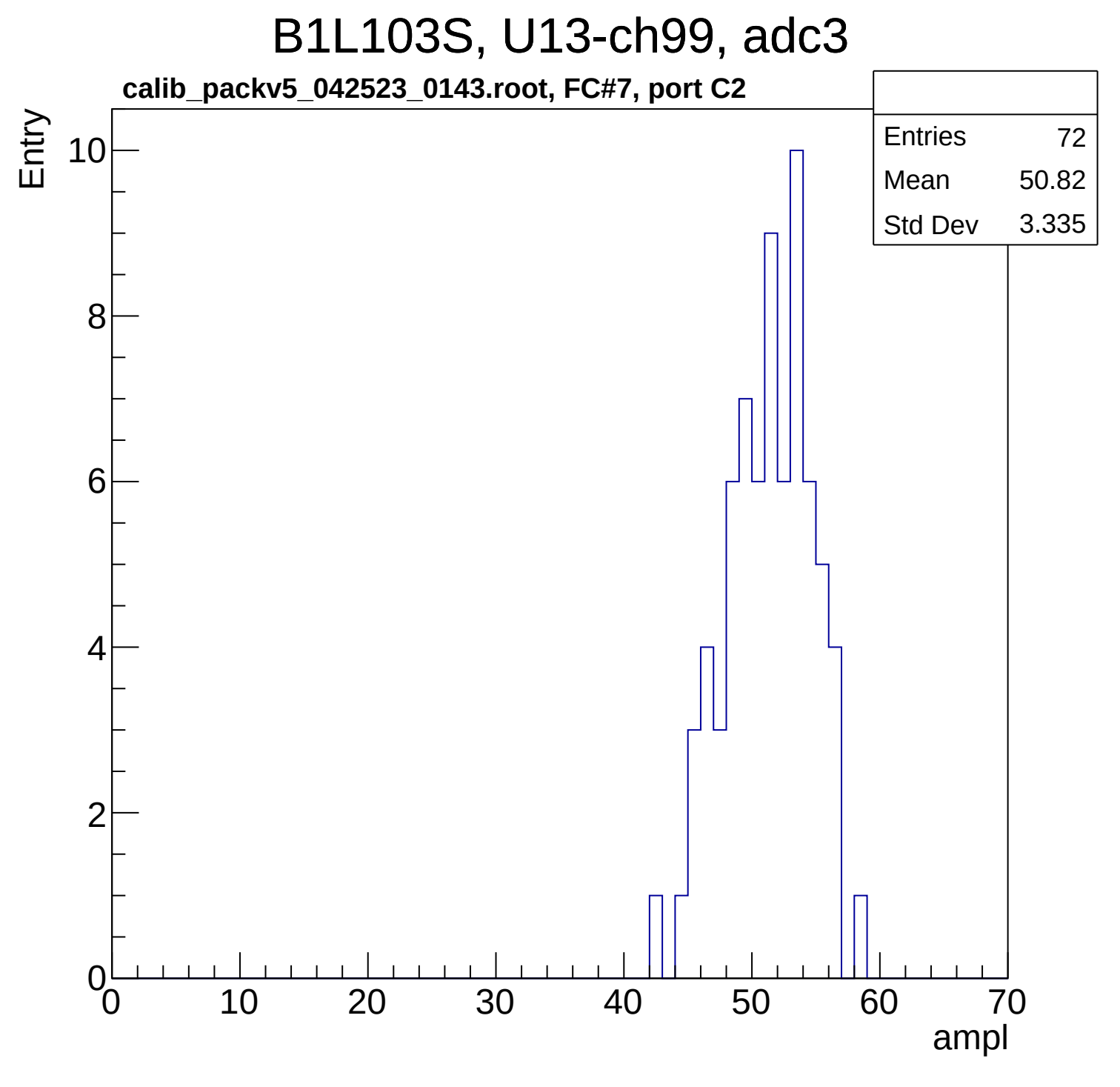
B1L103S, U13-ch99, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.82
Std Dev 3.335
Entry
ampl
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0
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60
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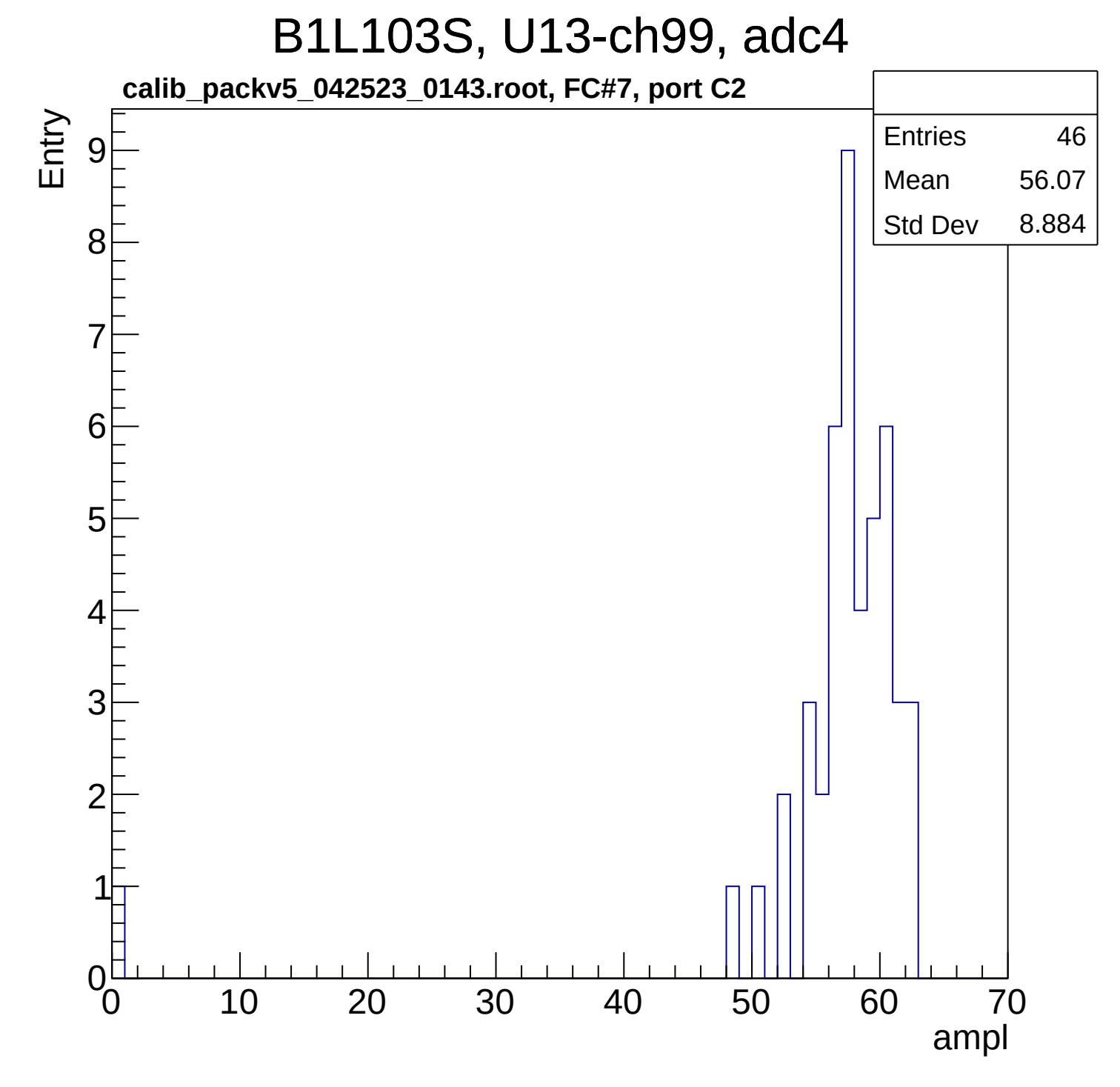
B1L103S, U13-ch99, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.07
Std Dev 8.884
Entry
ampl
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3
4
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6
7
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0
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60
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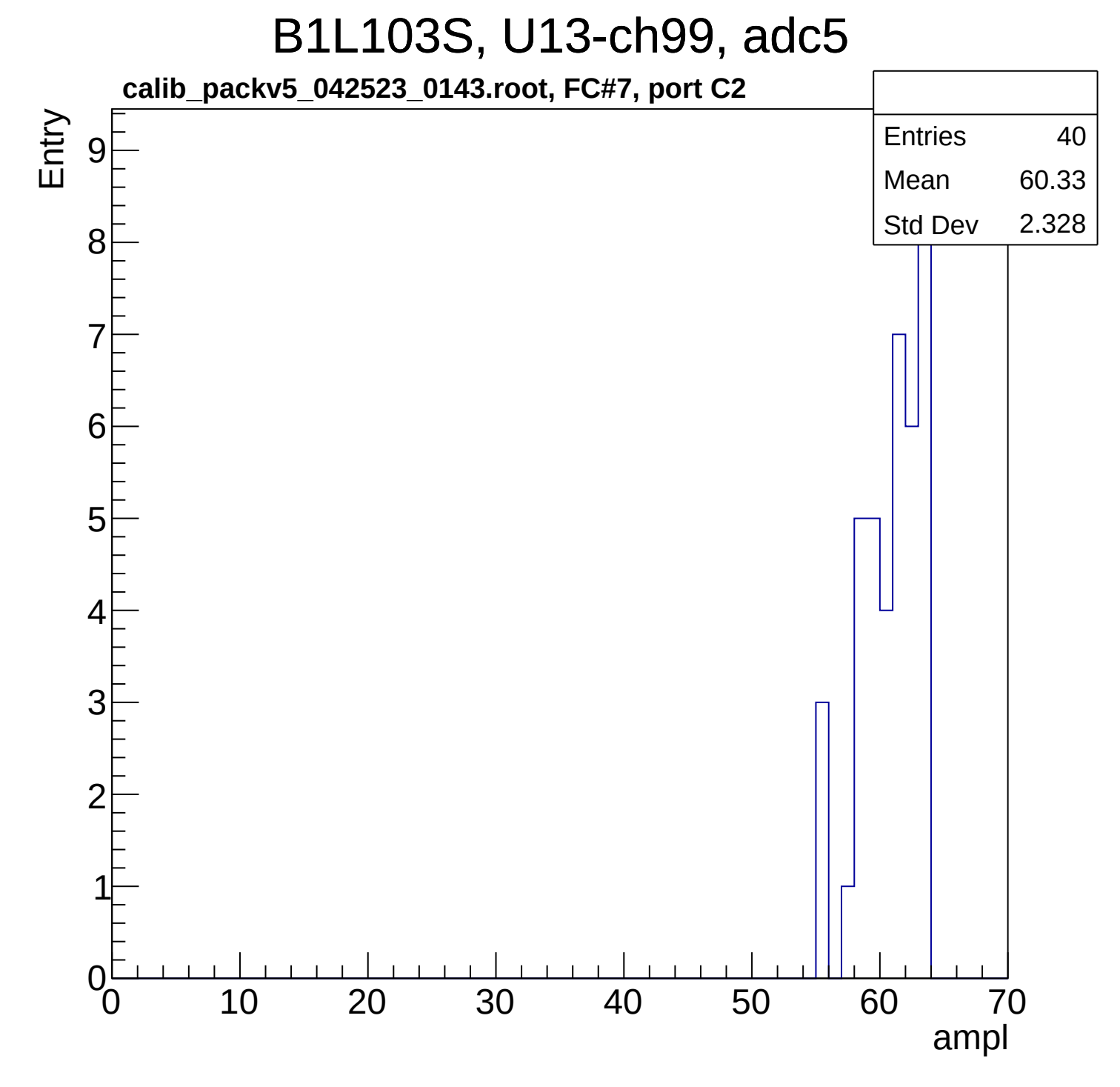
B1L103S, U13-ch99, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 60.33
Std Dev 2.328
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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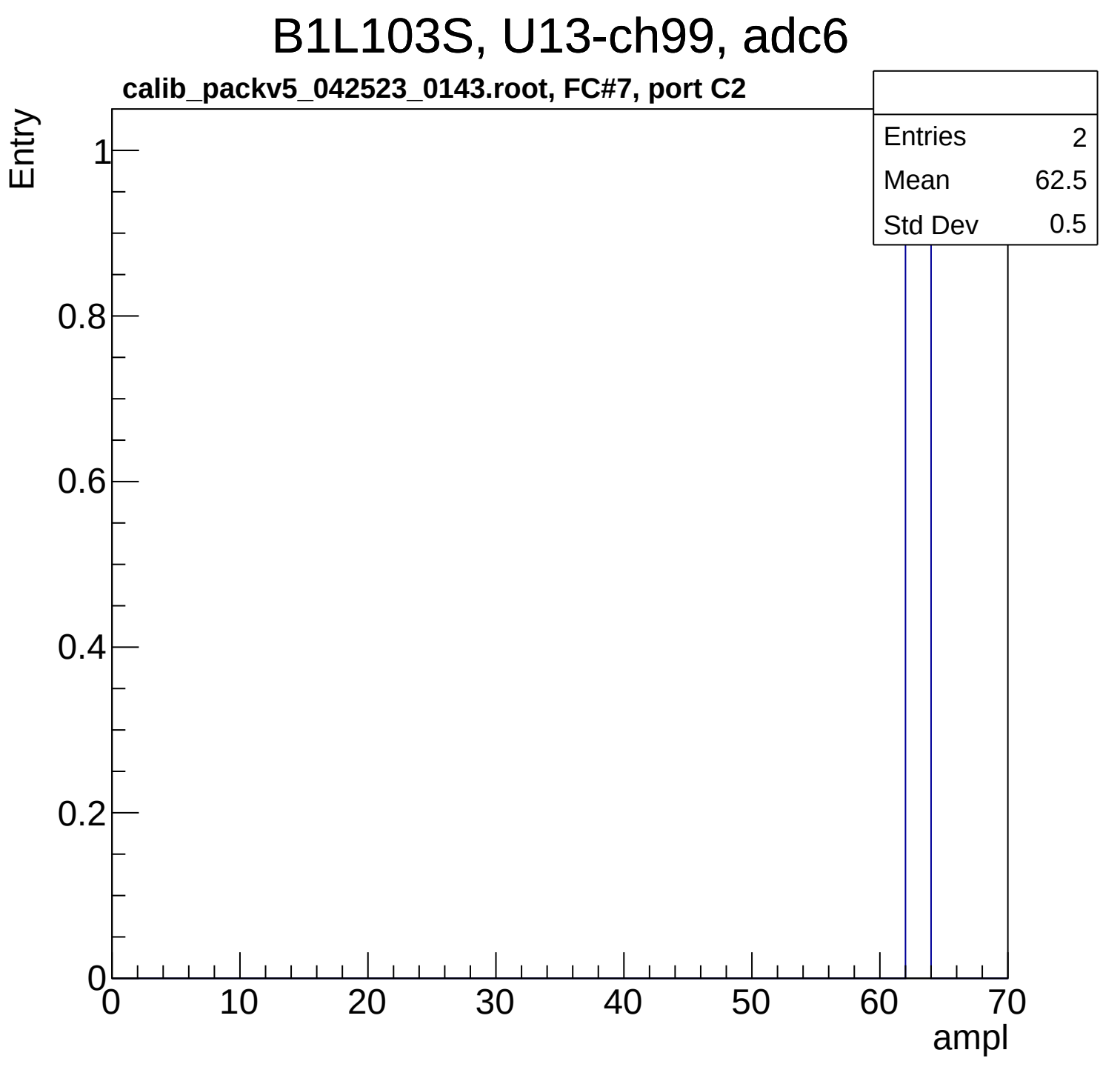
B1L103S, U13-ch99, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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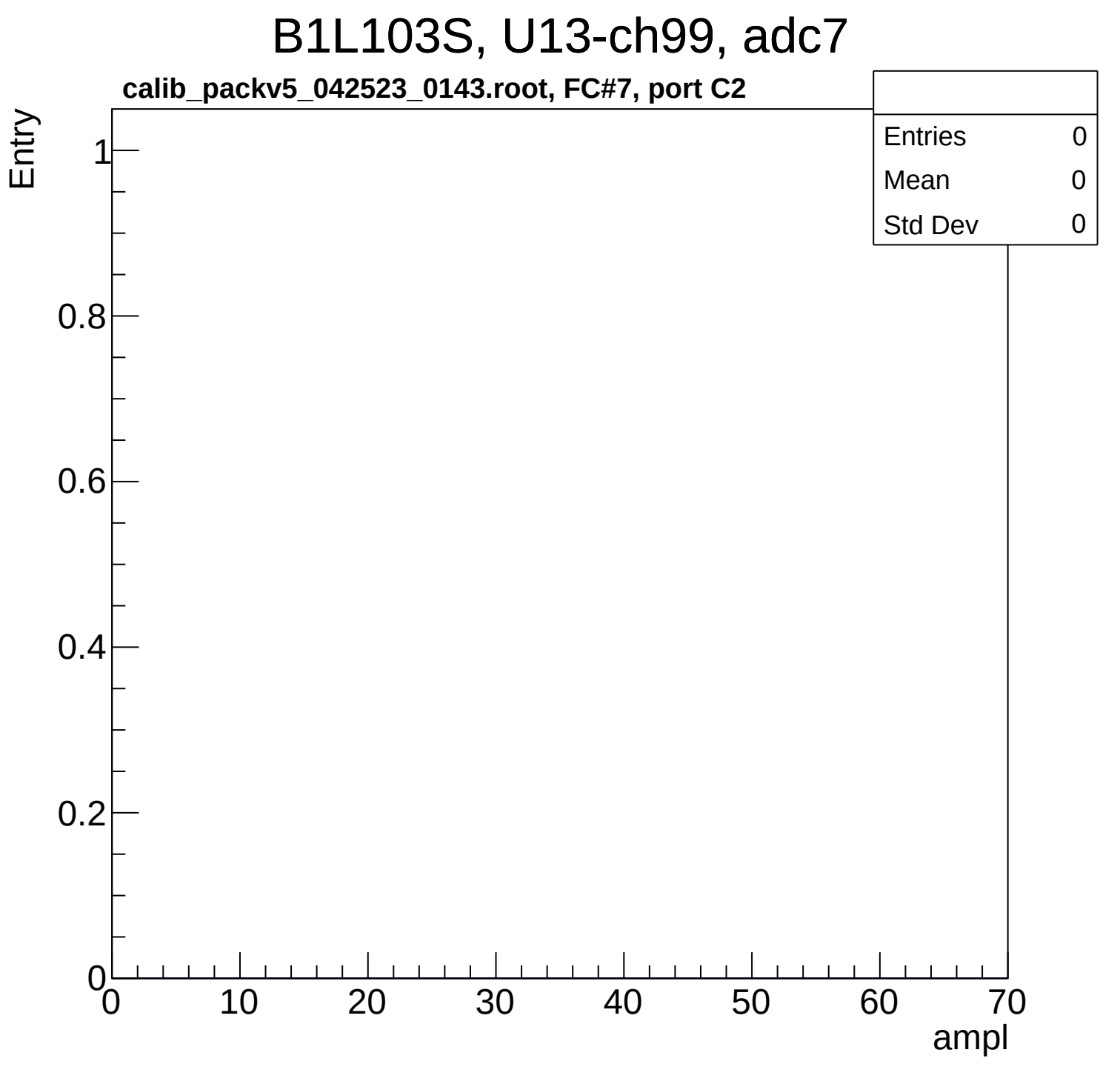
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch99, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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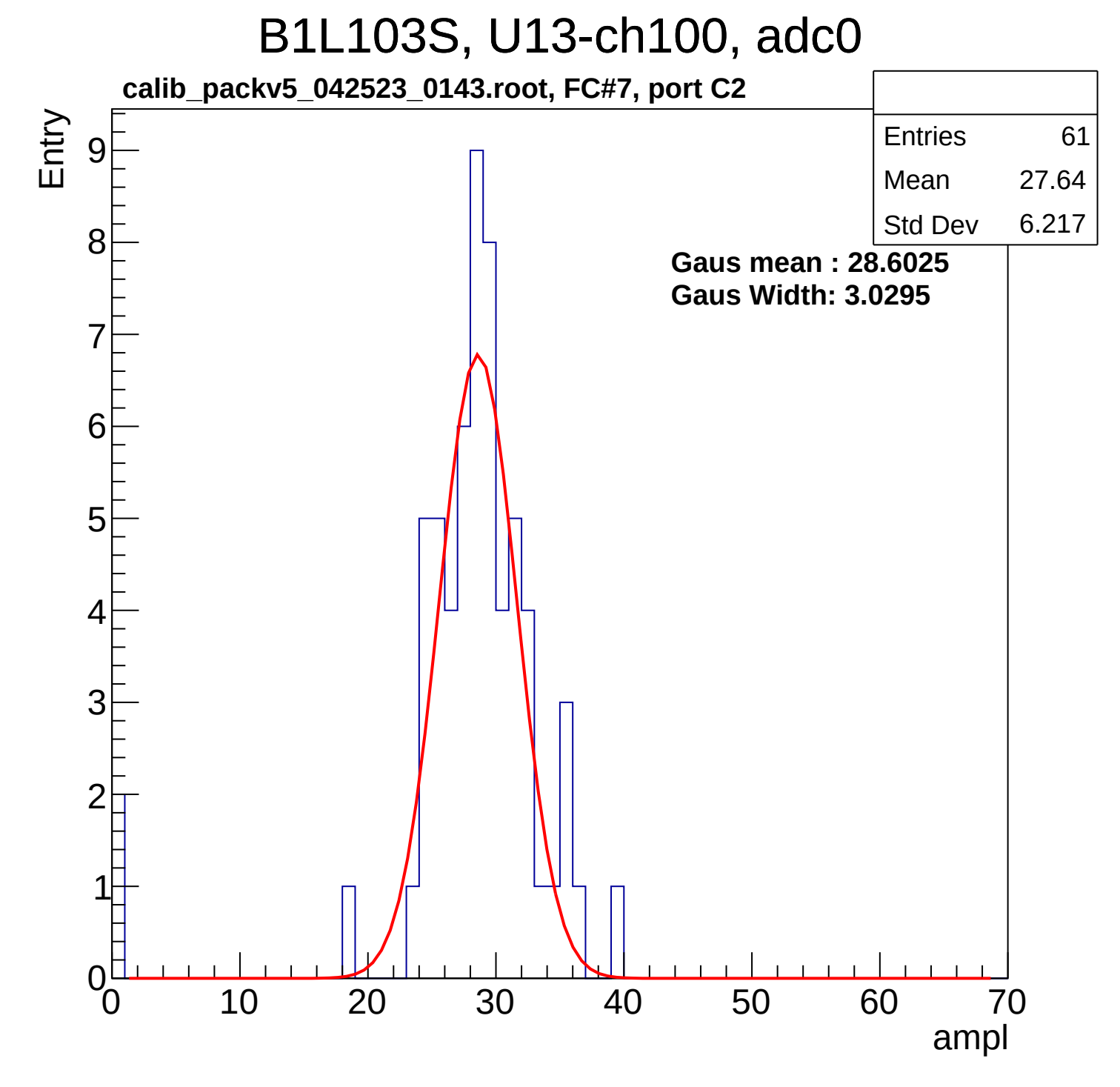
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch100, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 27.64
Std Dev 6.217
Gaus mean : 28.6025
Gaus Width: 3.0295
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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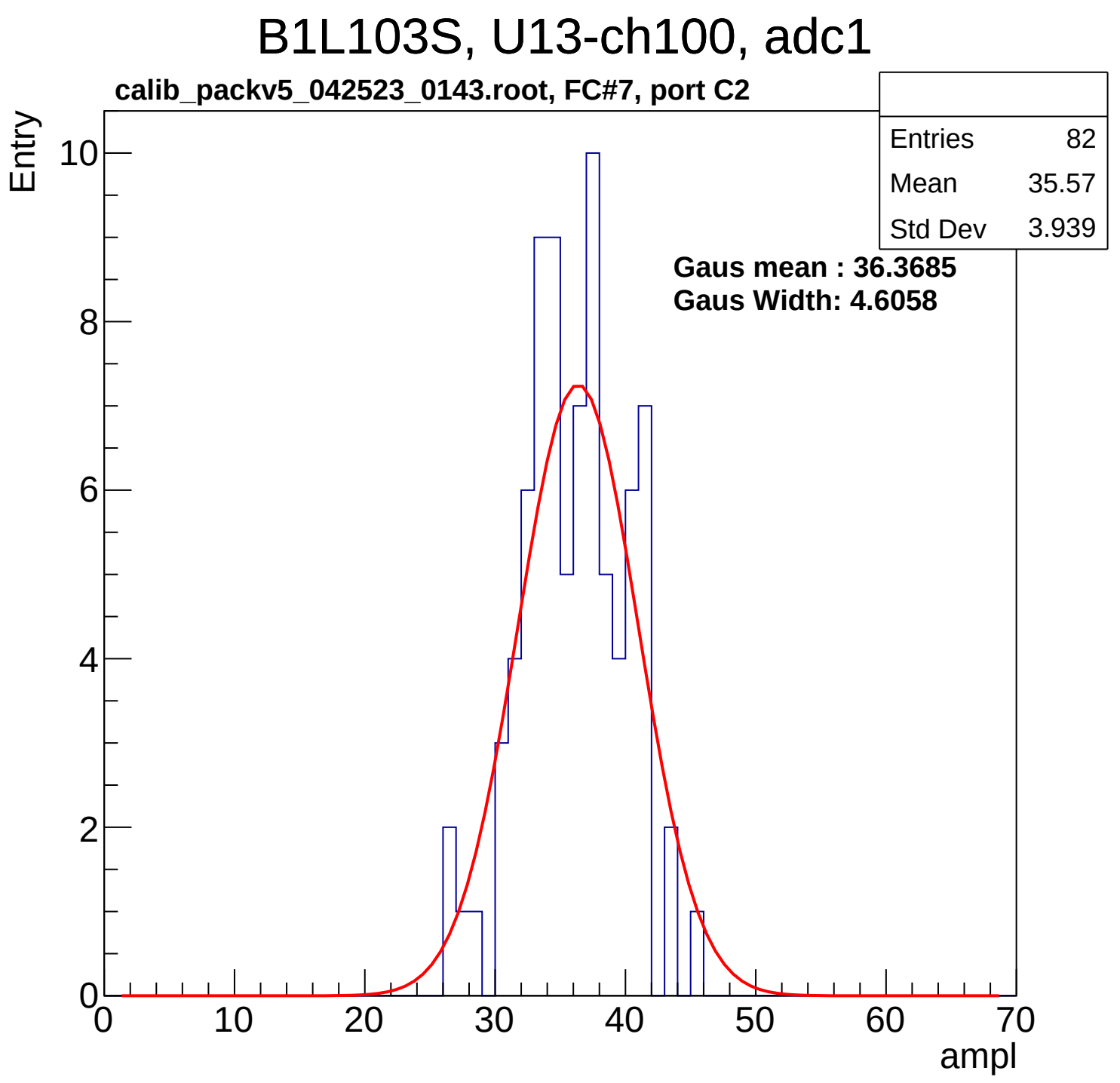
40
50
60
70


B1L103S, U13-ch100, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 35.57
Std Dev 3.939
Gaus mean : 36.3685
Gaus Width: 4.6058
Entry
ampl
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2
4
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8
10
0
10
20
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40
50
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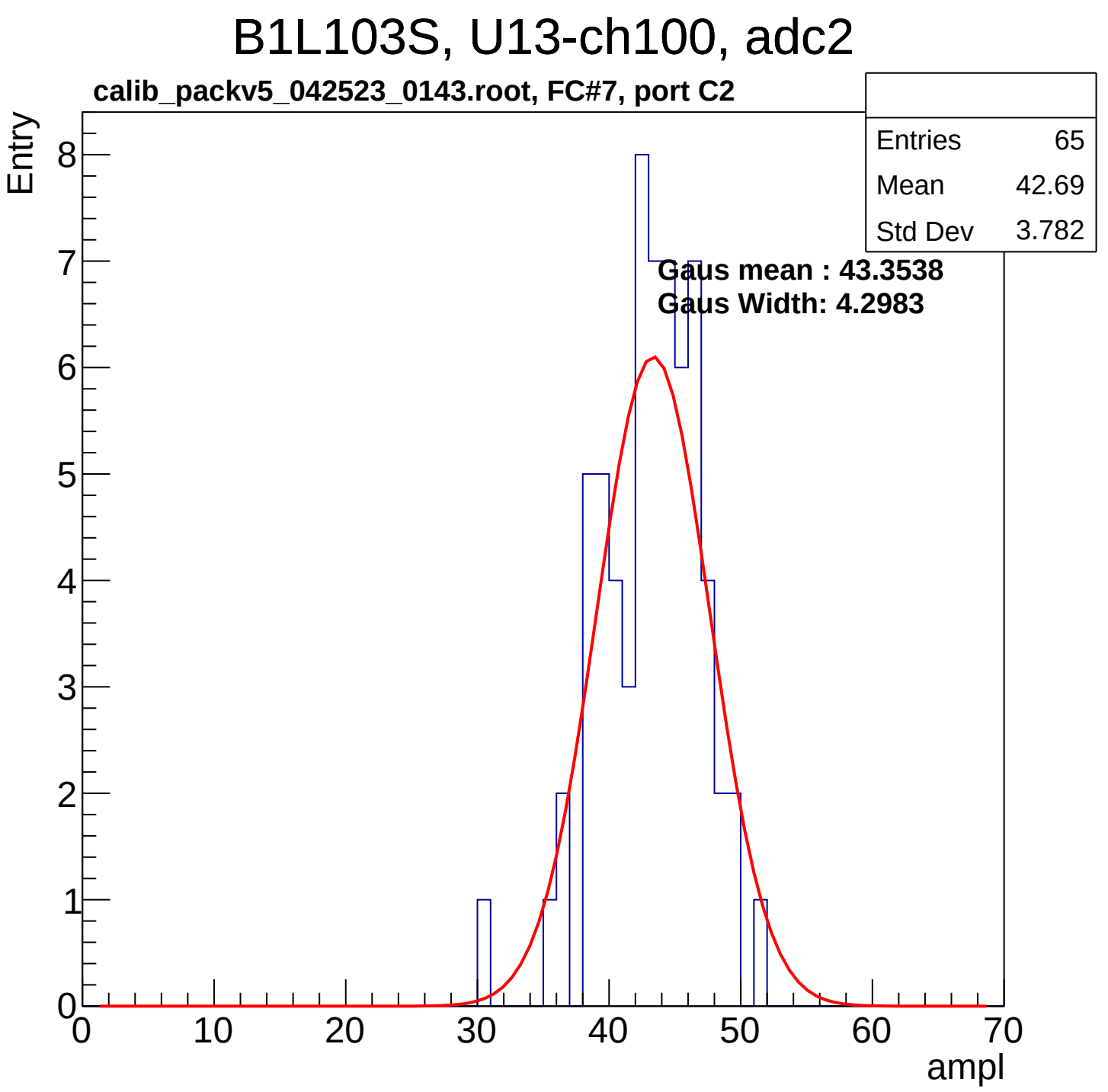
B1L103S, U13-ch100, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.69
Std Dev 3.782
Gaus mean : 43.3538
Gaus Width: 4.2983
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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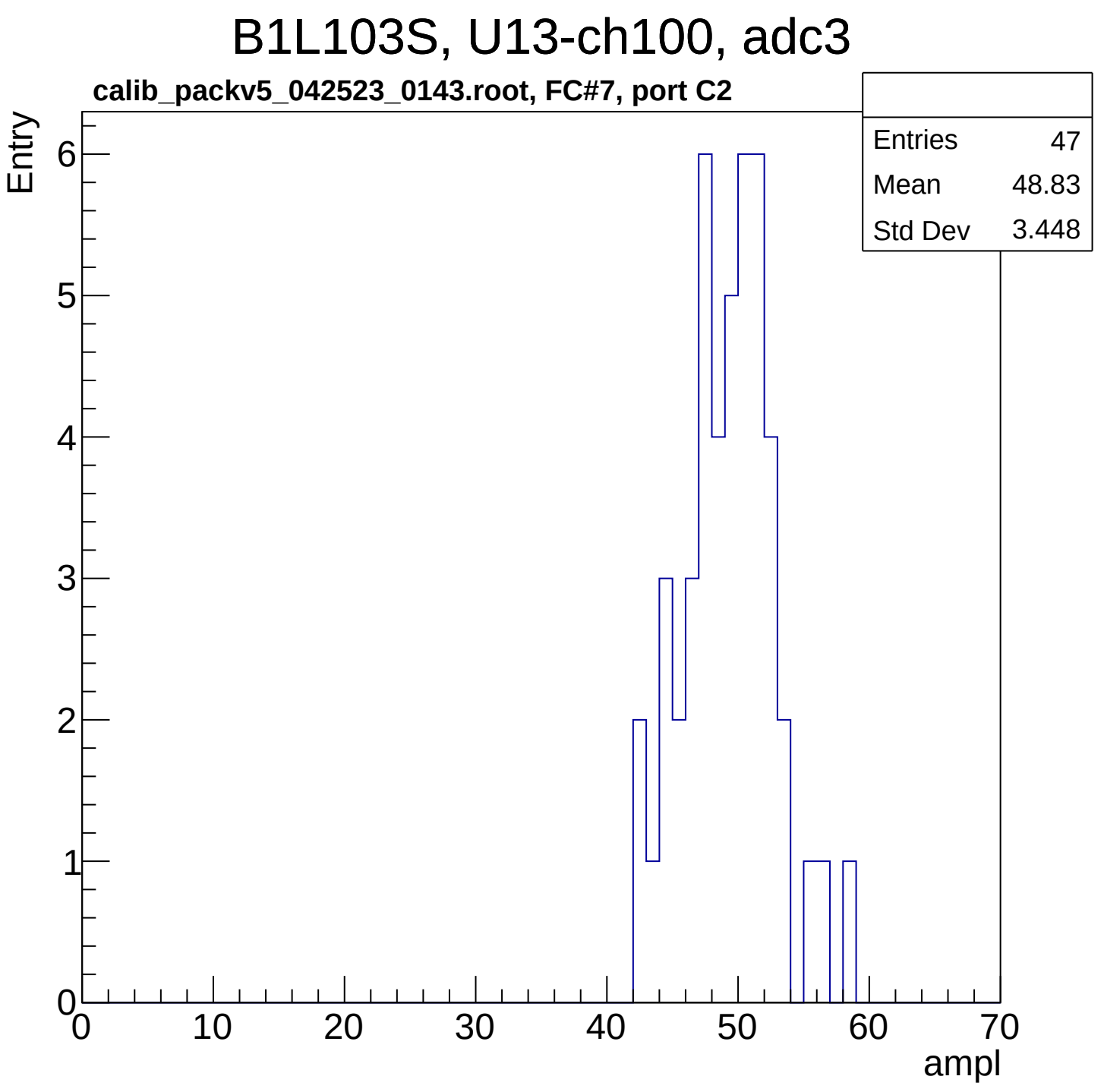
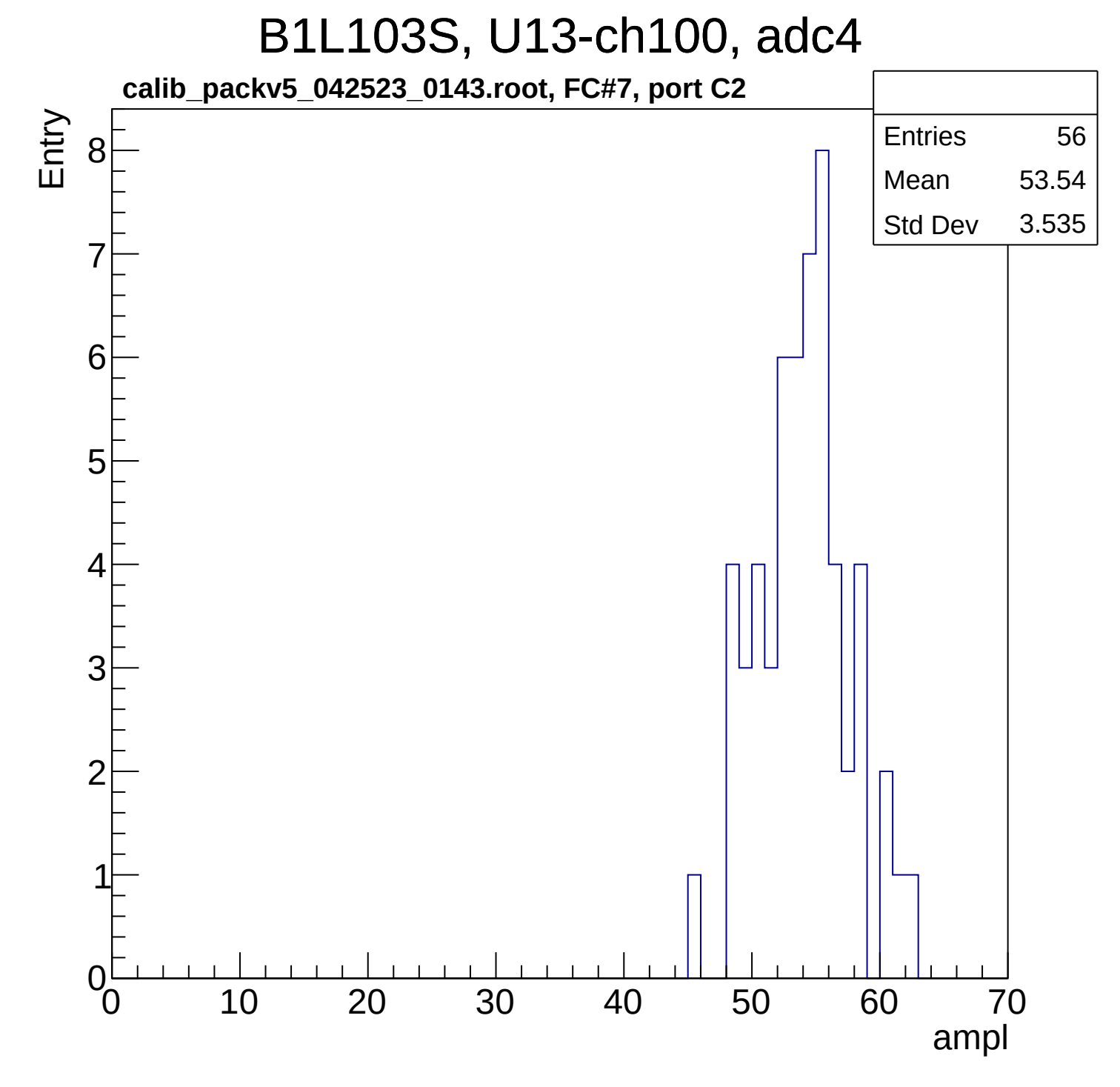

B1L103S, U13-ch100, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 48.83
Std Dev 3.448
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch100, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.54
Std Dev 3.535
Entry
ampl
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2
3
4
5
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7
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60
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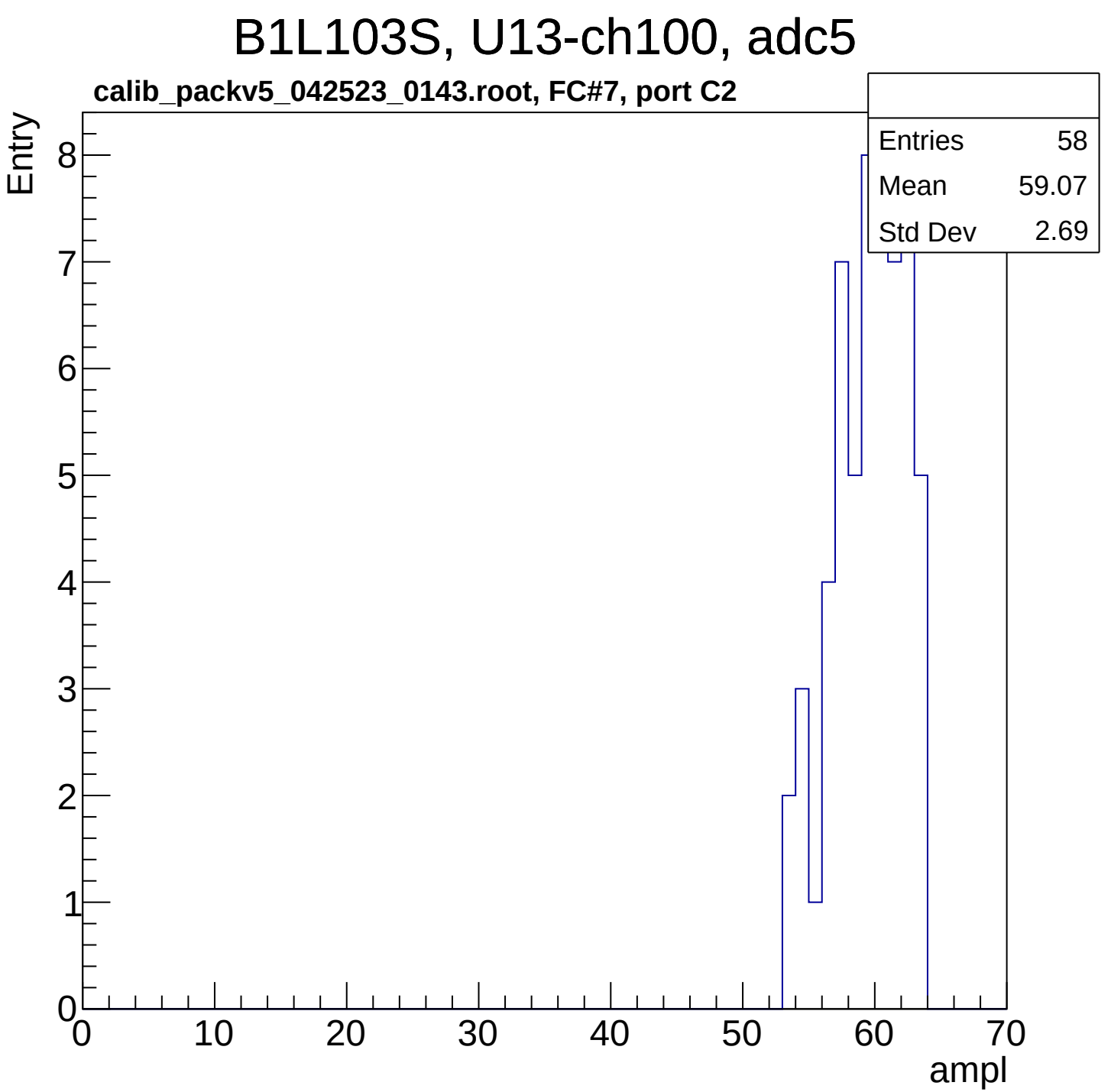
B1L103S, U13-ch100, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 59.07
Std Dev 2.69
Entry
ampl
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1
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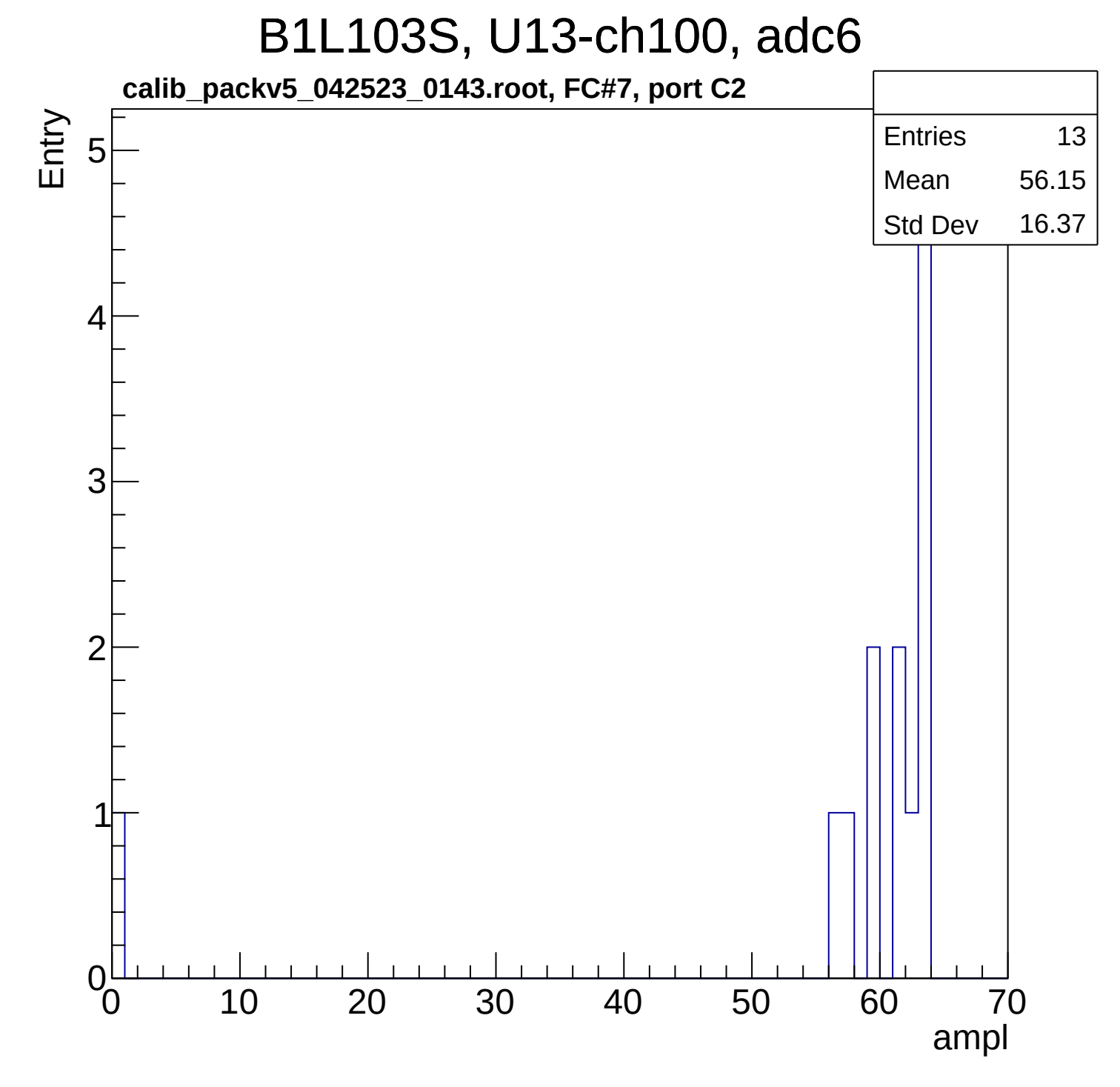
B1L103S, U13-ch100, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.15
Std Dev 16.37
Entry
ampl
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1
2
3
4
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0
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20
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40
50
60
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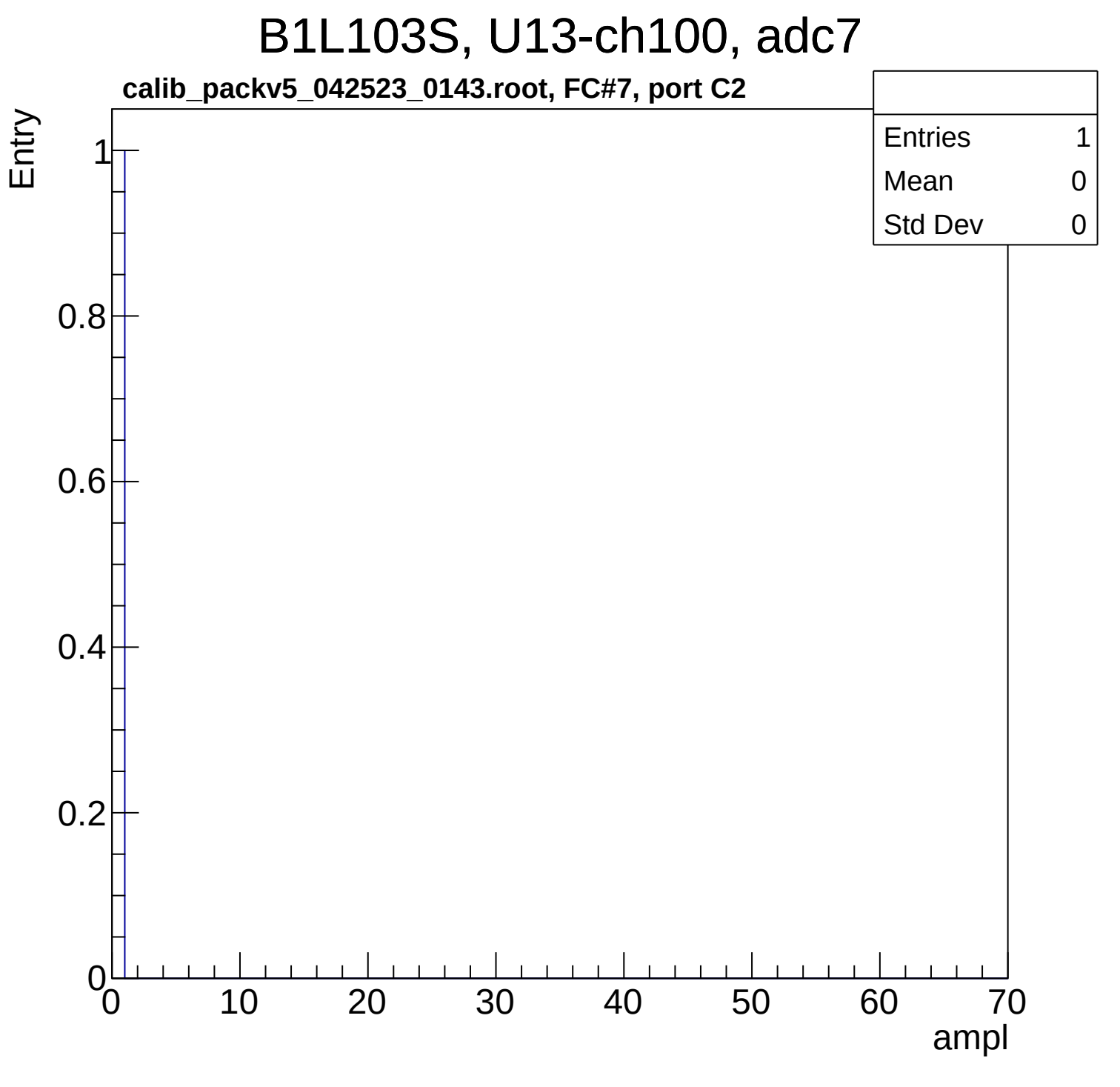
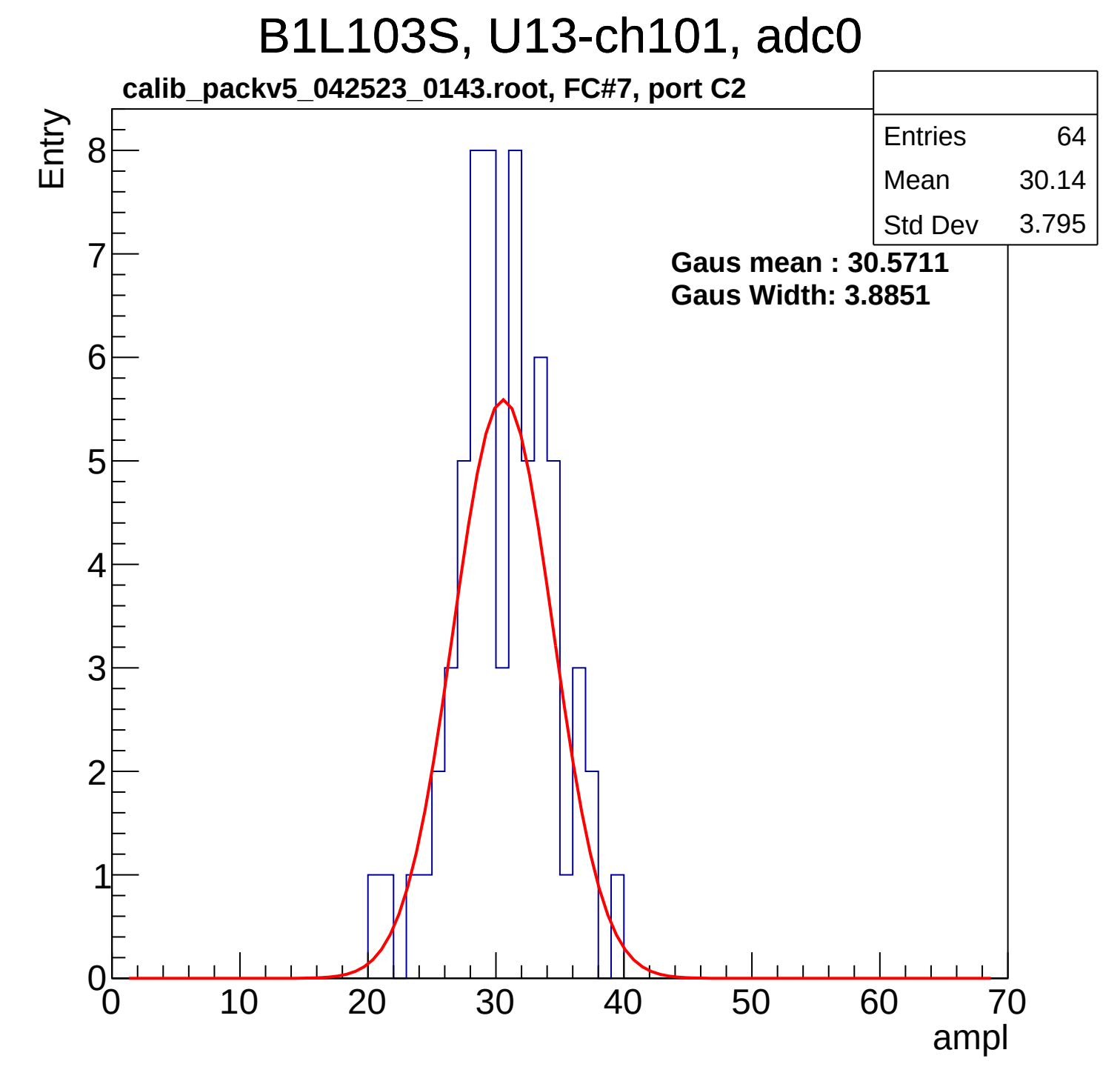

B1L103S, U13-ch100, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch101, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 30.14
Std Dev 3.795
Gaus mean : 30.5711
Gaus Width: 3.8851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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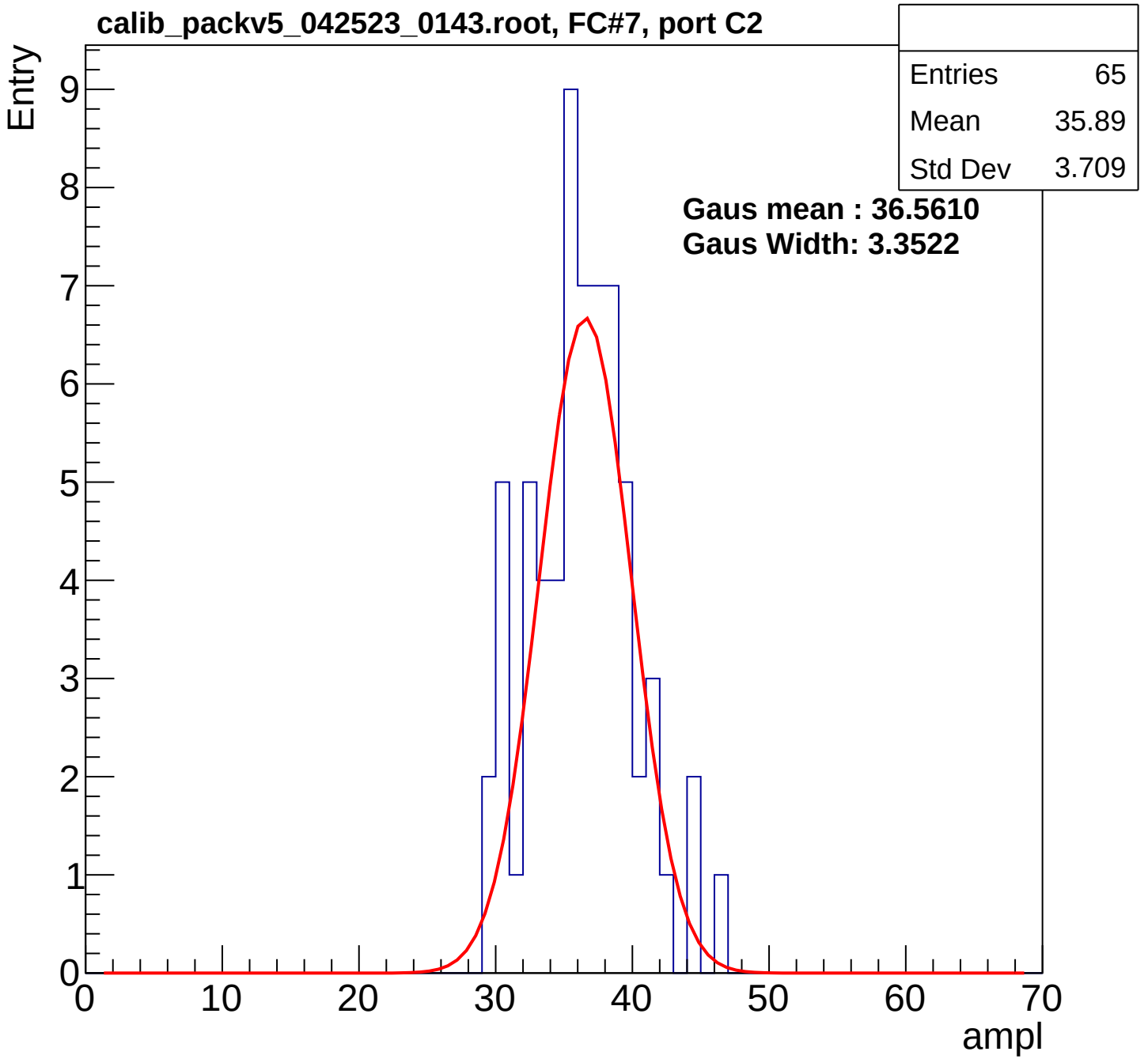
B1L103S, U13-ch101, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 35.89
Std Dev 3.709
Gaus mean : 36.5610
Gaus Width: 3.3522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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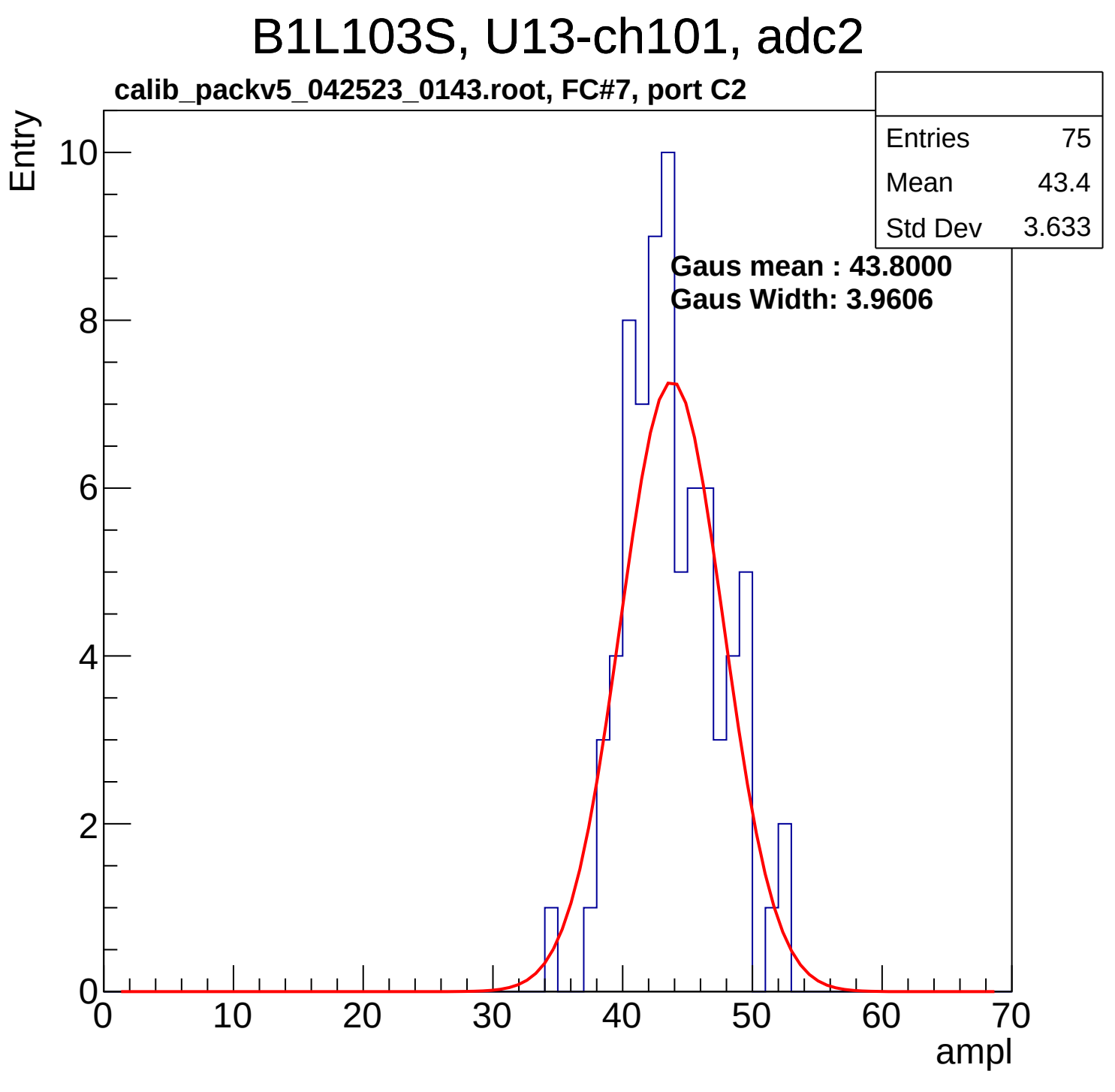
B1L103S, U13-ch101, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 43.4
Std Dev 3.633
Gaus mean : 43.8000
Gaus Width: 3.9606
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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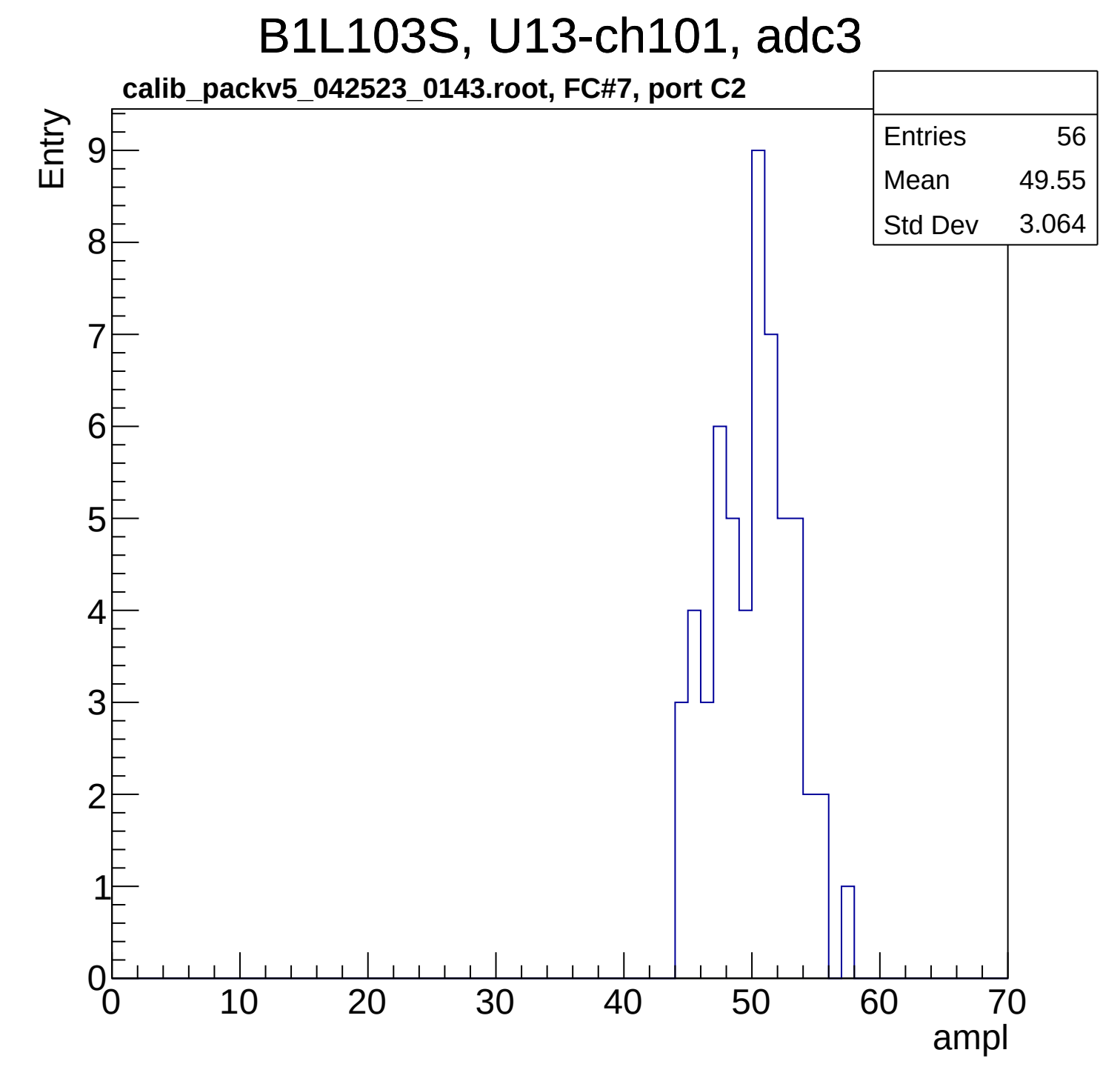
B1L103S, U13-ch101, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 49.55
Std Dev 3.064
Entry
ampl
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2
3
4
5
6
7
8
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0
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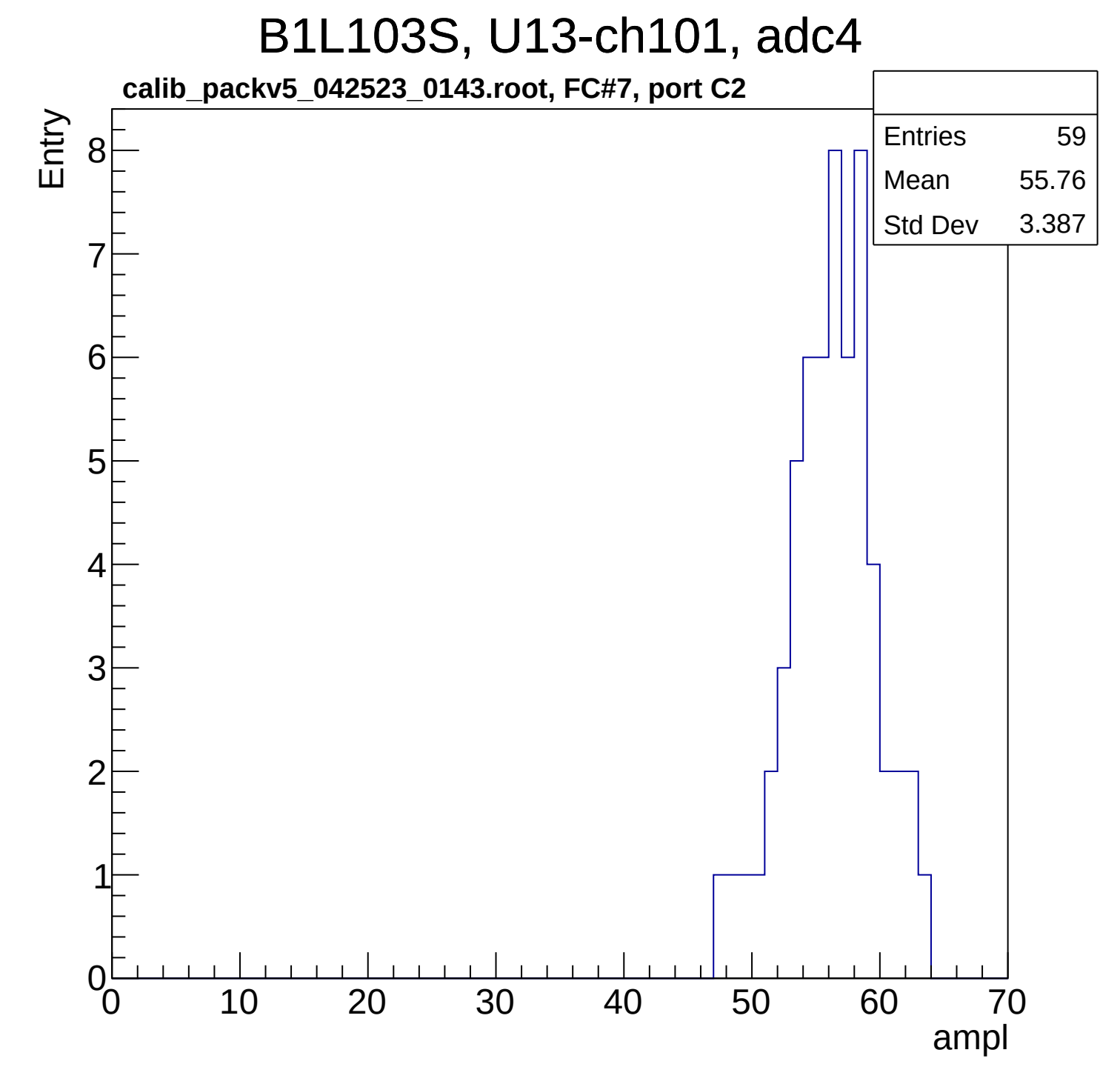
B1L103S, U13-ch101, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.76
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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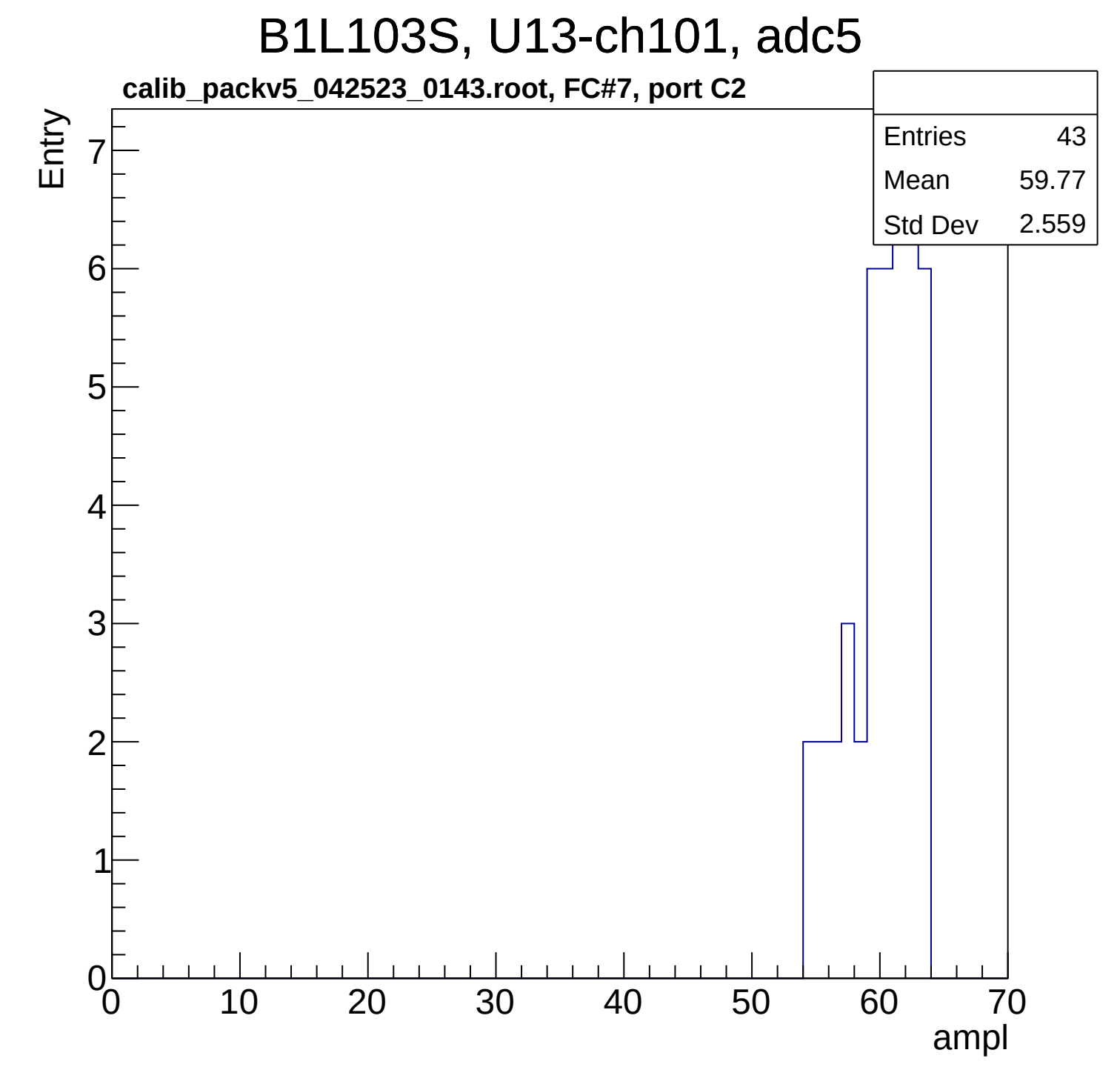
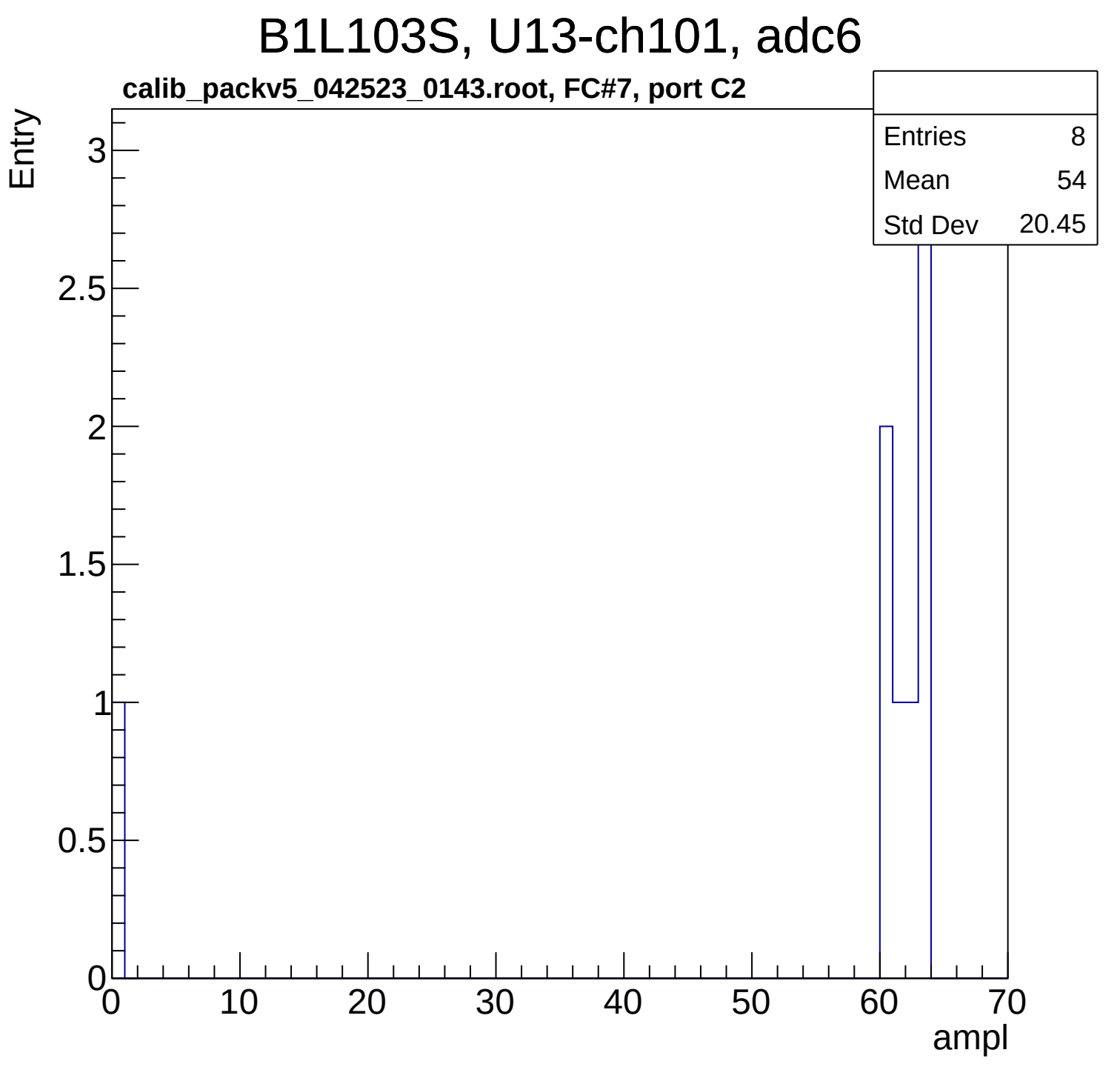
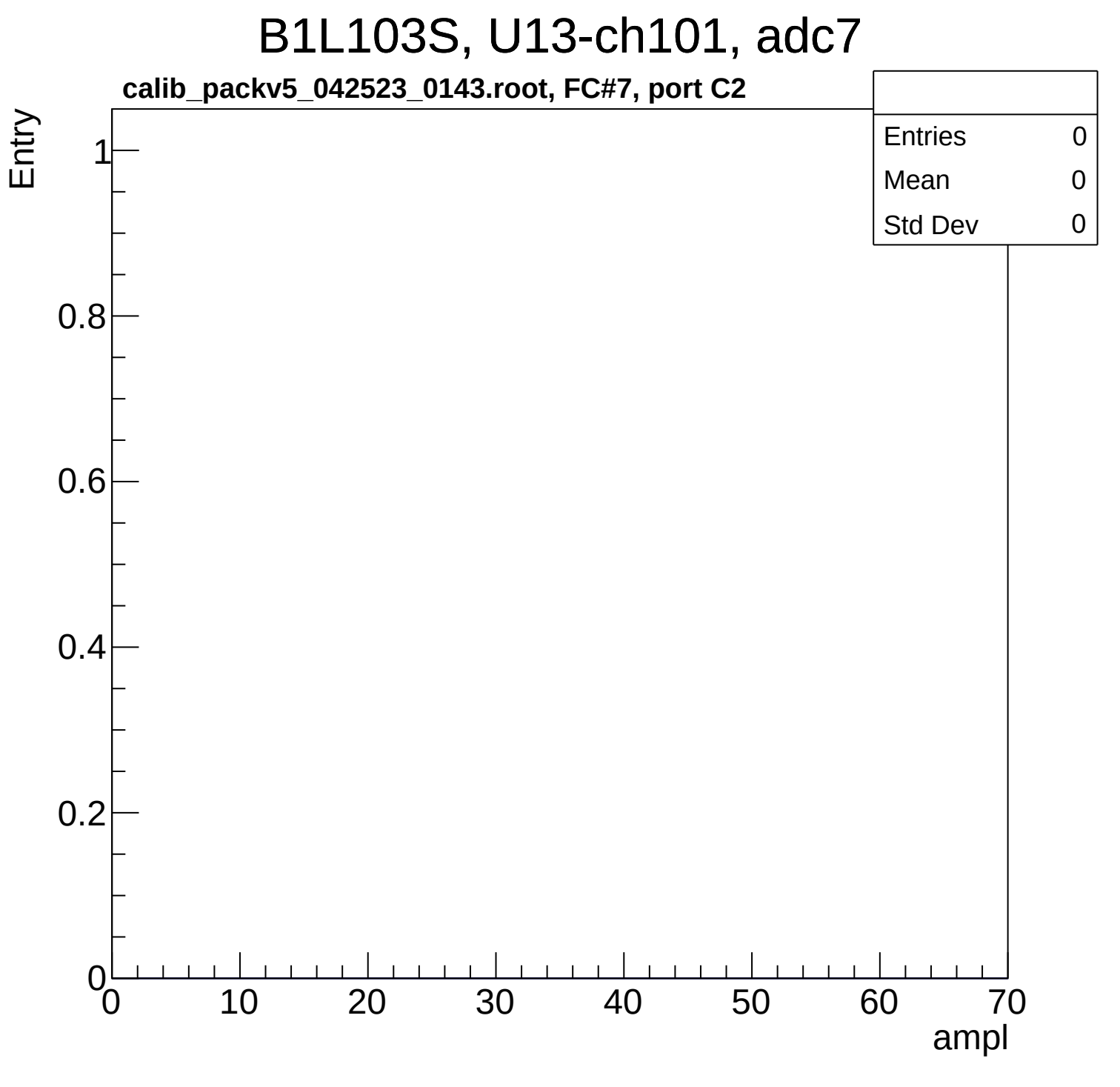

B1L103S, U13-ch101, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.77
Std Dev 2.559
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch101, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 54
Std Dev 20.45
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch101, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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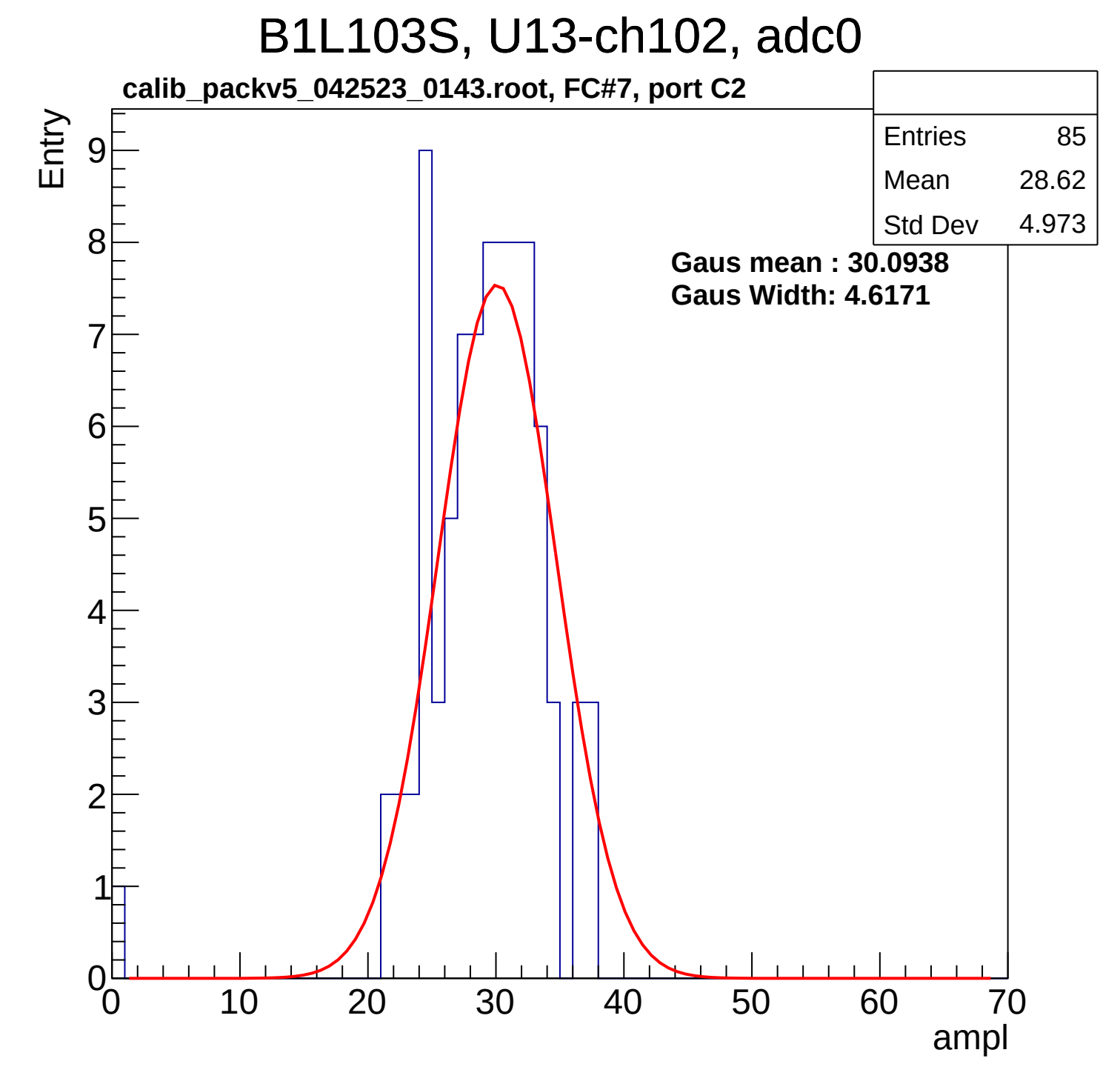
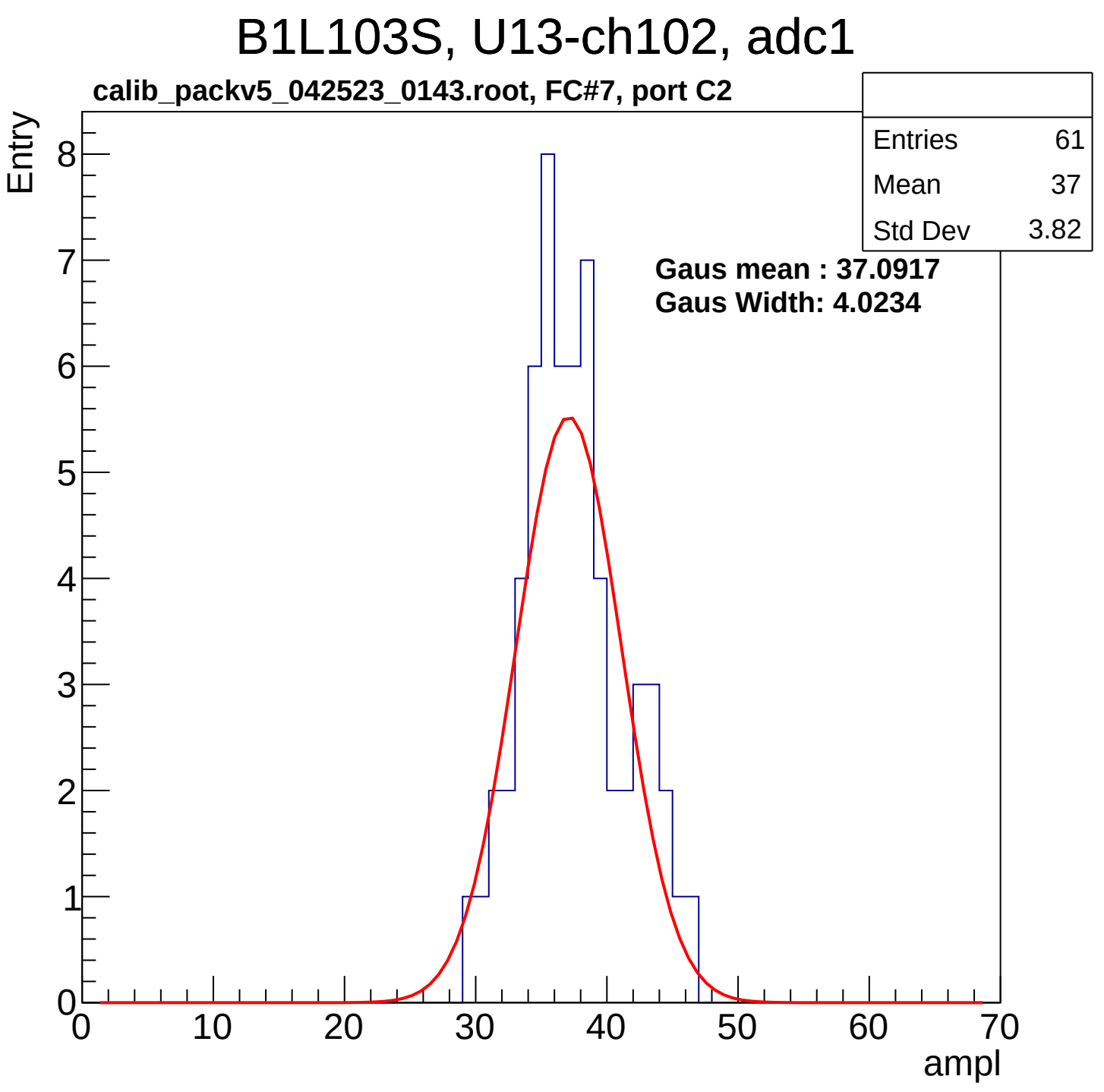
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch102, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 28.62
Std Dev 4.973
Gaus mean : 30.0938
Gaus Width: 4.6171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch102, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37
Std Dev 3.82
Gaus mean : 37.0917
Gaus Width: 4.0234
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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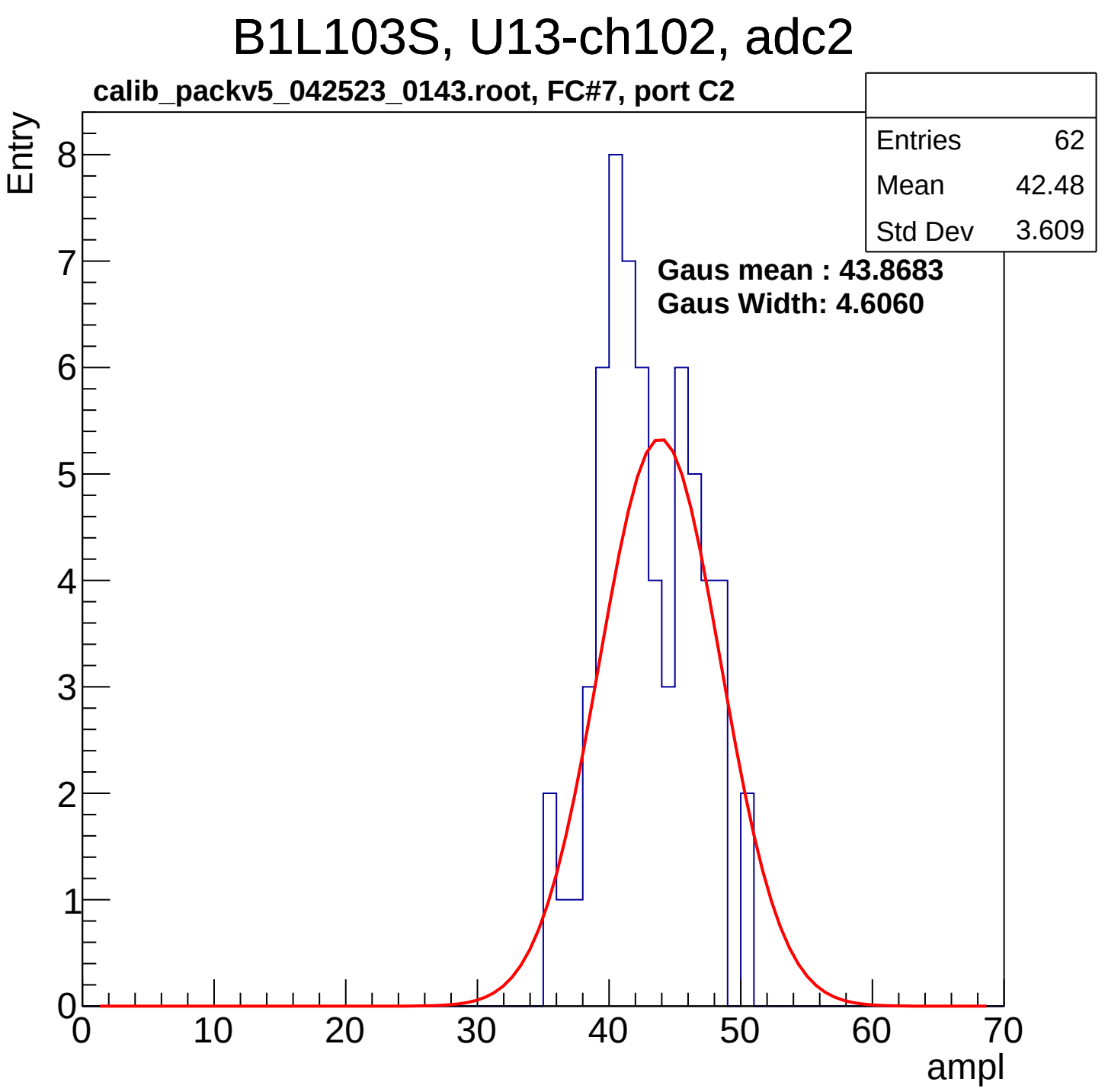
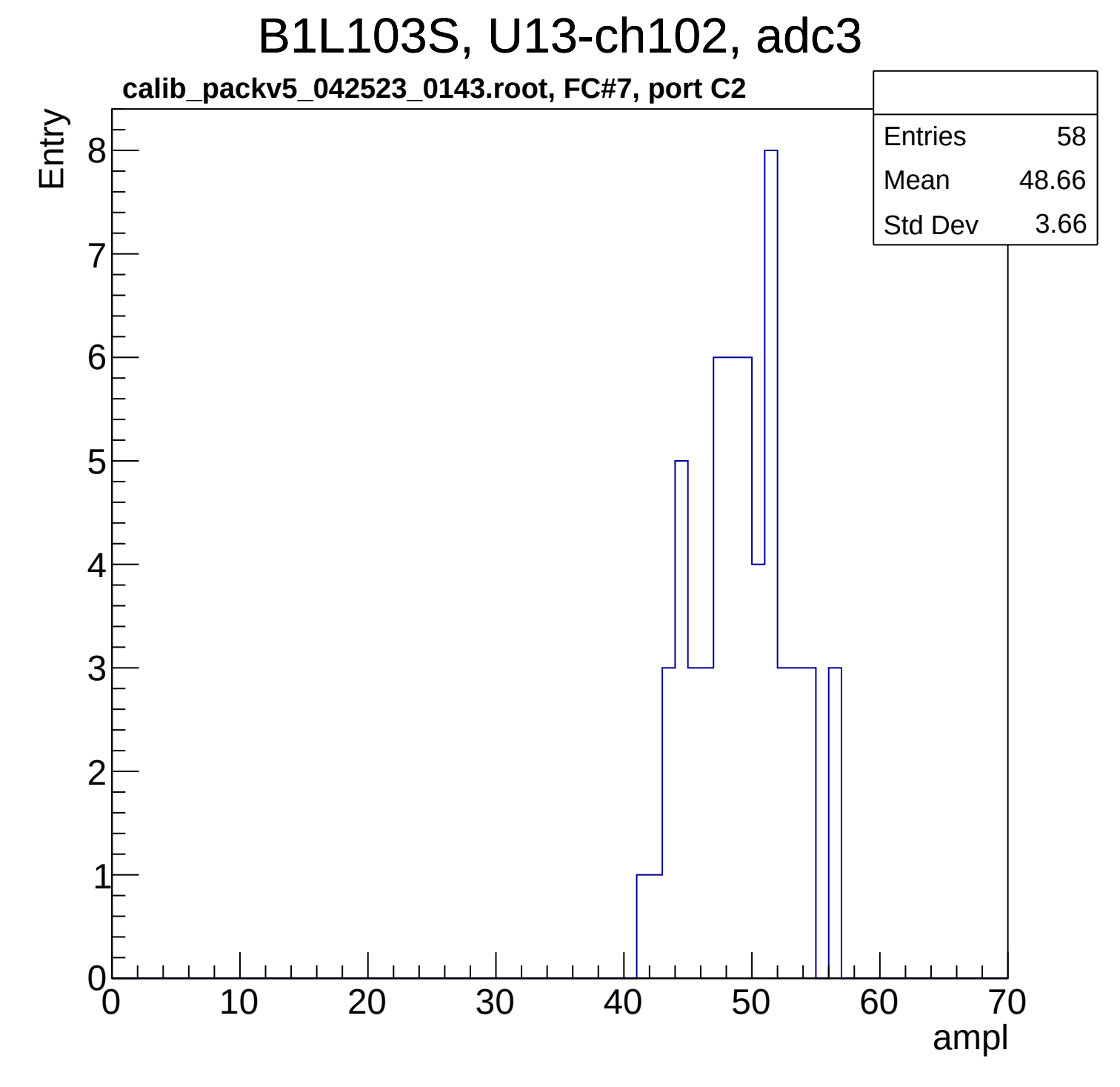
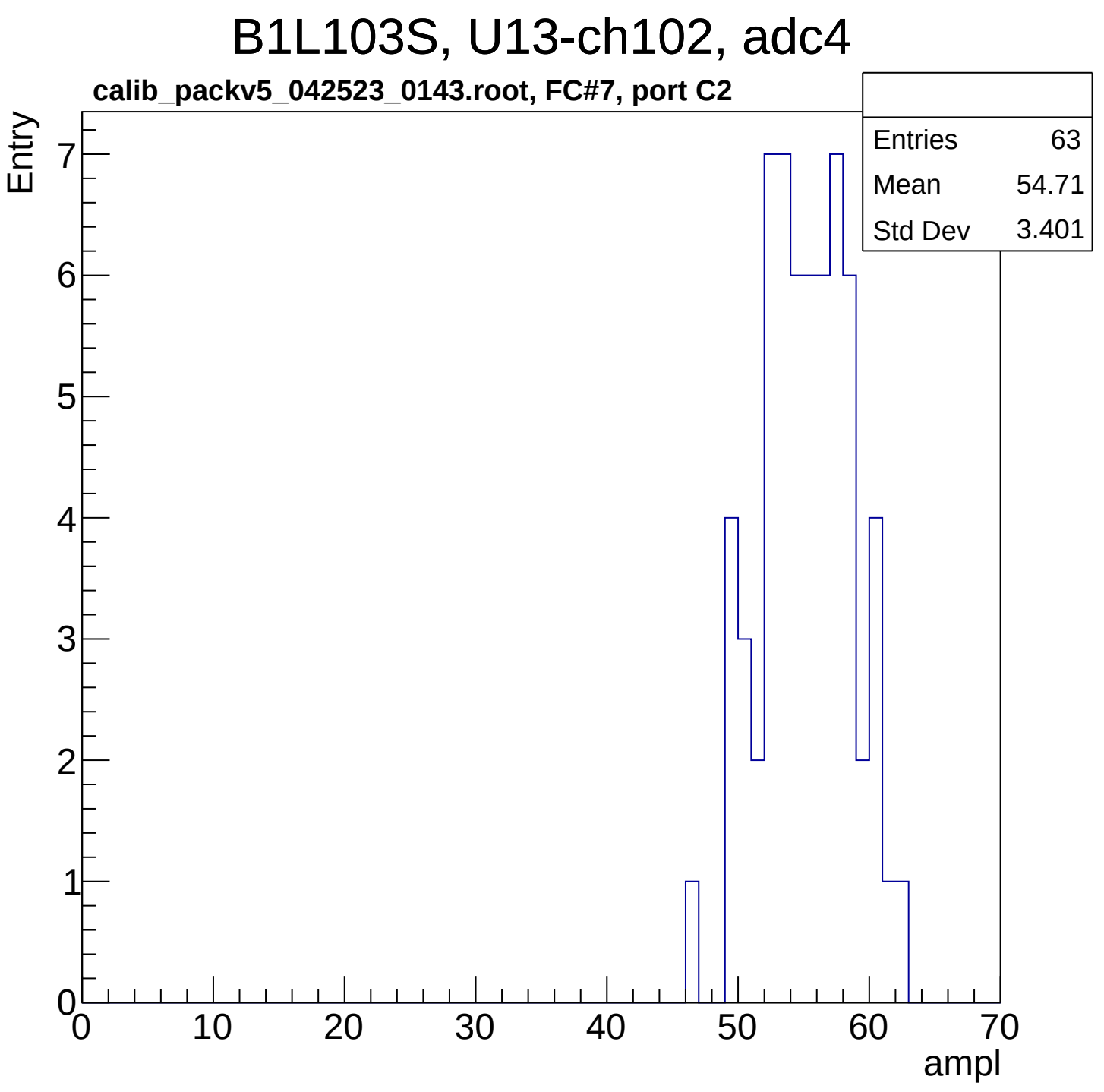
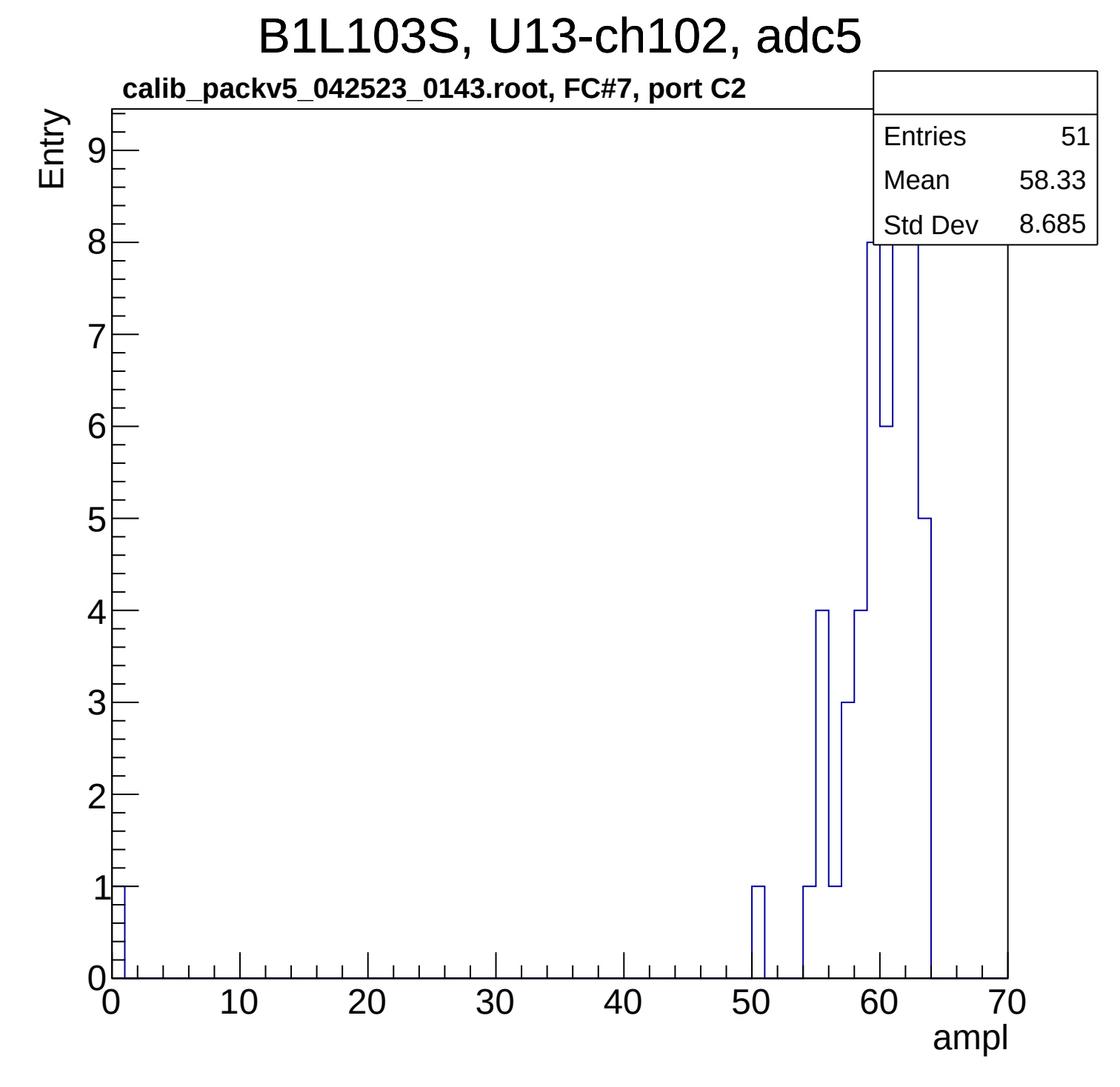

B1L103S, U13-ch102, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.48
Std Dev 3.609
Gaus mean : 43.8683
Gaus Width: 4.6060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch102, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 48.66
Std Dev 3.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch102, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.71
Std Dev 3.401
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch102, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.33
Std Dev 8.685
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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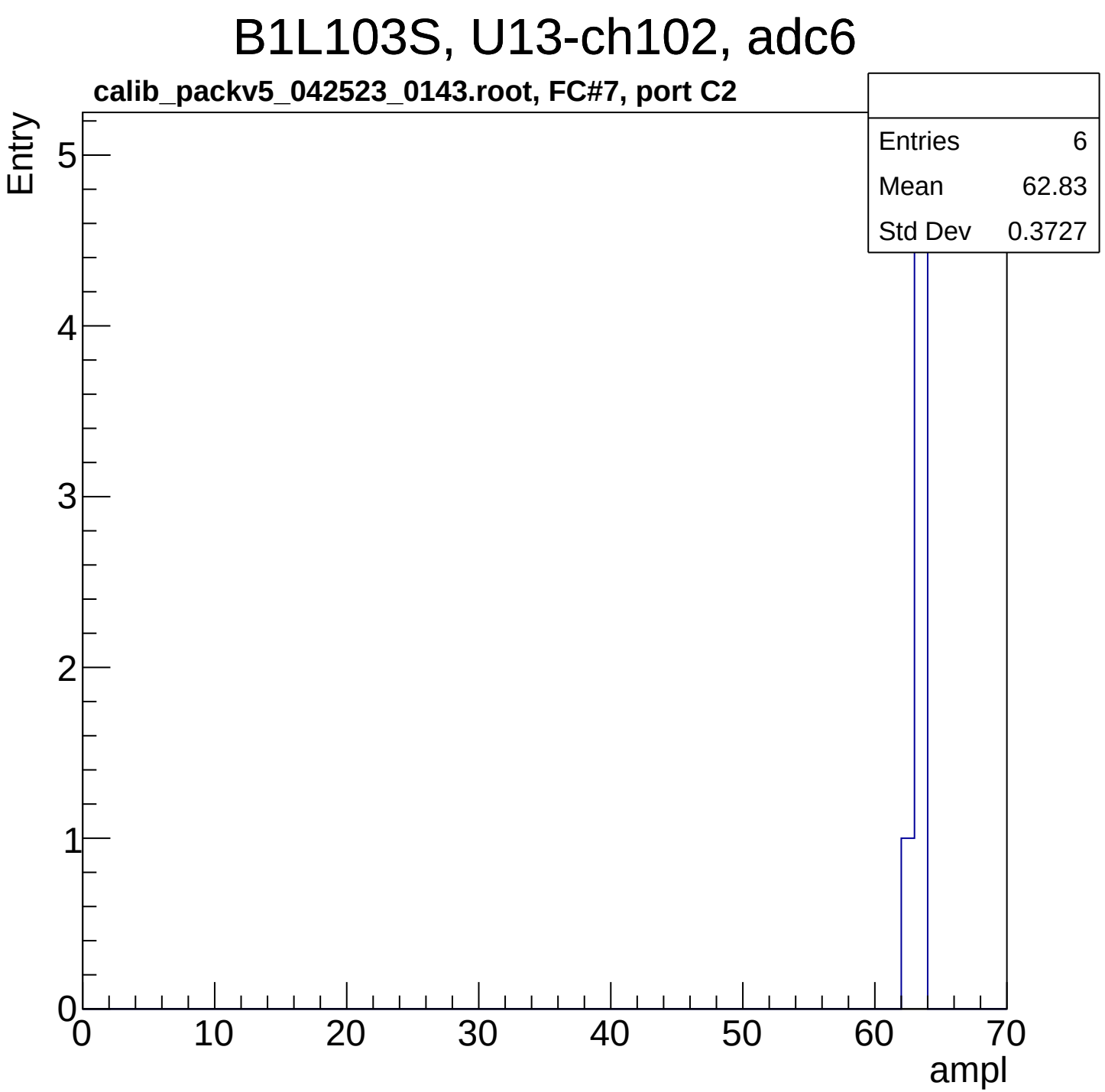
B1L103S, U13-ch102, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
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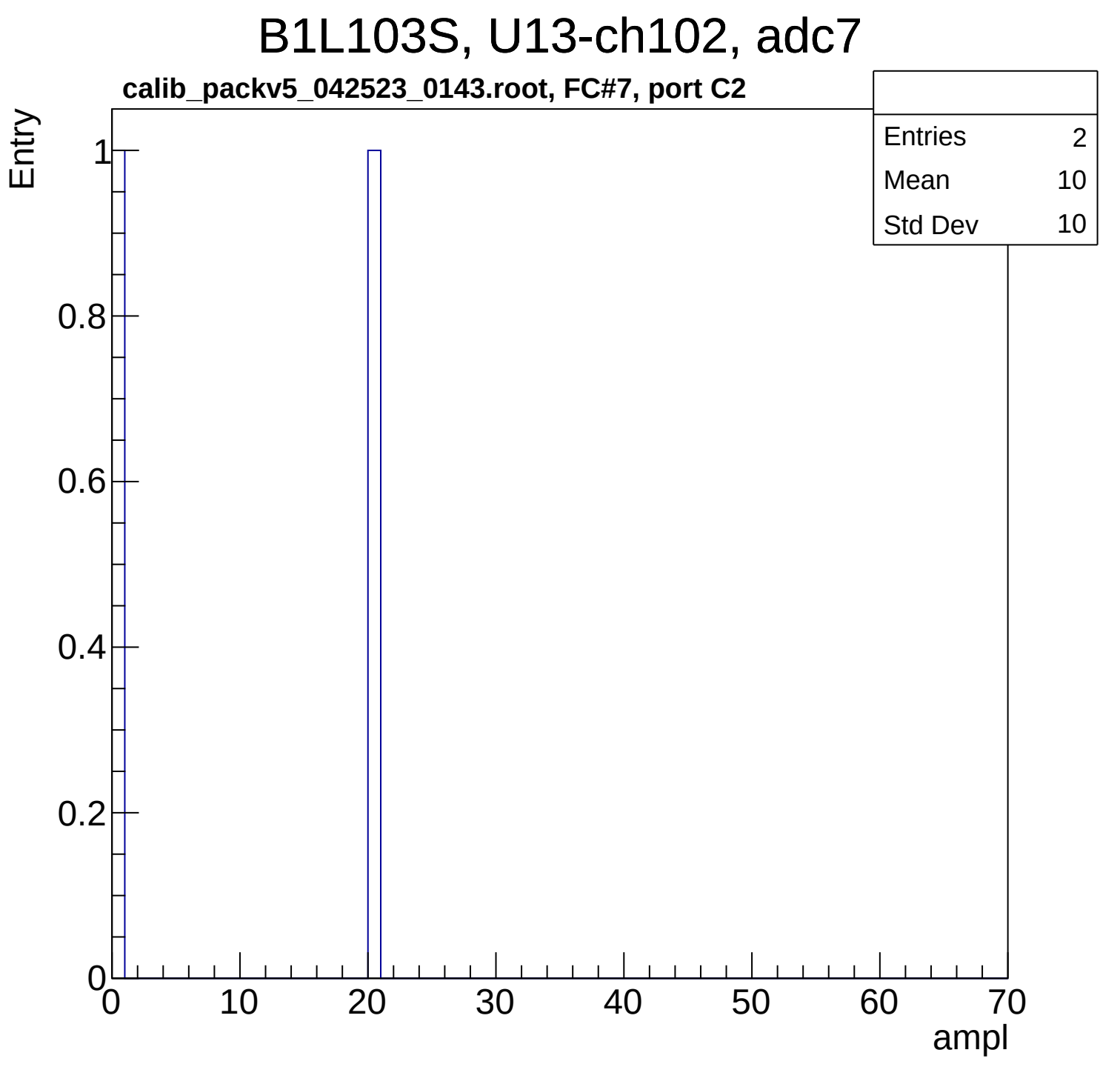
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch102, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10
Std Dev 10
Entry
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0.6
0.4
0.2
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10
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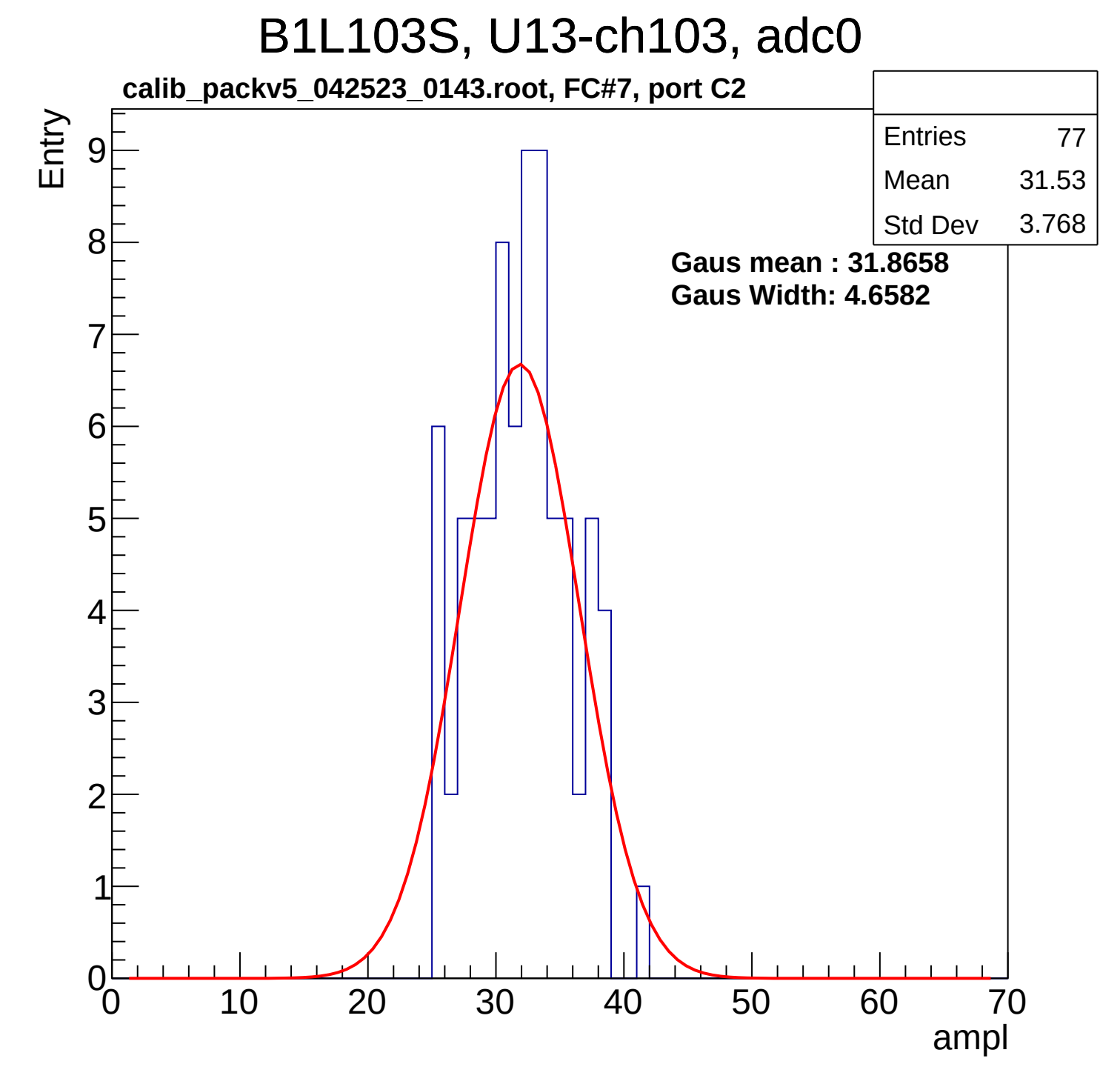
60
70
ampl


B1L103S, U13-ch103, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 31.53
Std Dev 3.768
Gaus mean : 31.8658
Gaus Width: 4.6582
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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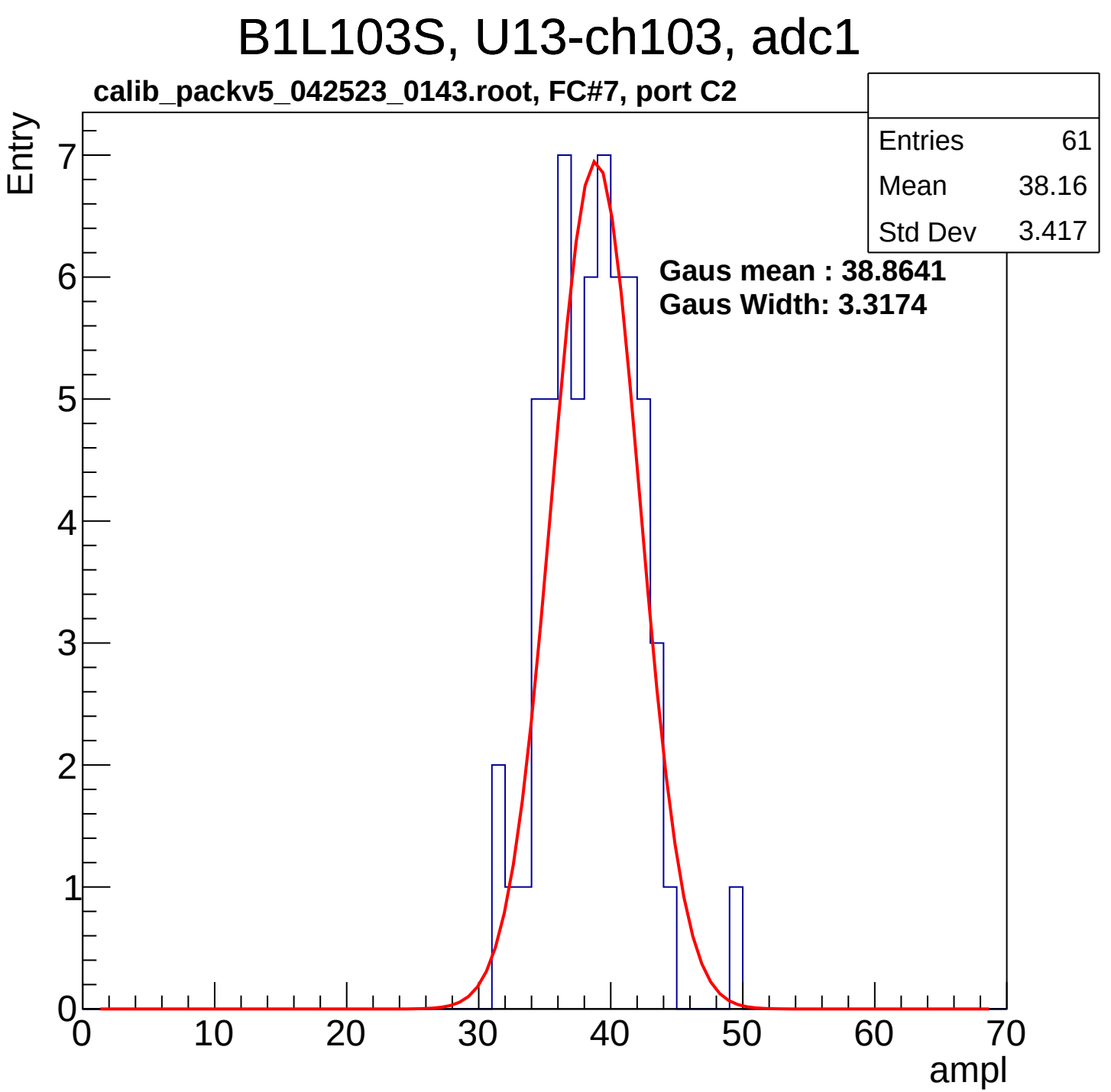
B1L103S, U13-ch103, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 38.16
Std Dev 3.417
Gaus mean : 38.8641
Gaus Width: 3.3174
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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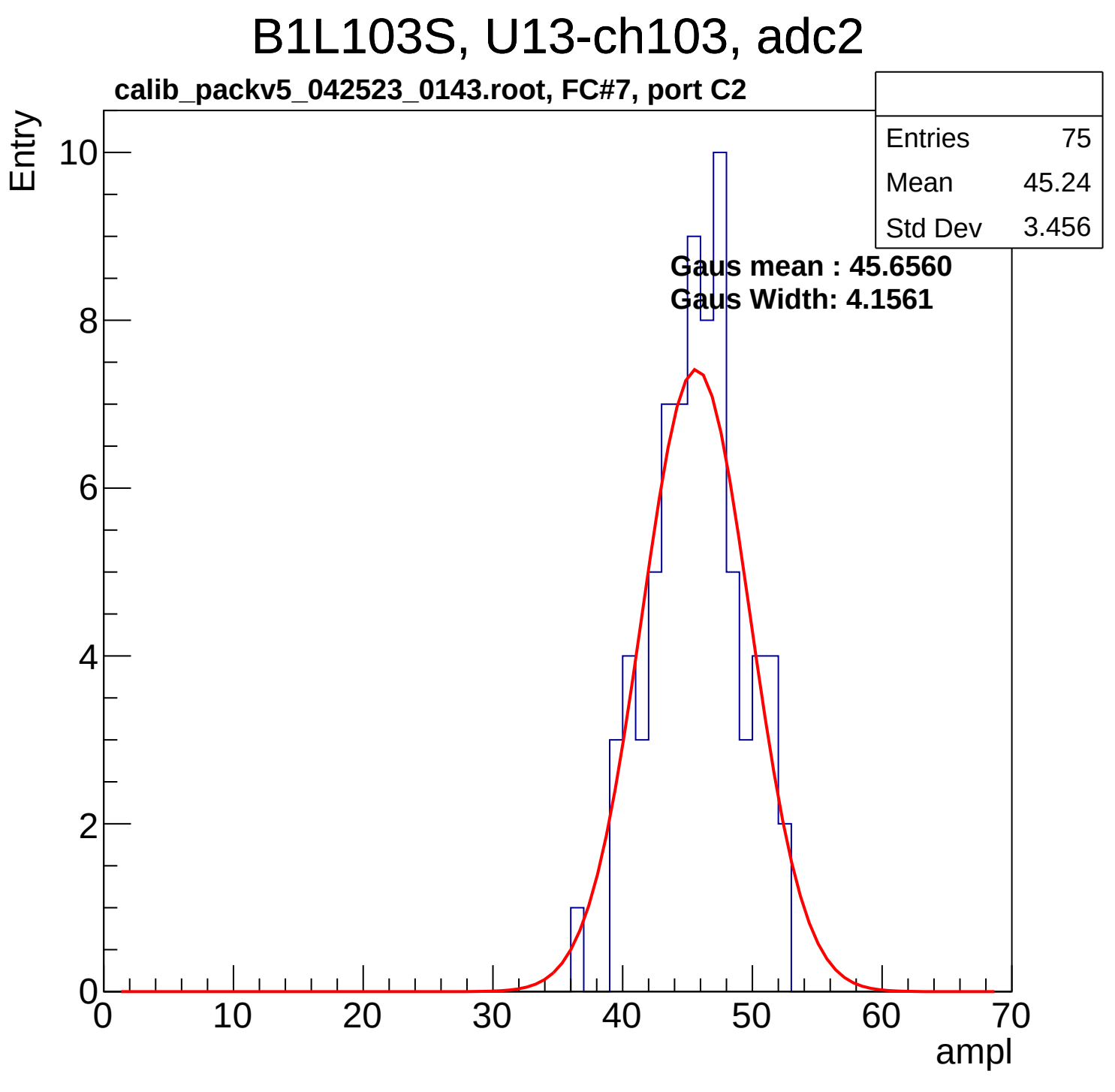
B1L103S, U13-ch103, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 45.24
Std Dev 3.456
Gaus mean : 45.6560
Gaus Width: 4.1561
Entry
ampl
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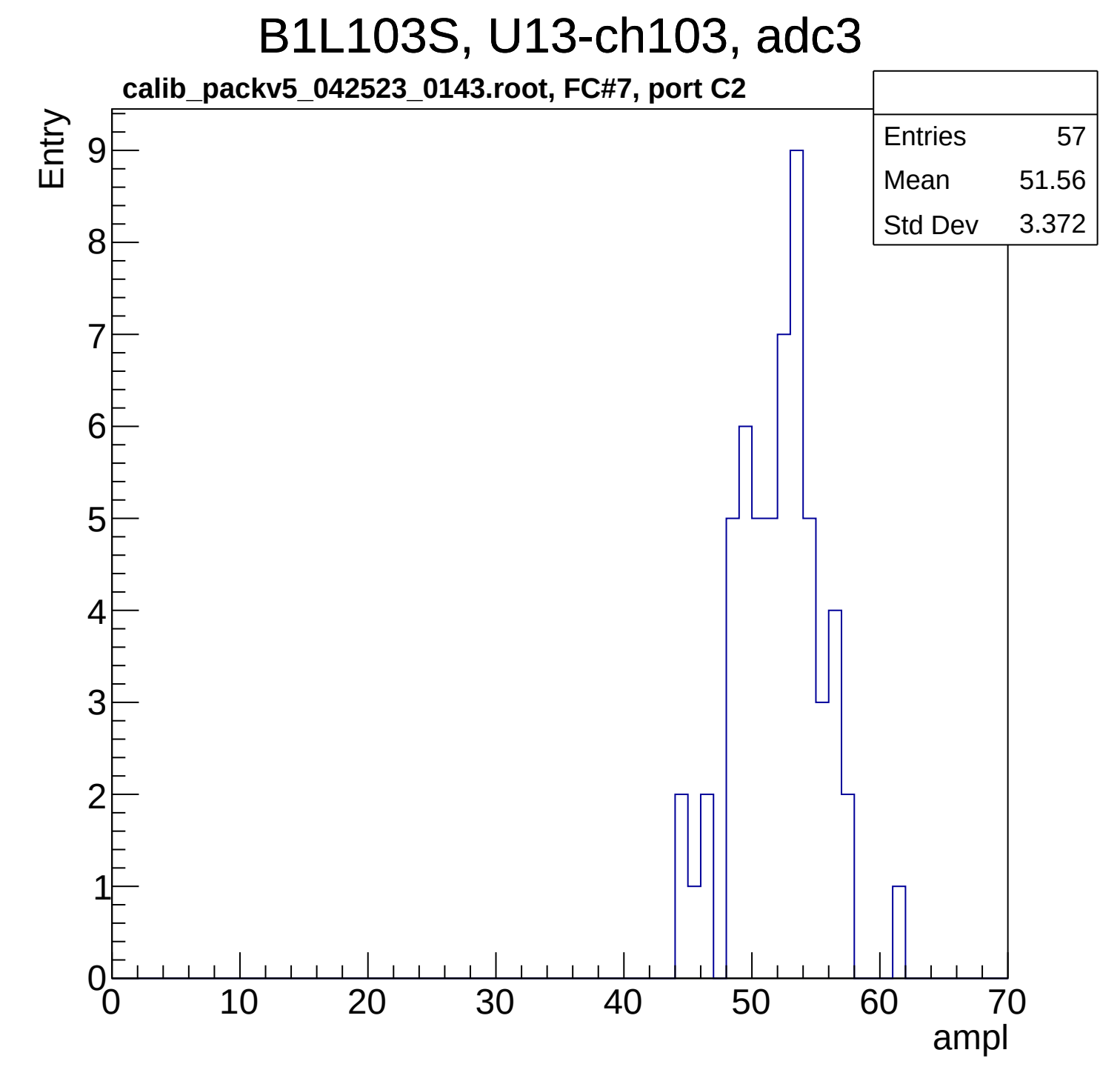
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch103, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 51.56
Std Dev 3.372
Entry
ampl
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3
4
5
6
7
8
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0
10
20
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60
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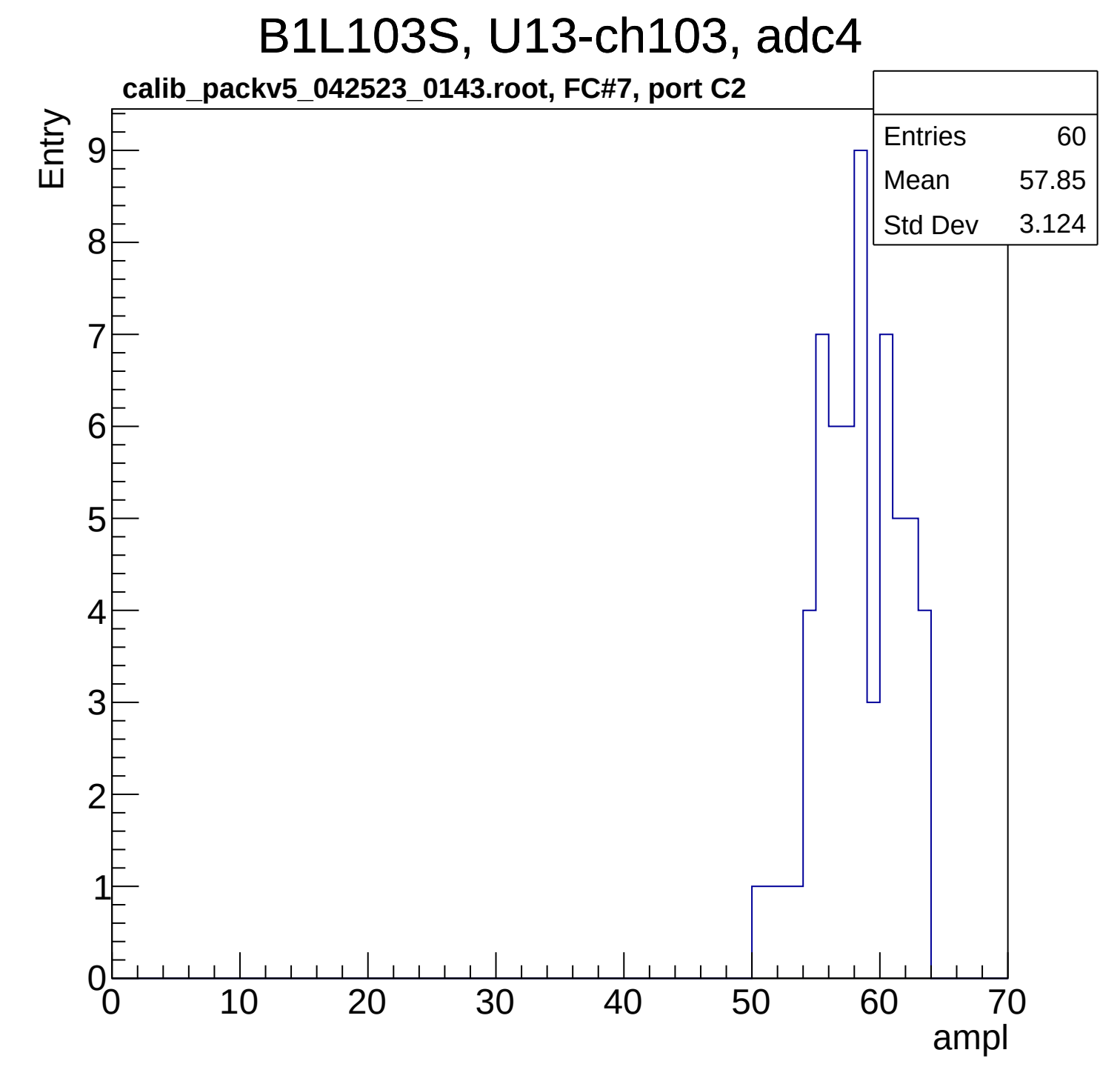
B1L103S, U13-ch103, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 57.85
Std Dev 3.124
Entry
ampl
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4
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6
7
8
9
0
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40
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60
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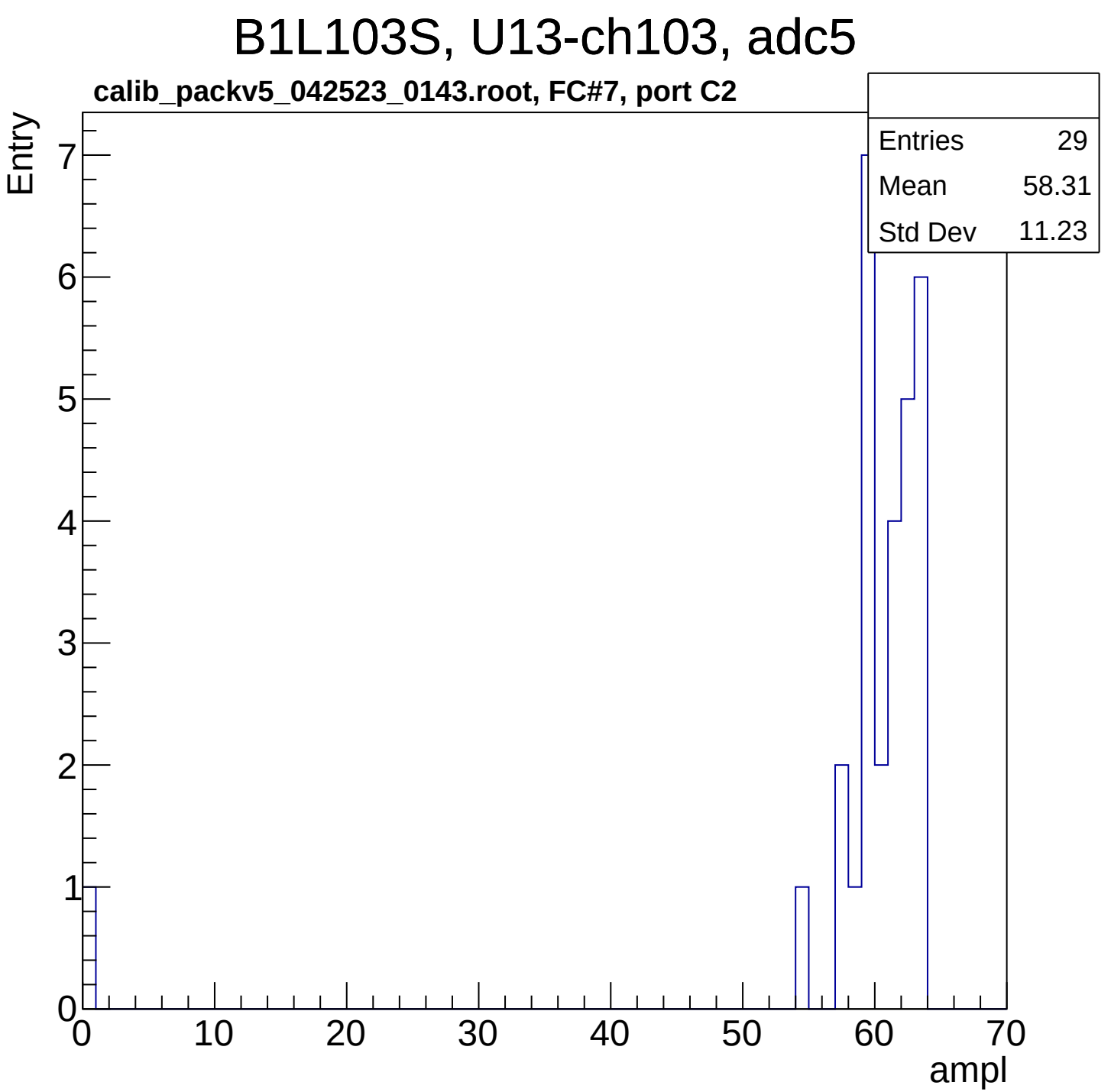
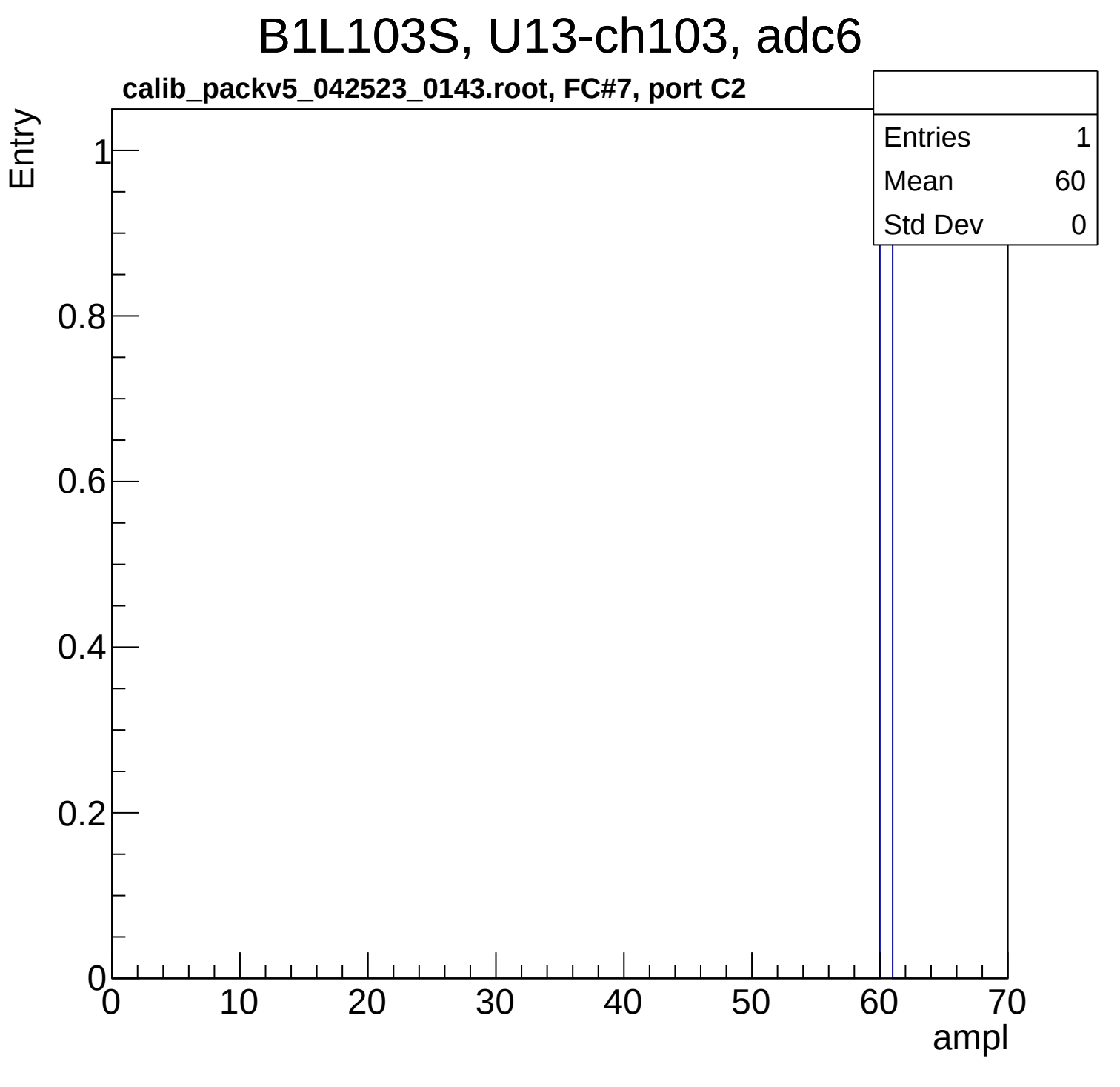

B1L103S, U13-ch103, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 58.31
Std Dev 11.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch103, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 60
Std Dev 0
Entry
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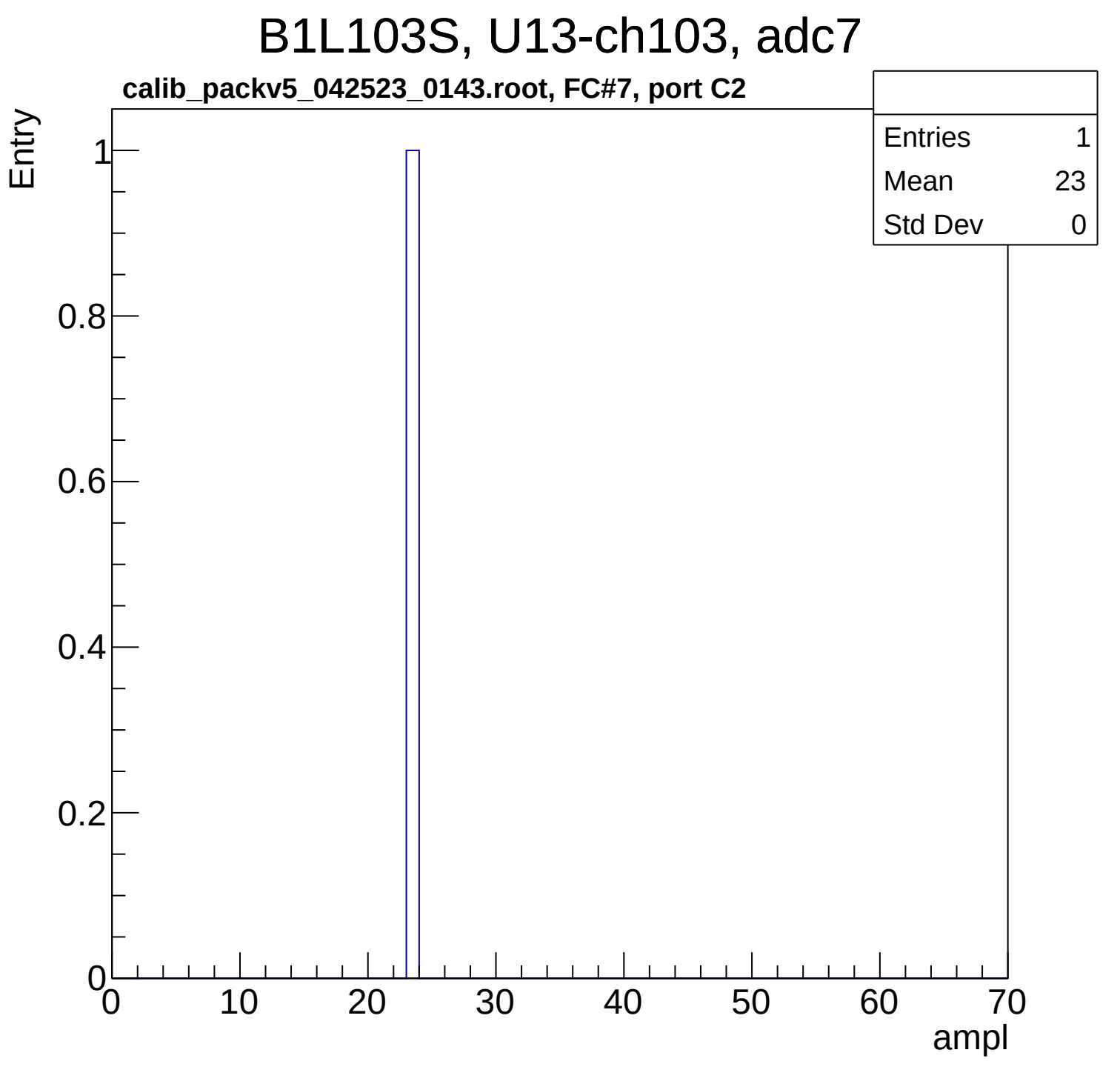
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch103, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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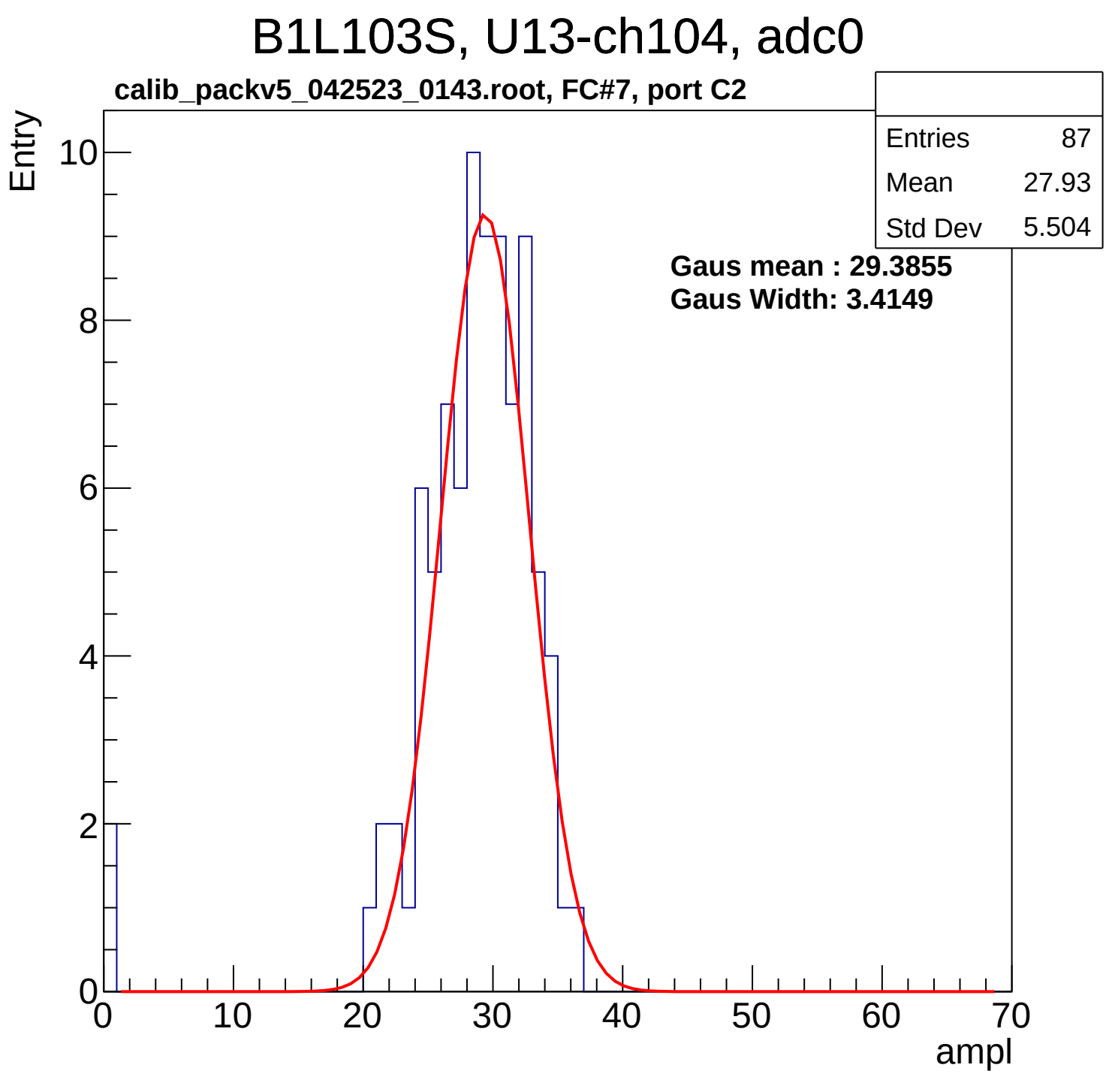
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch104, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 27.93
Std Dev 5.504
Gaus mean : 29.3855
Gaus Width: 3.4149
Entry
ampl
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8
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0
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40
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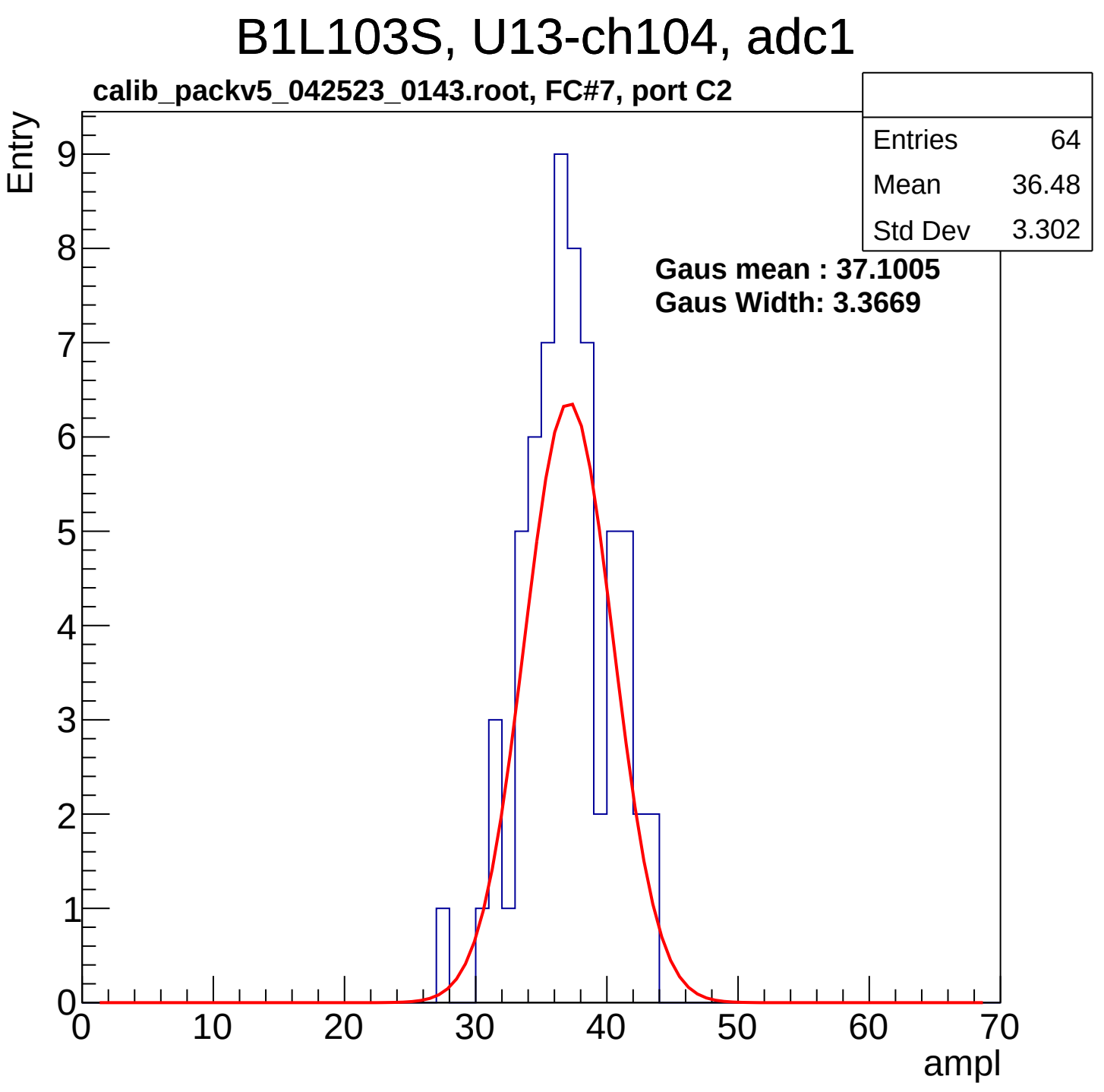
B1L103S, U13-ch104, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 36.48
Std Dev 3.302
Gaus mean : 37.1005
Gaus Width: 3.3669
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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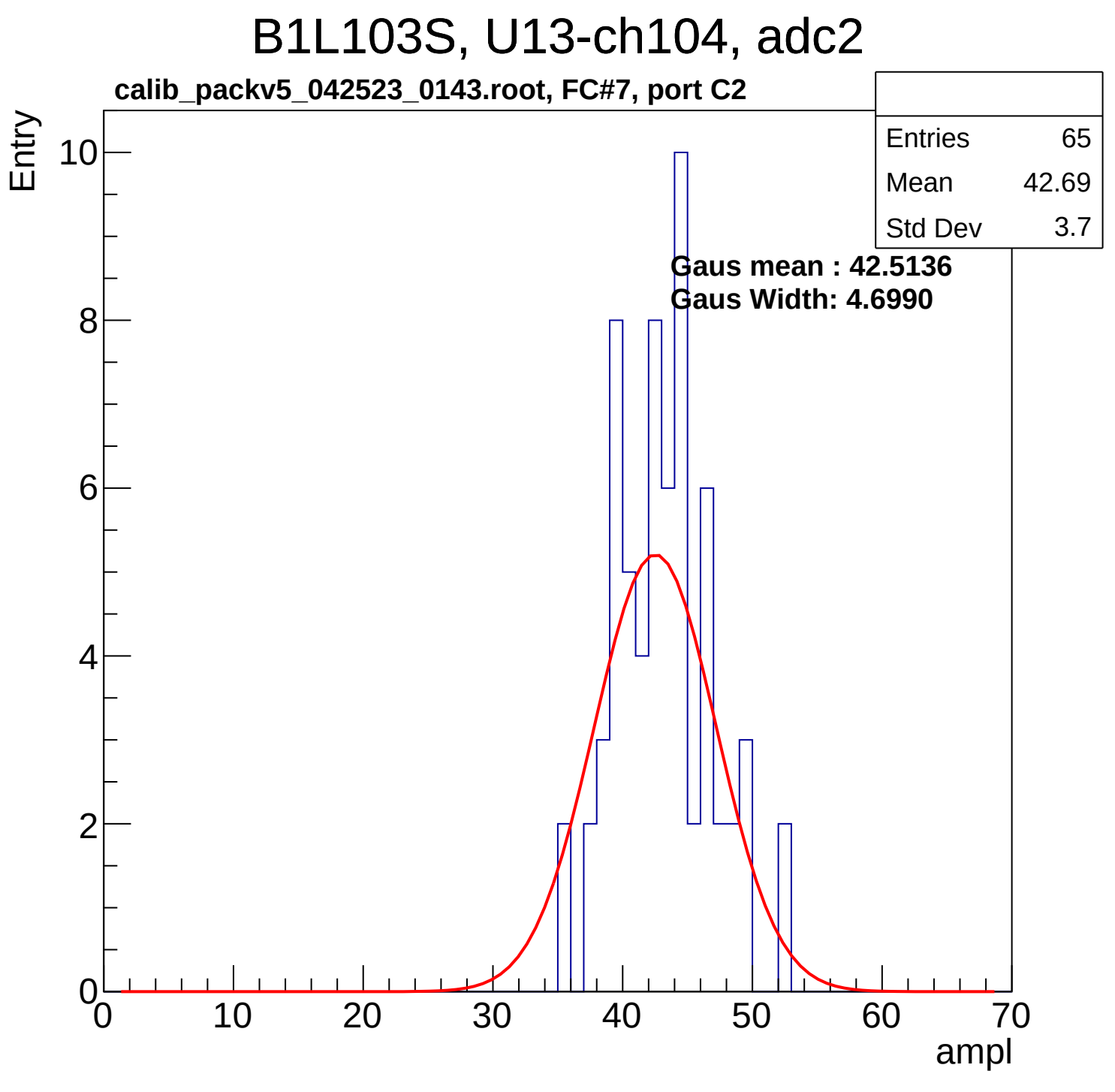
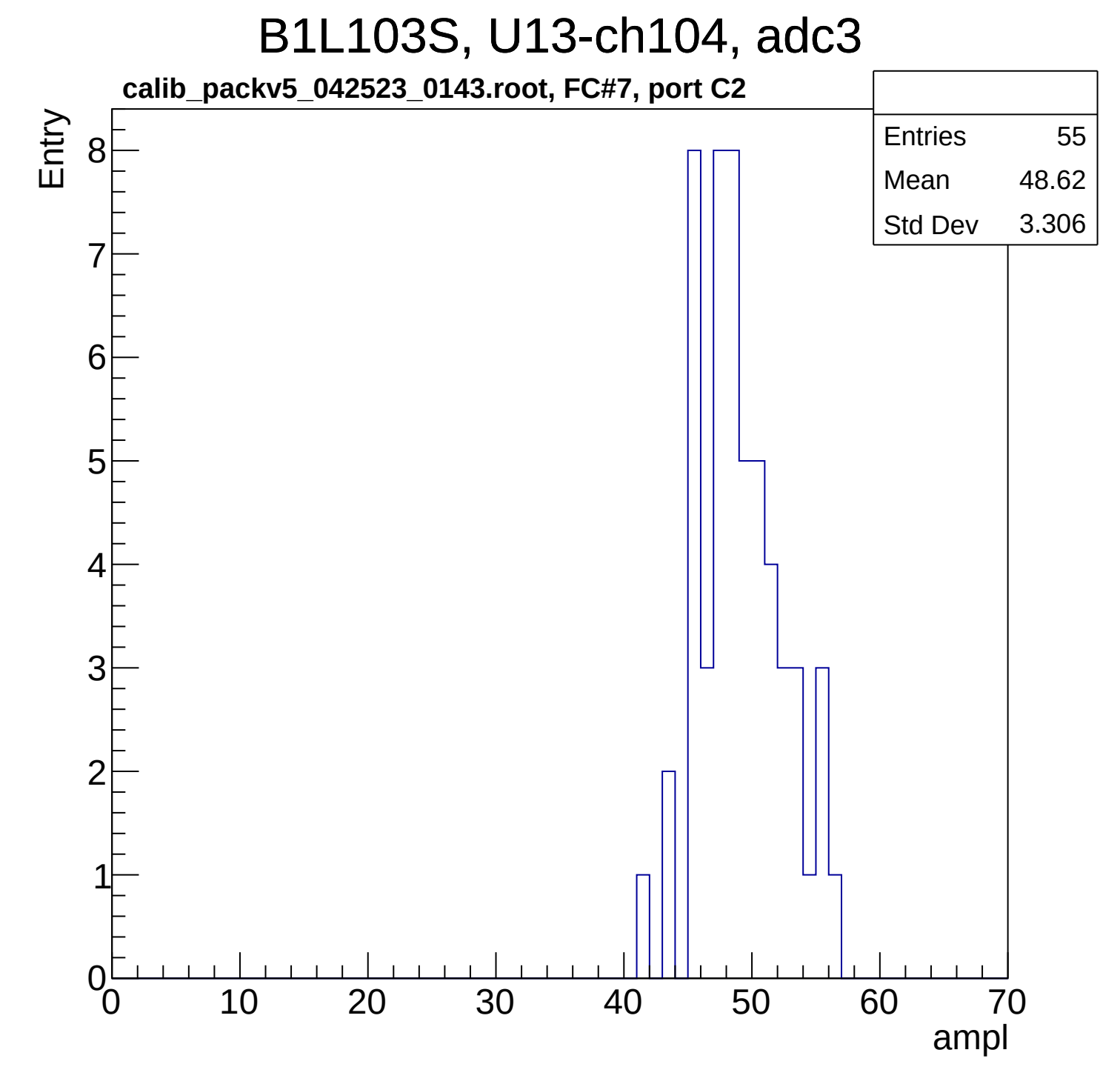

B1L103S, U13-ch104, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.69
Std Dev 3.7
Gaus mean : 42.5136
Gaus Width: 4.6990
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch104, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 48.62
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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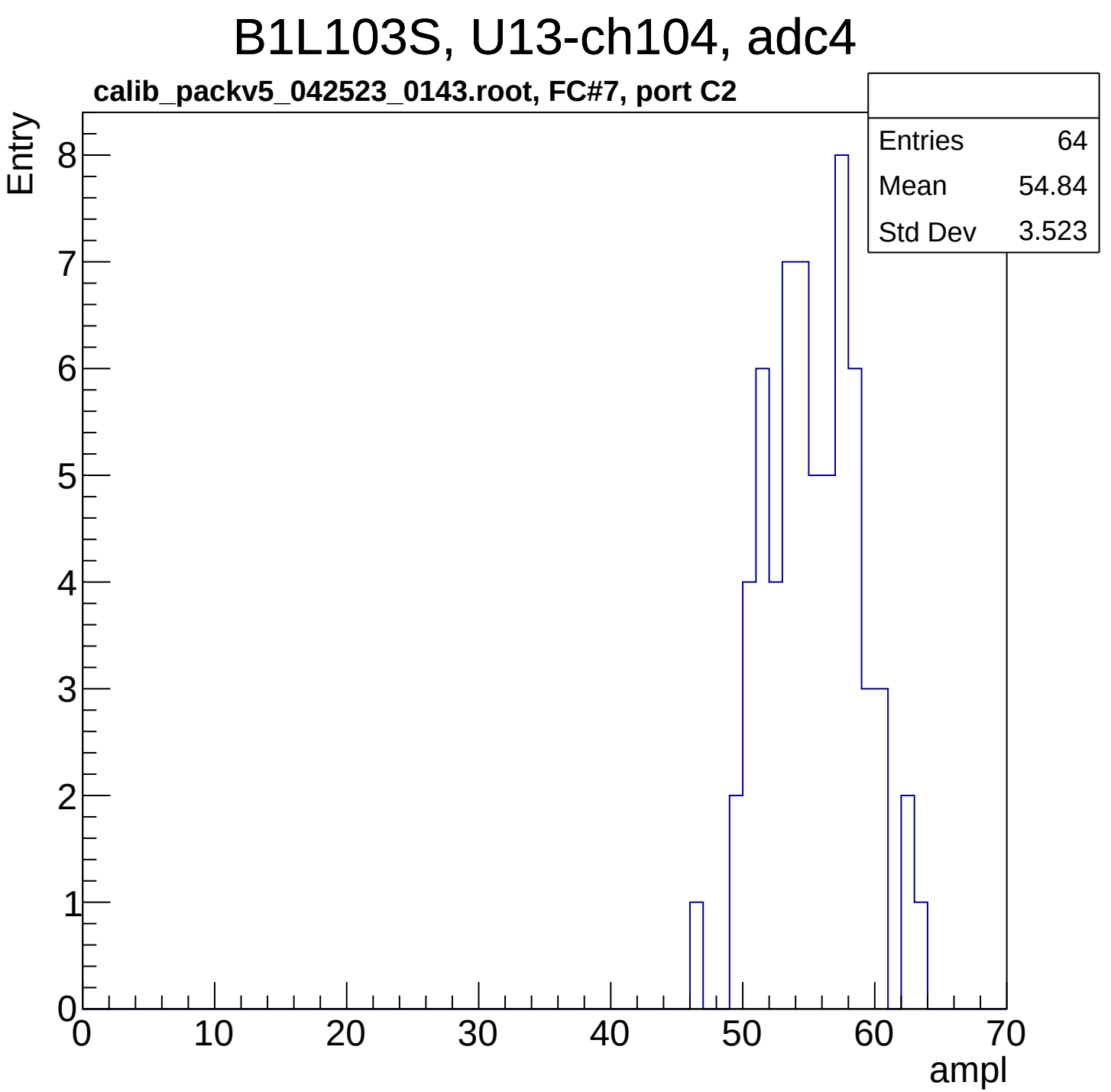
B1L103S, U13-ch104, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 54.84
Std Dev 3.523
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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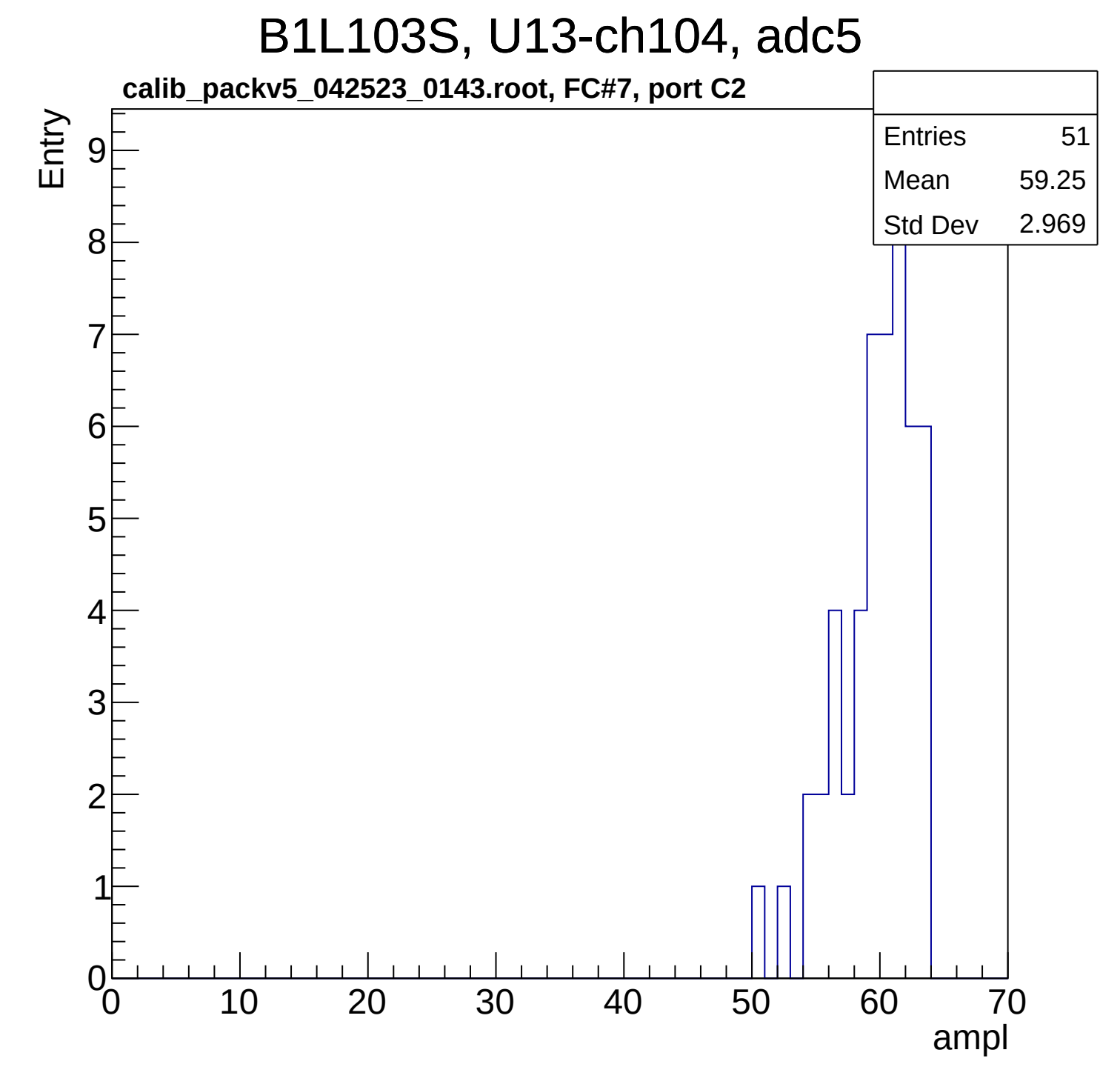
B1L103S, U13-ch104, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 59.25
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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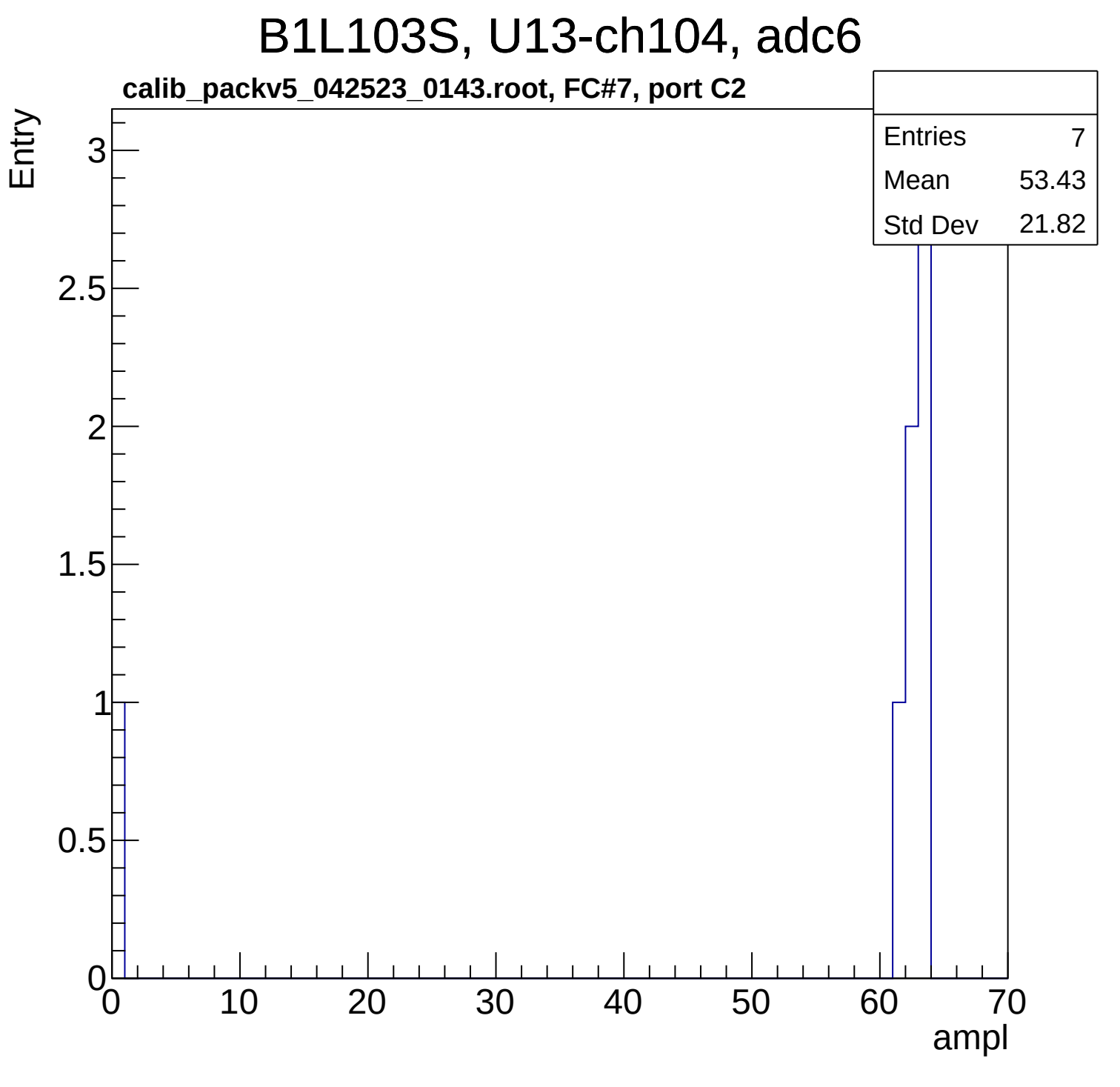
B1L103S, U13-ch104, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 53.43
Std Dev 21.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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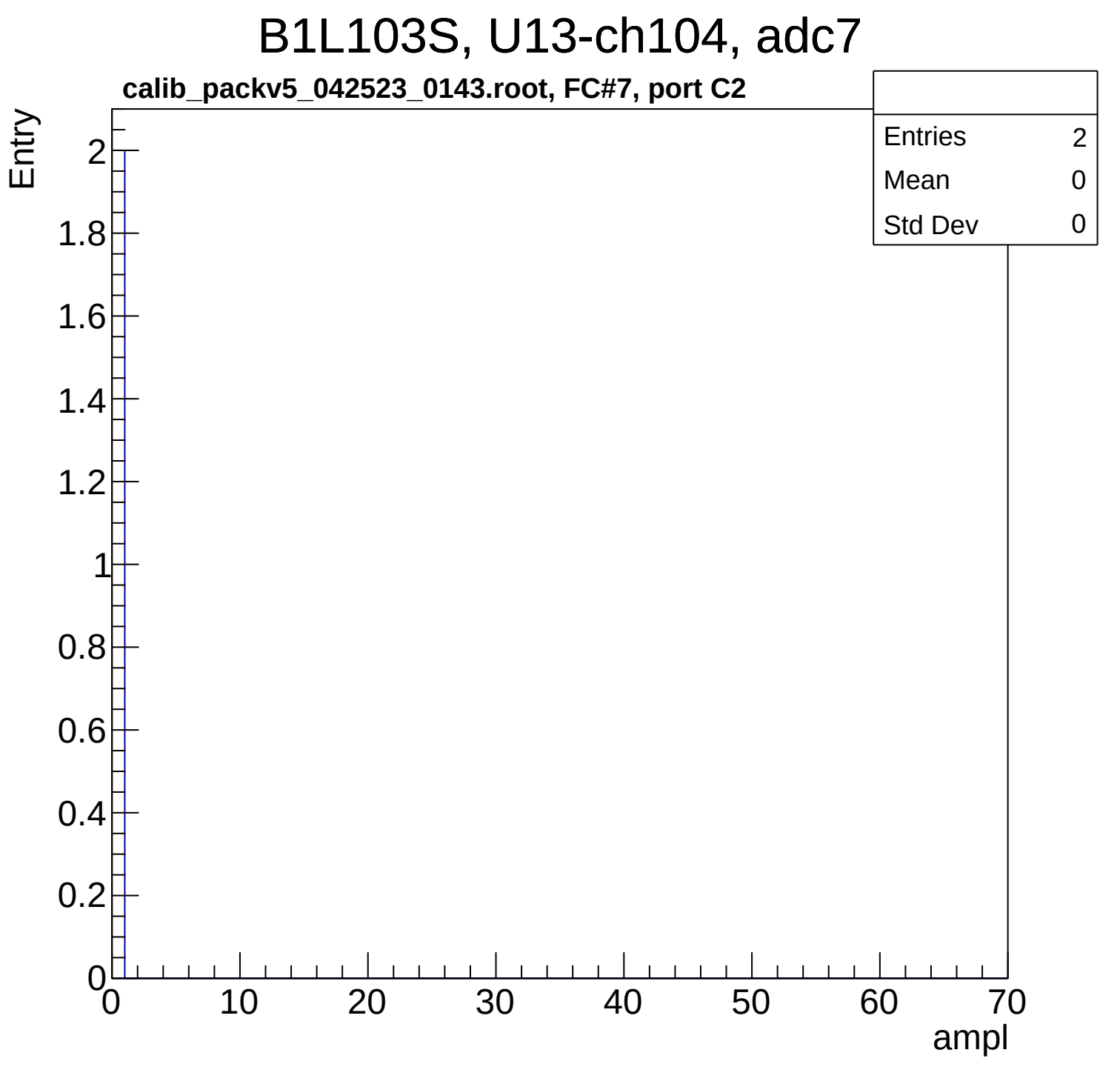
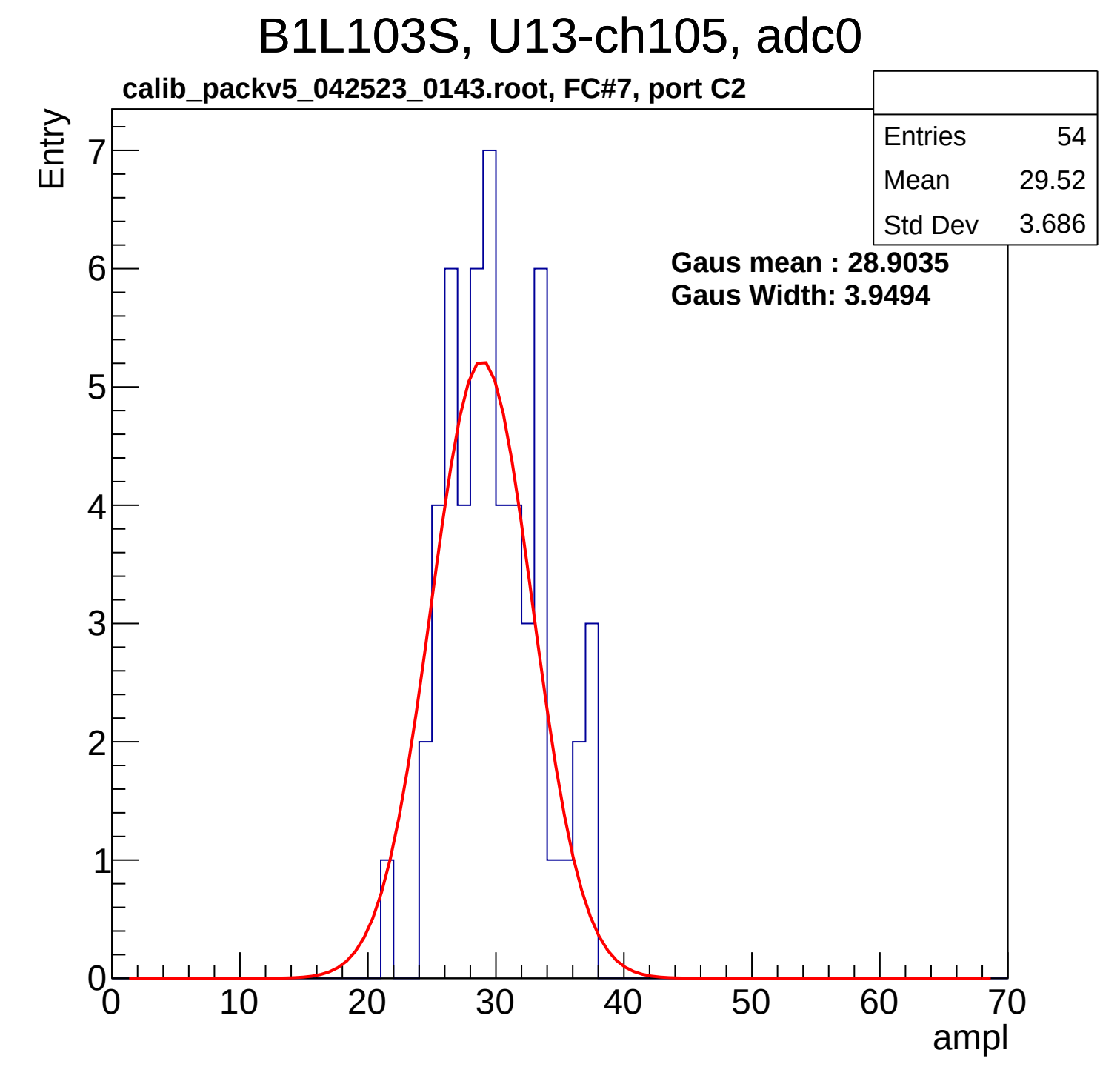

B1L103S, U13-ch104, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch105, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 29.52
Std Dev 3.686
Gaus mean : 28.9035
Gaus Width: 3.9494
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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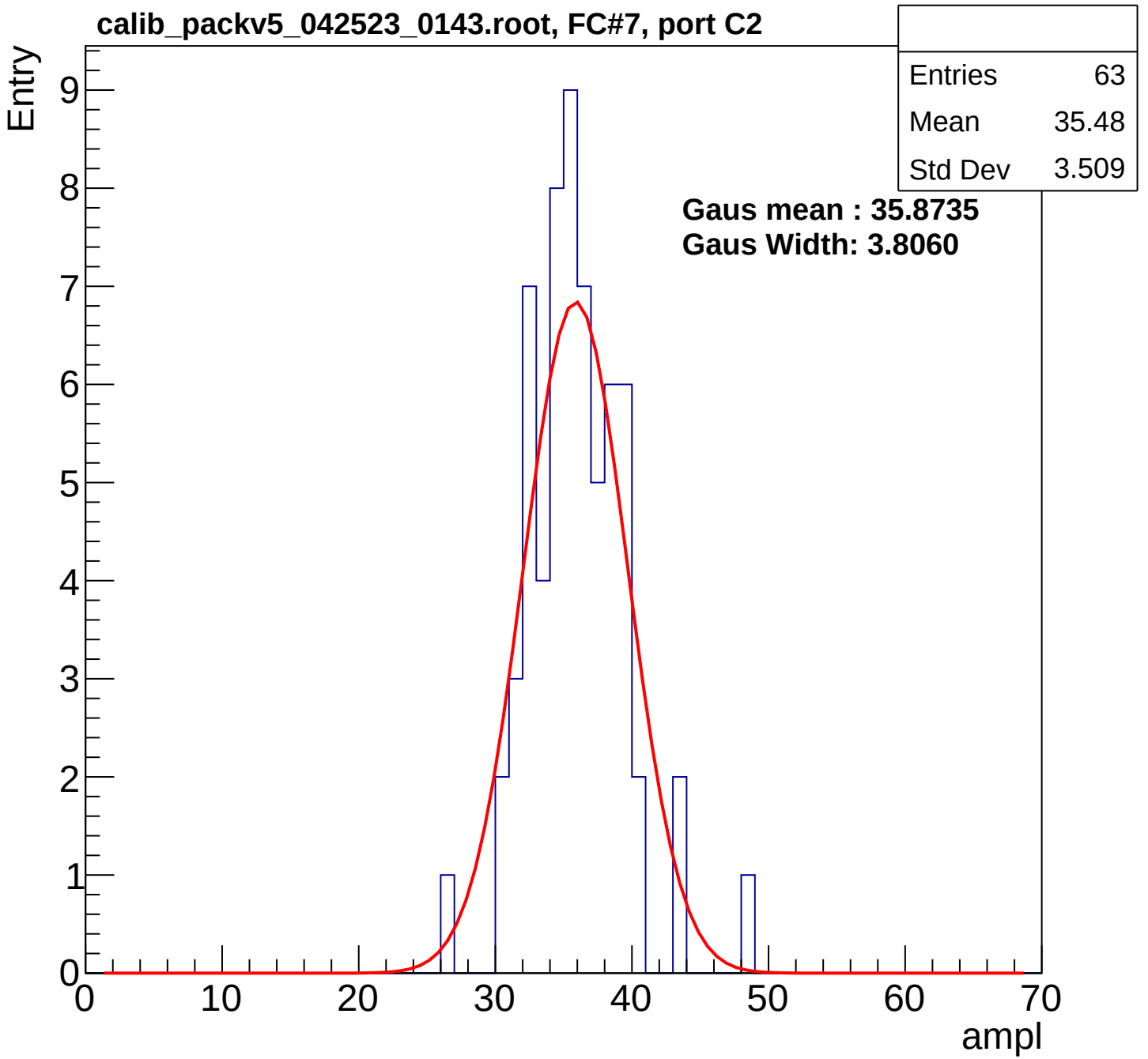
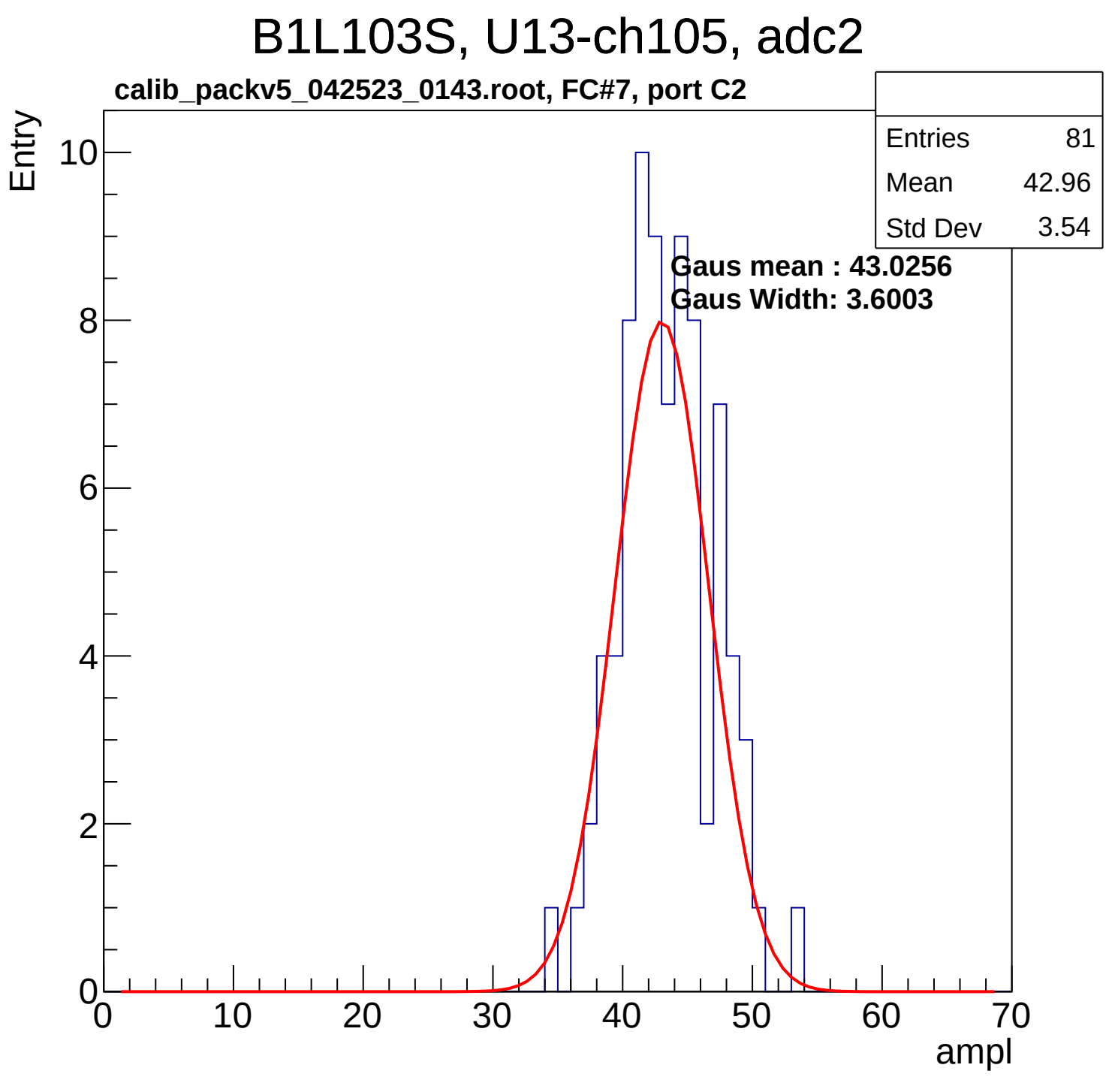

B1L103S, U13-ch105, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.48
Std Dev 3.509
Gaus mean : 35.8735
Gaus Width: 3.8060
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch105, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 42.96
Std Dev 3.54
Gaus mean : 43.0256
Gaus Width: 3.6003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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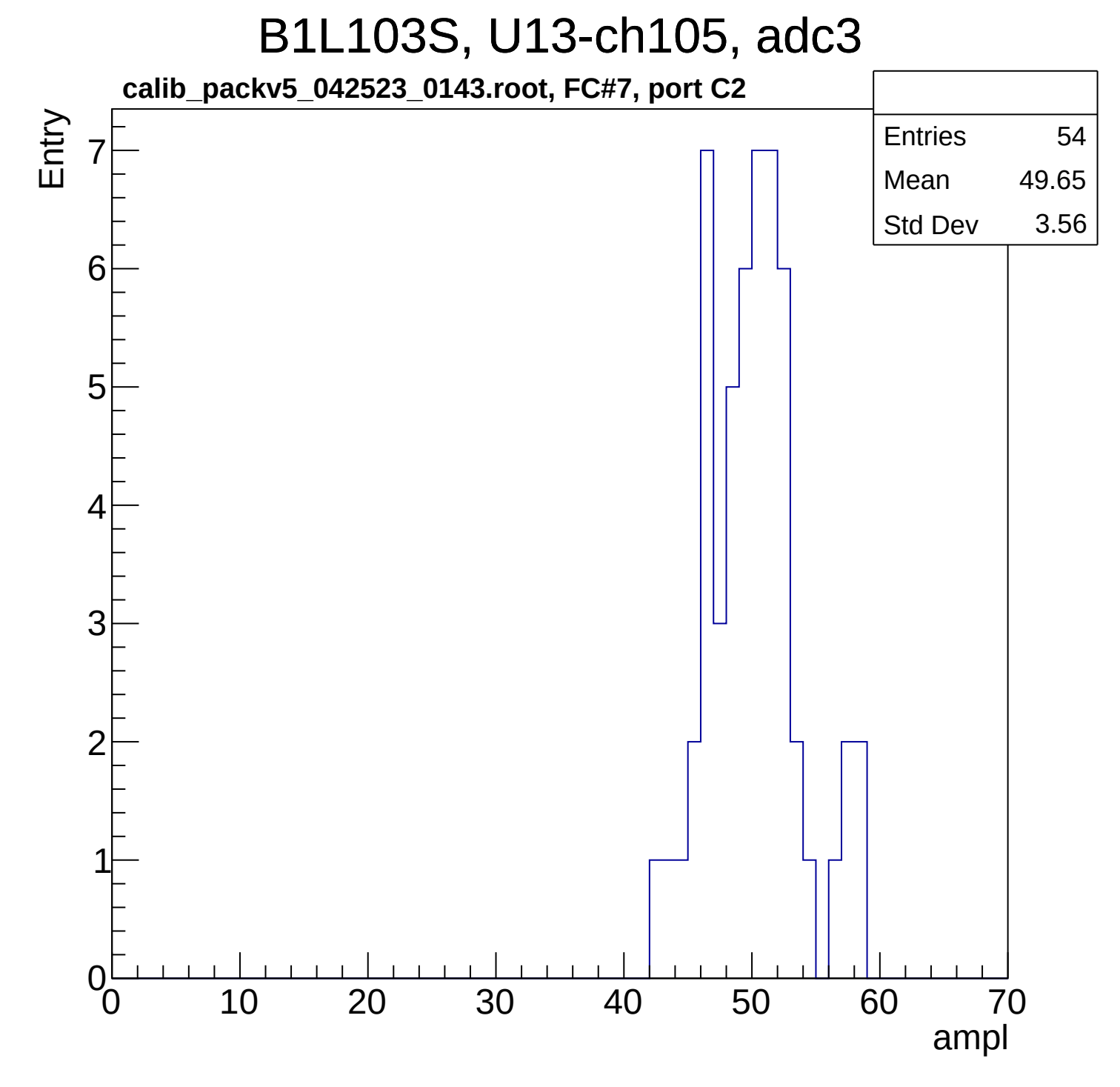
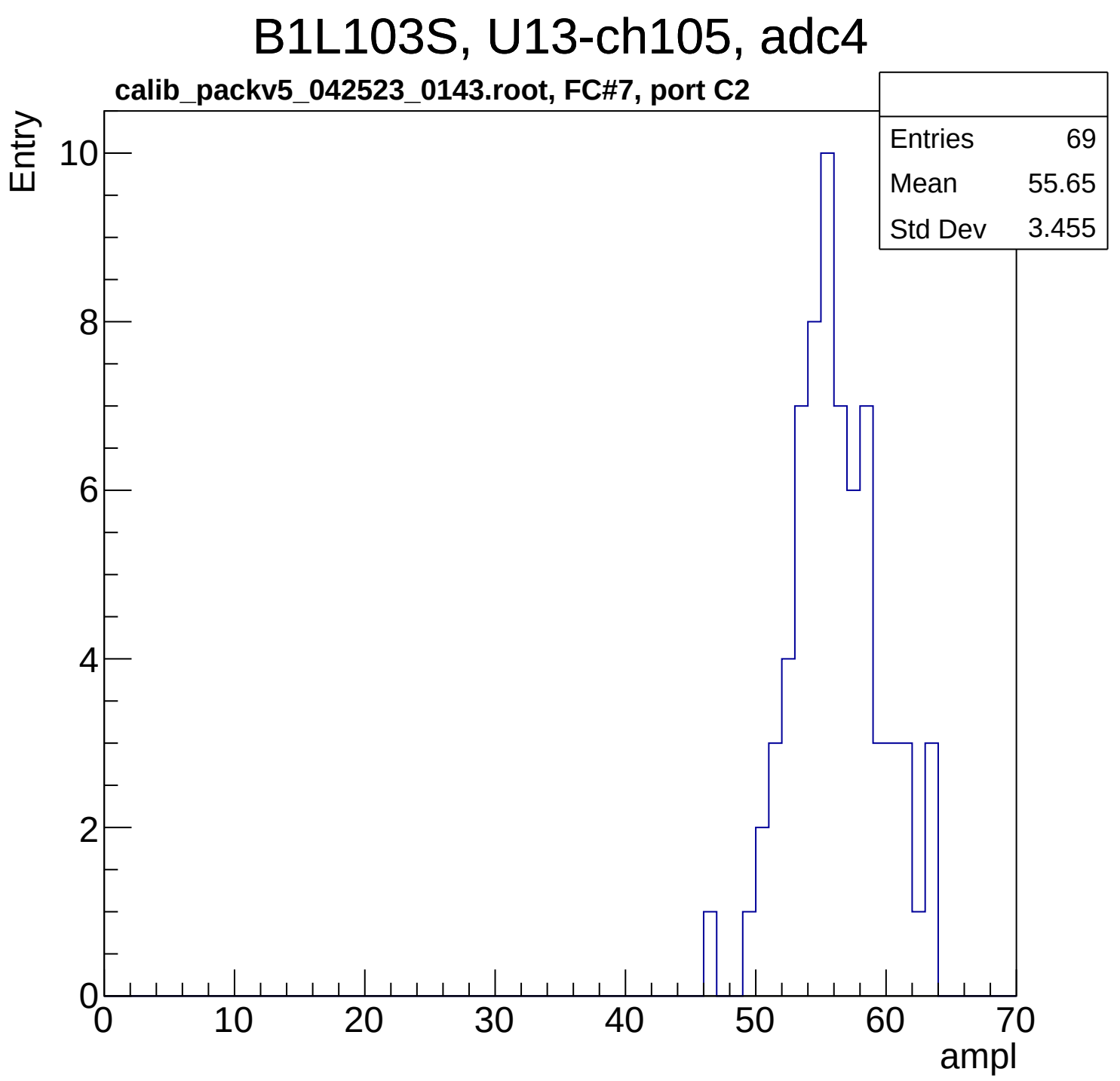
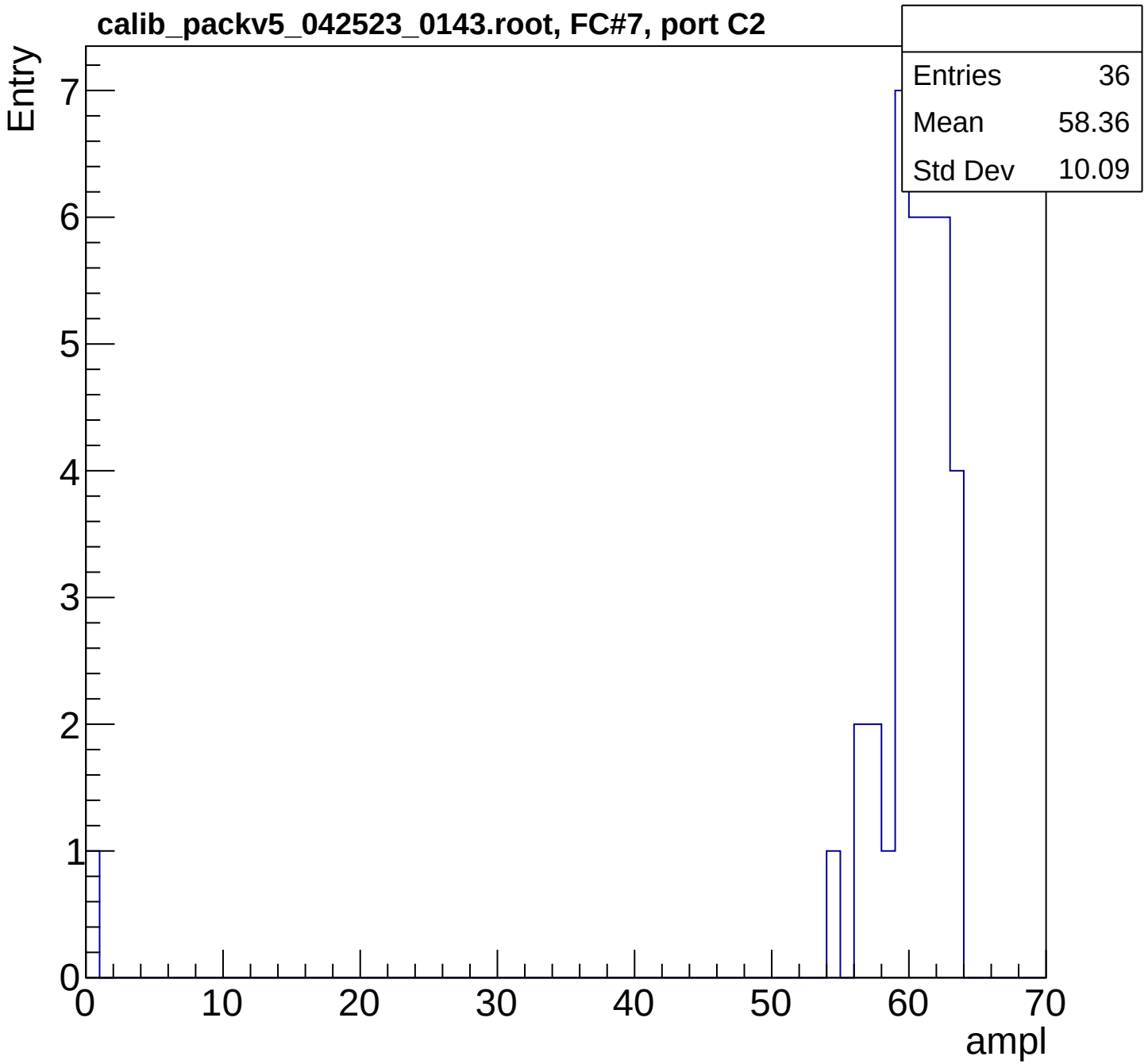
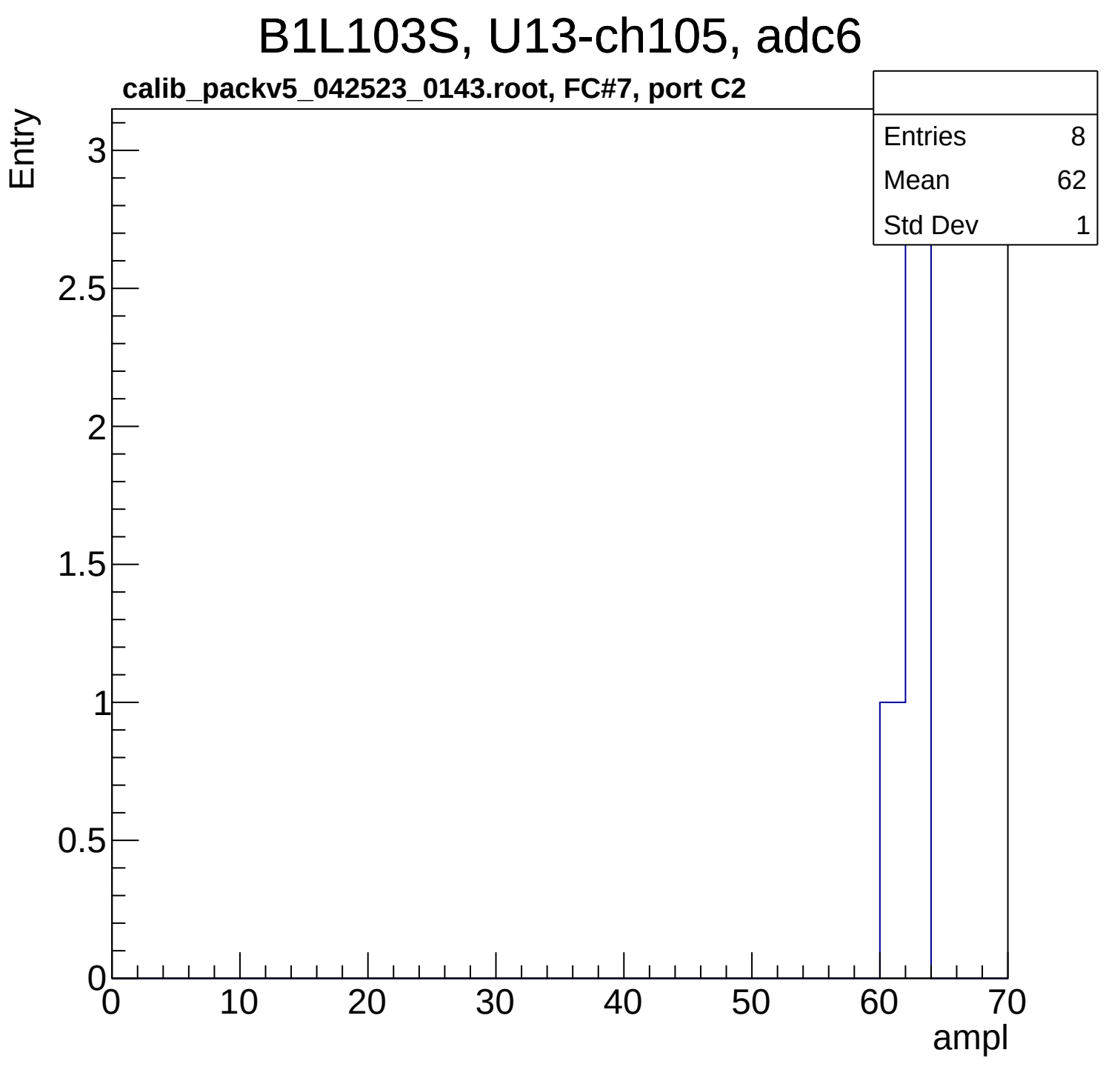
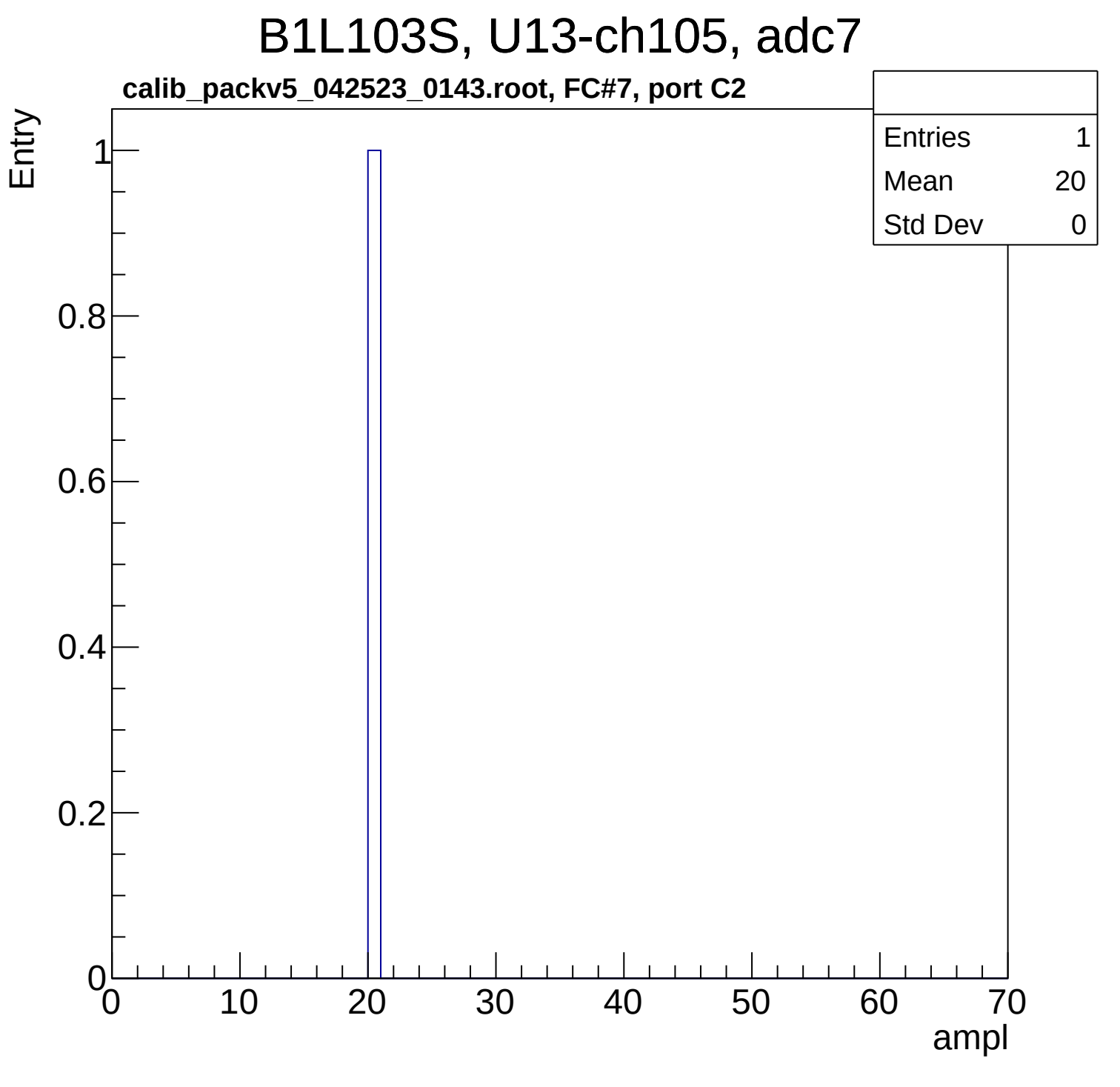
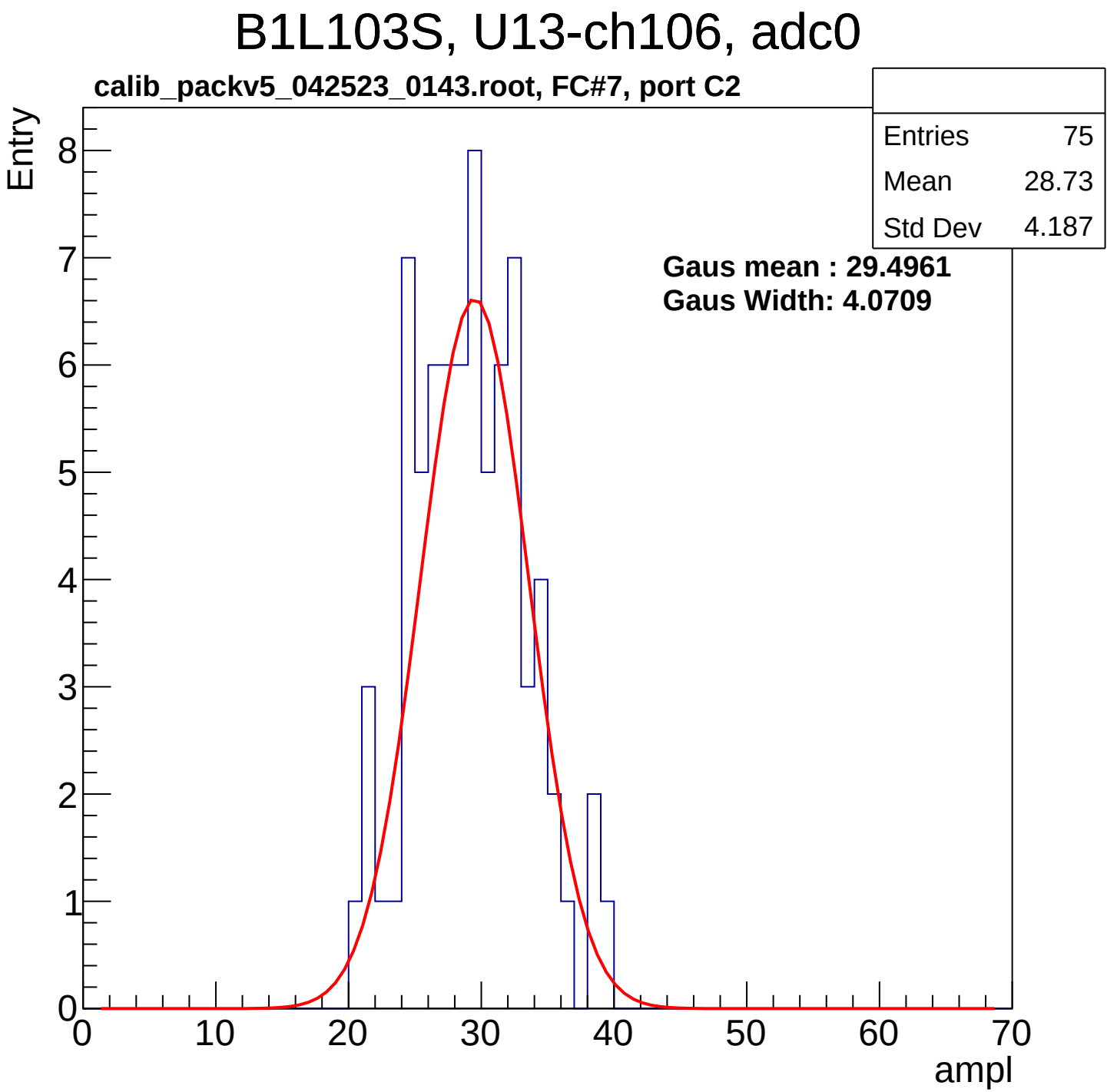

B1L103S, U13-ch105, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.65
Std Dev 3.56
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

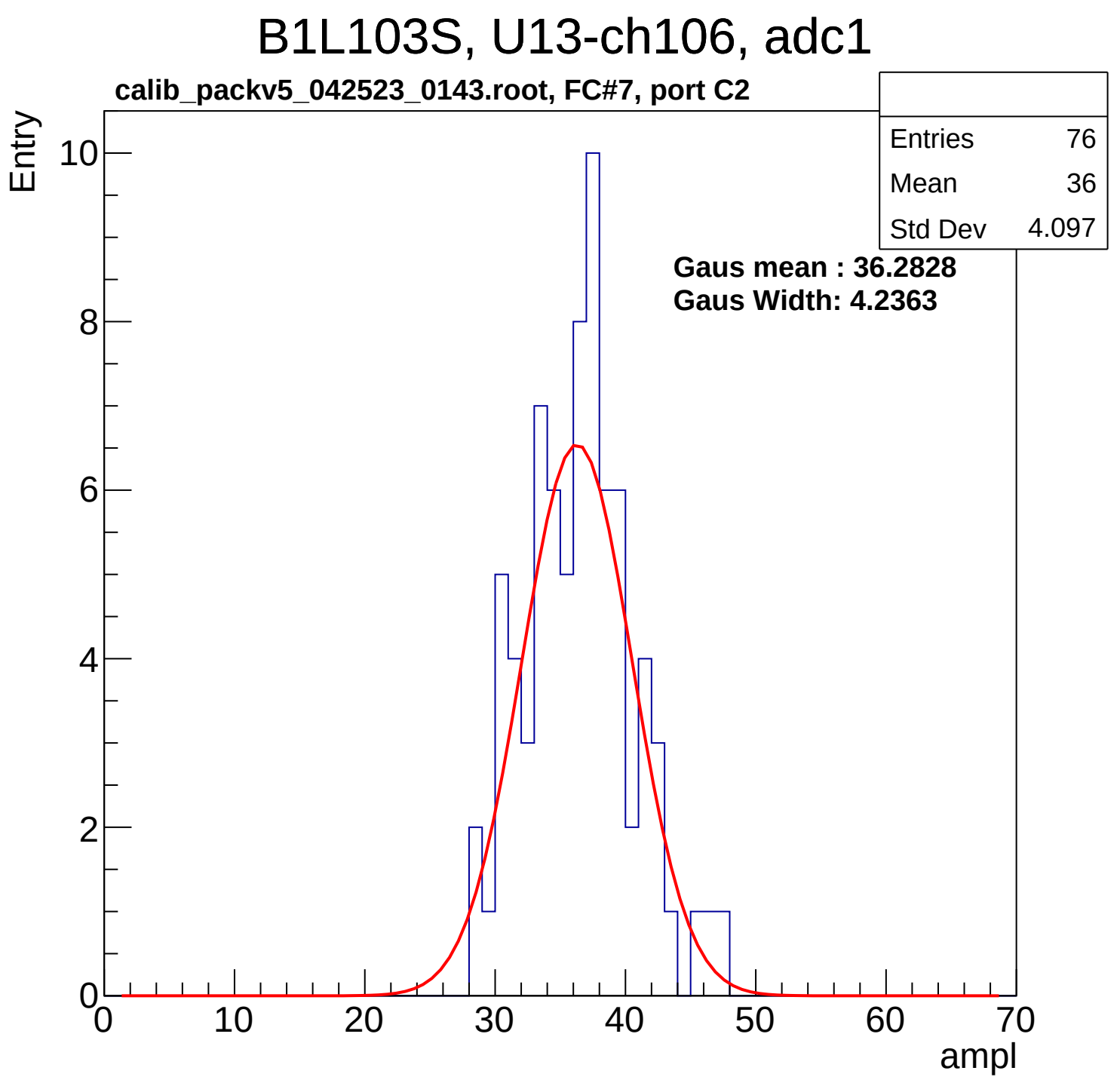

B1L103S, U13-ch105, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 55.65
Std Dev 3.455
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

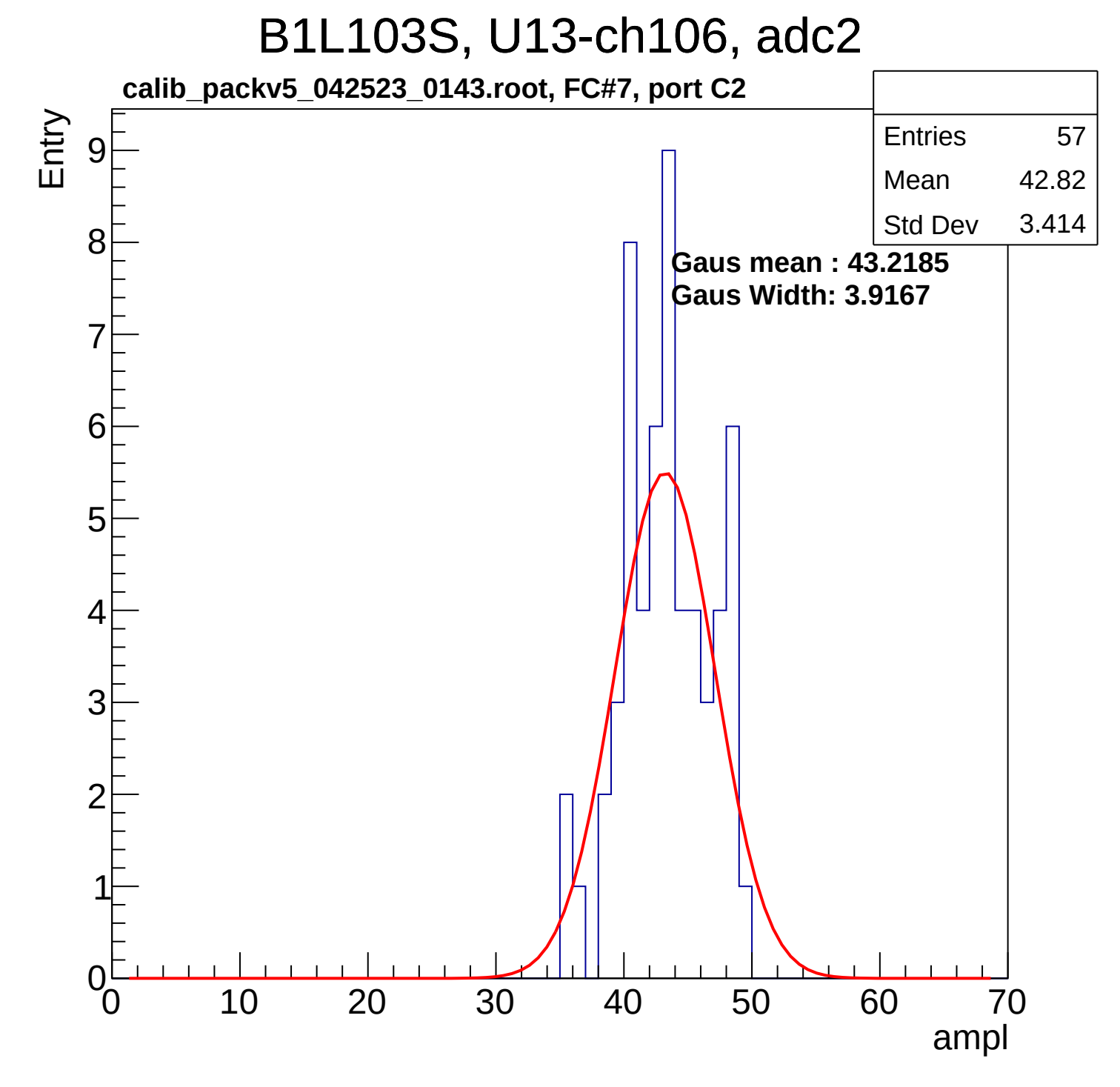

B1L103S, U13-ch105, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.36
Std Dev 10.09
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch105, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch105, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch106, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 28.73
Std Dev 4.187
Gaus mean : 29.4961
Gaus Width: 4.0709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch106, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36
Std Dev 4.097
Gaus mean : 36.2828
Gaus Width: 4.2363
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch106, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.82
Std Dev 3.414
Gaus mean : 43.2185
Gaus Width: 3.9167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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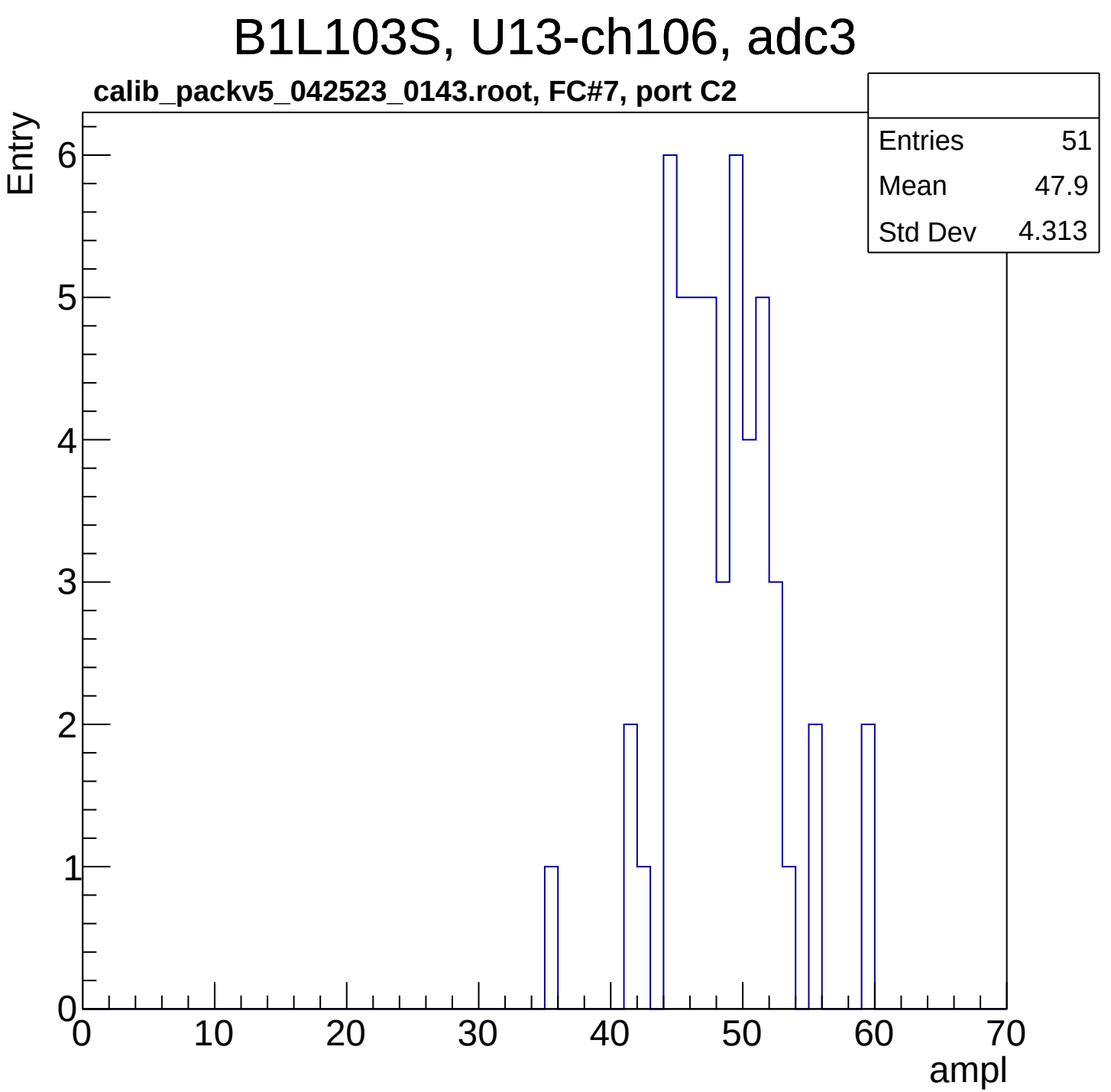
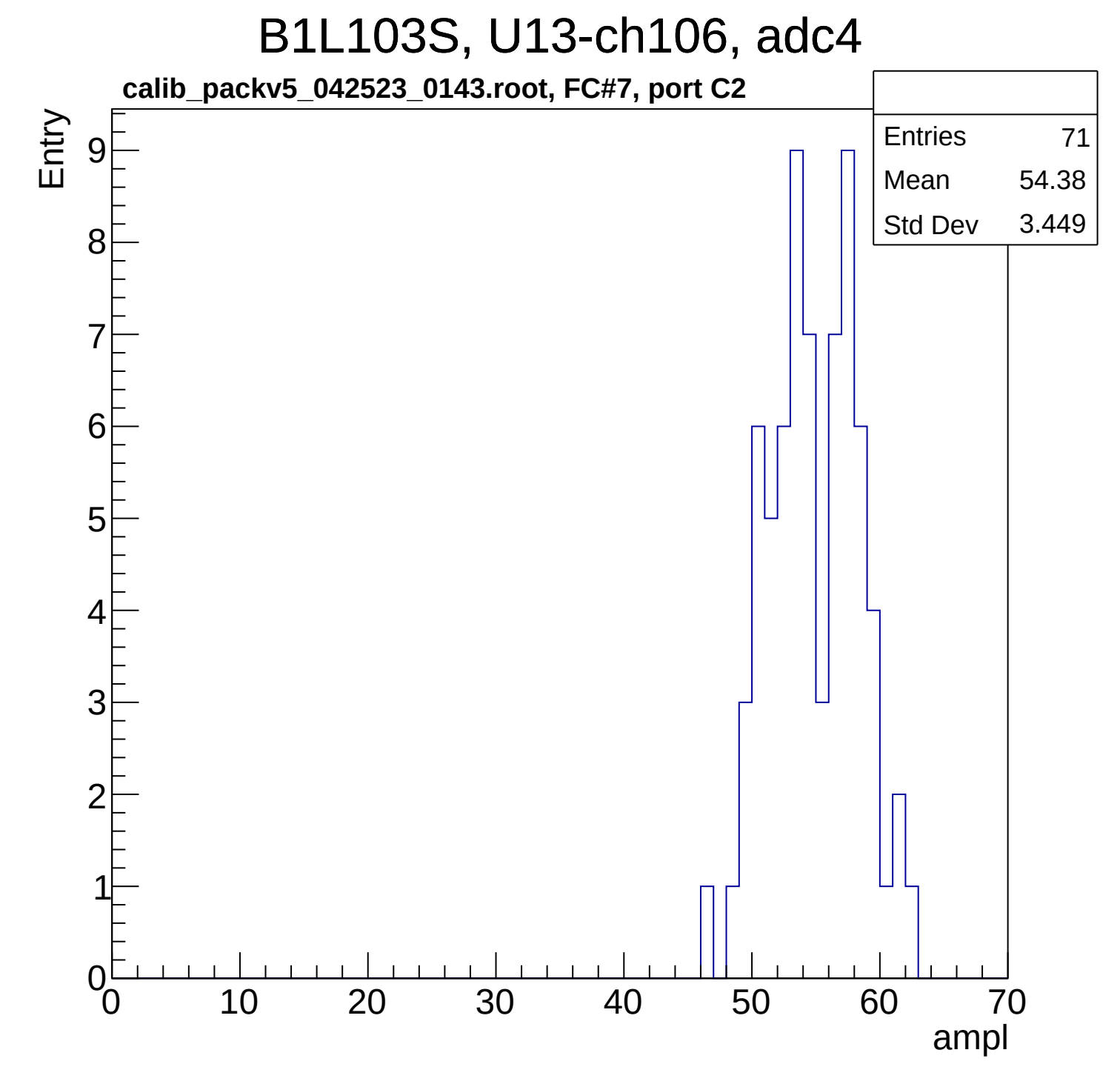
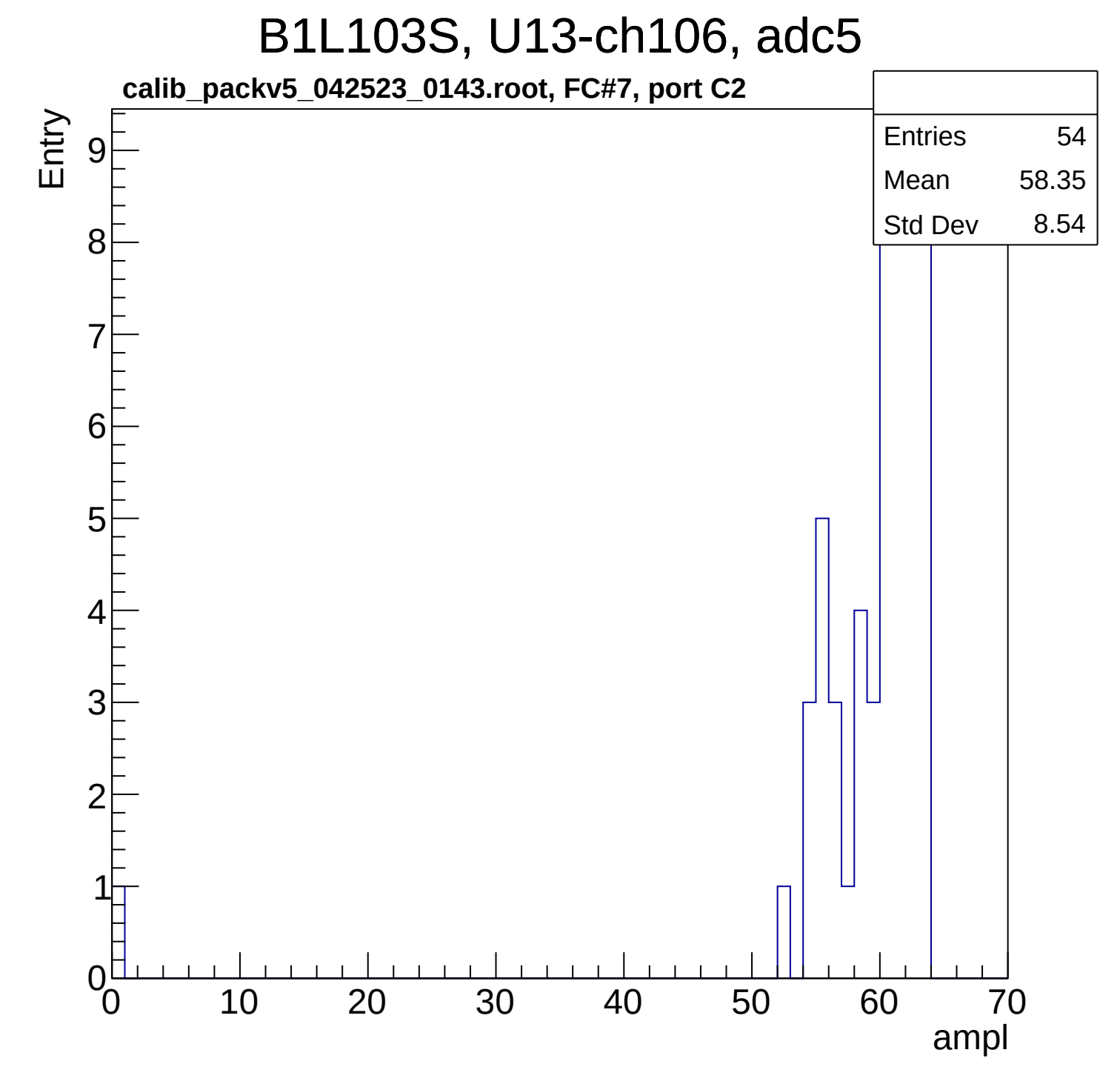

B1L103S, U13-ch106, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 47.9
Std Dev 4.313
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch106, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 54.38
Std Dev 3.449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch106, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.35
Std Dev 8.54
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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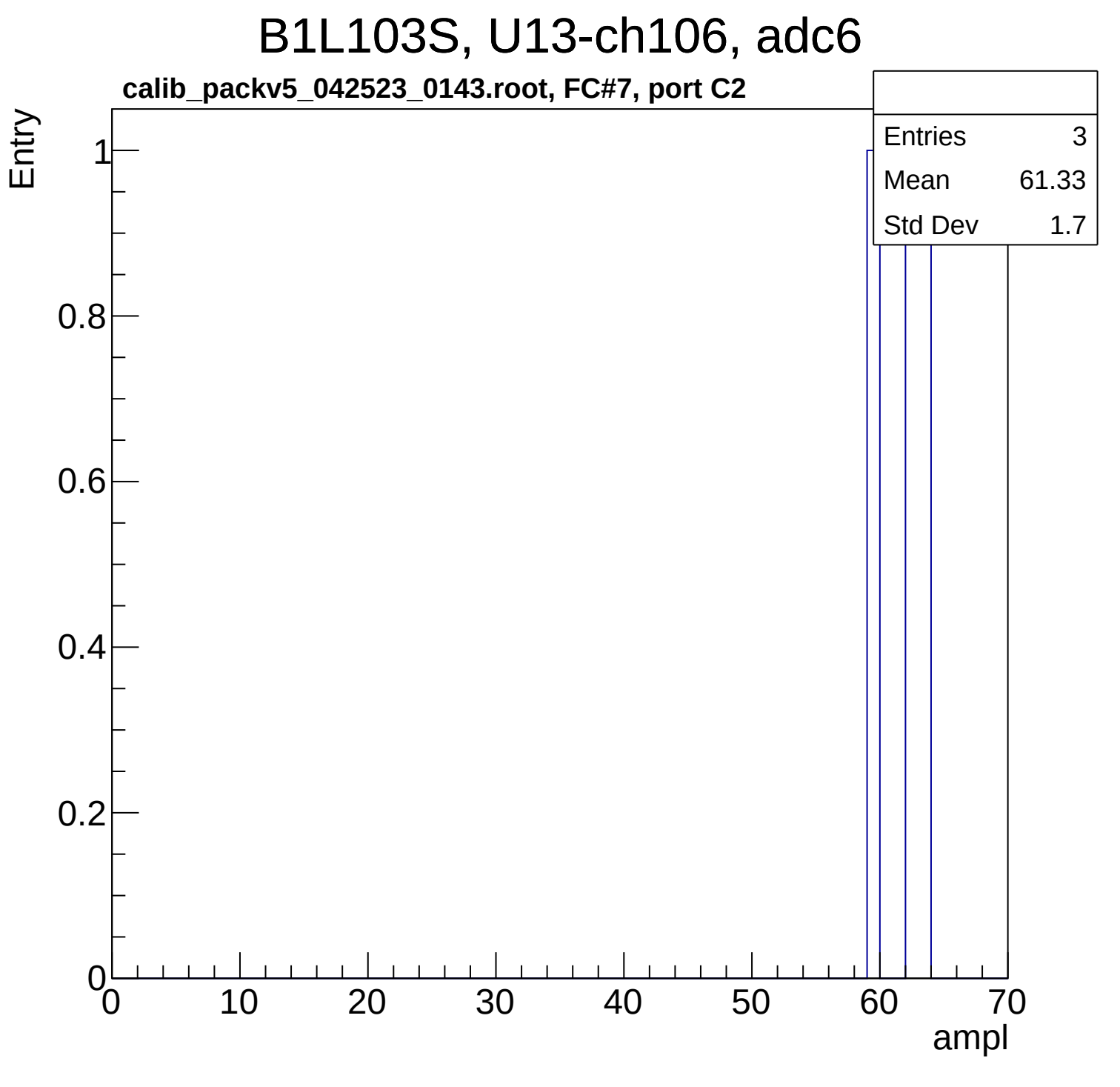
B1L103S, U13-ch106, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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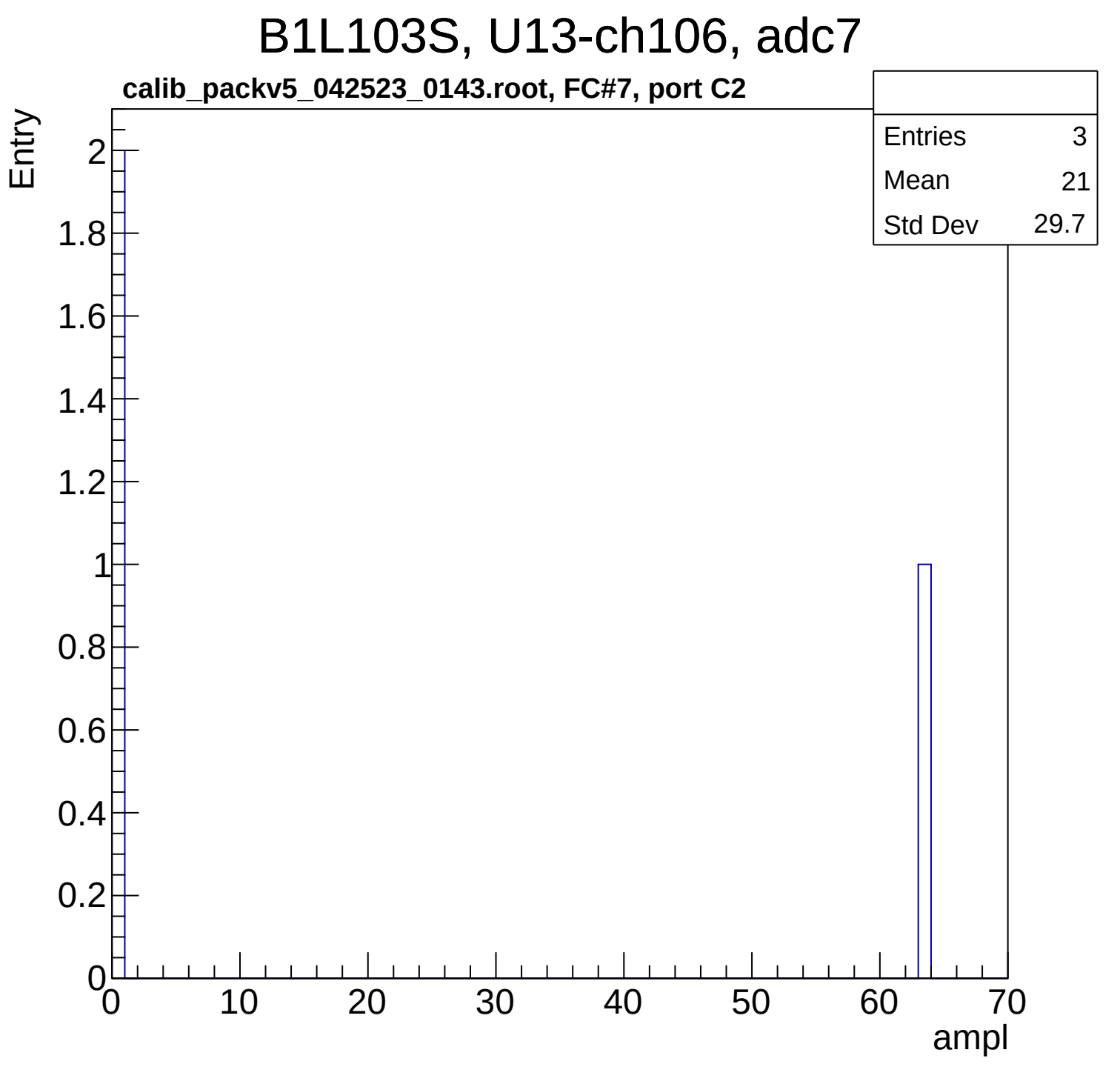
B1L103S, U13-ch106, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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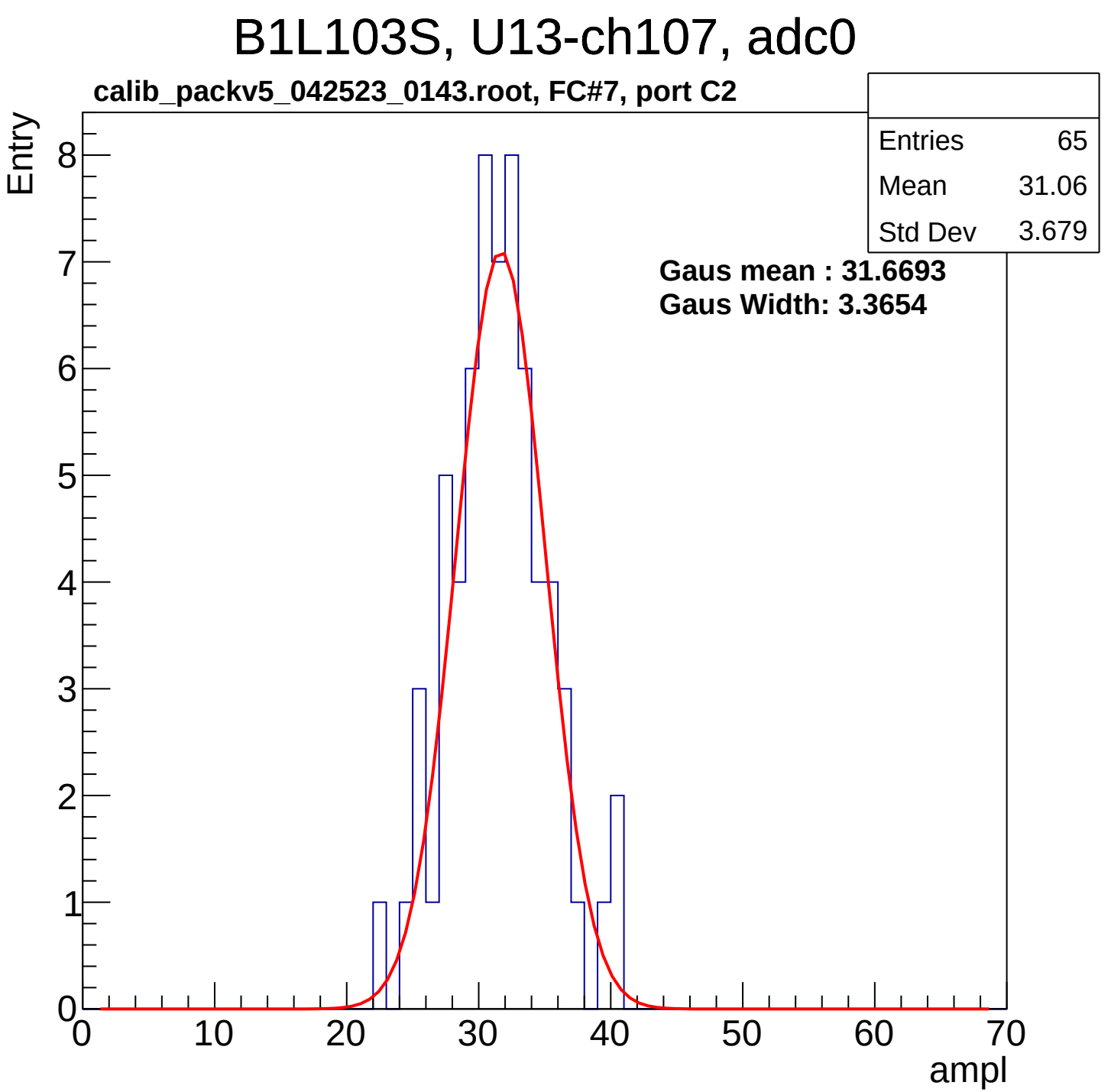
B1L103S, U13-ch107, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 31.06
Std Dev 3.679
Gaus mean : 31.6693
Gaus Width: 3.3654
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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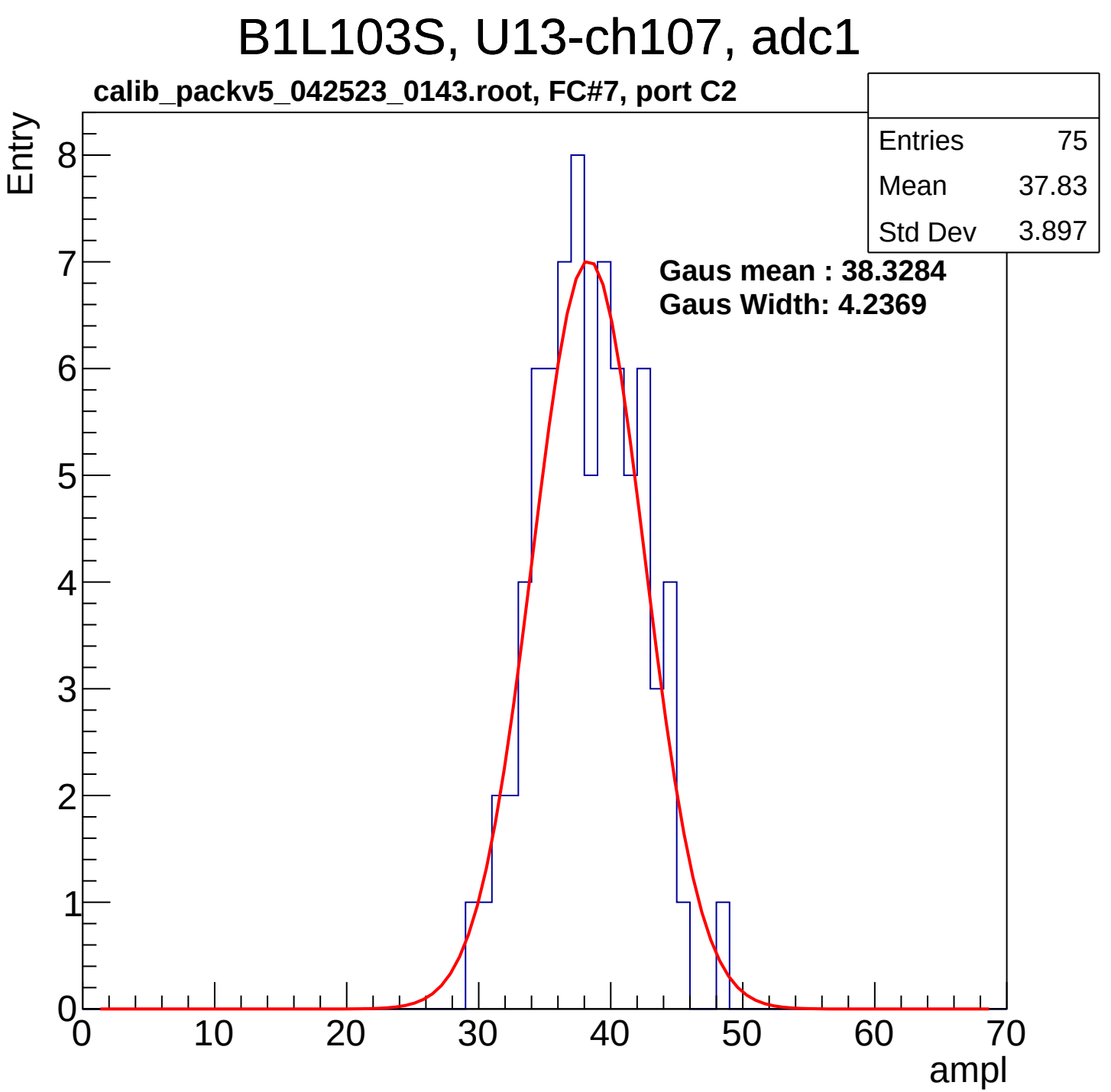
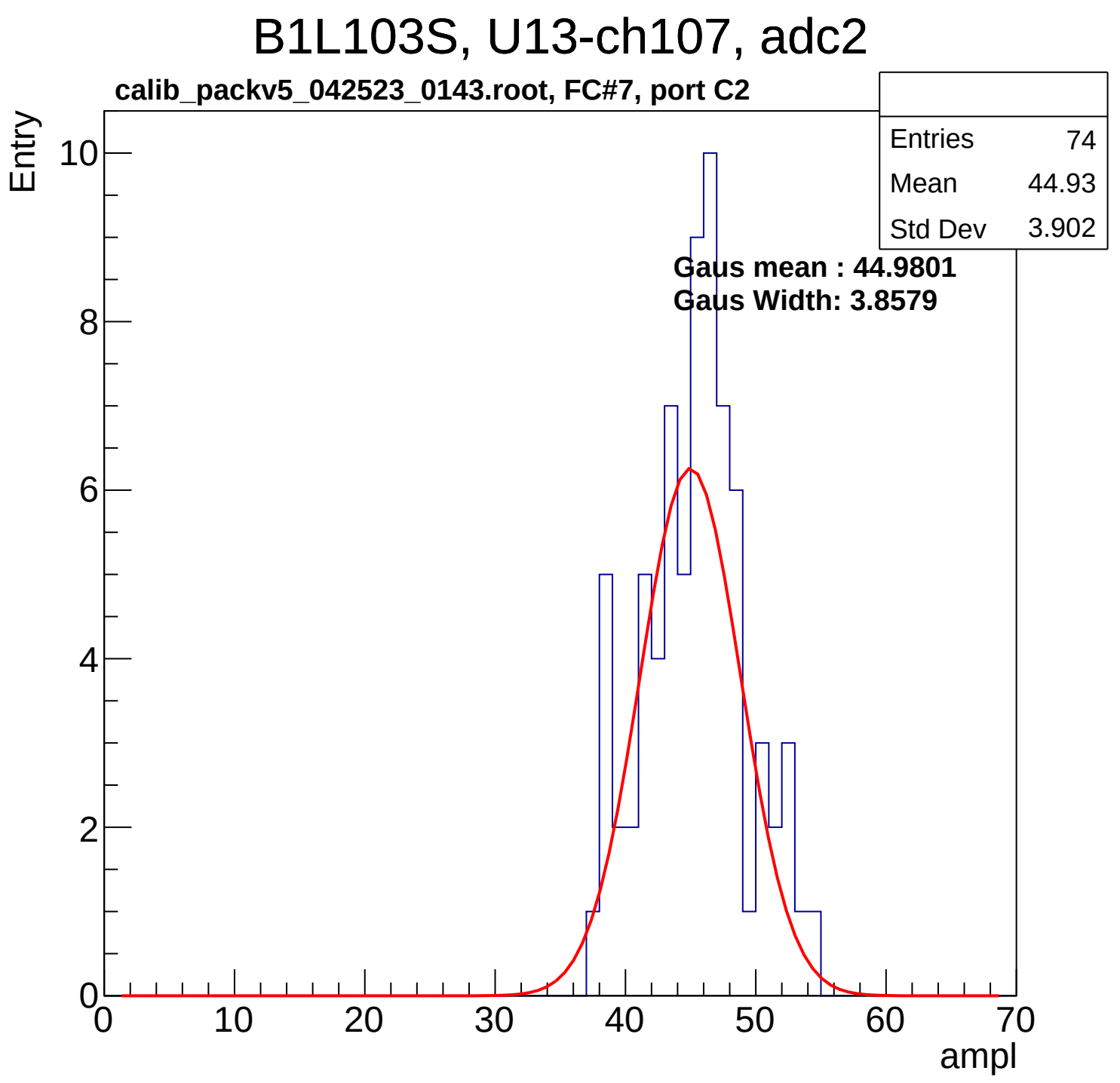
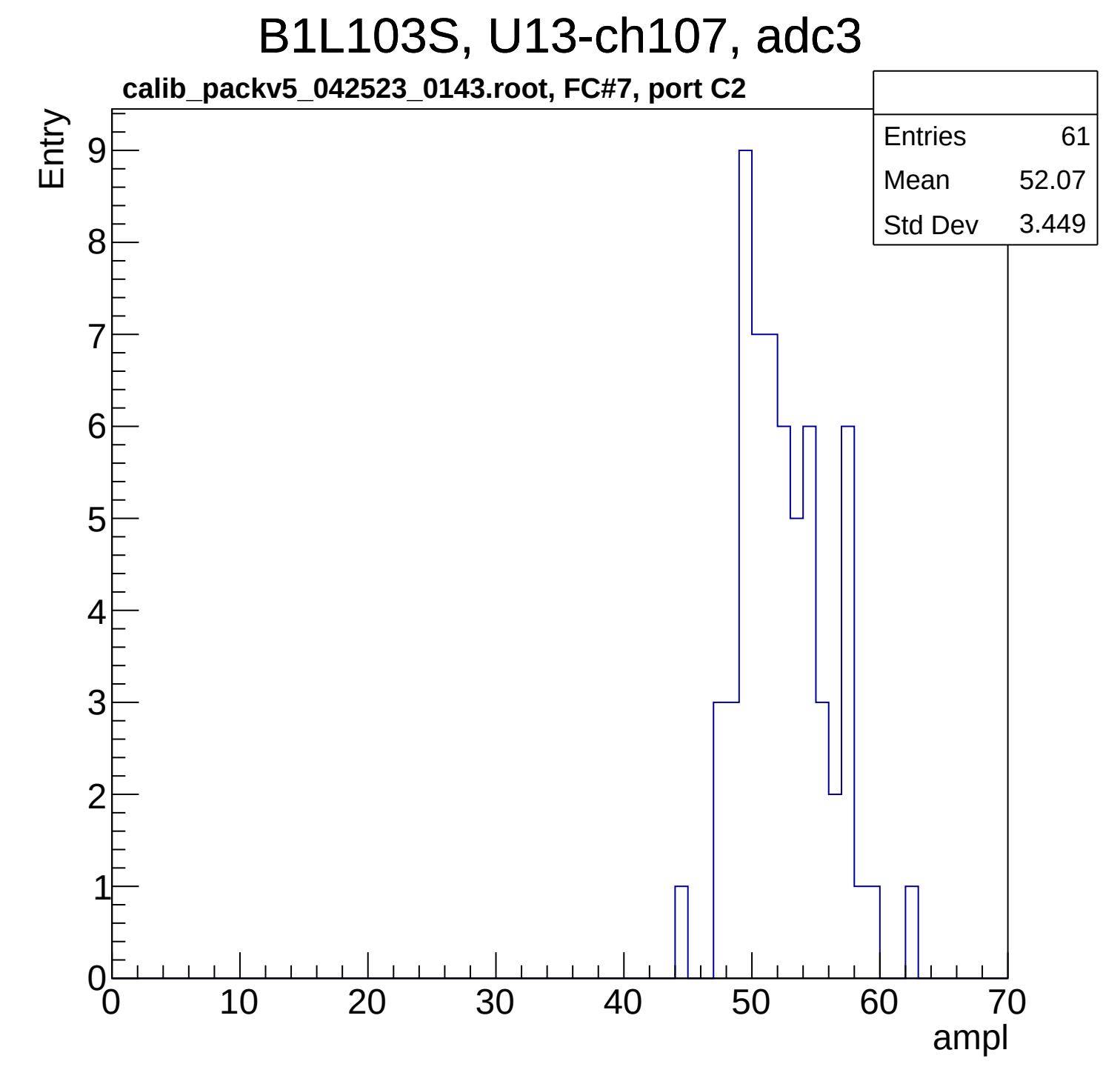

B1L103S, U13-ch107, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 37.83
Std Dev 3.897
Gaus mean : 38.3284
Gaus Width: 4.2369
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch107, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 44.93
Std Dev 3.902
Gaus mean : 44.9801
Gaus Width: 3.8579
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch107, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 52.07
Std Dev 3.449
Entry
ampl
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2
3
4
5
6
7
8
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0
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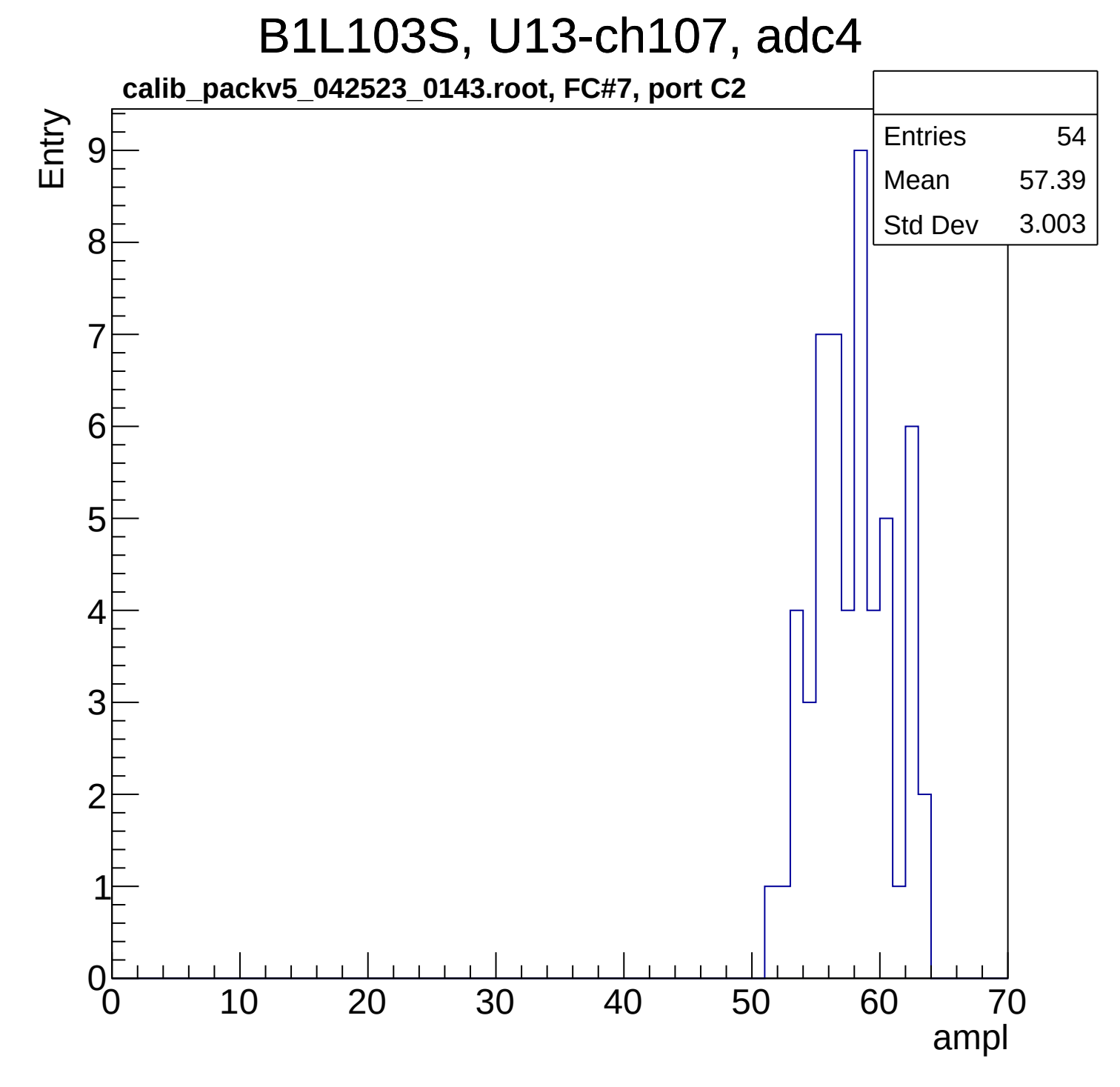
B1L103S, U13-ch107, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.39
Std Dev 3.003
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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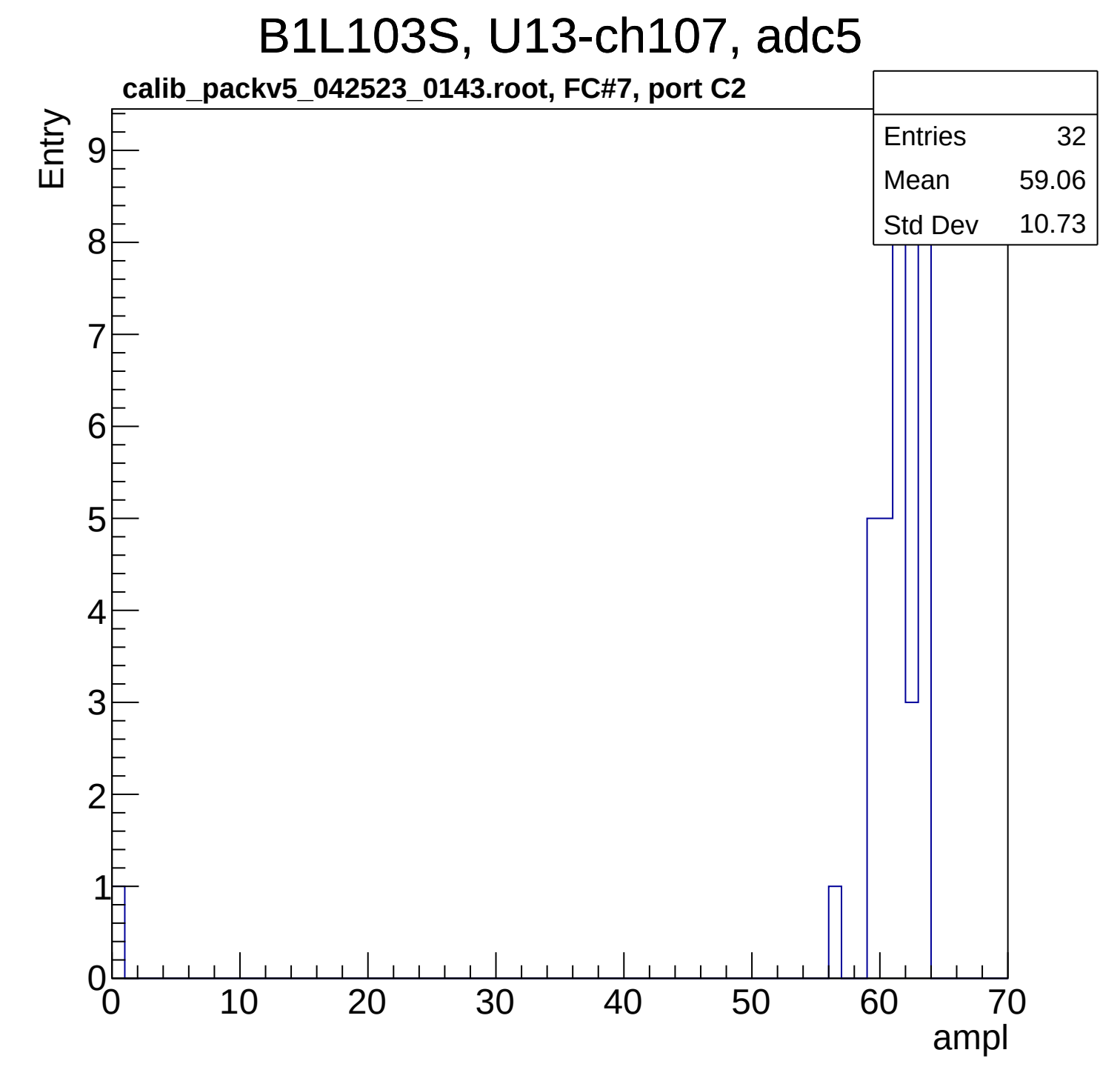
B1L103S, U13-ch107, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 59.06
Std Dev 10.73
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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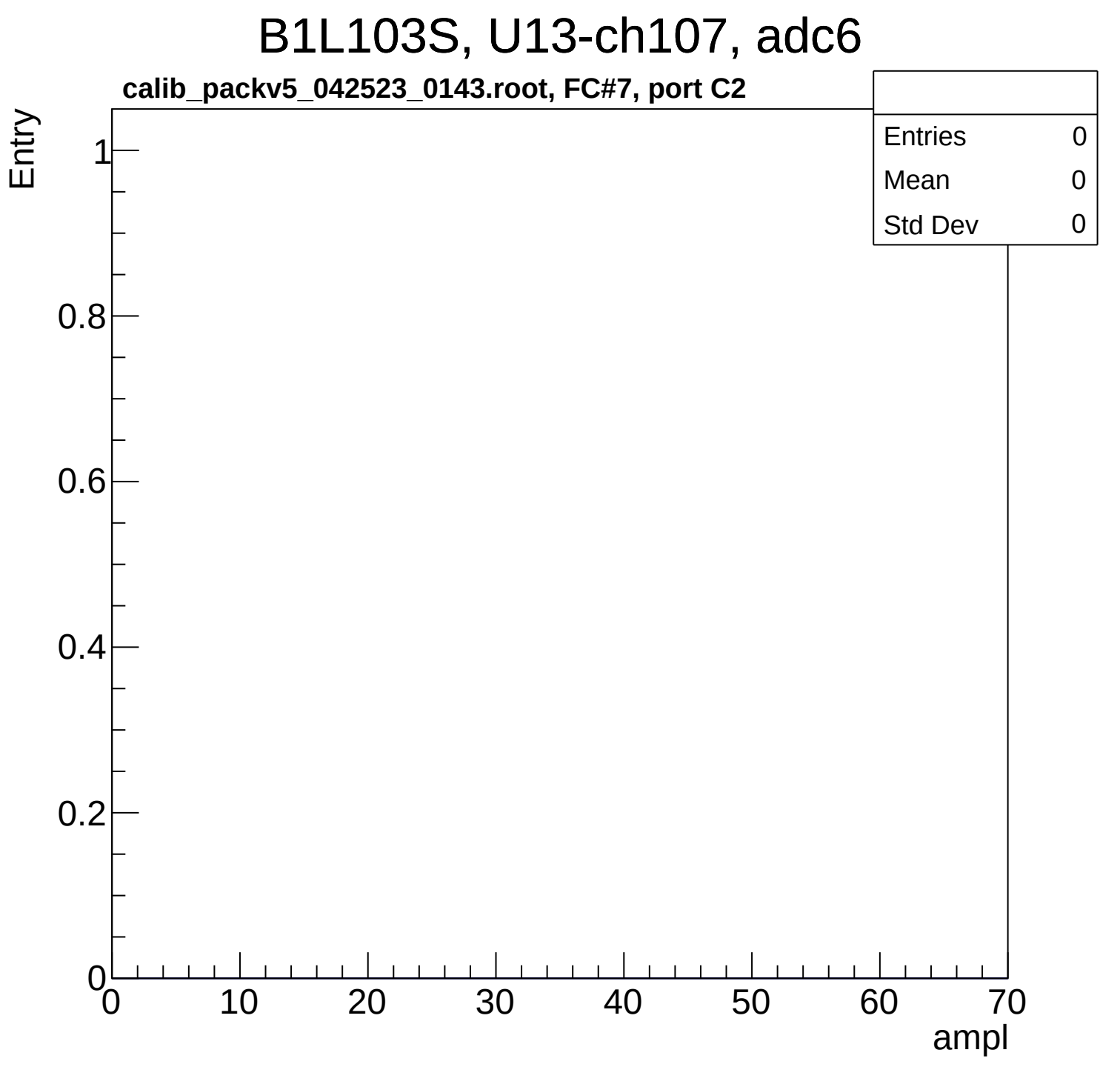
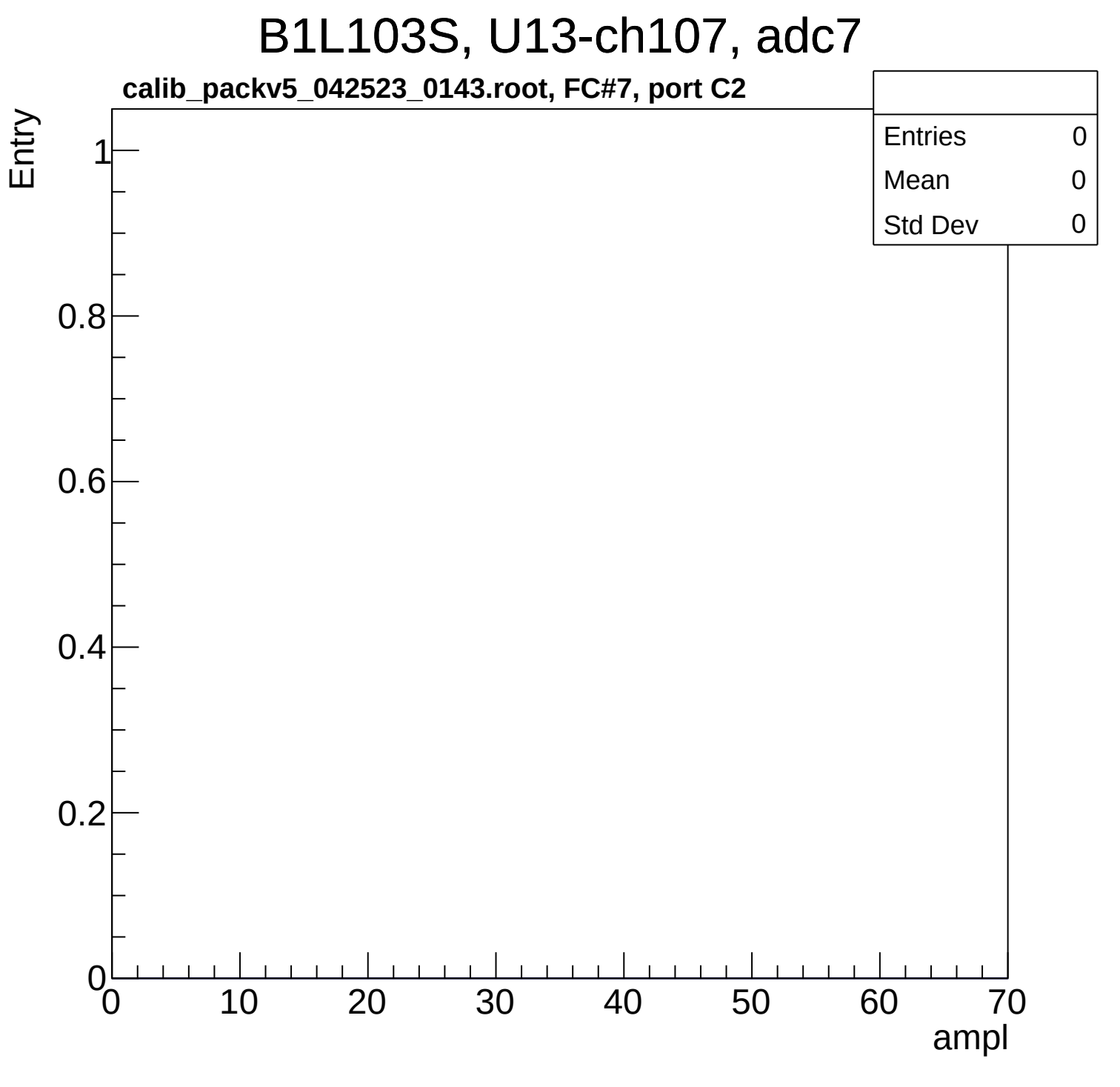

B1L103S, U13-ch107, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch107, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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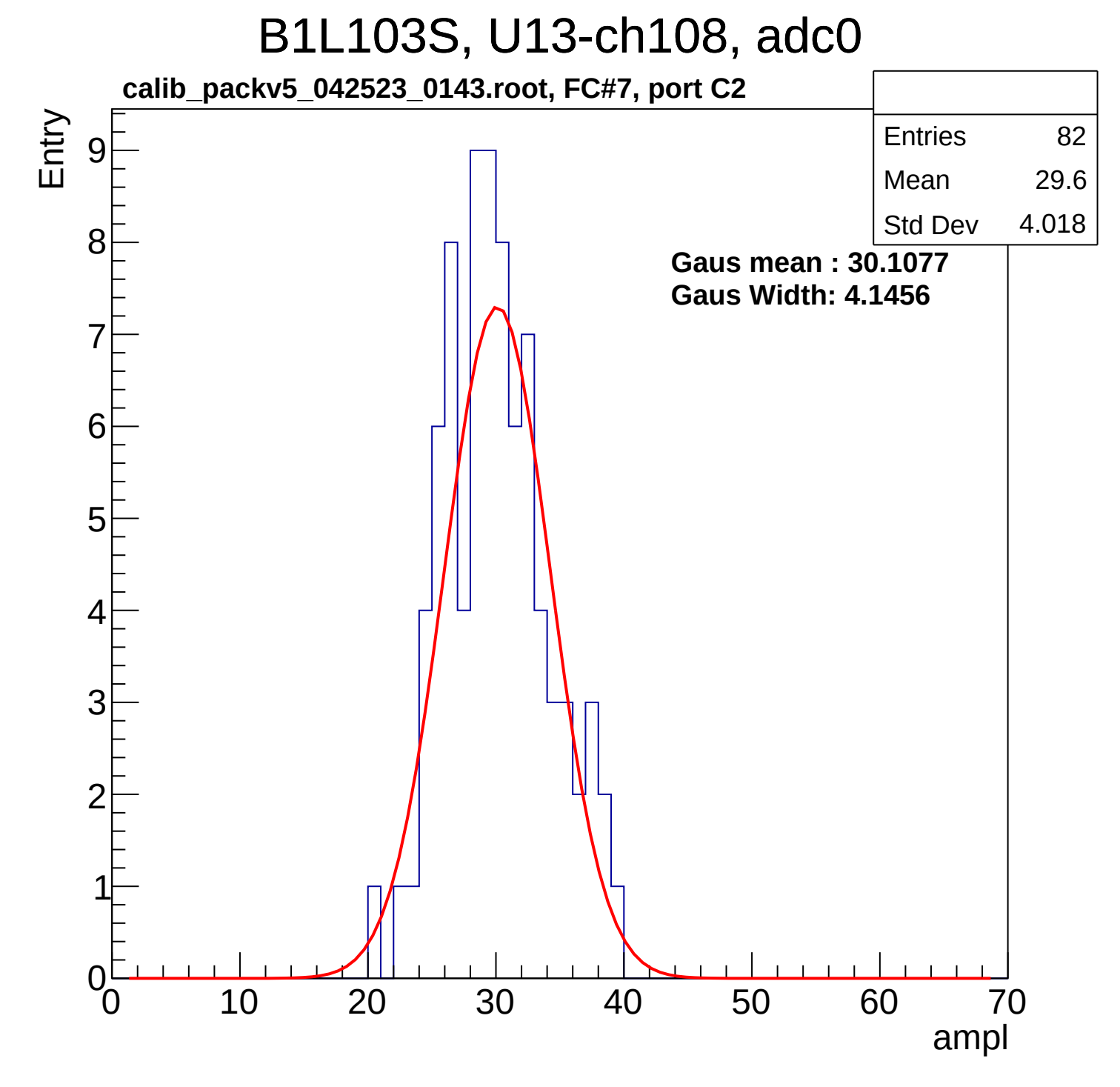
50
60
70
ampl


B1L103S, U13-ch108, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 29.6
Std Dev 4.018
Gaus mean : 30.1077
Gaus Width: 4.1456
Entry
ampl
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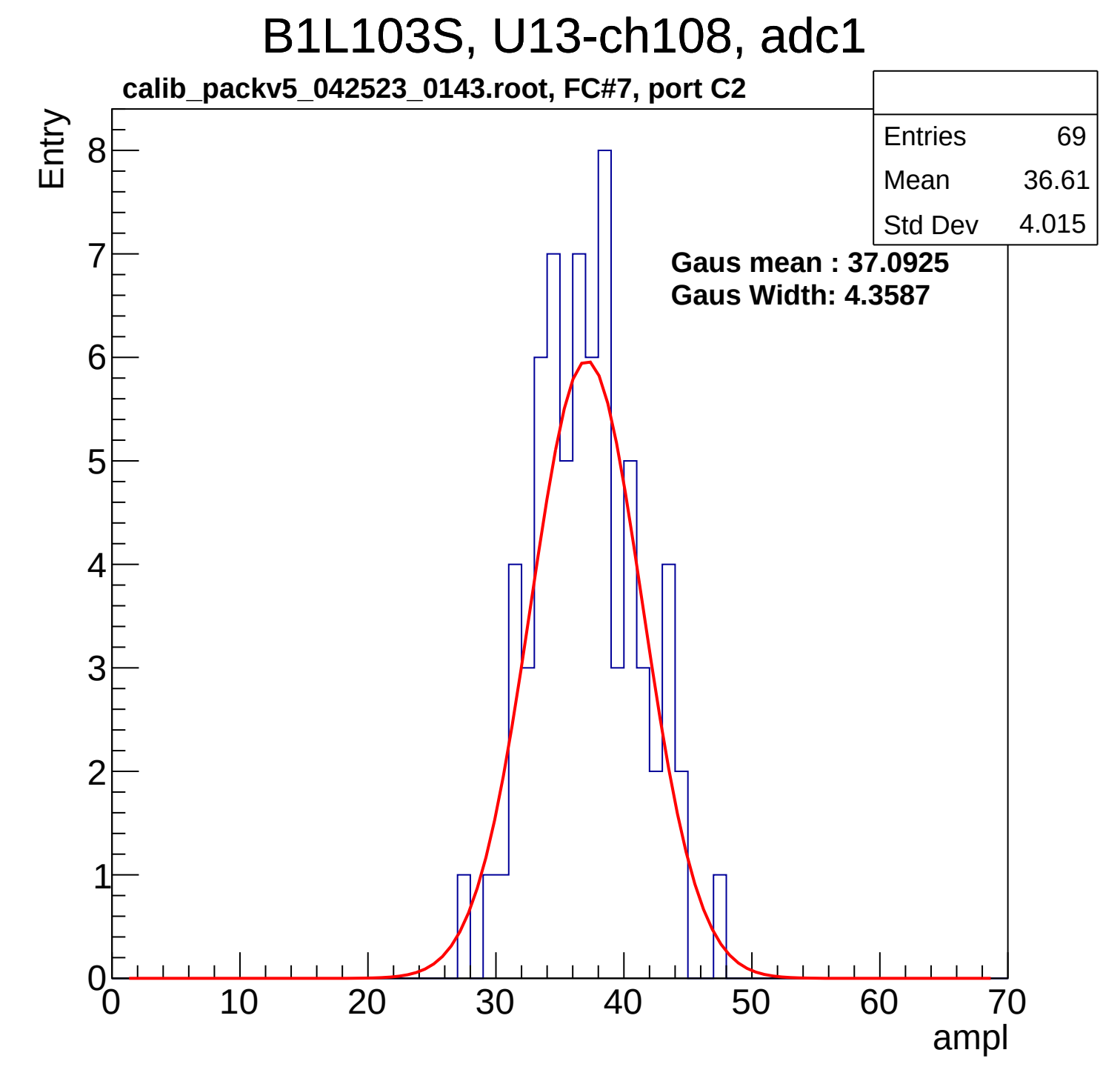
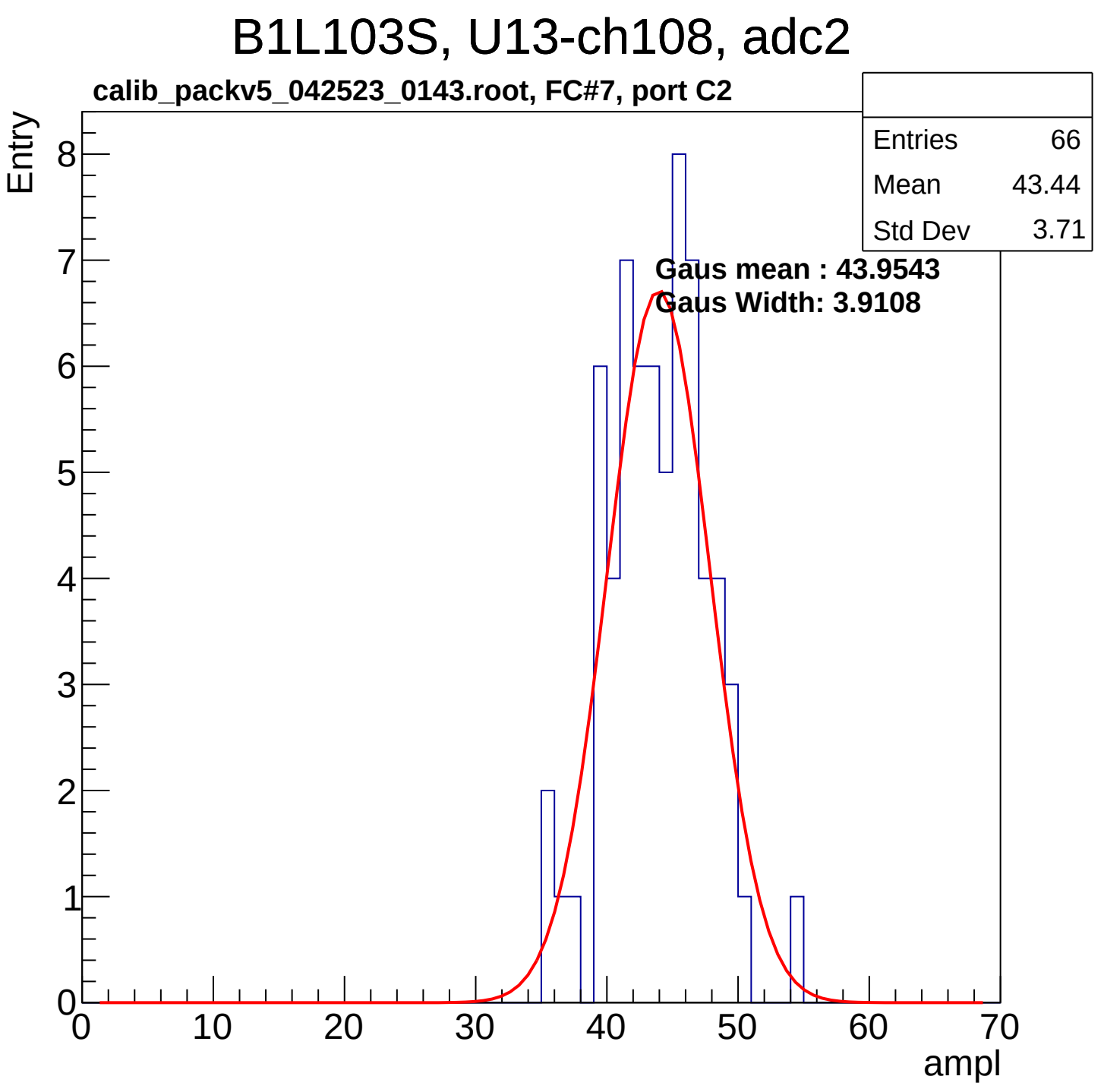
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch108, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.61
Std Dev 4.015
Gaus mean : 37.0925
Gaus Width: 4.3587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch108, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43.44
Std Dev 3.71
Gaus mean : 43.9543
Gaus Width: 3.9108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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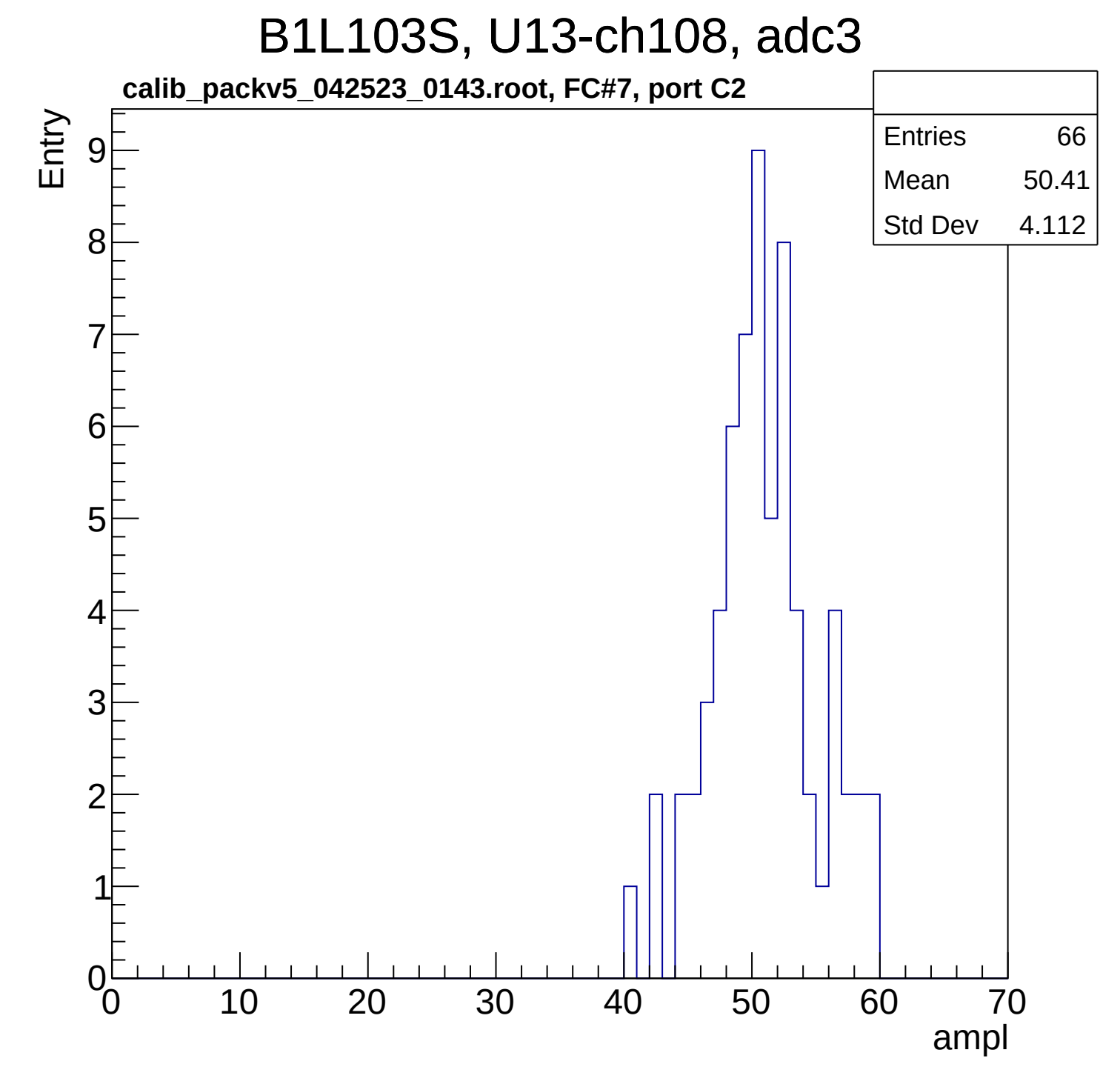
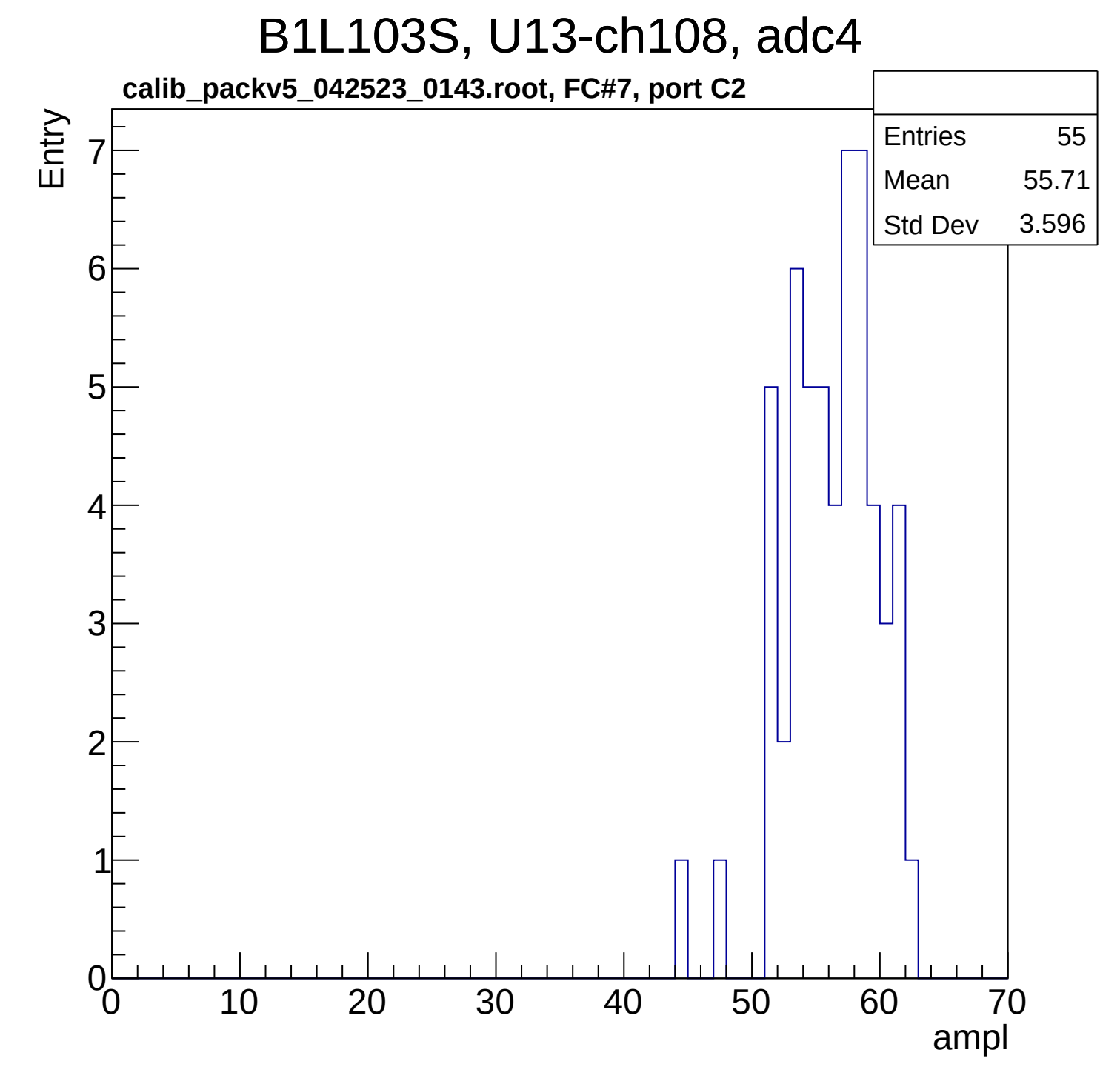
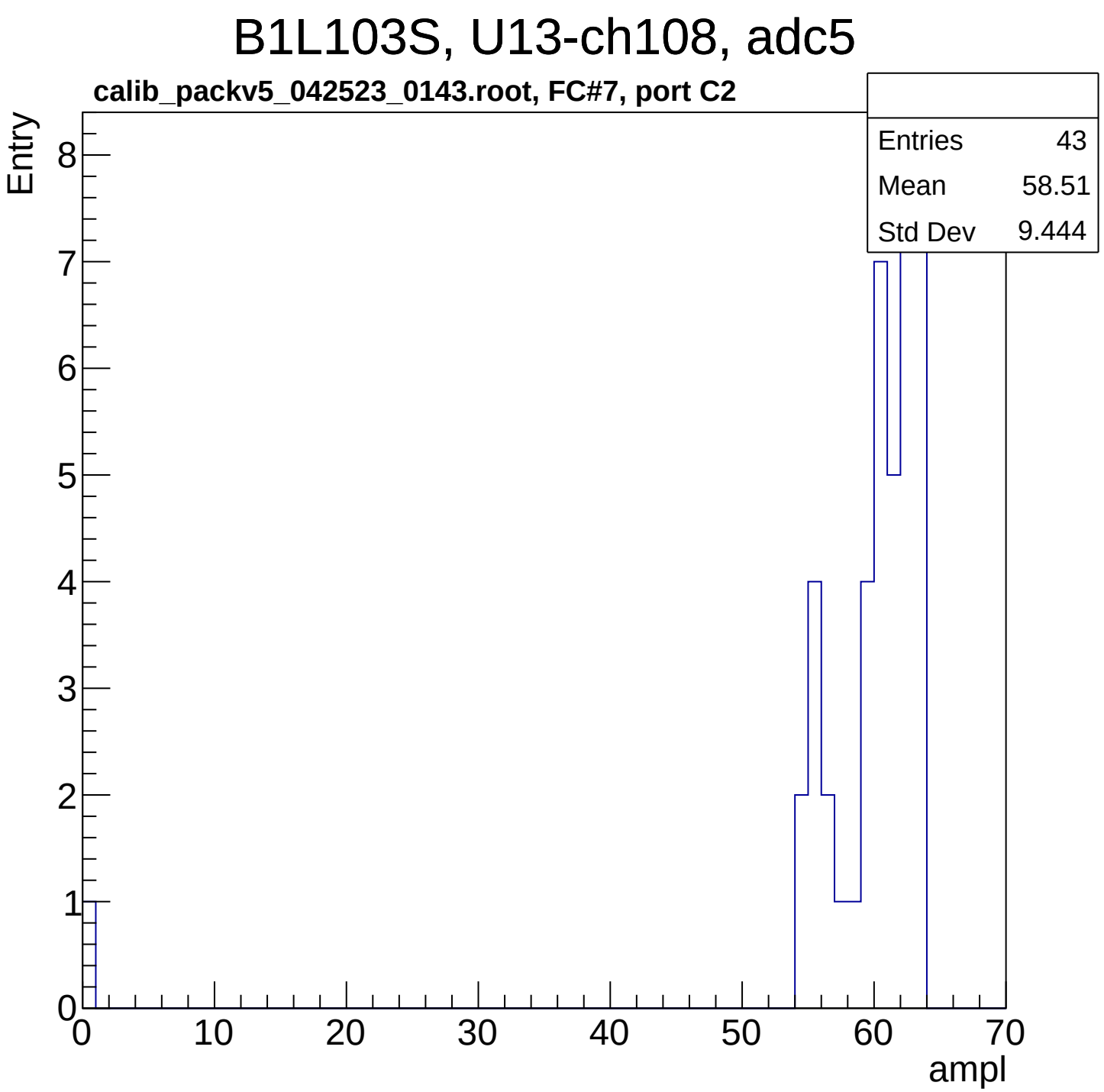

B1L103S, U13-ch108, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 50.41
Std Dev 4.112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch108, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.71
Std Dev 3.596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch108, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 58.51
Std Dev 9.444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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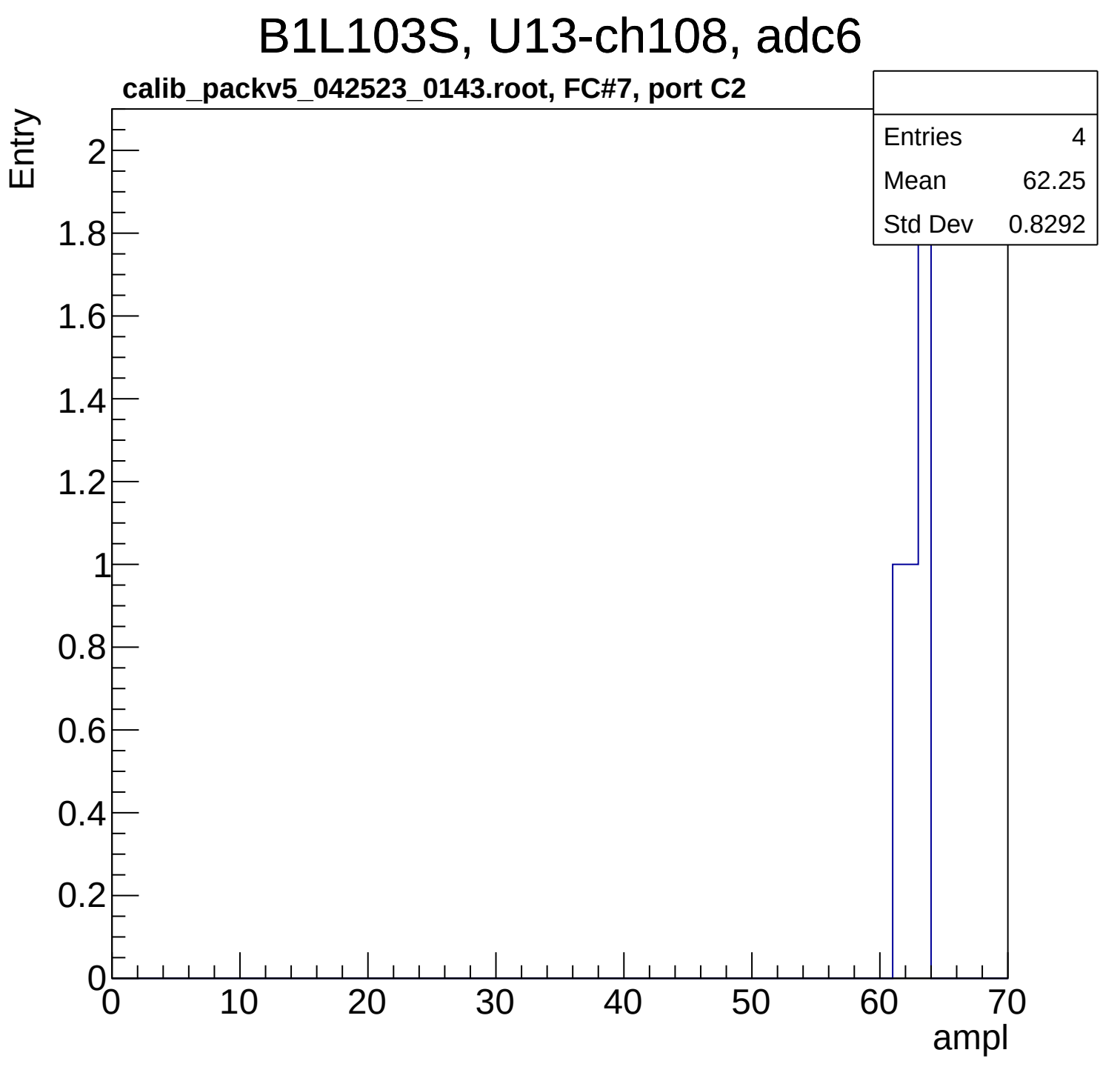
B1L103S, U13-ch108, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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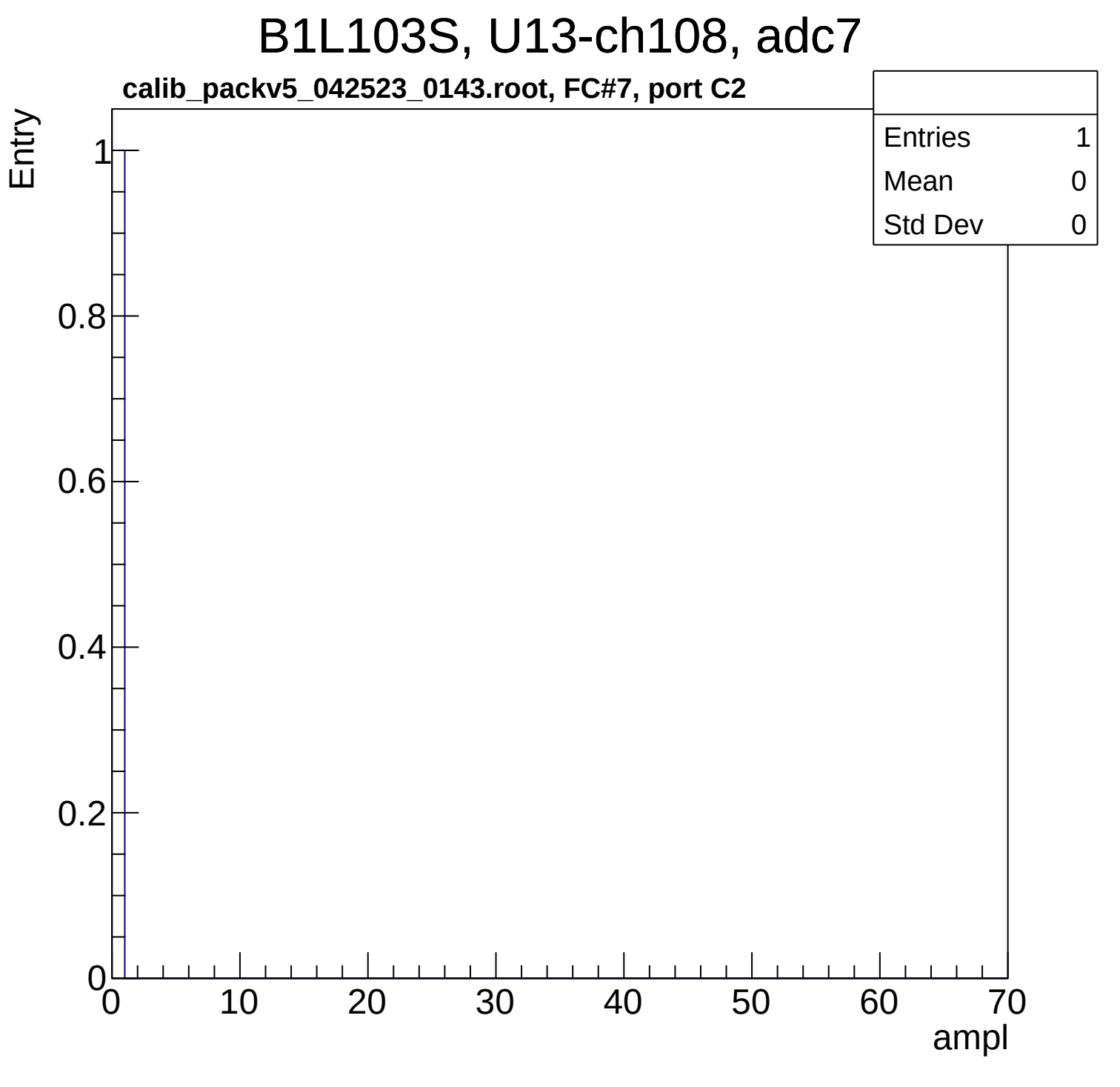
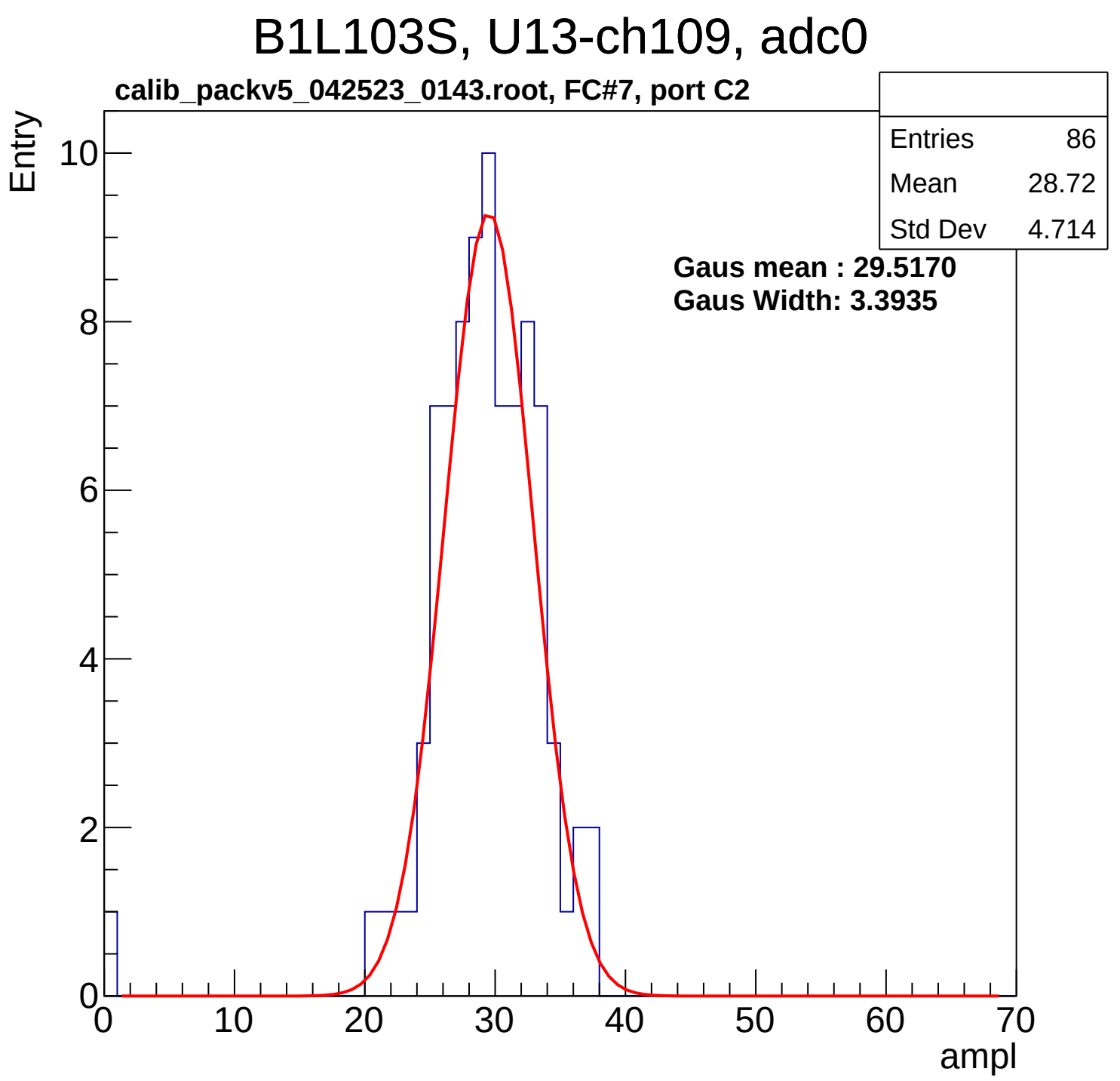
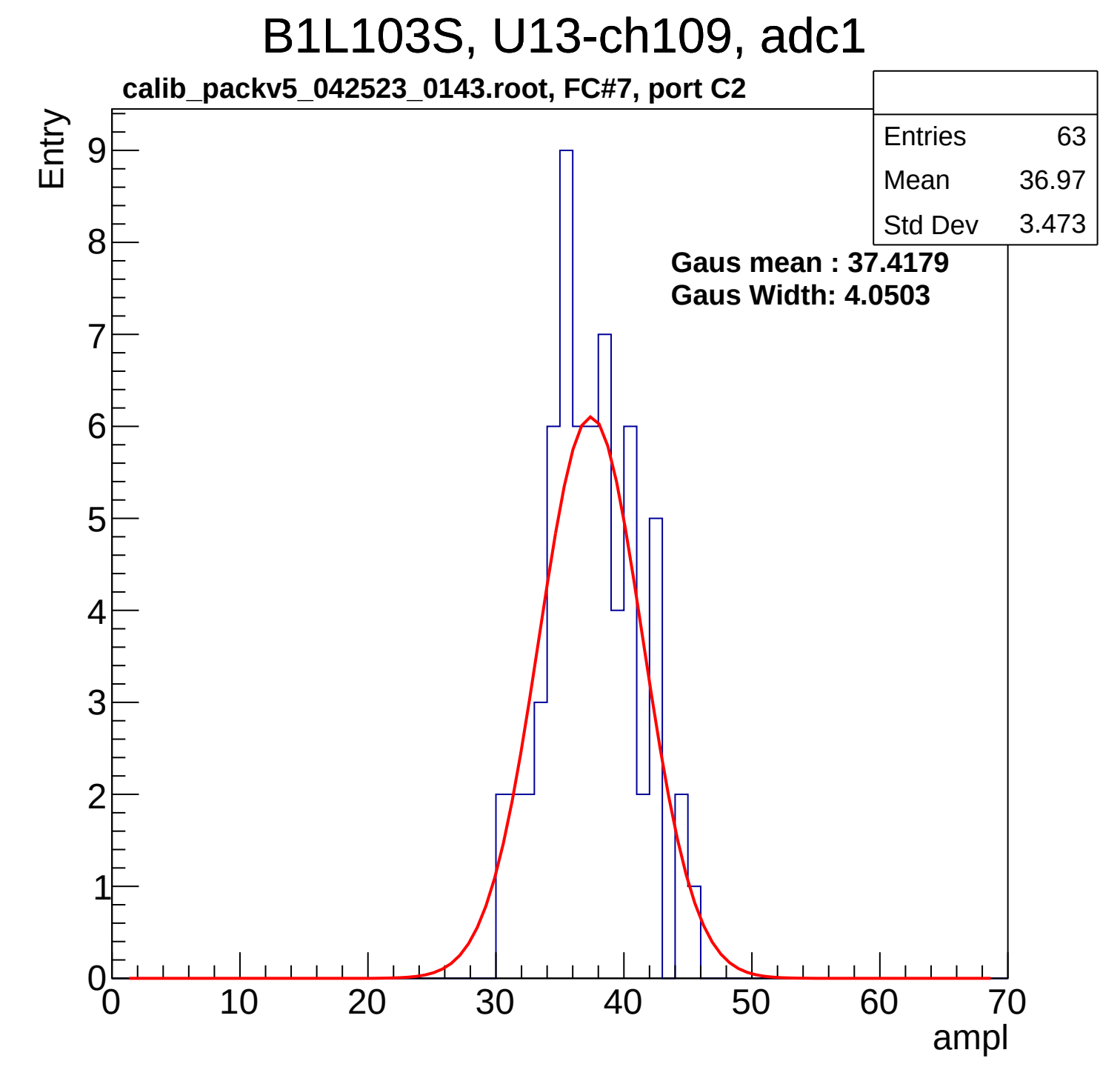
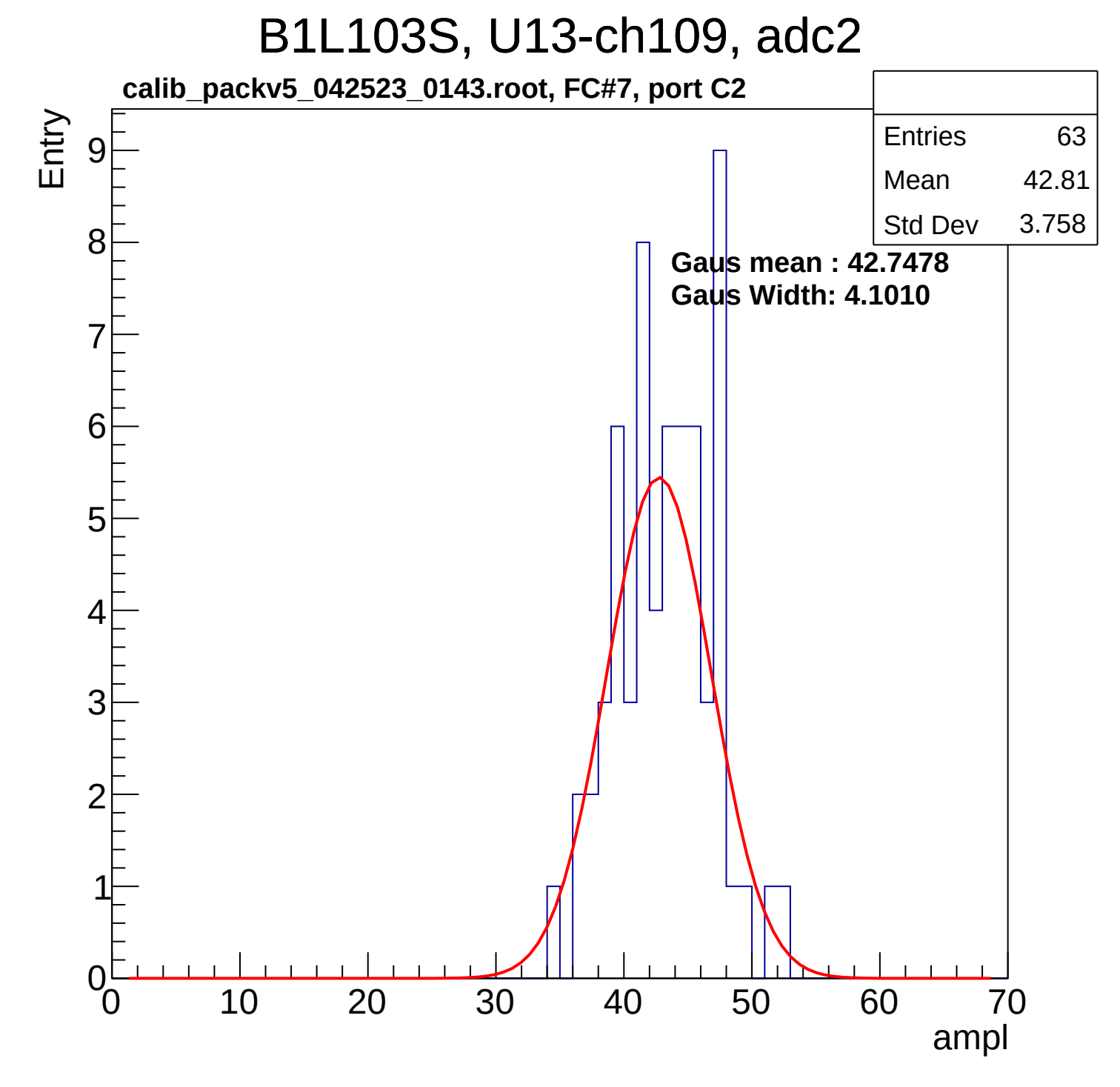
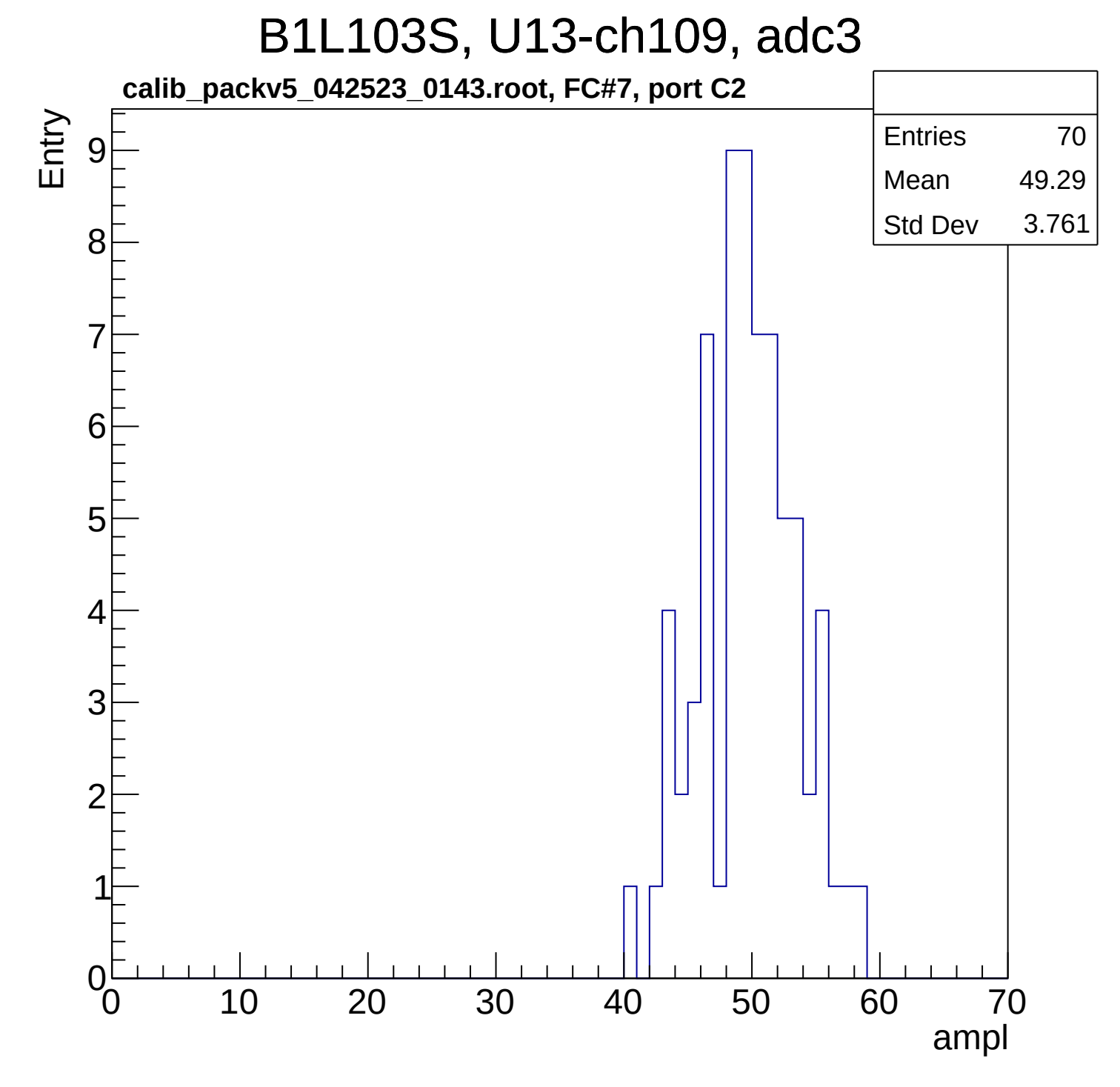
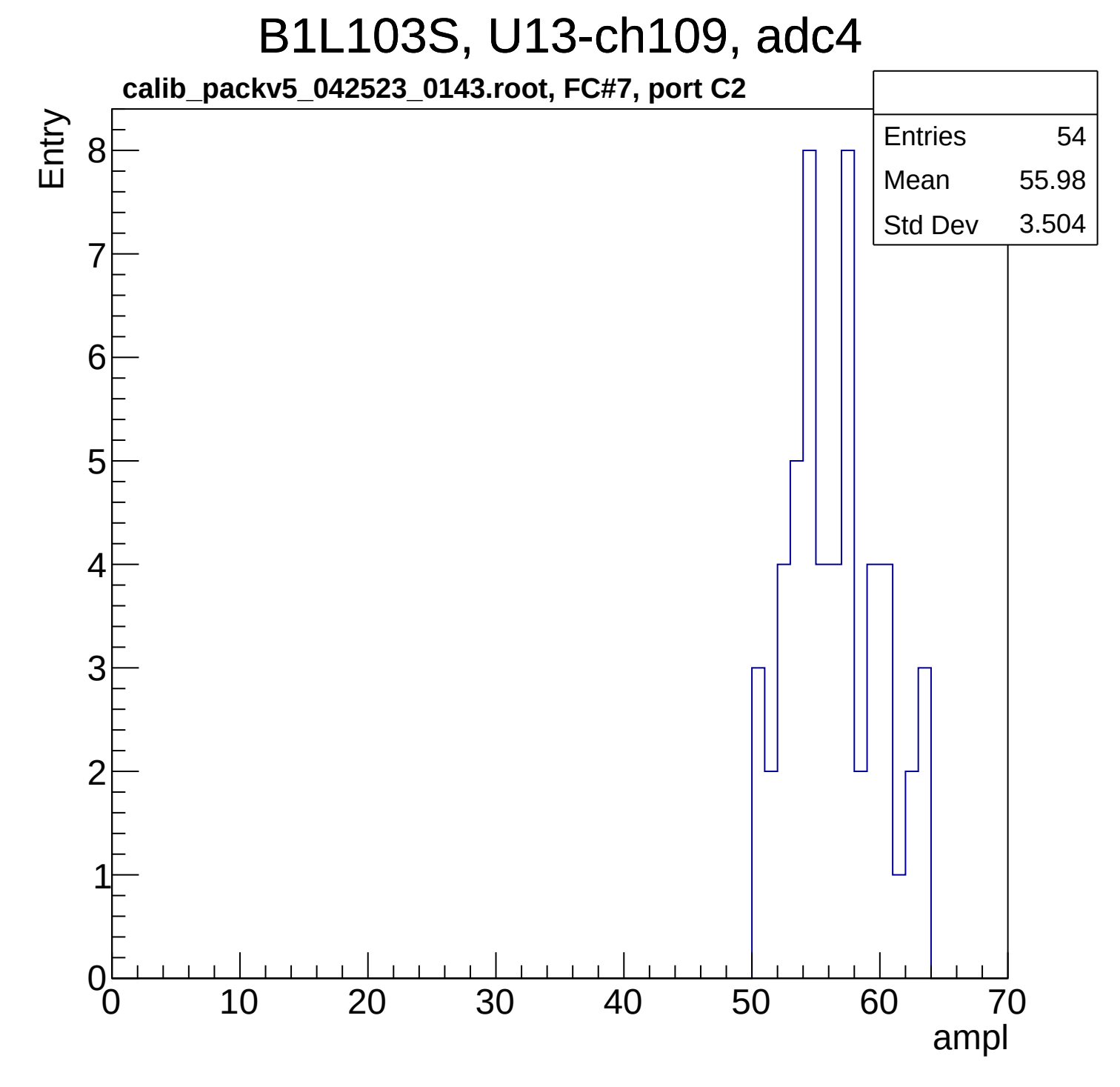

B1L103S, U13-ch108, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

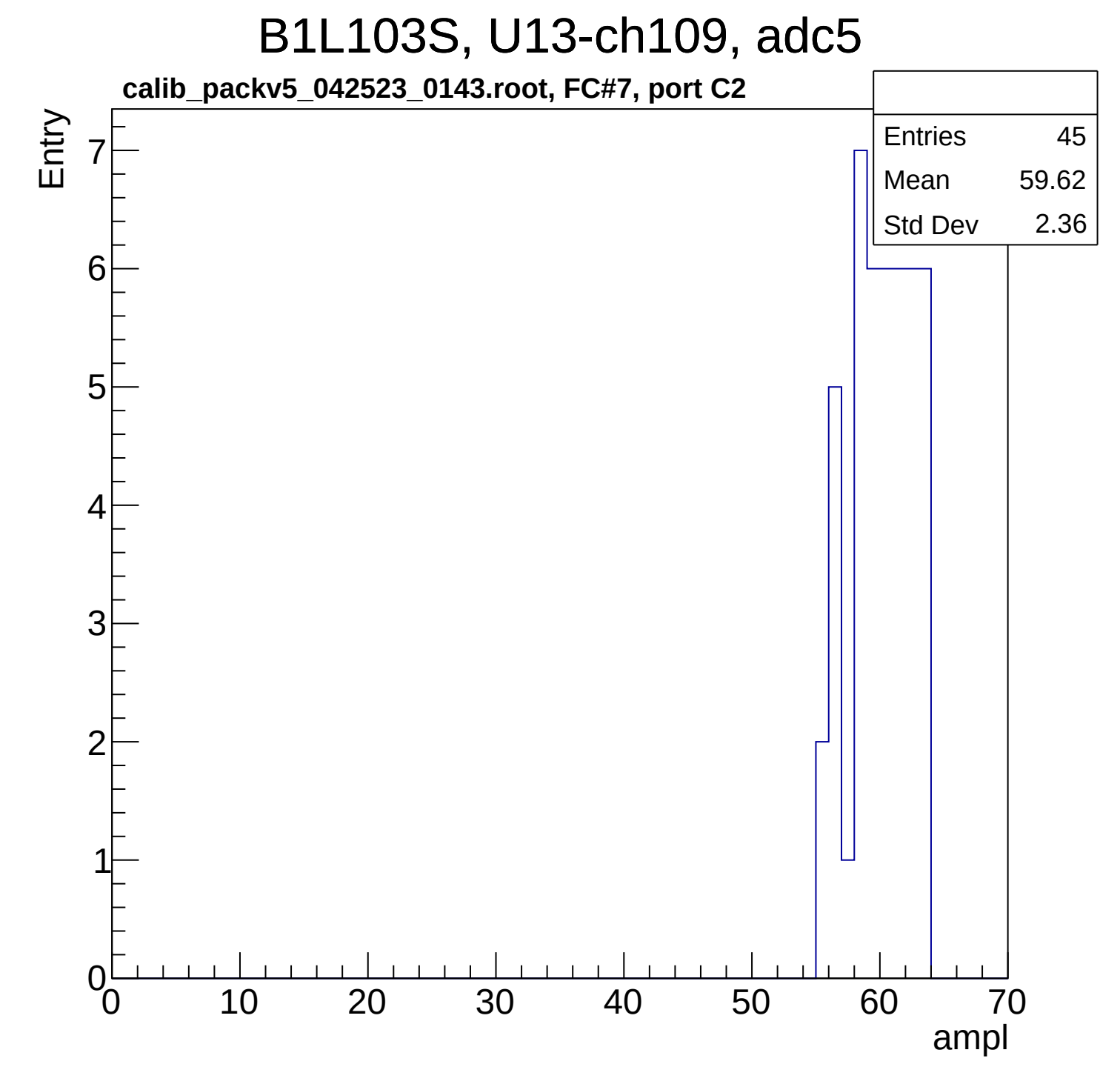

B1L103S, U13-ch109, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 28.72
Std Dev 4.714
Gaus mean : 29.5170
Gaus Width: 3.3935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

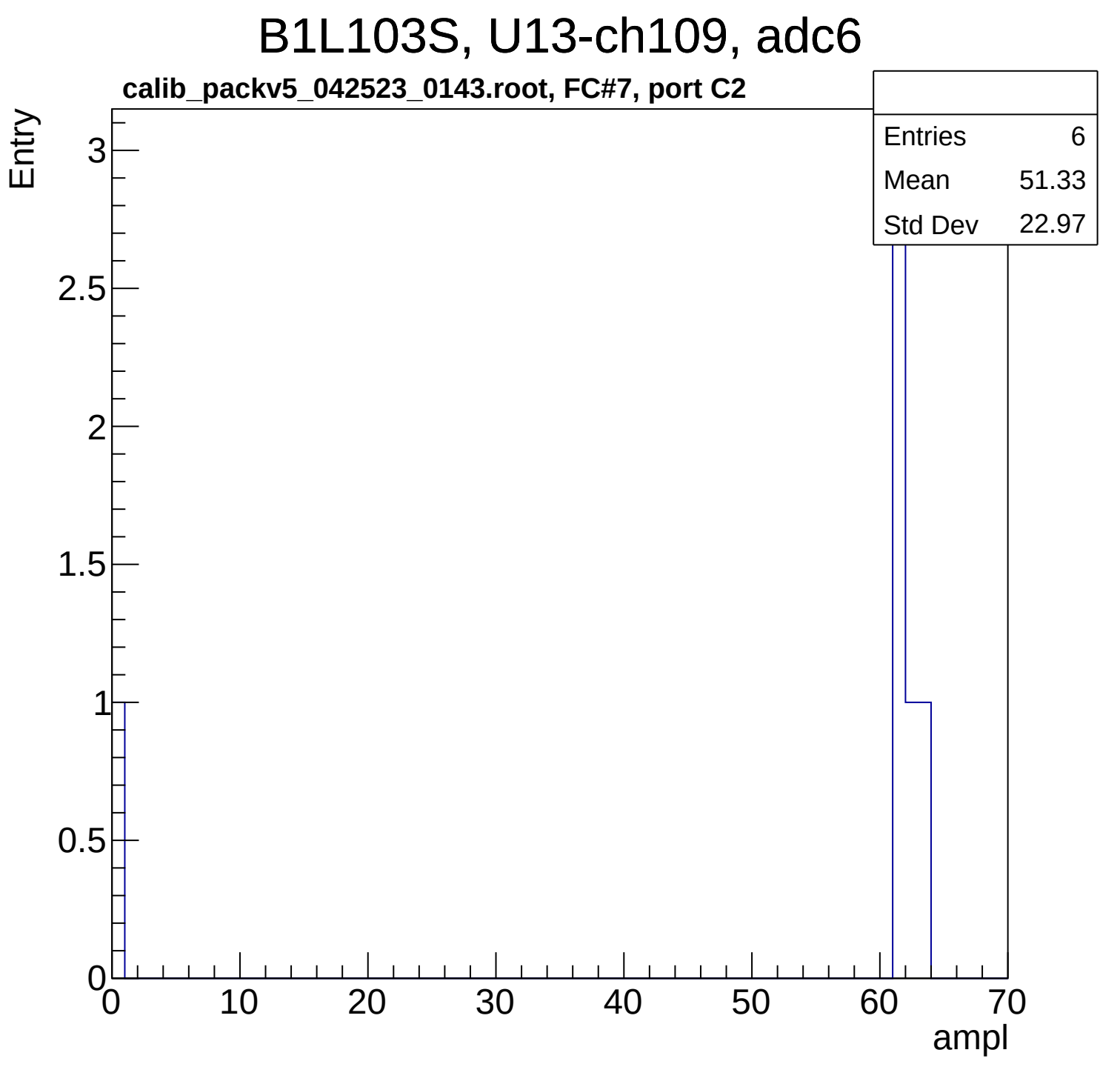

B1L103S, U13-ch109, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 36.97
Std Dev 3.473
Gaus mean : 37.4179
Gaus Width: 4.0503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 42.81
Std Dev 3.758
Gaus mean : 42.7478
Gaus Width: 4.1010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.29
Std Dev 3.761
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.98
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.62
Std Dev 2.36
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.33
Std Dev 22.97
Entry
ampl
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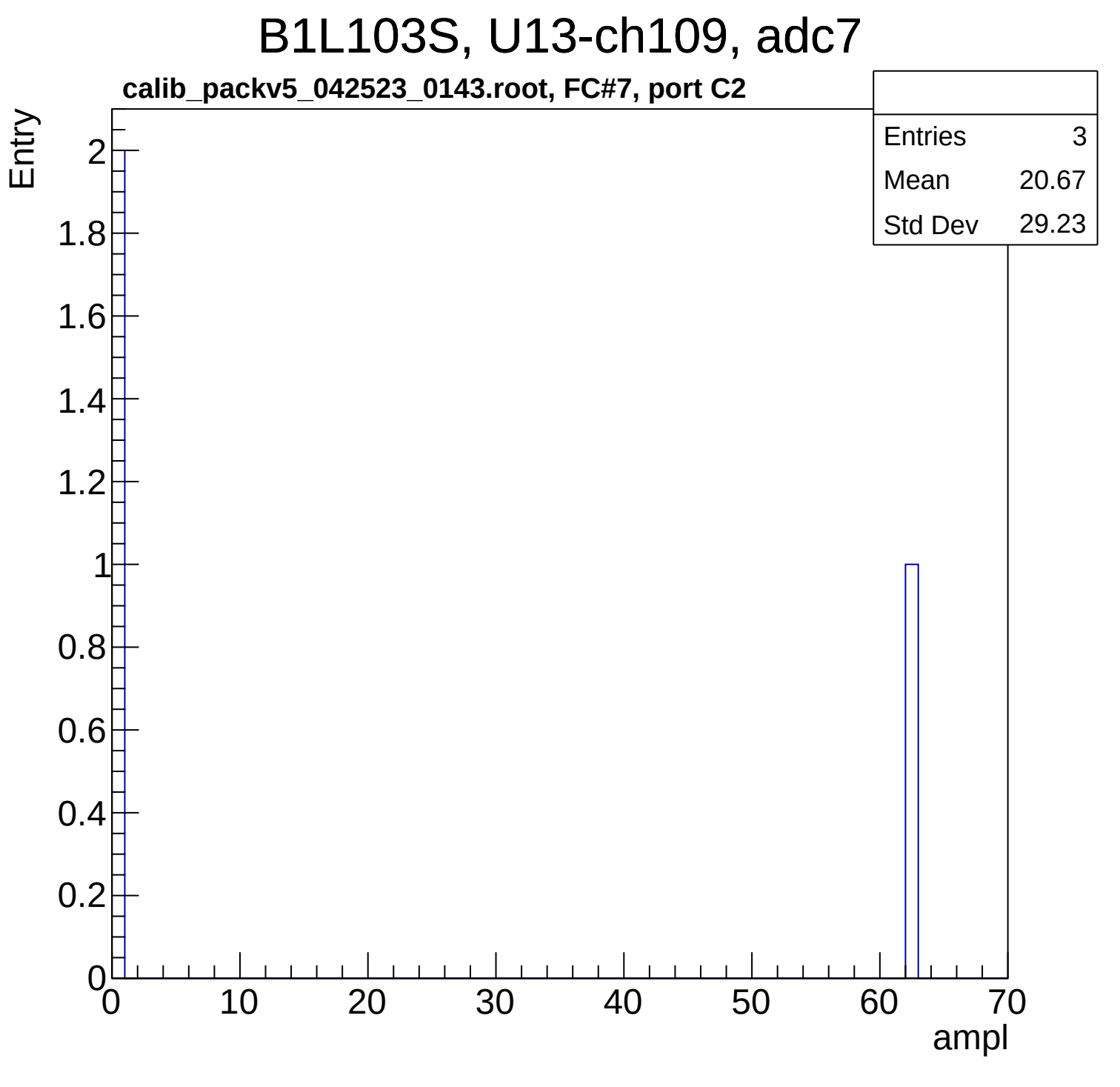
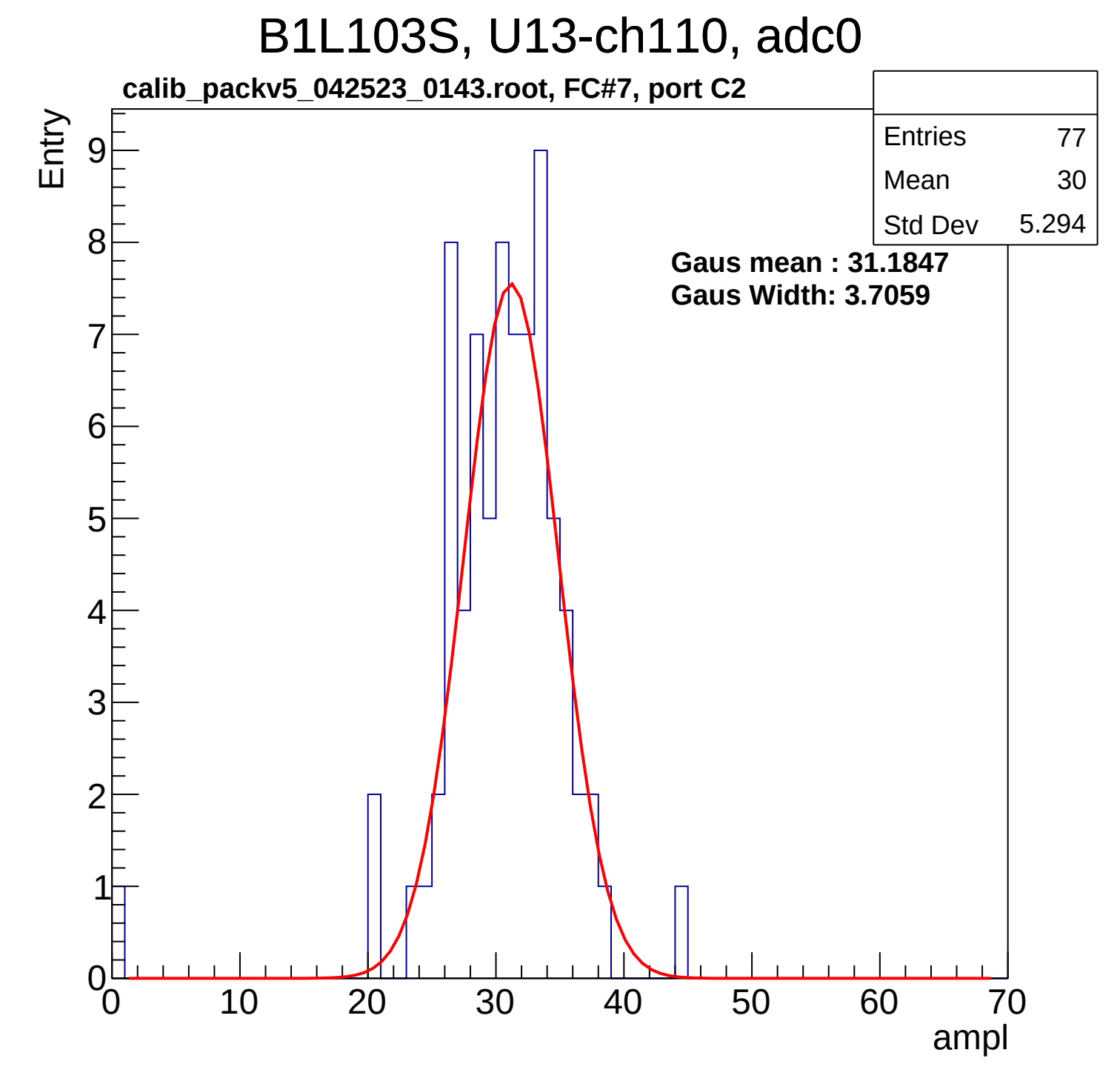
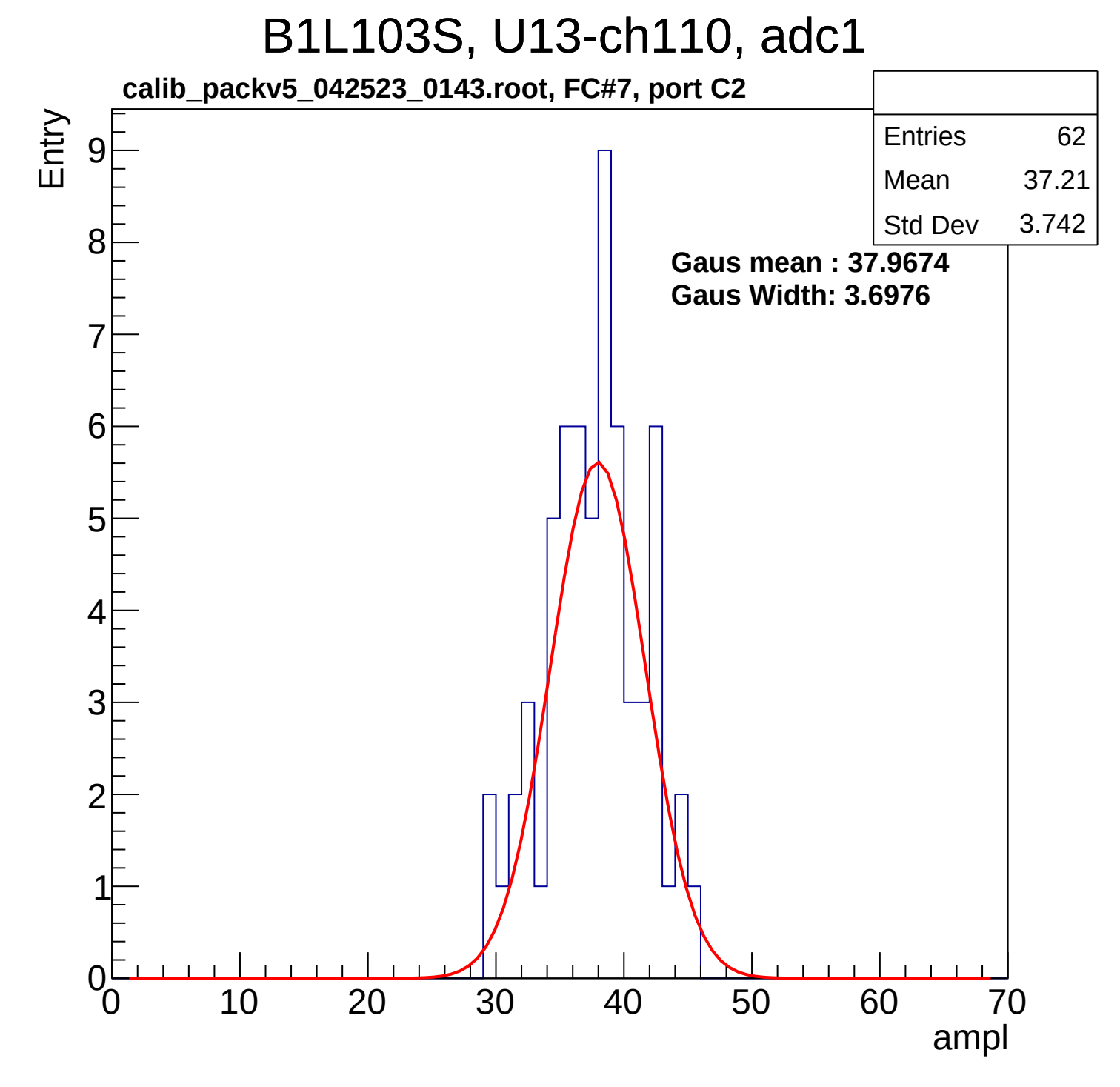
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch109, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch110, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30
Std Dev 5.294
Gaus mean : 31.1847
Gaus Width: 3.7059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch110, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 37.21
Std Dev 3.742
Gaus mean : 37.9674
Gaus Width: 3.6976
Entry
ampl
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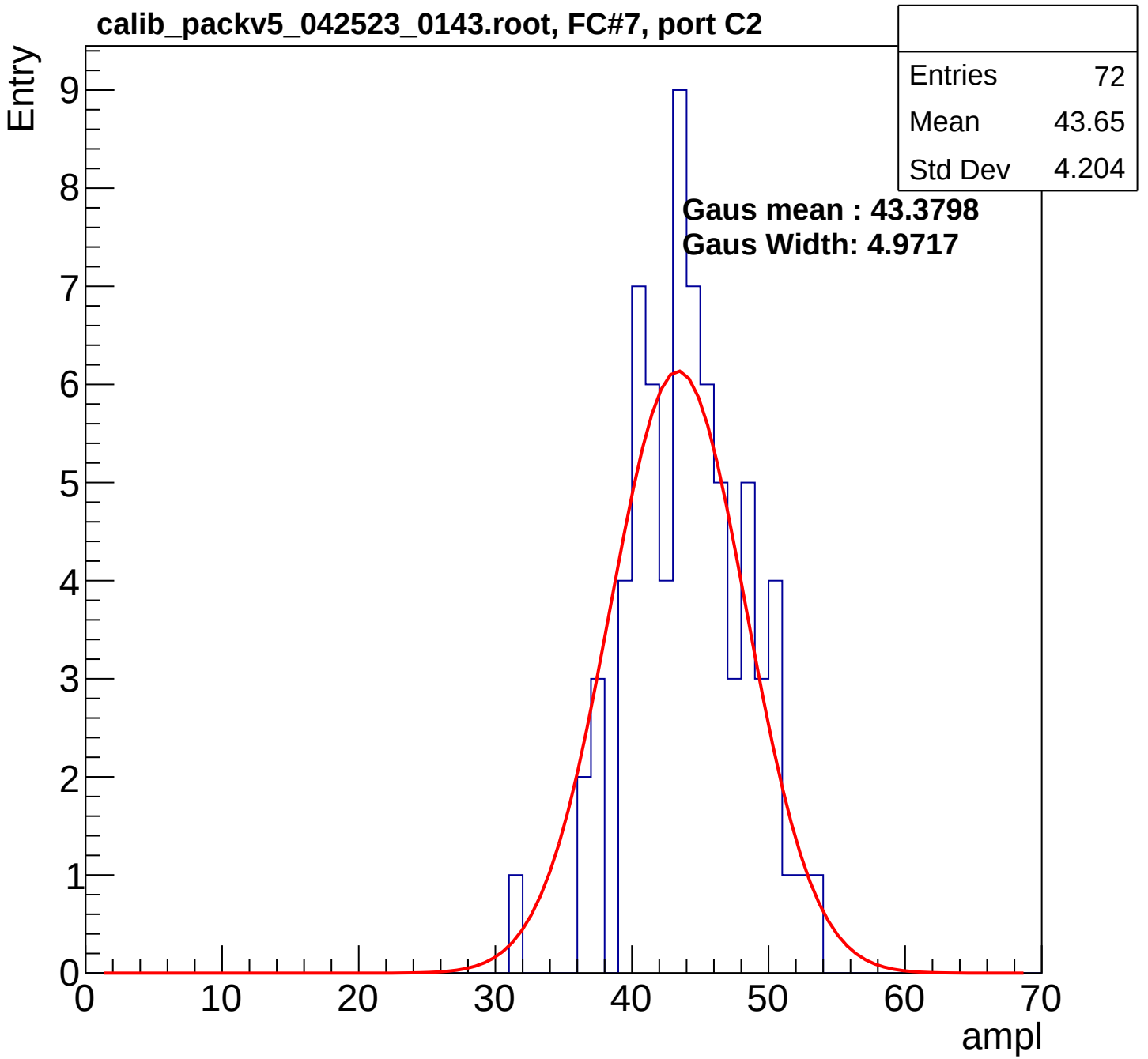
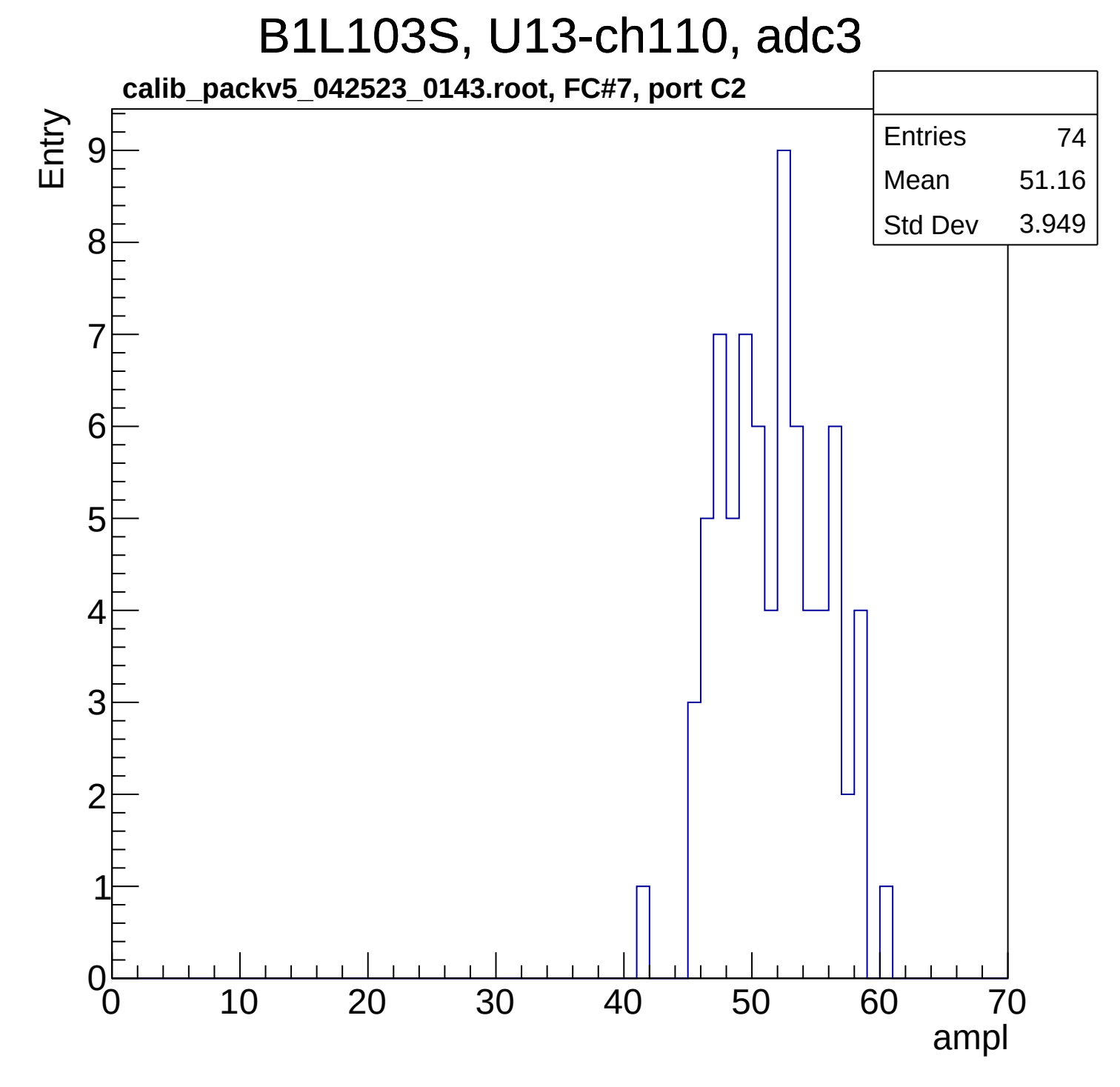
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch110, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.65
Std Dev 4.204
Gaus mean : 43.3798
Gaus Width: 4.9717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch110, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 51.16
Std Dev 3.949
Entry
ampl
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2
3
4
5
6
7
8
9
0
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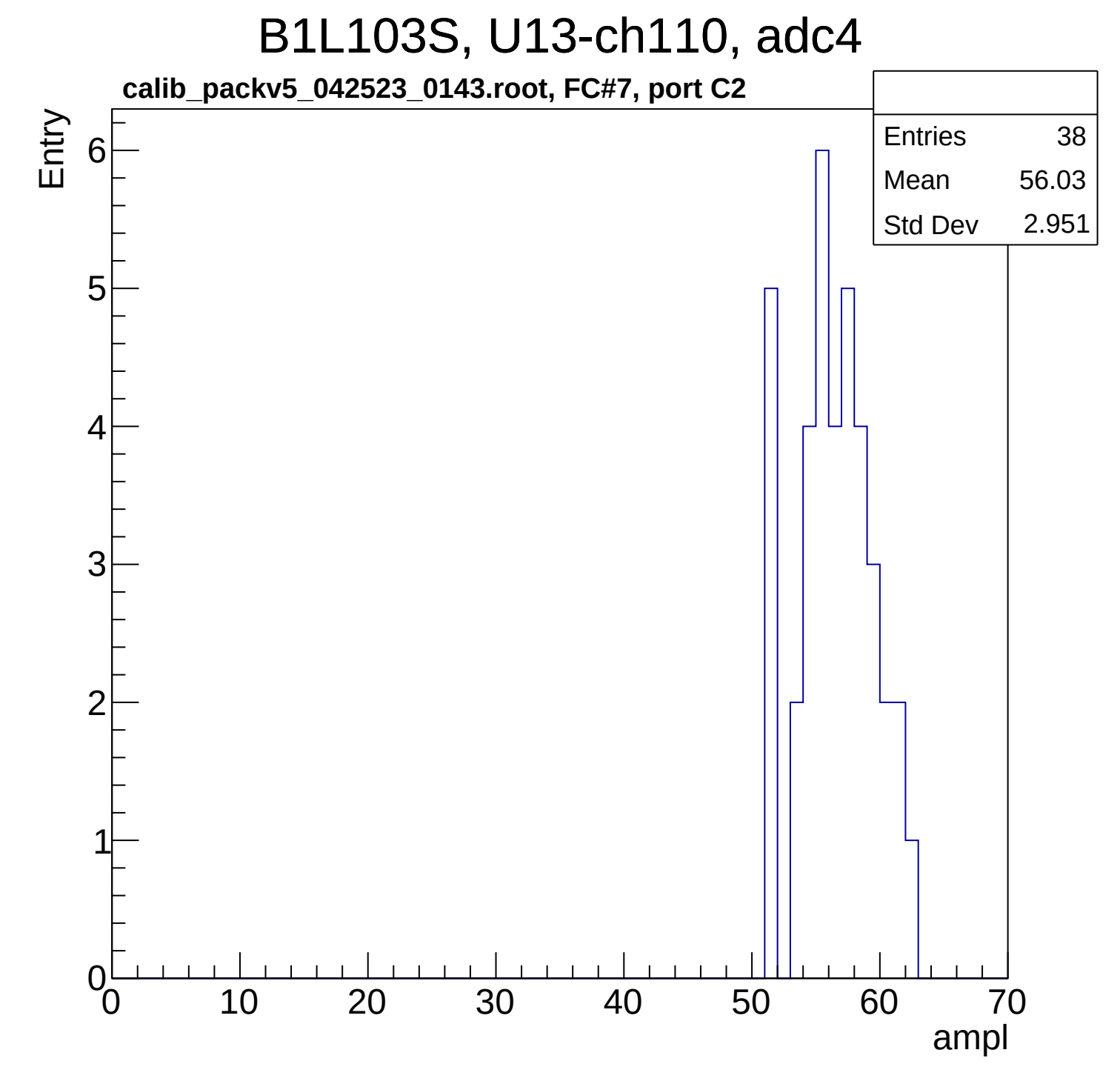
20
30
40
50
60
70


B1L103S, U13-ch110, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 56.03
Std Dev 2.951
Entry
ampl
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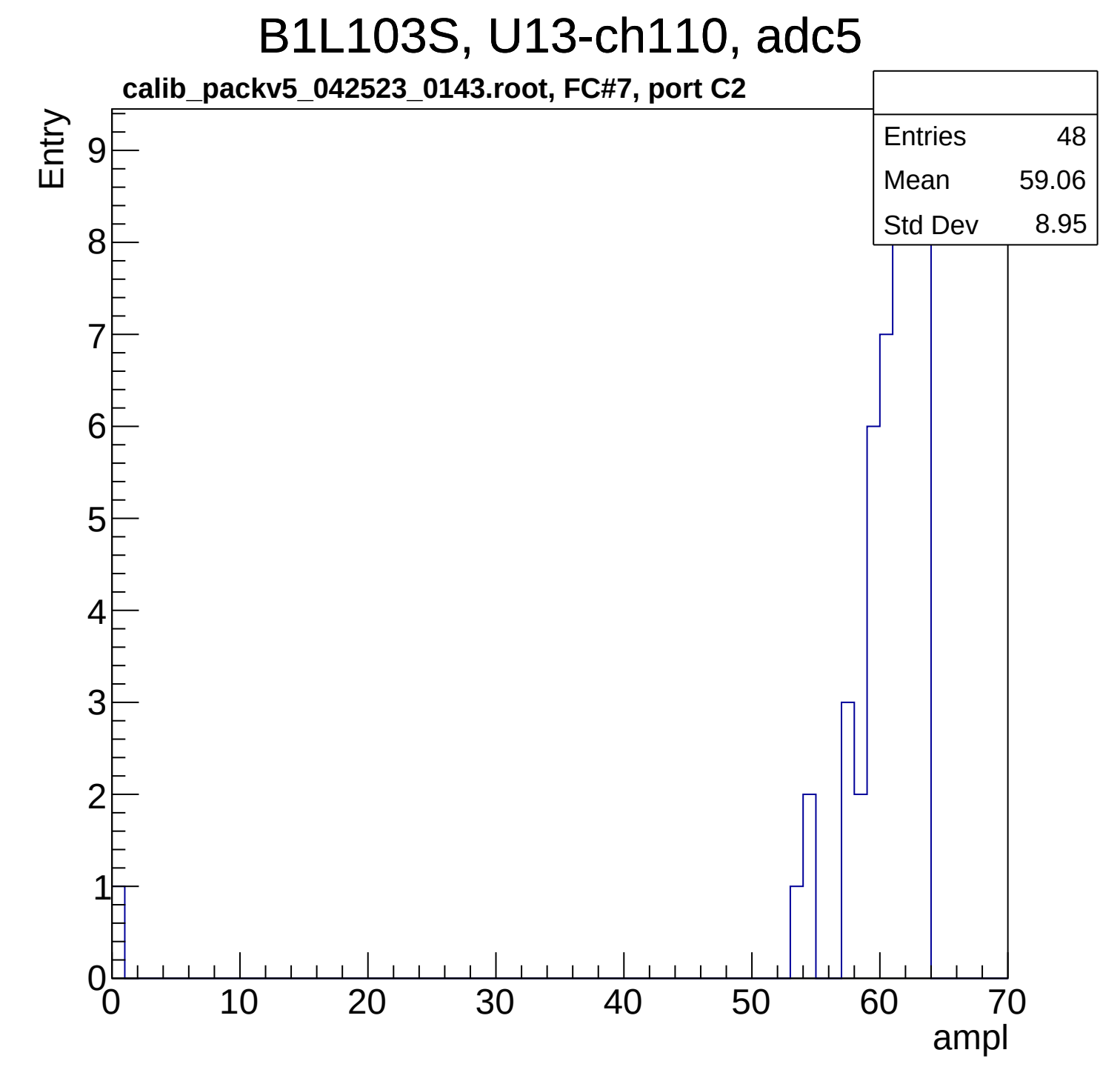
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch110, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.06
Std Dev 8.95
Entry
ampl
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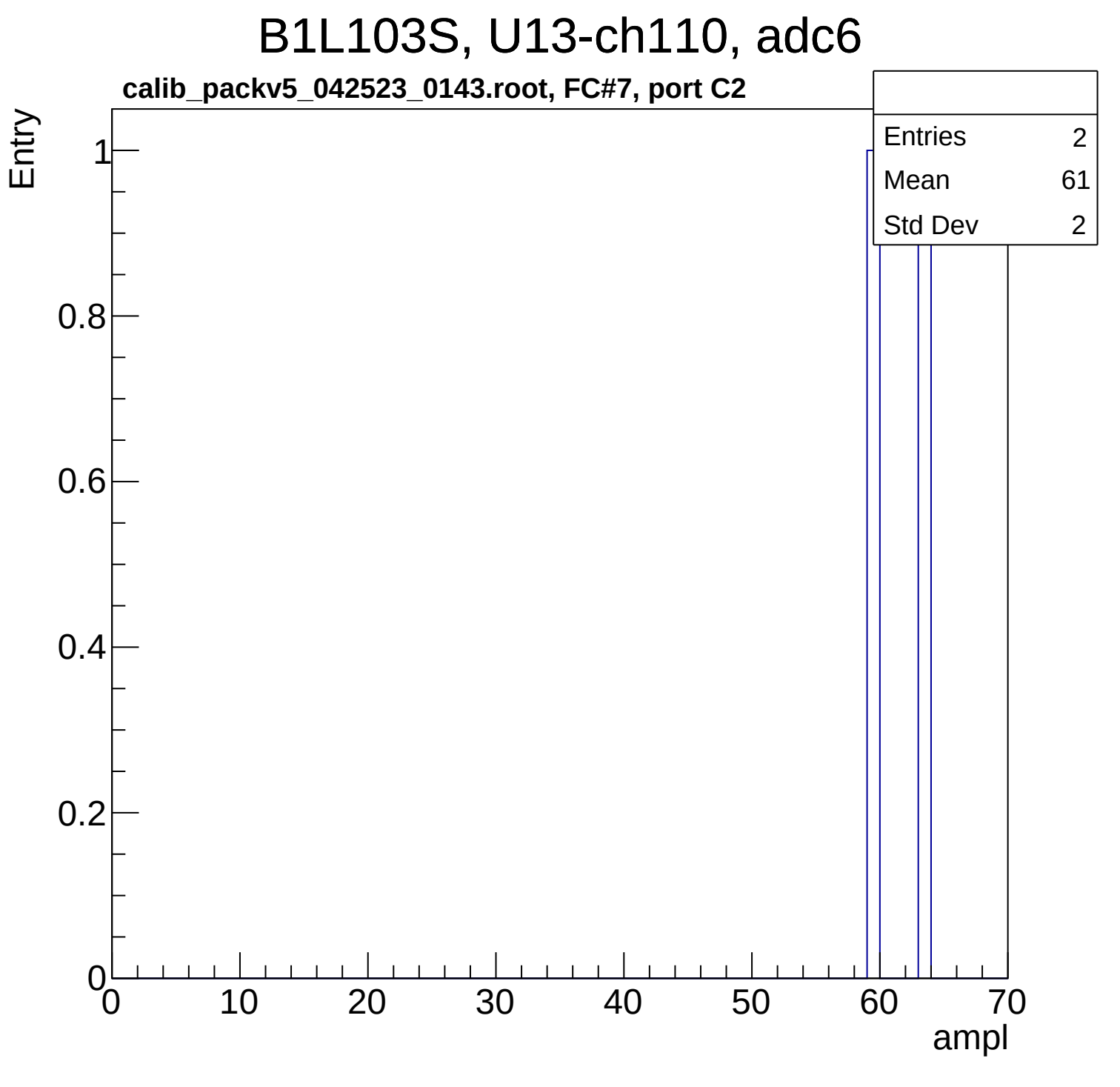
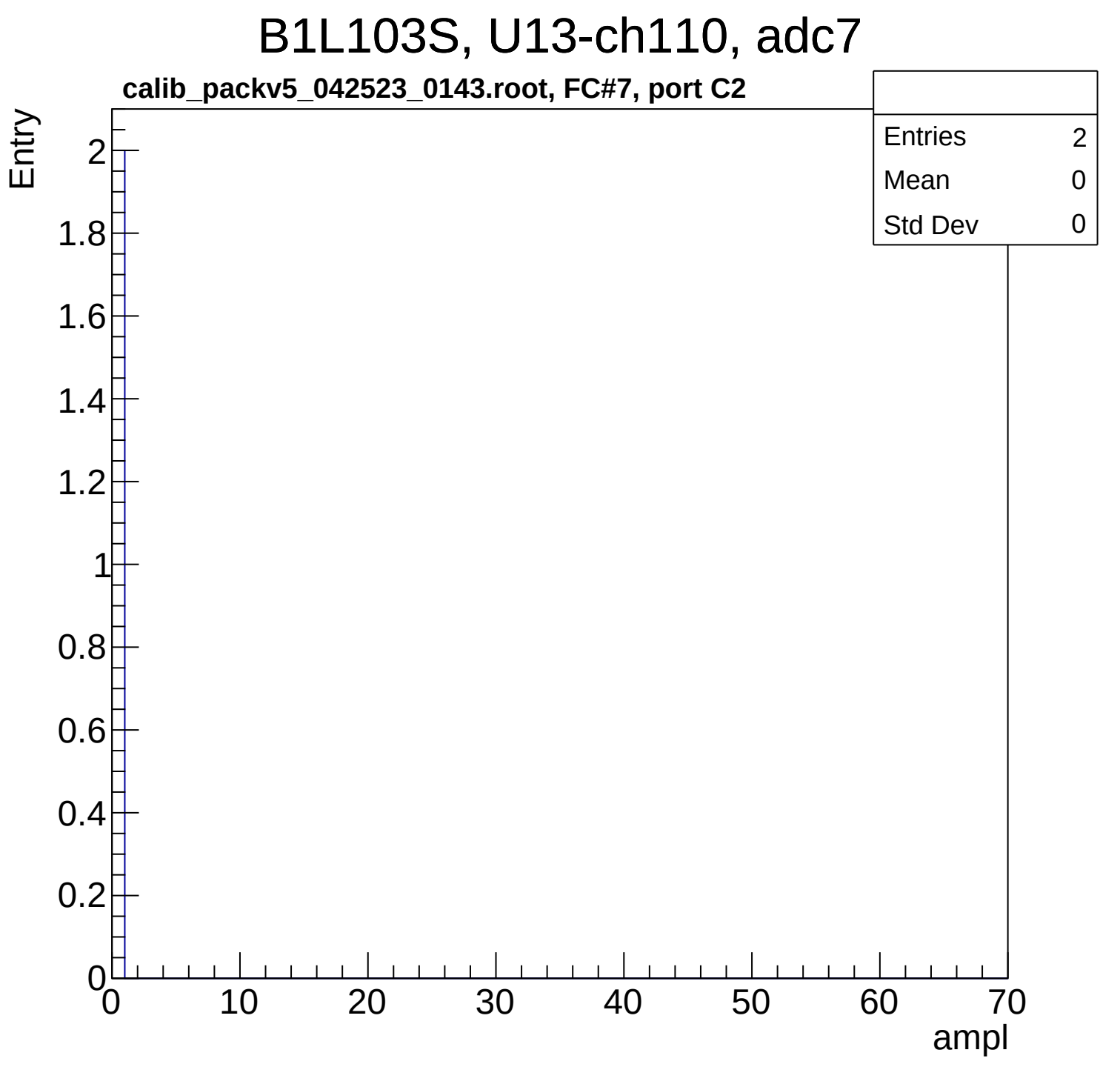
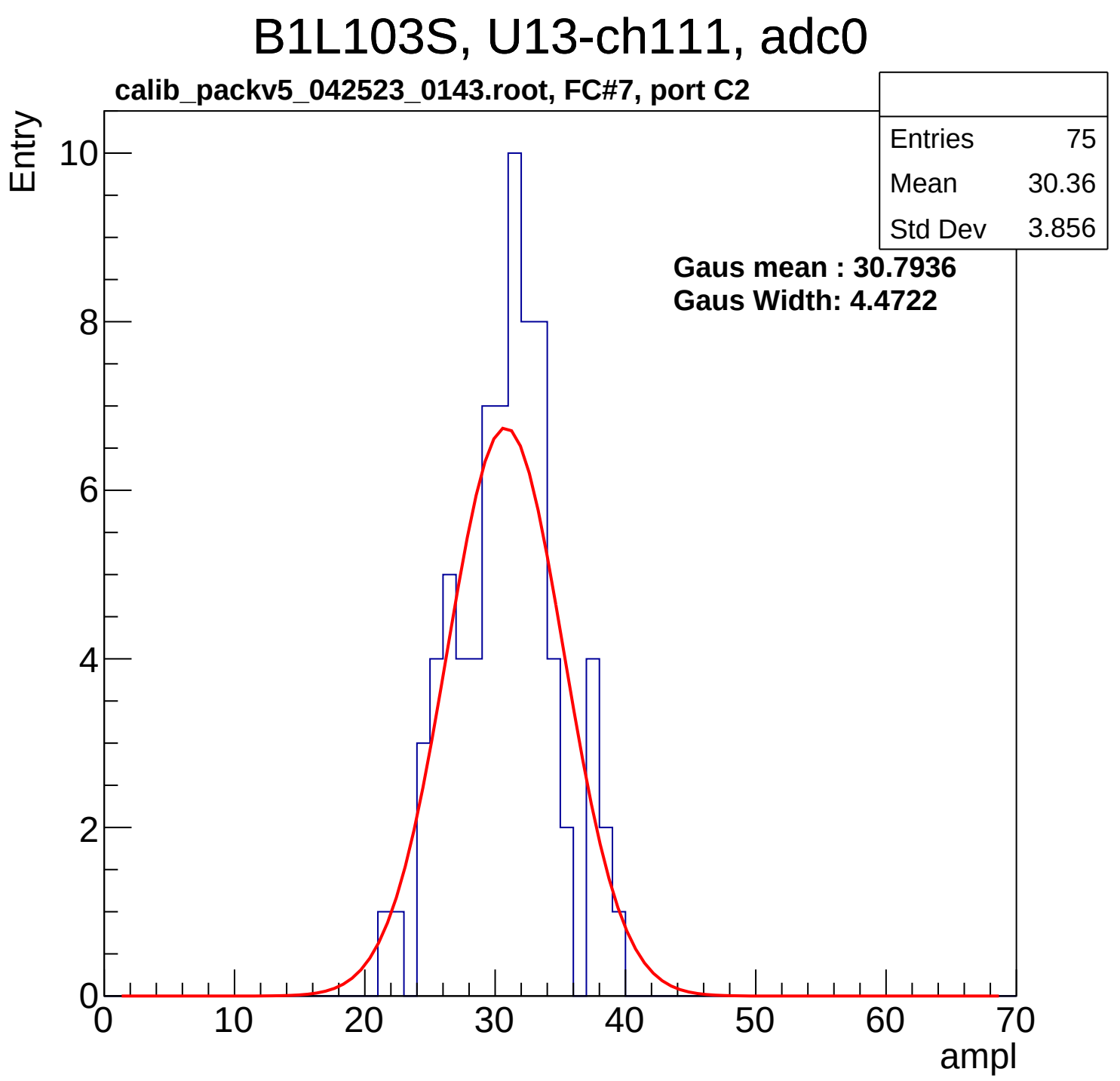
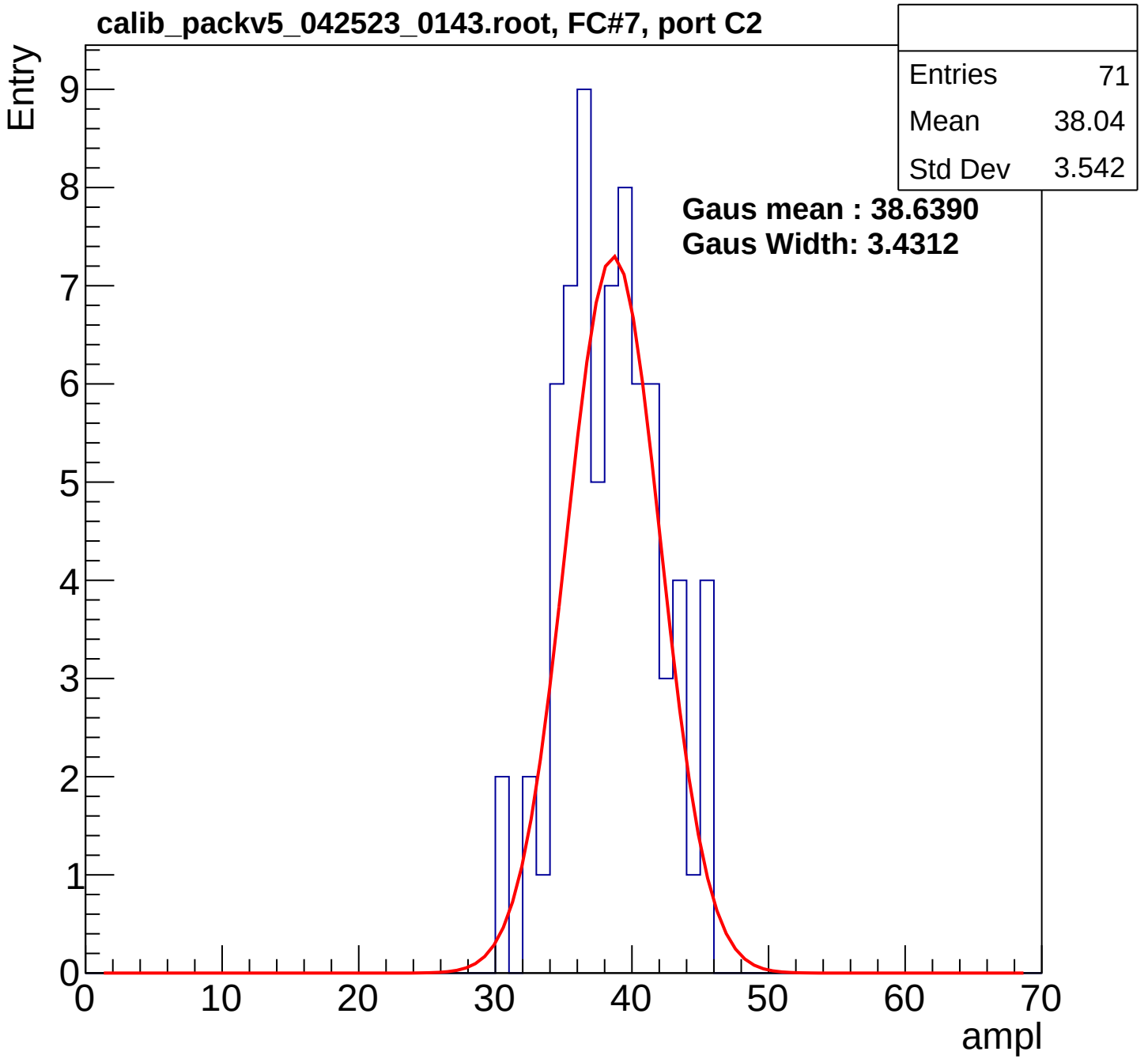
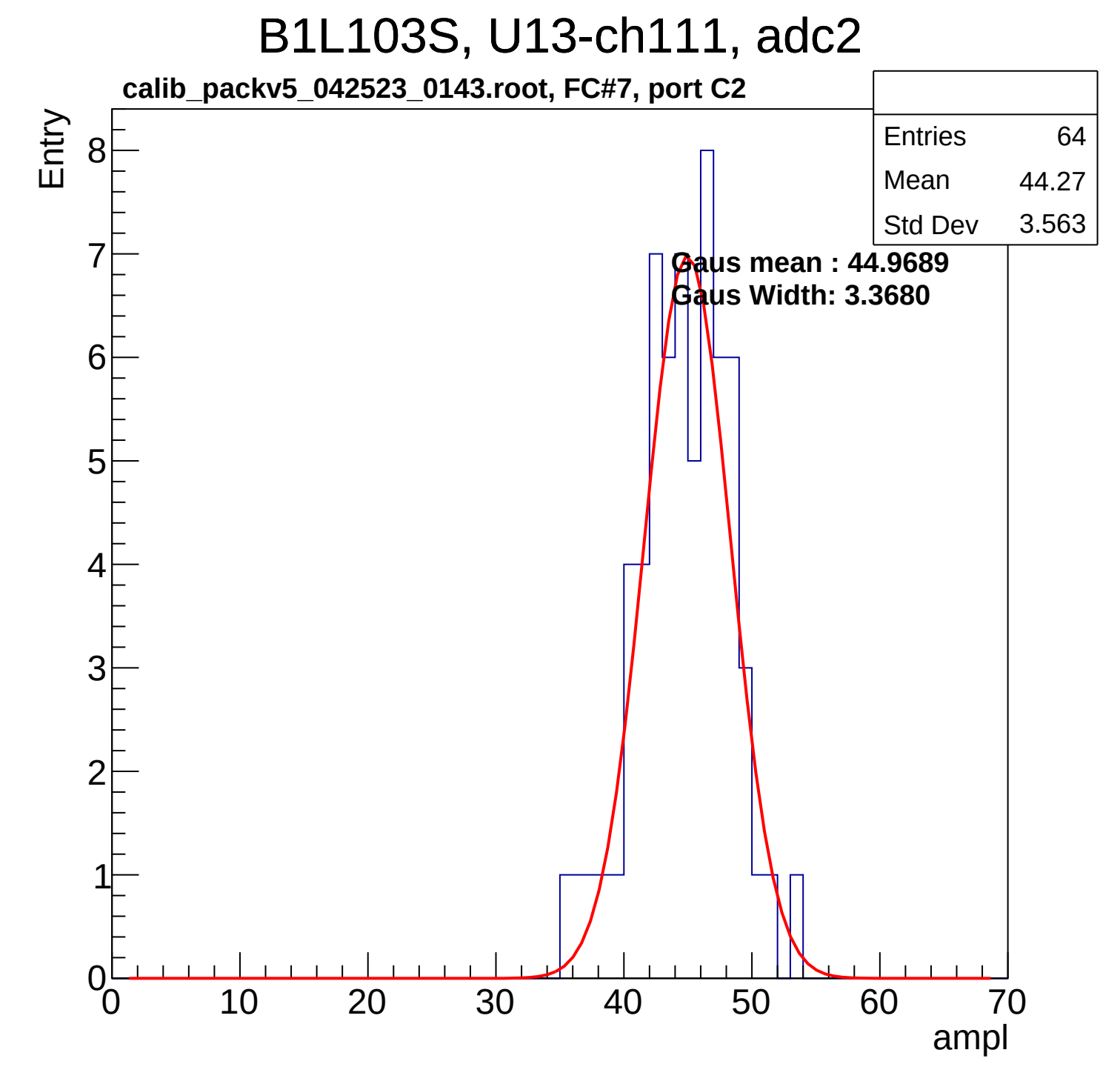
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch110, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch110, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch111, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.36
Std Dev 3.856
Gaus mean : 30.7936
Gaus Width: 4.4722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch111, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 38.04
Std Dev 3.542
Gaus mean : 38.6390
Gaus Width: 3.4312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch111, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.27
Std Dev 3.563
Gaus mean : 44.9689
Gaus Width: 3.3680
Entry
ampl
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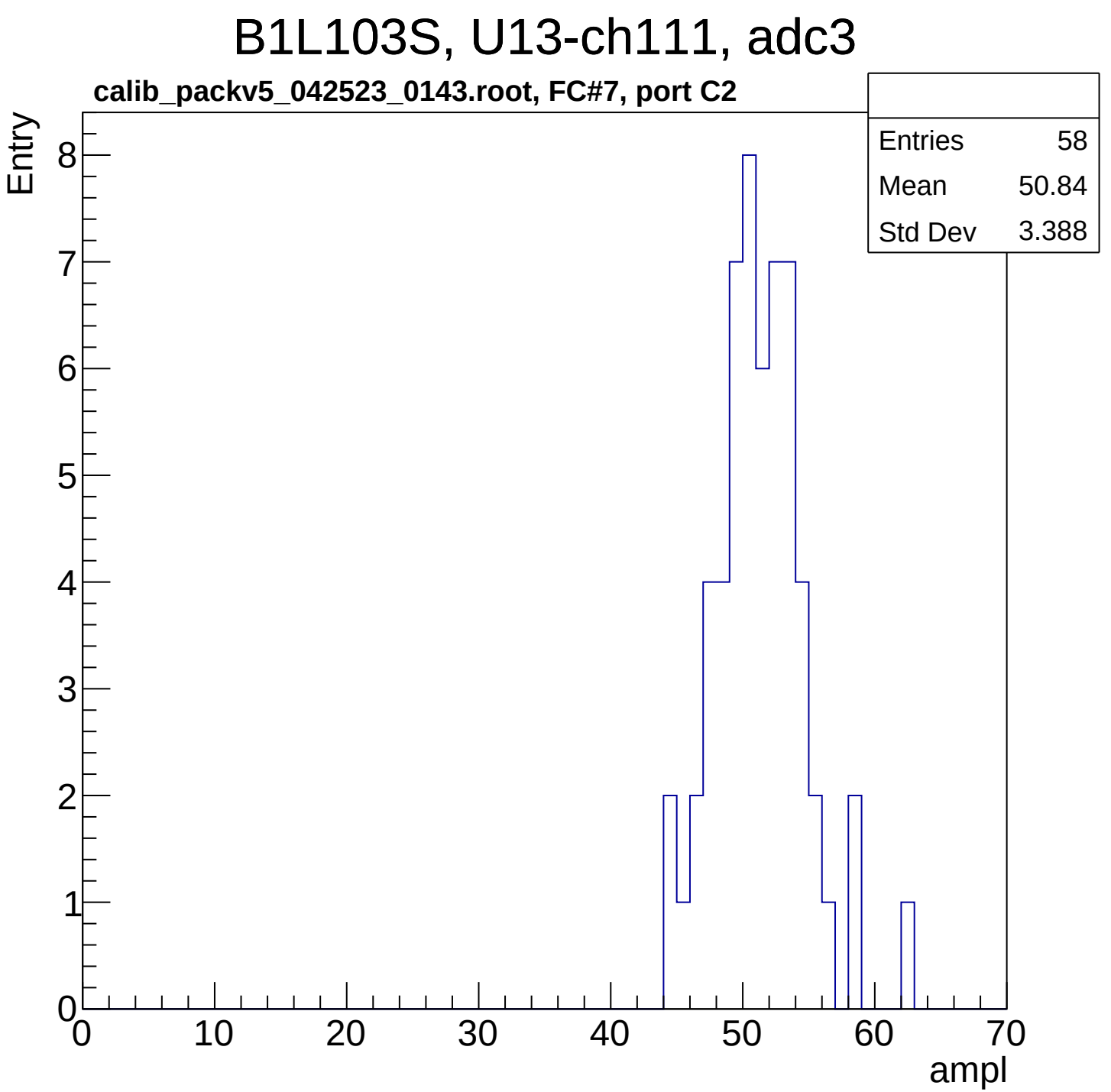
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch111, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 50.84
Std Dev 3.388
Entry
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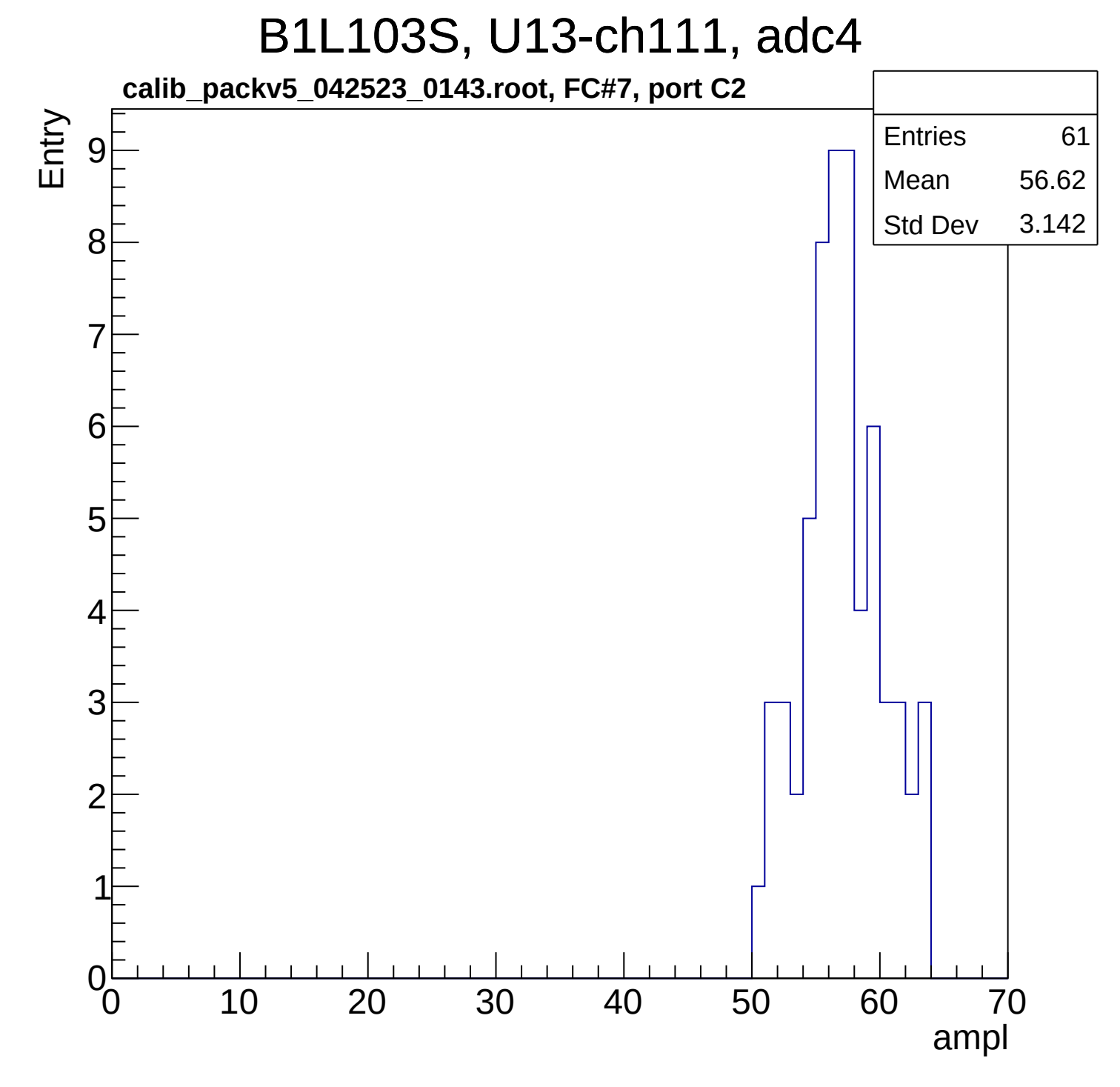
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch111, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.62
Std Dev 3.142
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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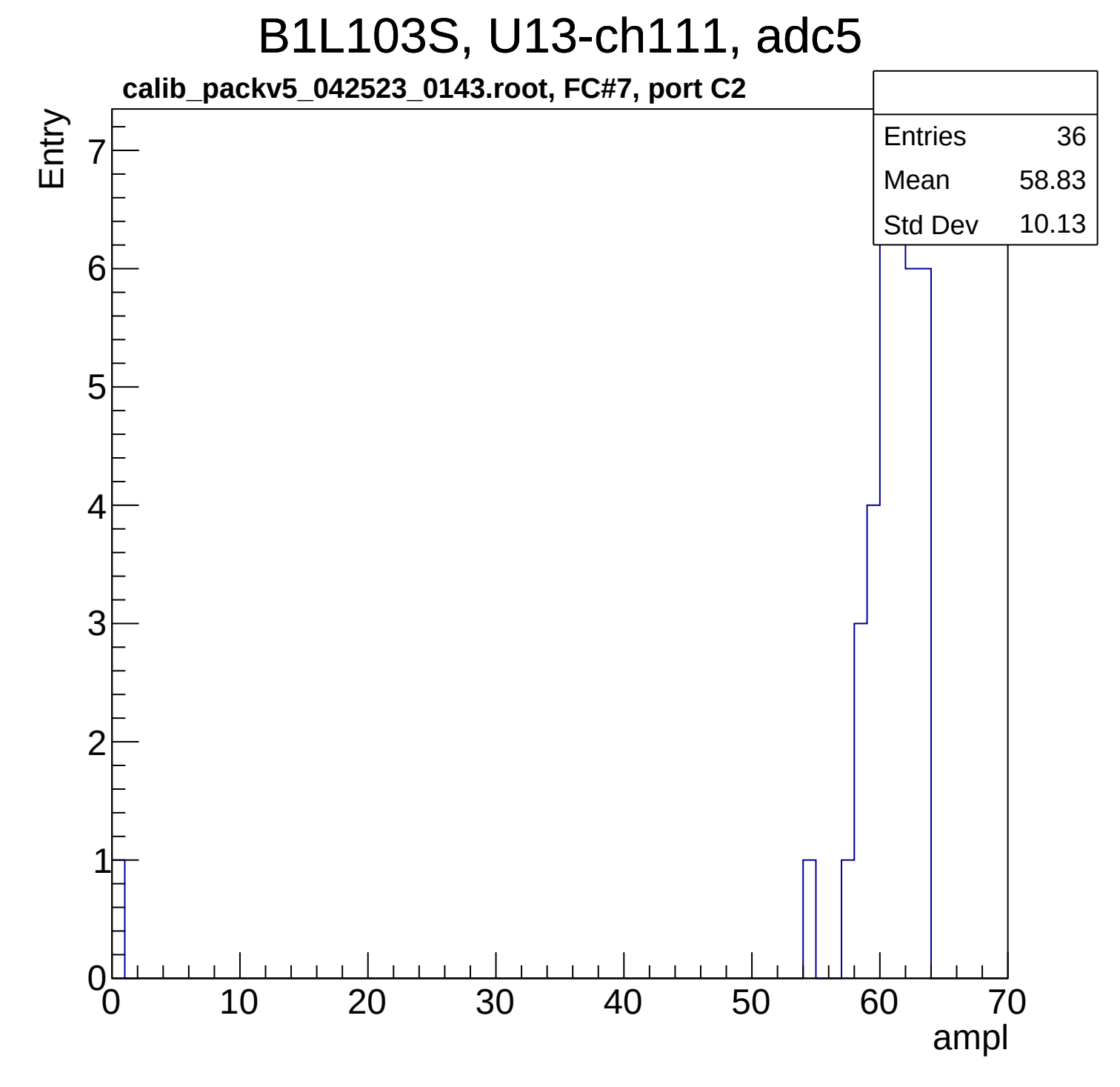
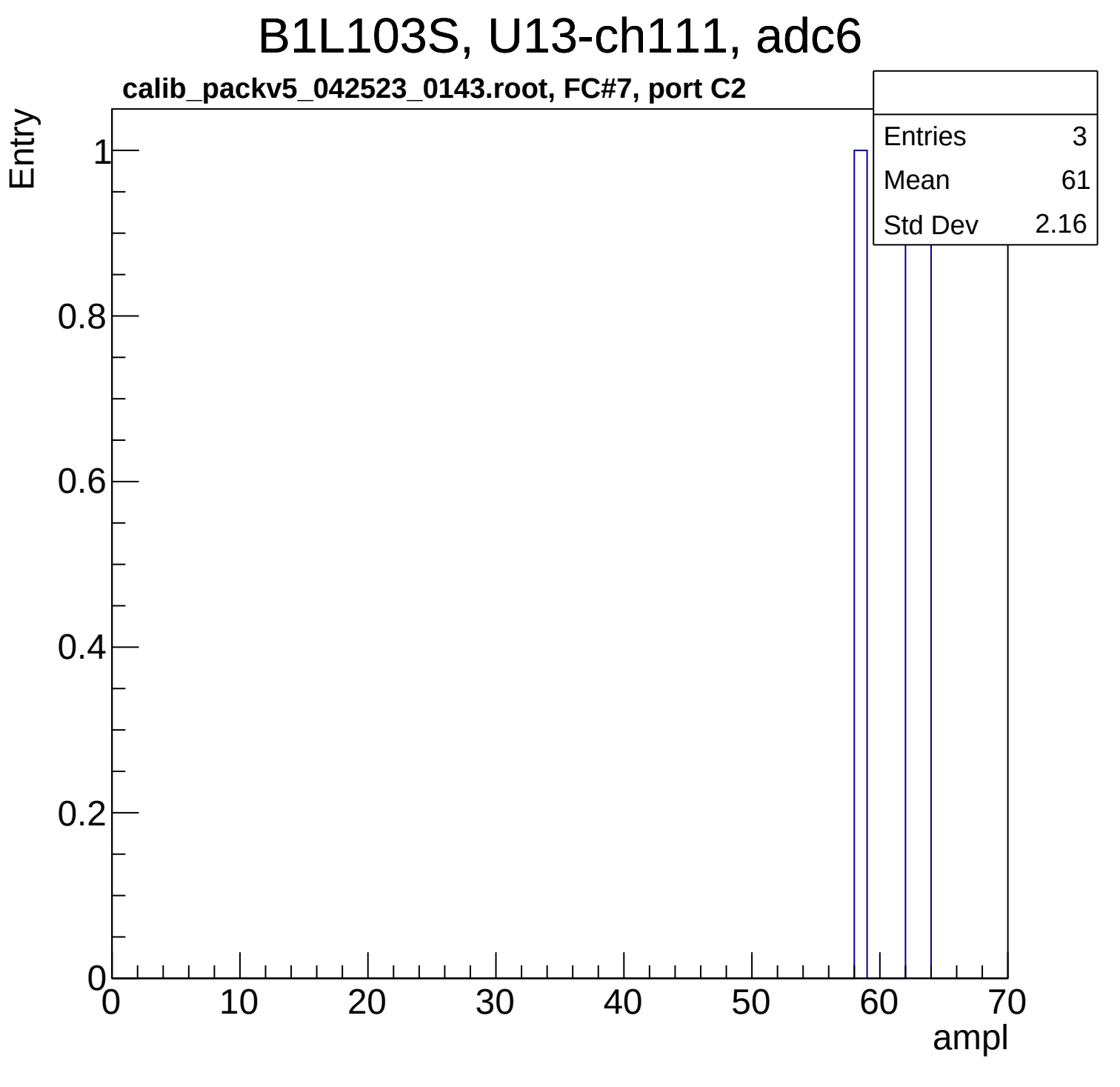
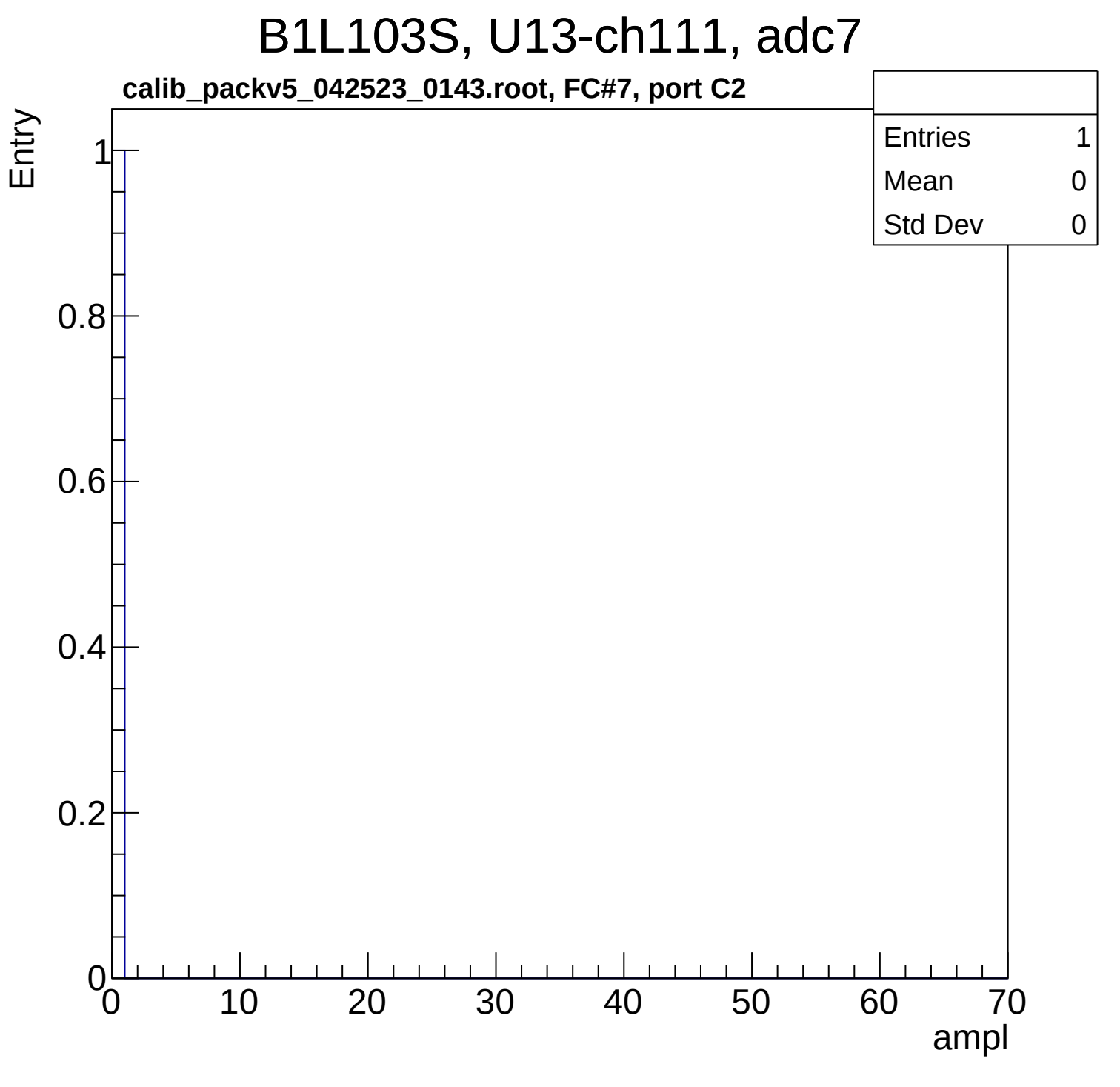

B1L103S, U13-ch111, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.83
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch111, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61
Std Dev 2.16
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch111, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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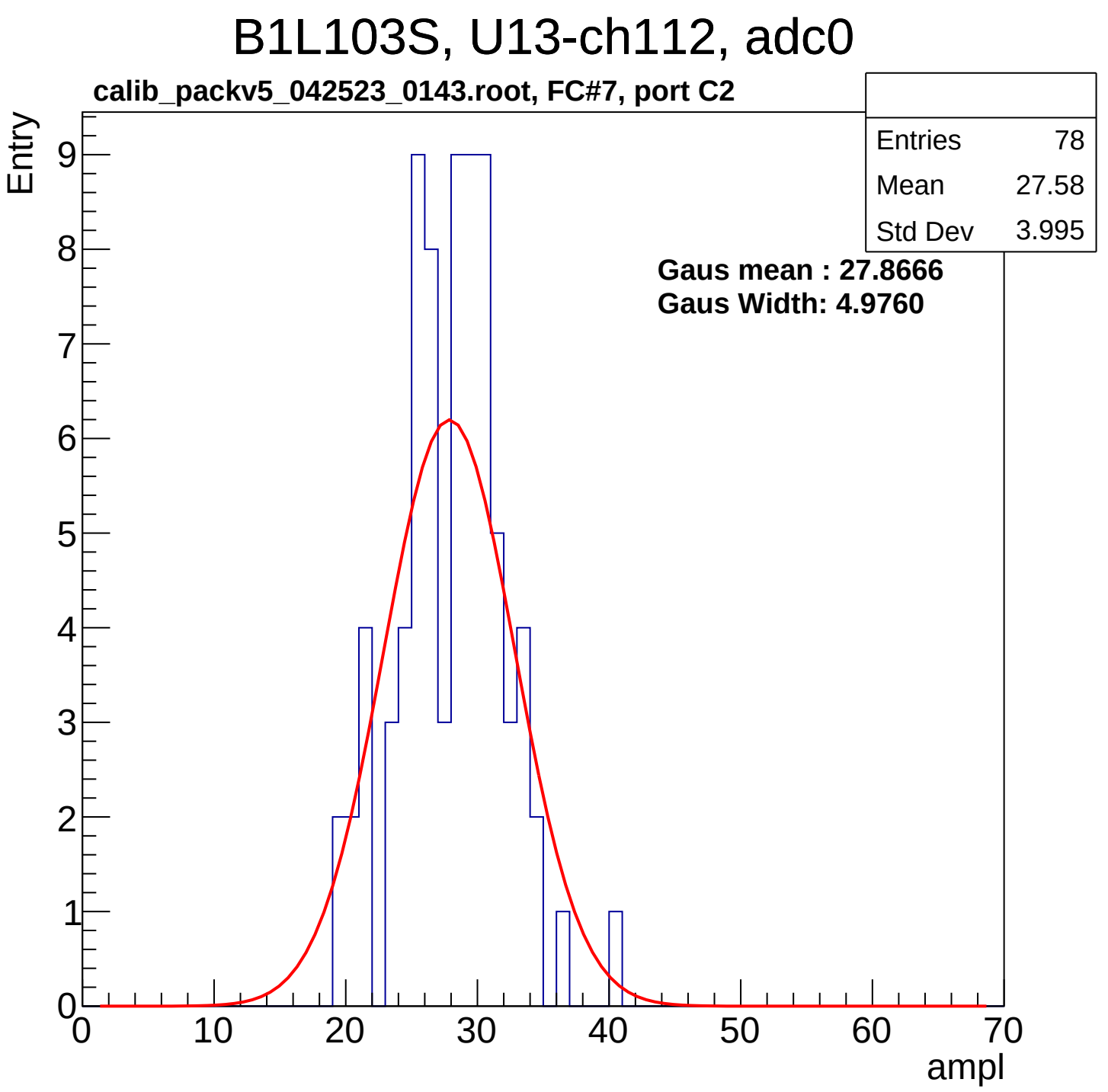
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch112, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 27.58
Std Dev 3.995
Gaus mean : 27.8666
Gaus Width: 4.9760
Entry
ampl
0
1
2
3
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5
6
7
8
9
0
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60
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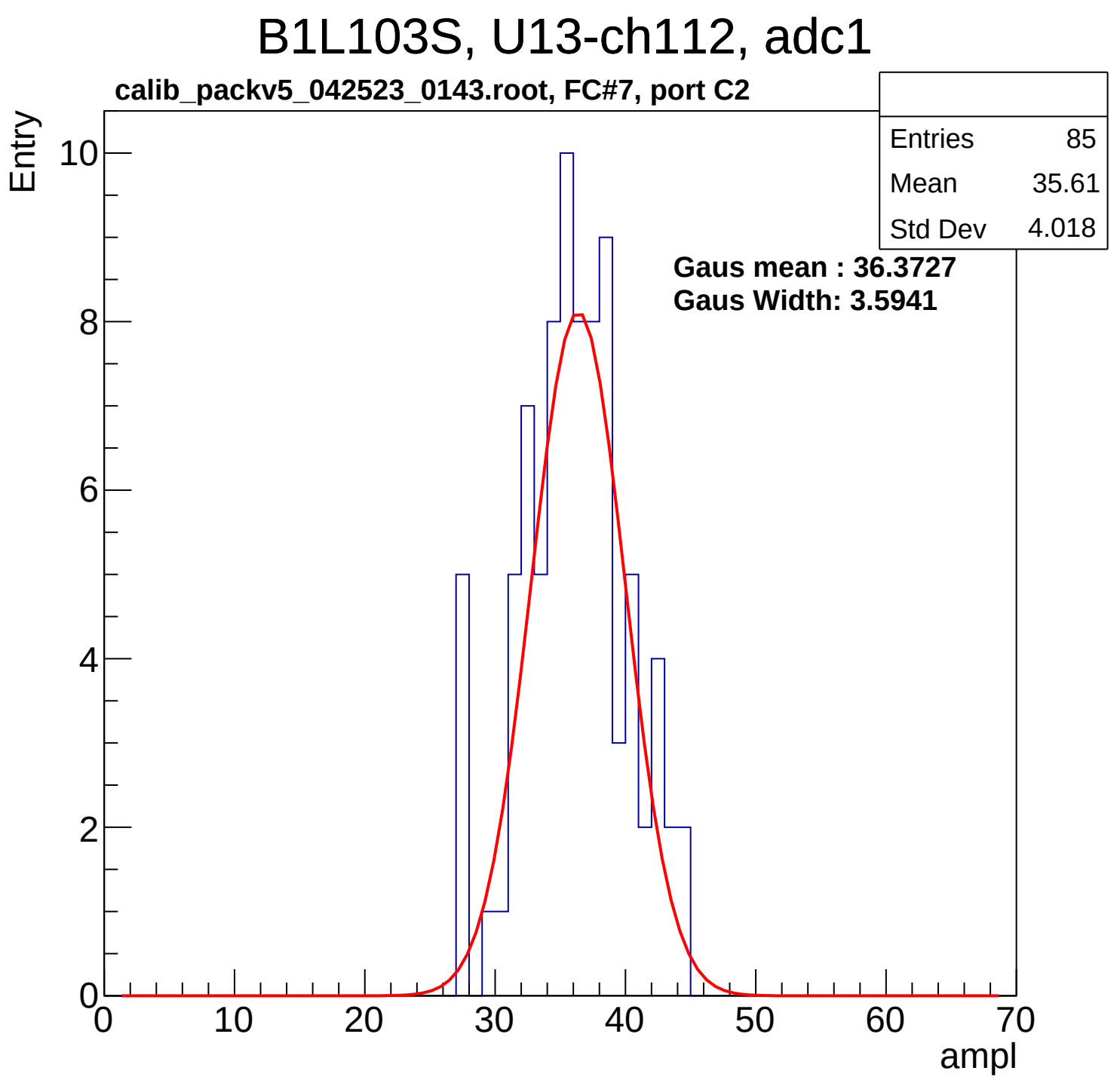
B1L103S, U13-ch112, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 35.61
Std Dev 4.018
Gaus mean : 36.3727
Gaus Width: 3.5941
Entry
ampl
0
2
4
6
8
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0
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40
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60
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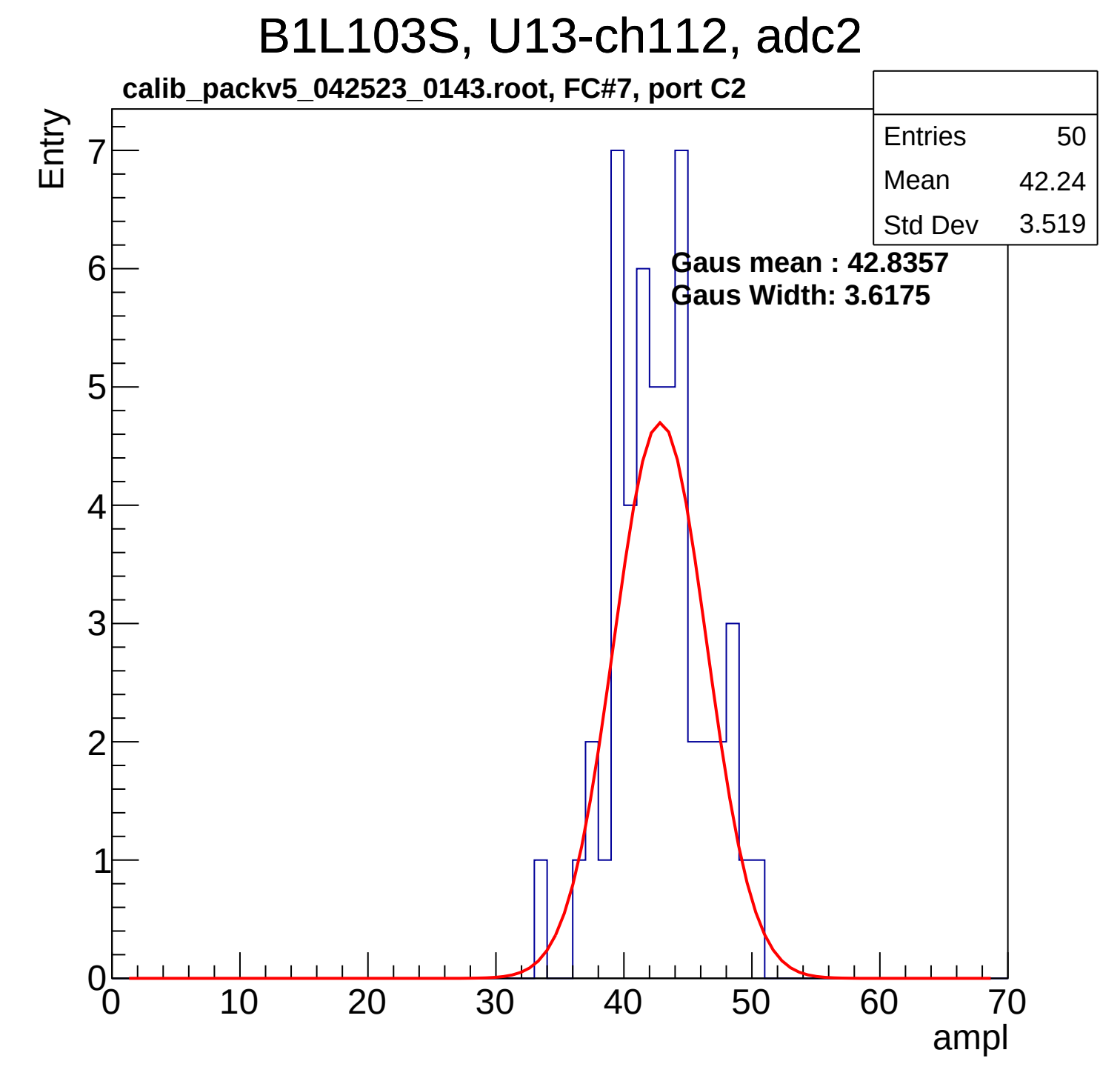
B1L103S, U13-ch112, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 42.24
Std Dev 3.519
Gaus mean : 42.8357
Gaus Width: 3.6175
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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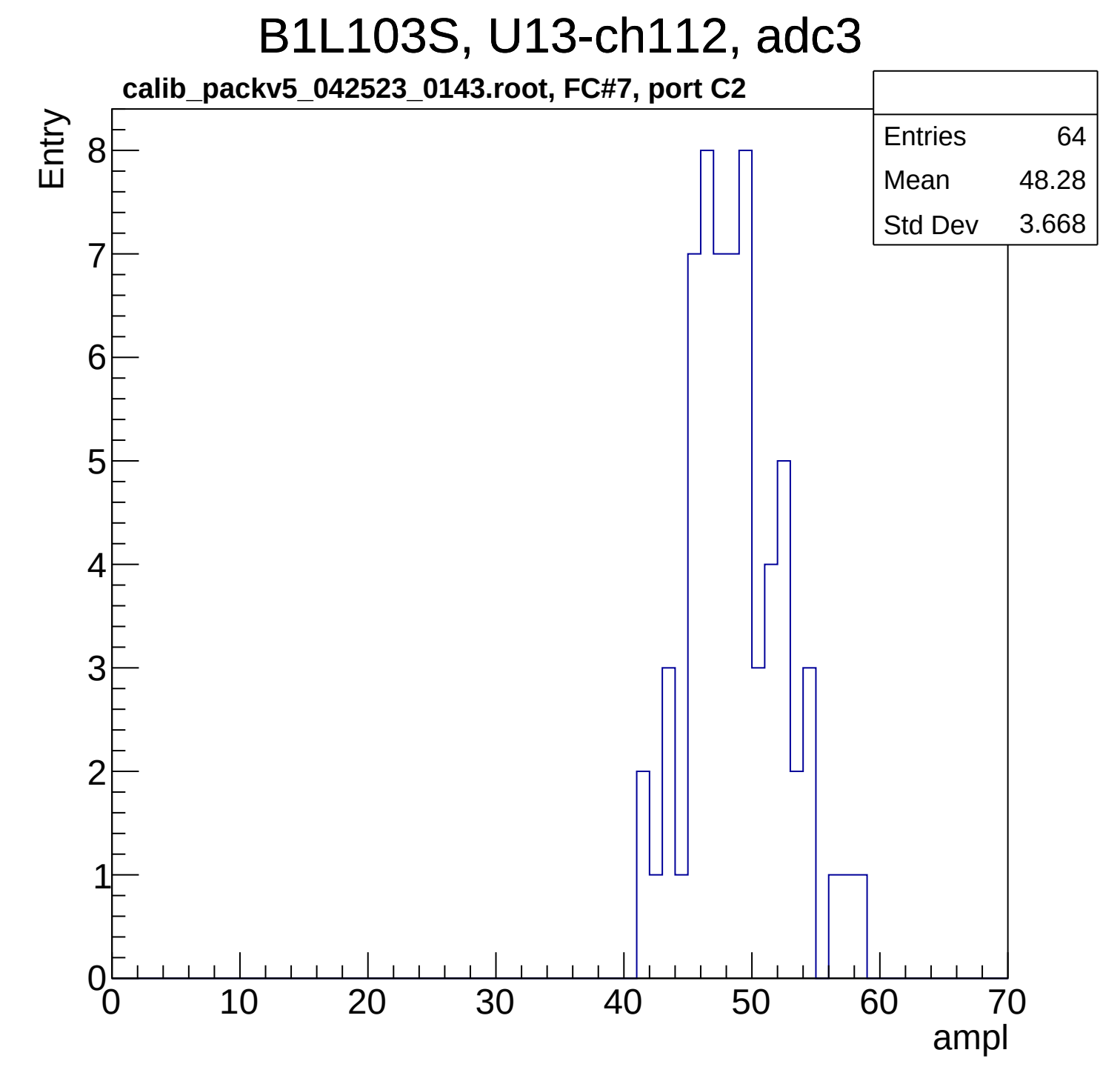
B1L103S, U13-ch112, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.28
Std Dev 3.668
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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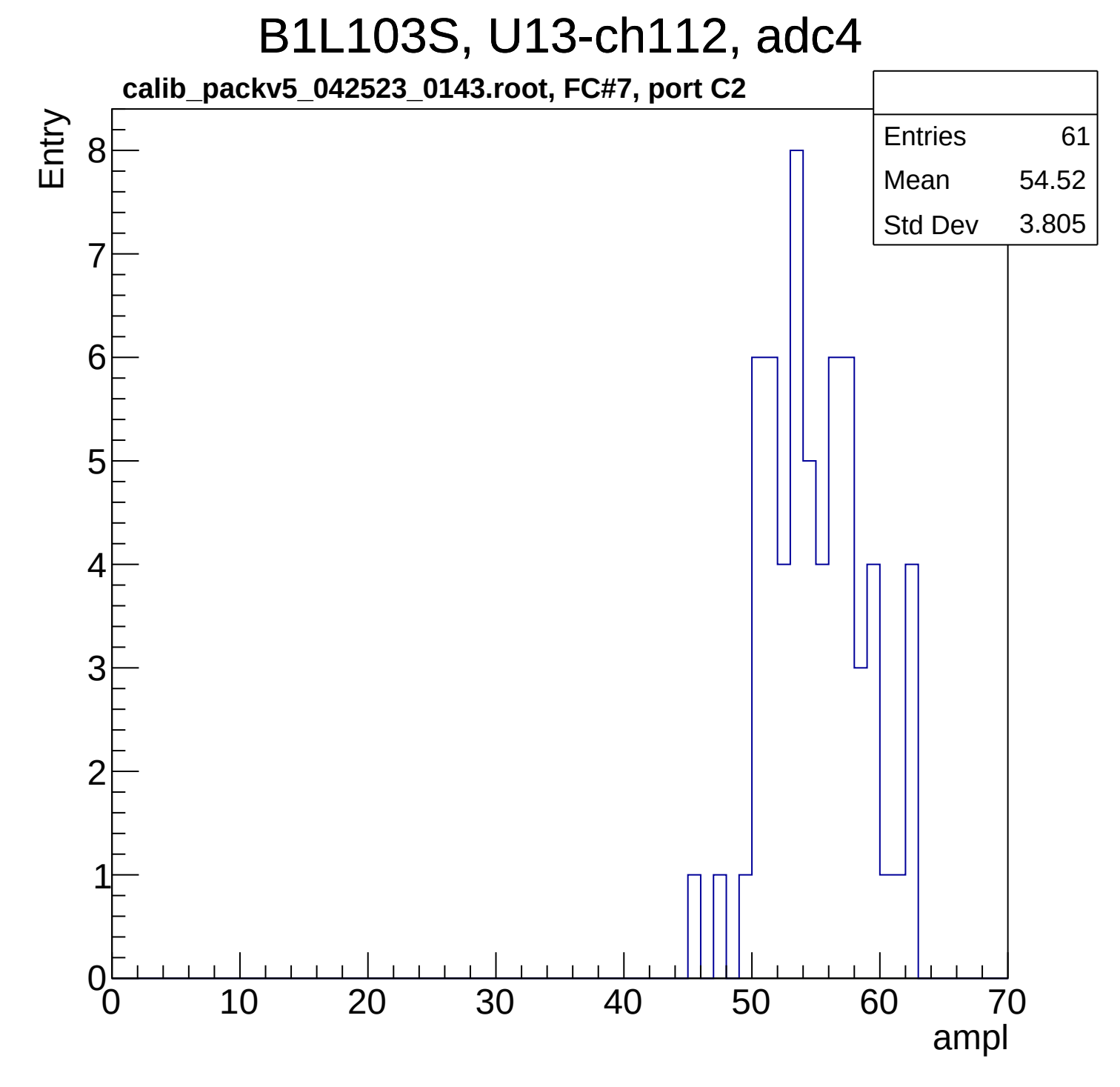
B1L103S, U13-ch112, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 54.52
Std Dev 3.805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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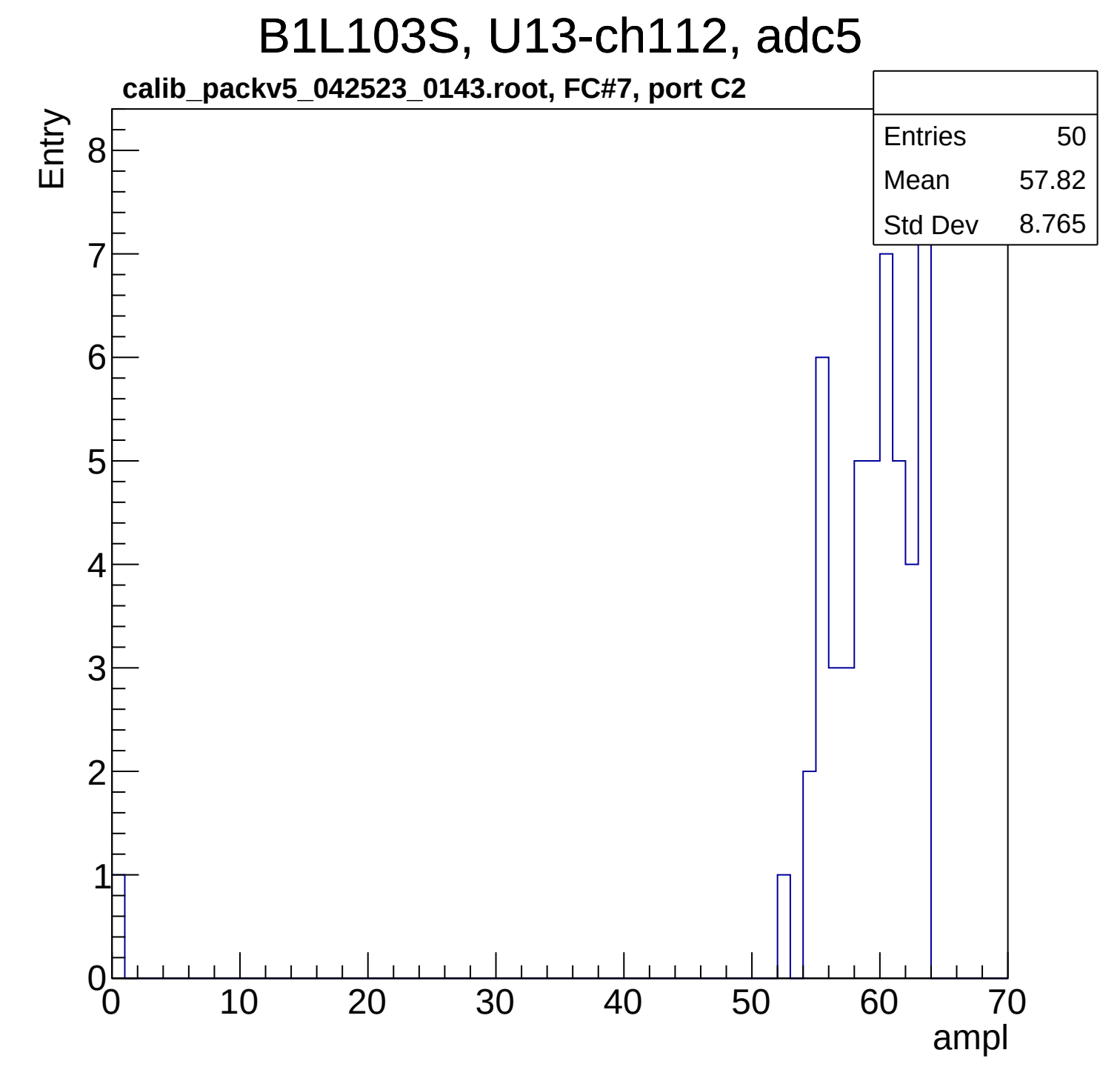
B1L103S, U13-ch112, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 57.82
Std Dev 8.765
Entry
ampl
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1
2
3
4
5
6
7
8
0
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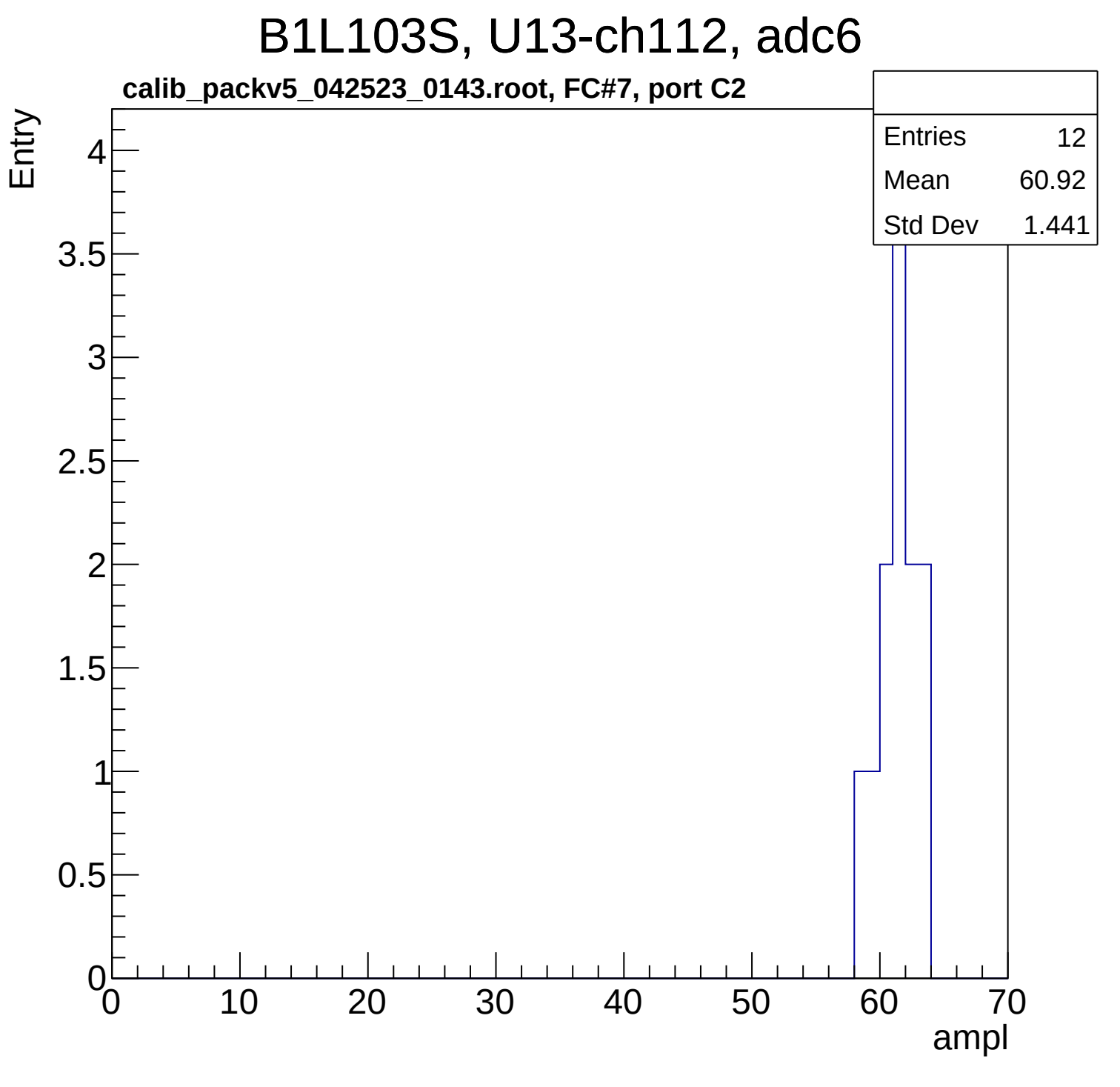
B1L103S, U13-ch112, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 60.92
Std Dev 1.441
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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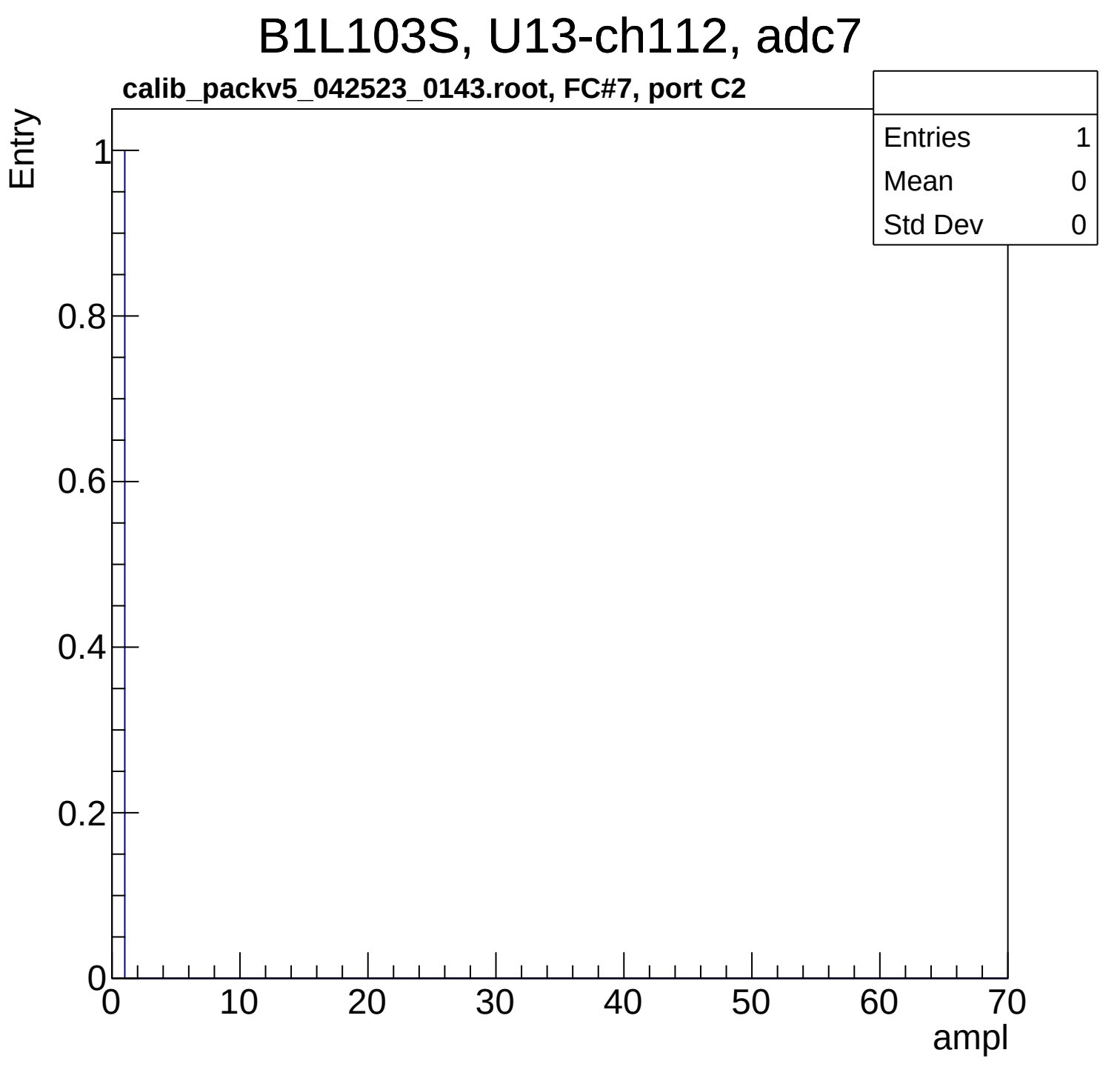
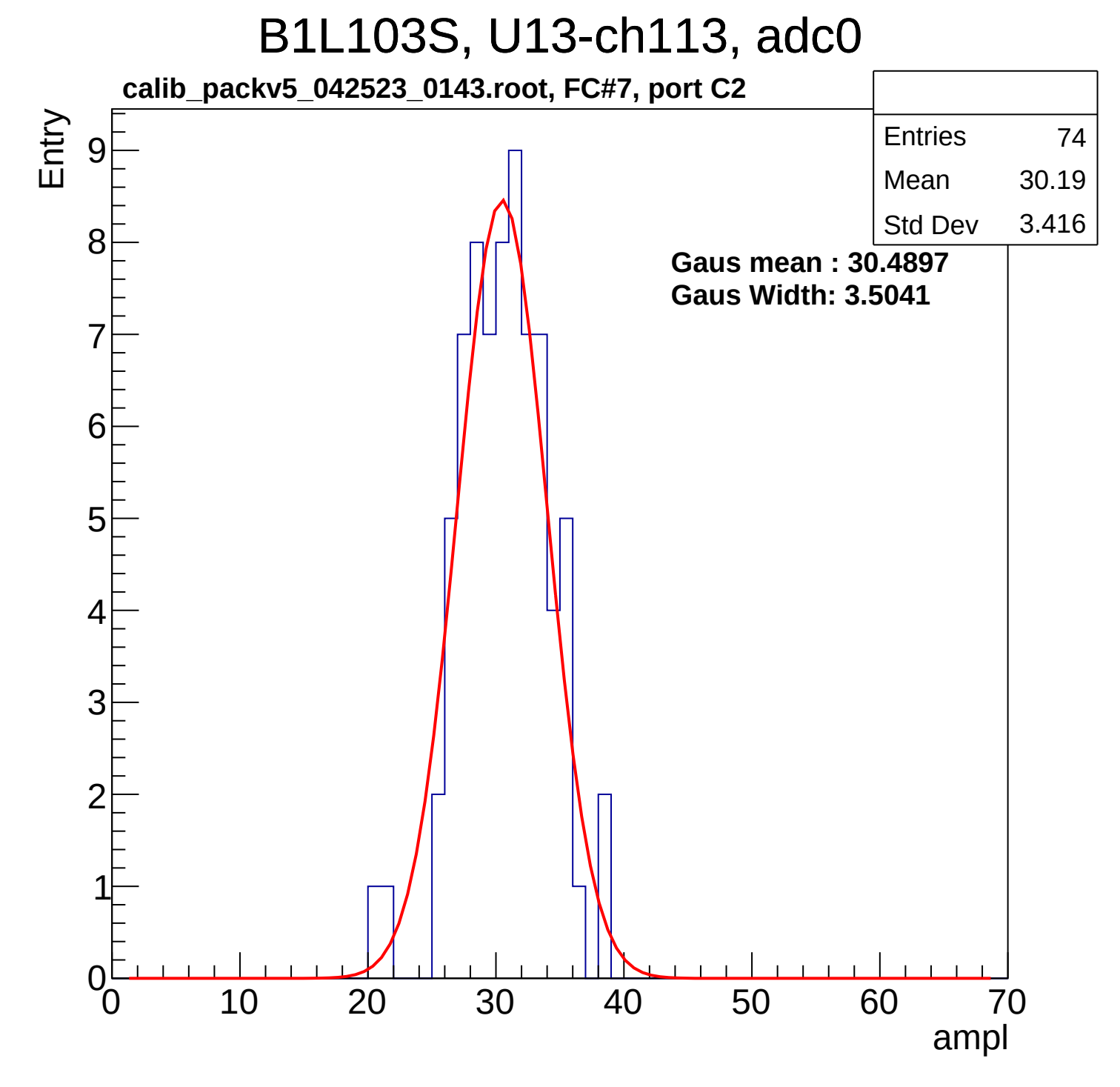

B1L103S, U13-ch112, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch113, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.19
Std Dev 3.416
Gaus mean : 30.4897
Gaus Width: 3.5041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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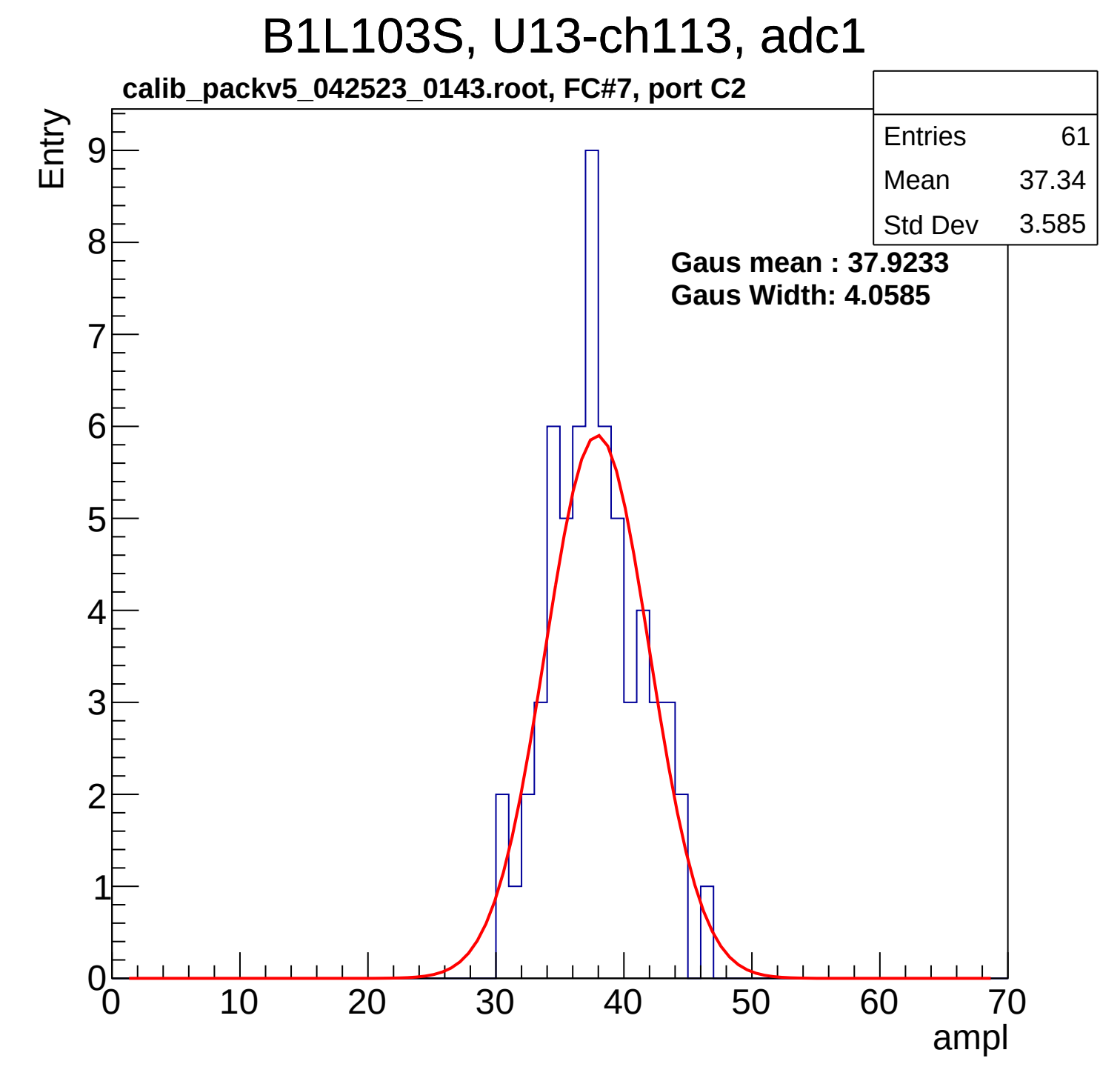
B1L103S, U13-ch113, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37.34
Std Dev 3.585
Gaus mean : 37.9233
Gaus Width: 4.0585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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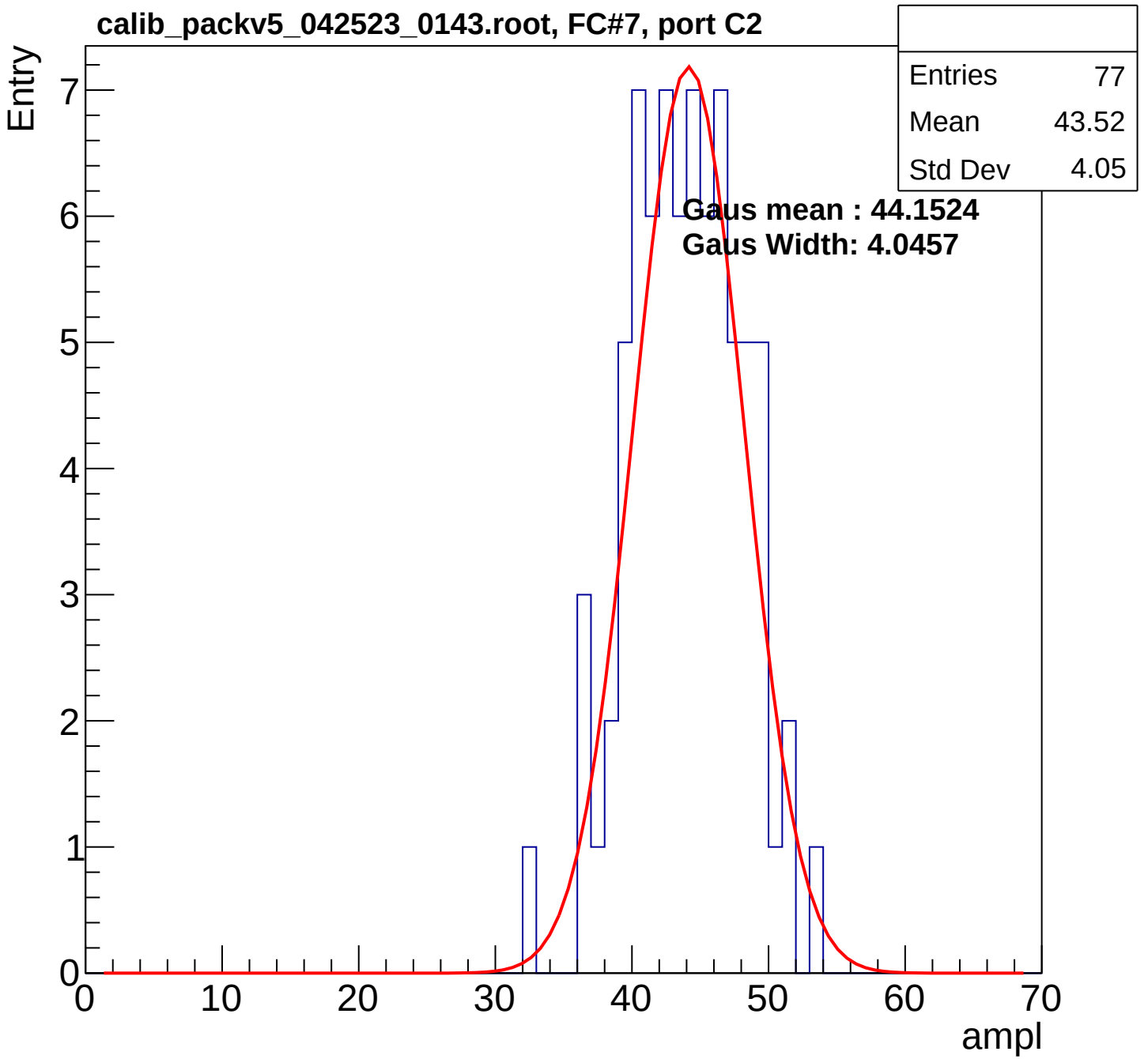
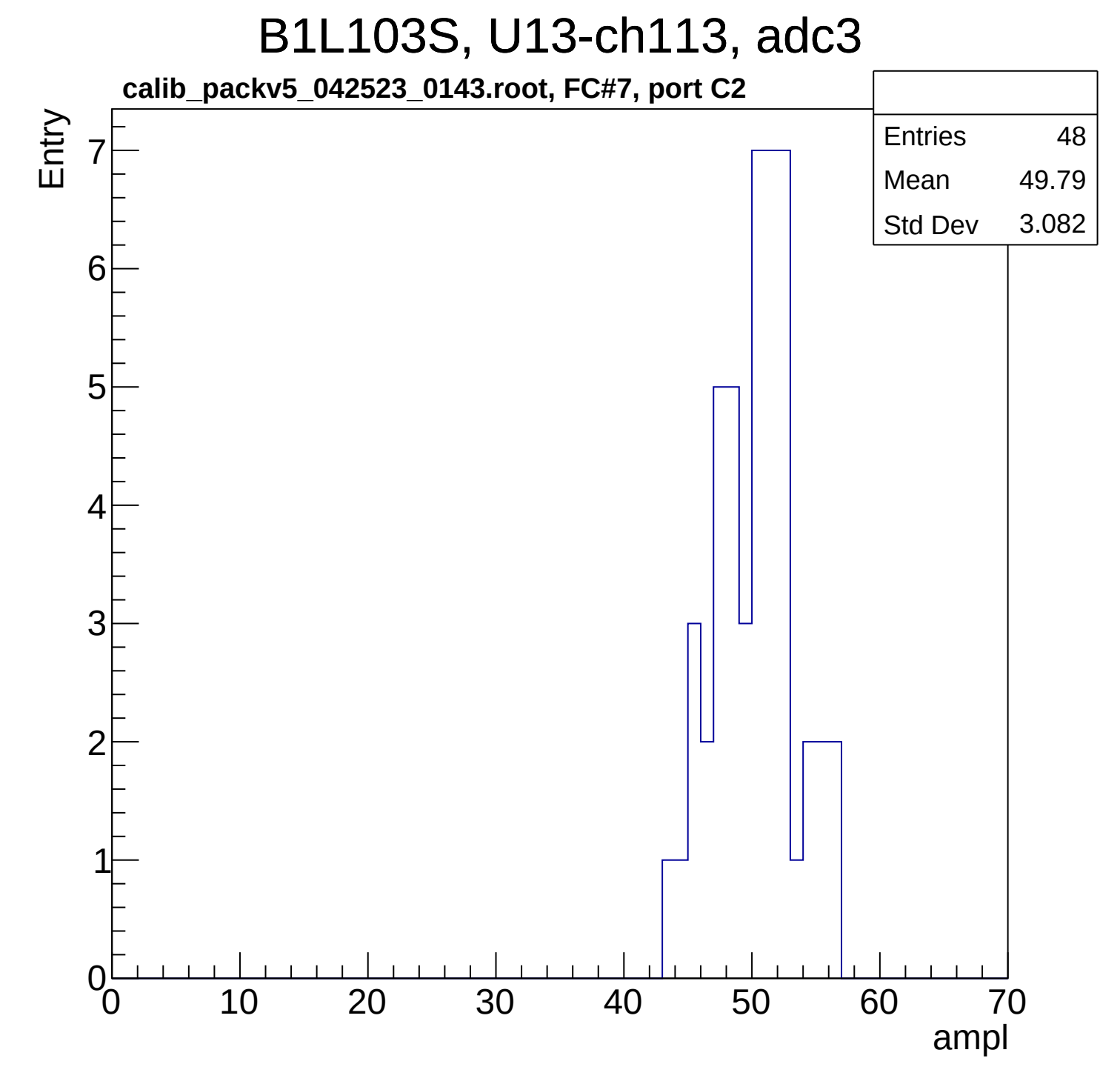
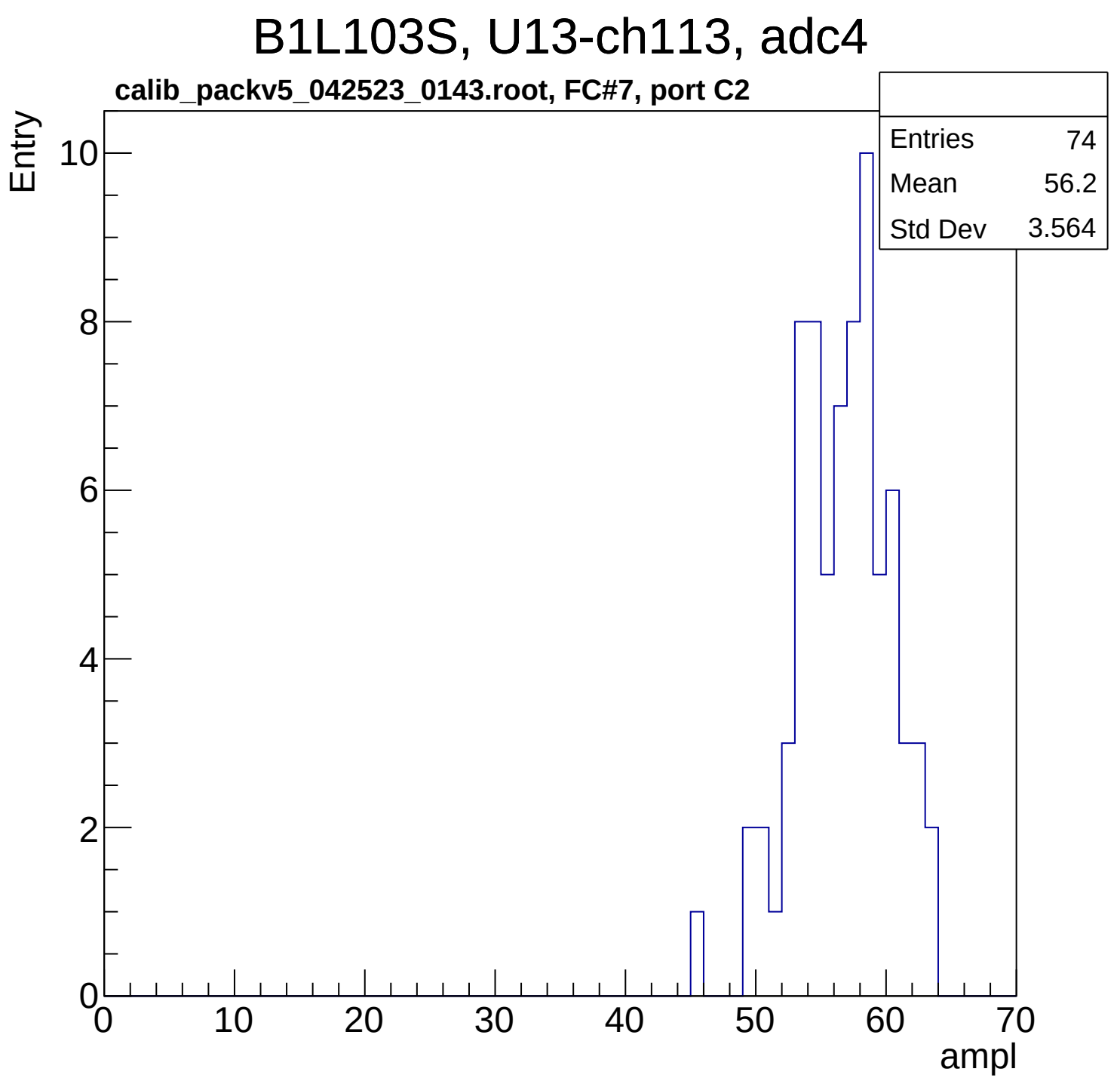

B1L103S, U13-ch113, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 43.52
Std Dev 4.05
Gaus mean : 44.1524
Gaus Width: 4.0457
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch113, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 49.79
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch113, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 56.2
Std Dev 3.564
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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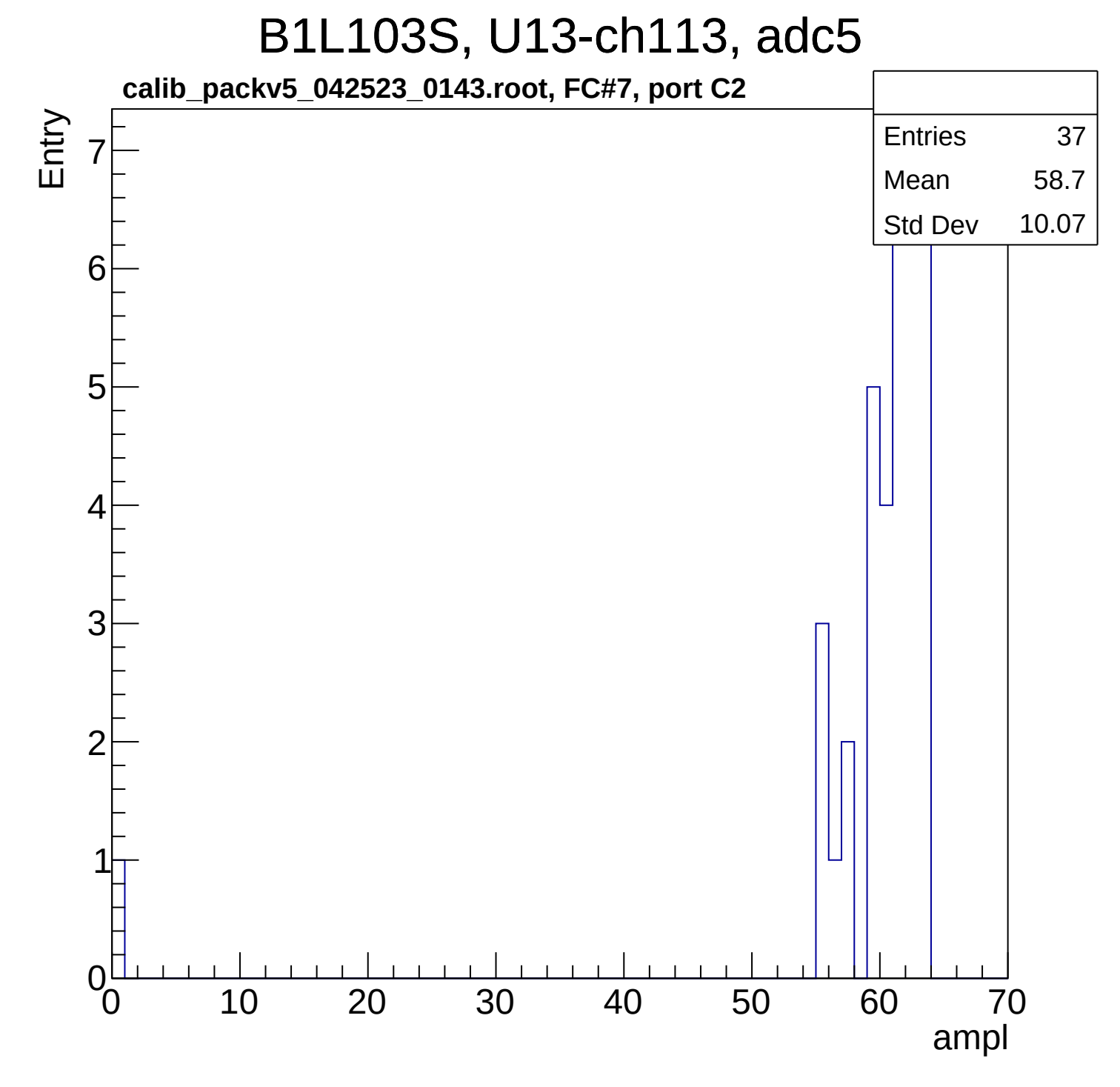
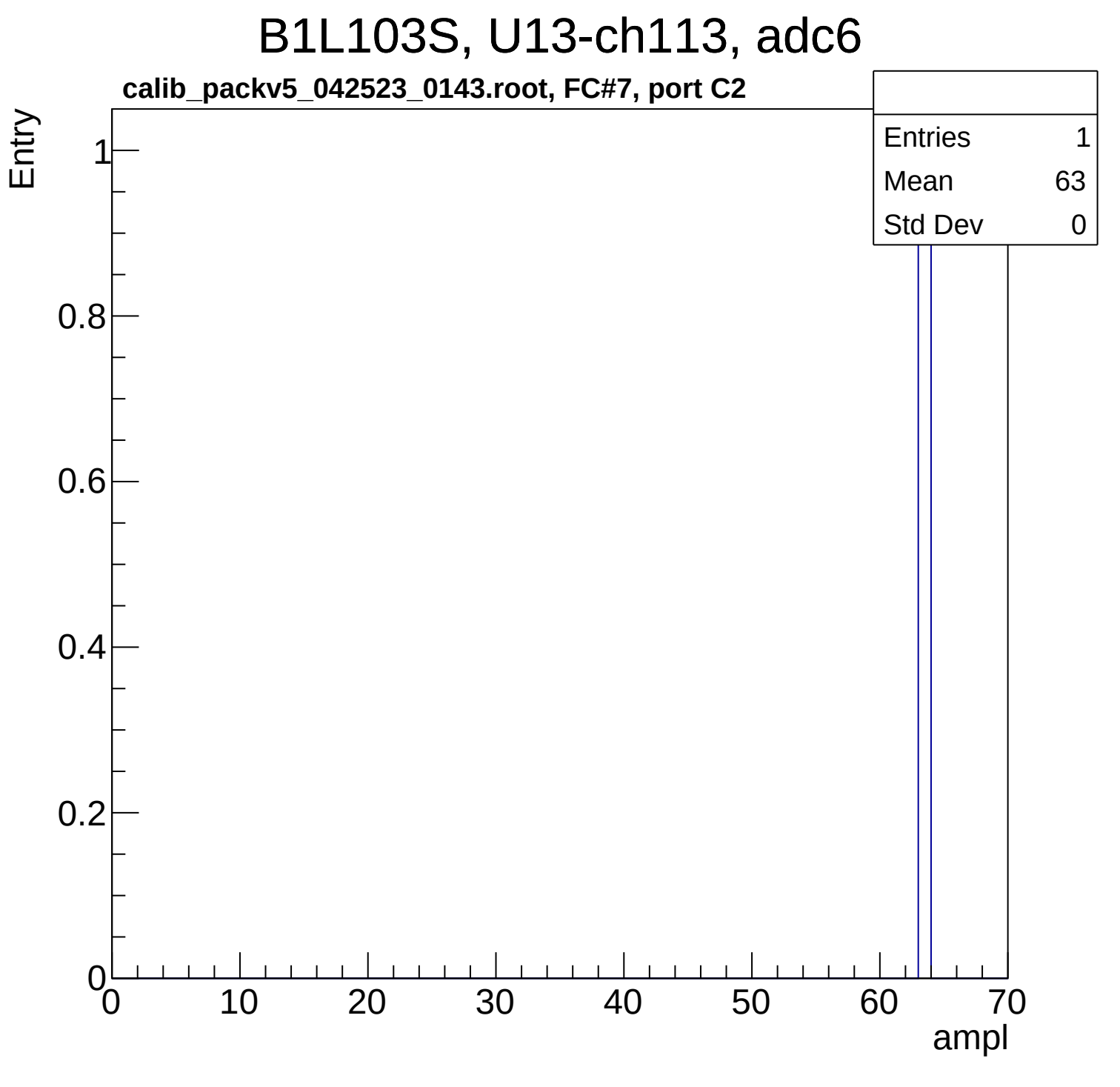
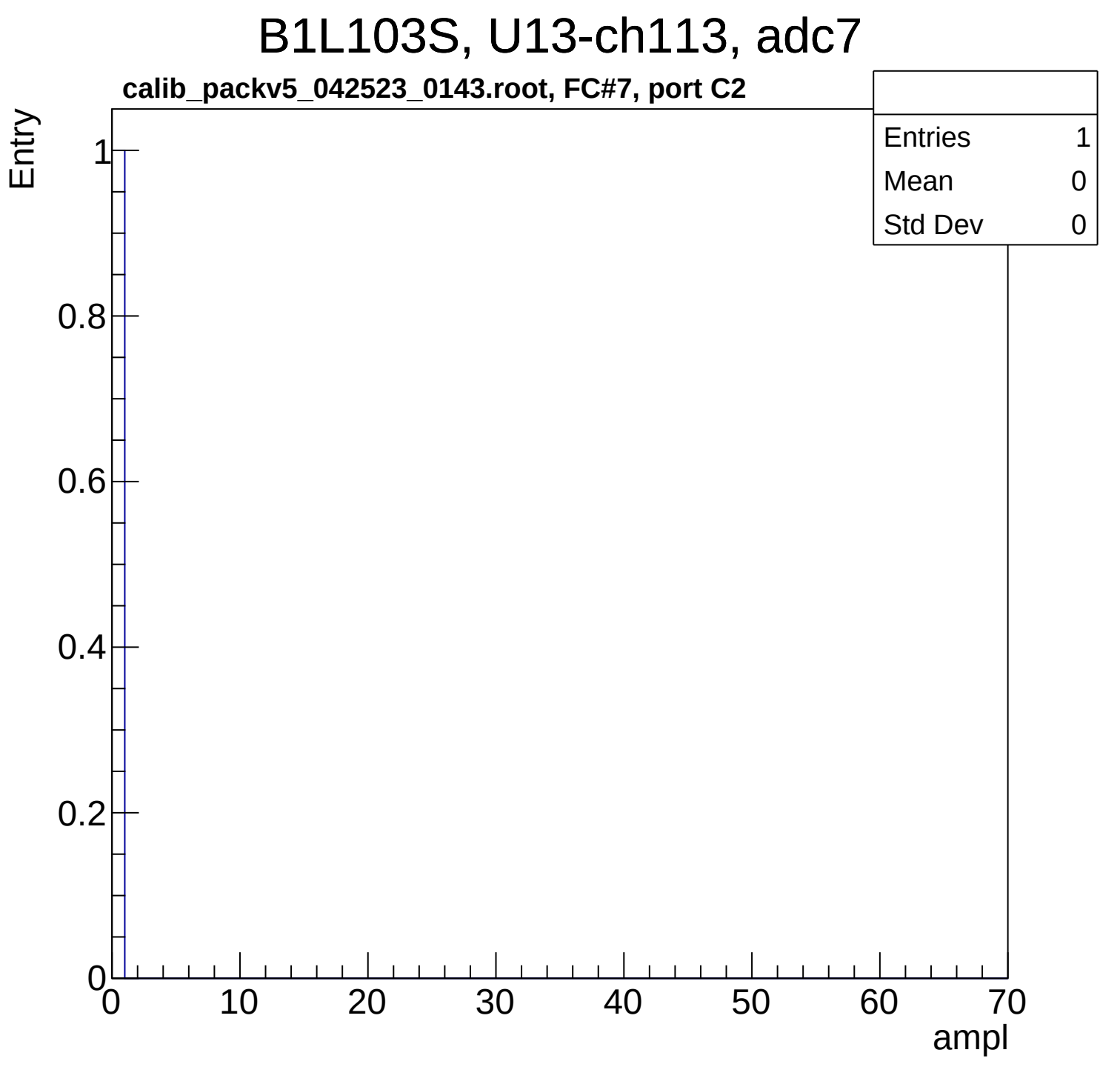
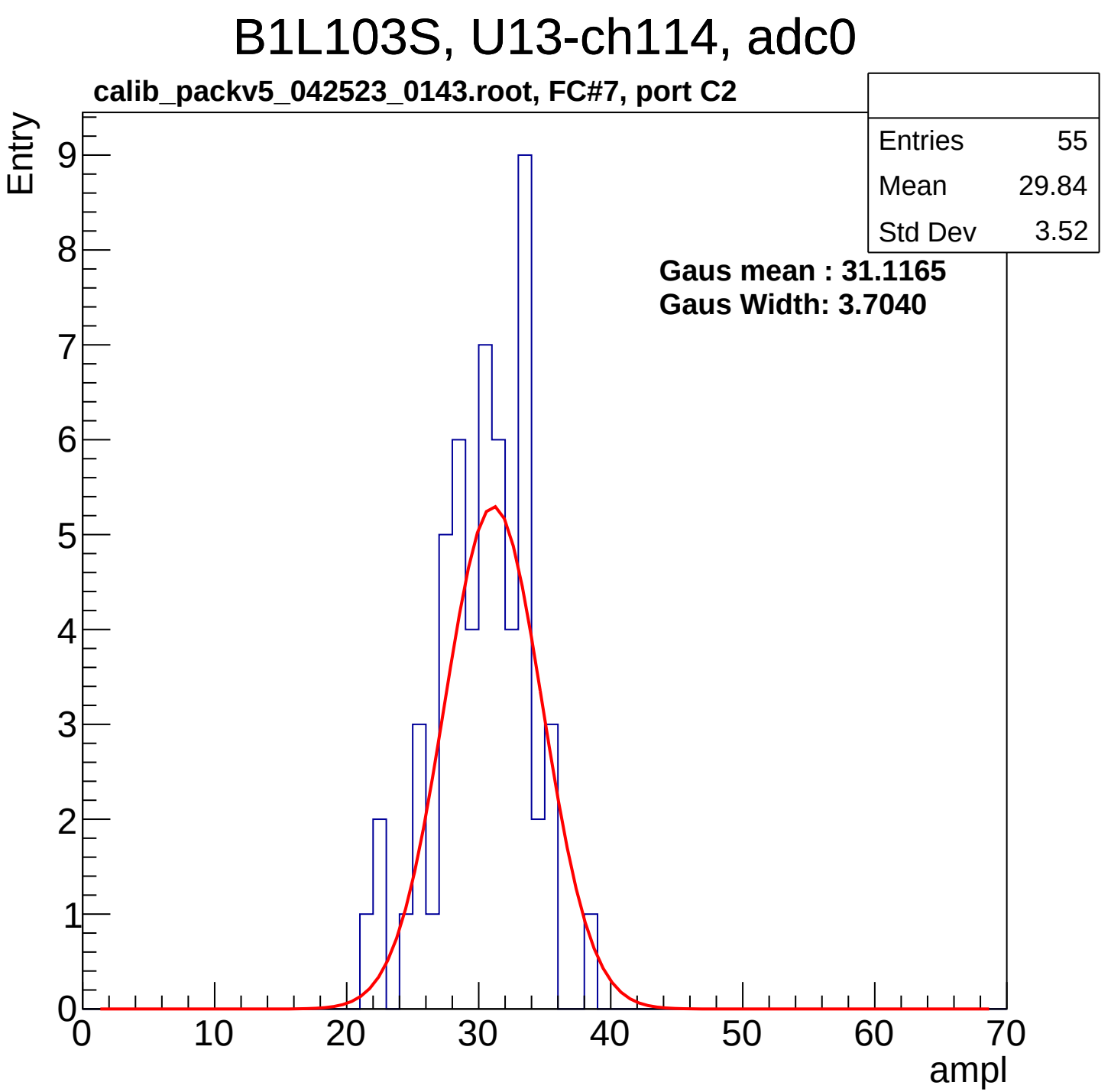
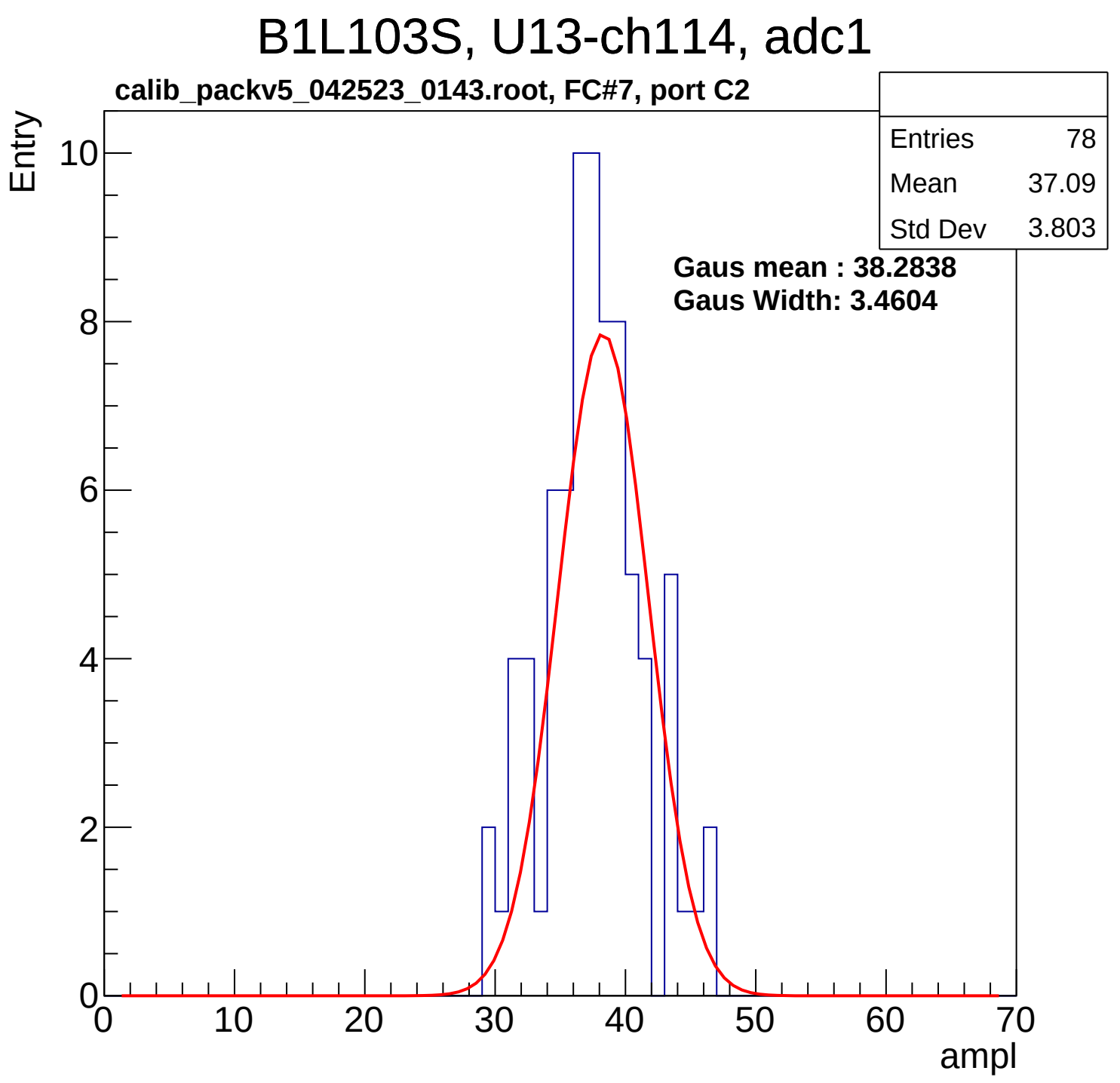

B1L103S, U13-ch113, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 58.7
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch113, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch113, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch114, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 29.84
Std Dev 3.52
Gaus mean : 31.1165
Gaus Width: 3.7040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch114, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 37.09
Std Dev 3.803
Gaus mean : 38.2838
Gaus Width: 3.4604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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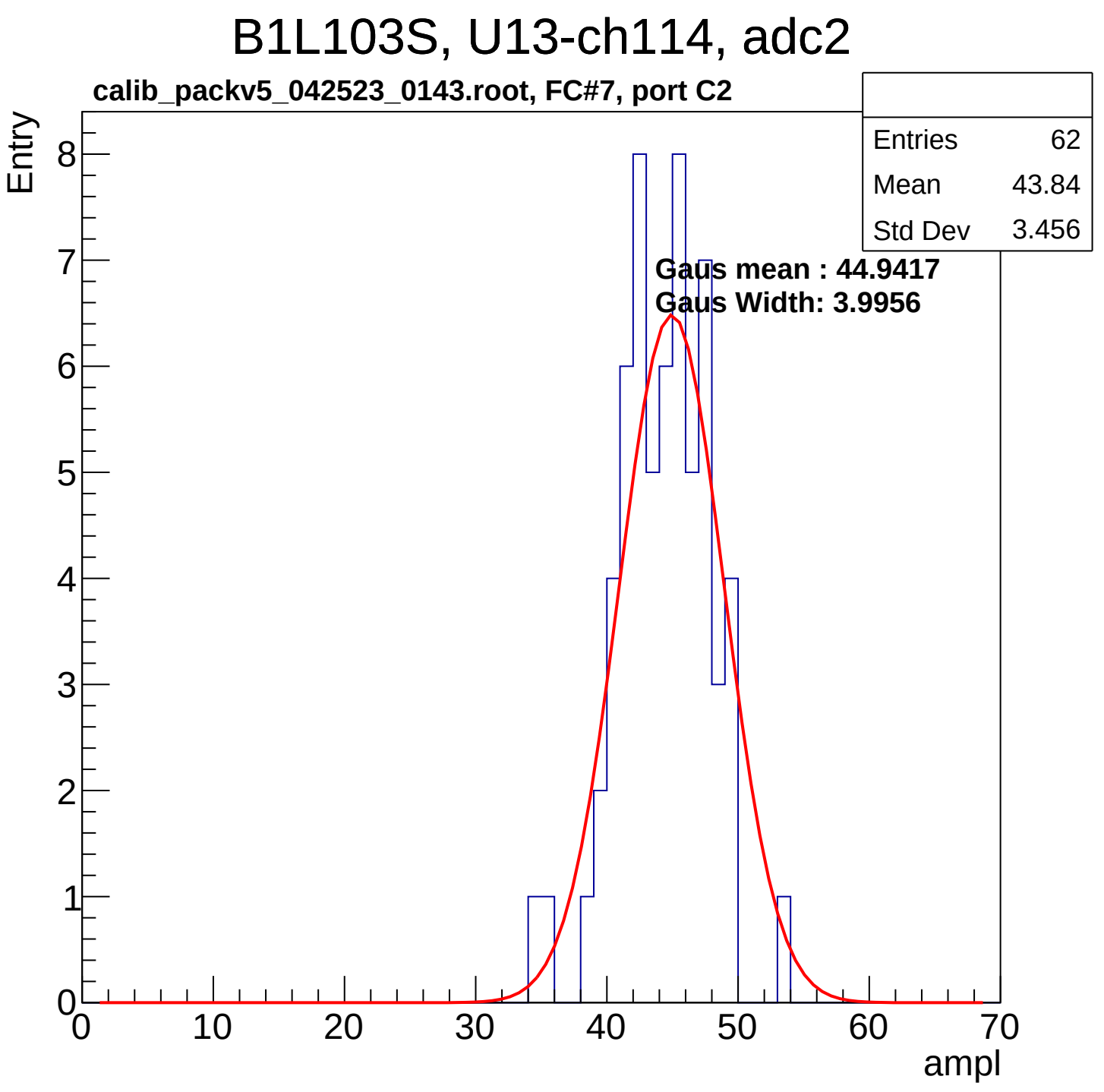
B1L103S, U13-ch114, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 43.84
Std Dev 3.456
Gaus mean : 44.9417
Gaus Width: 3.9956
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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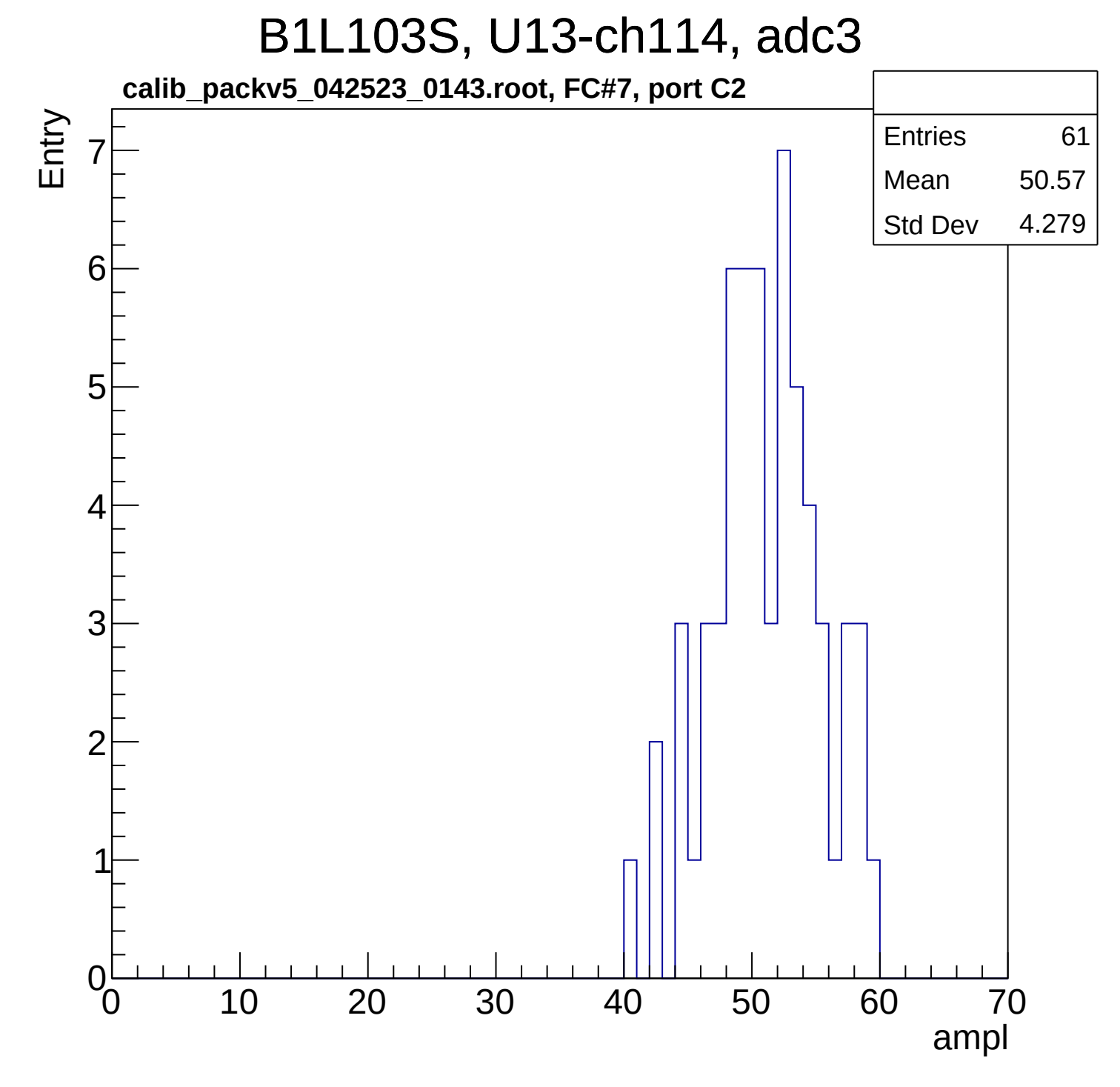
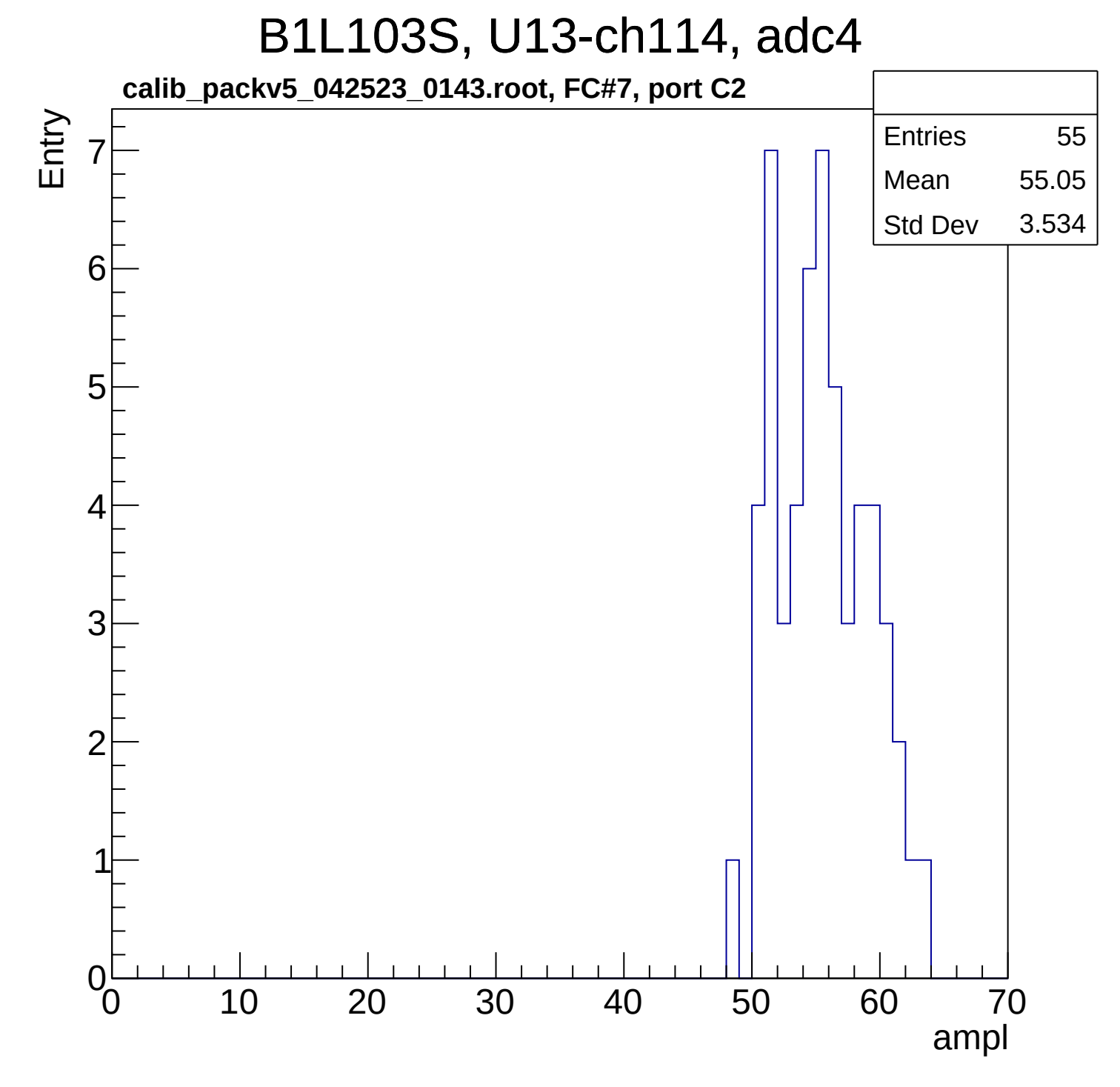

B1L103S, U13-ch114, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 50.57
Std Dev 4.279
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch114, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.05
Std Dev 3.534
Entry
ampl
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4
5
6
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0
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20
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40
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60
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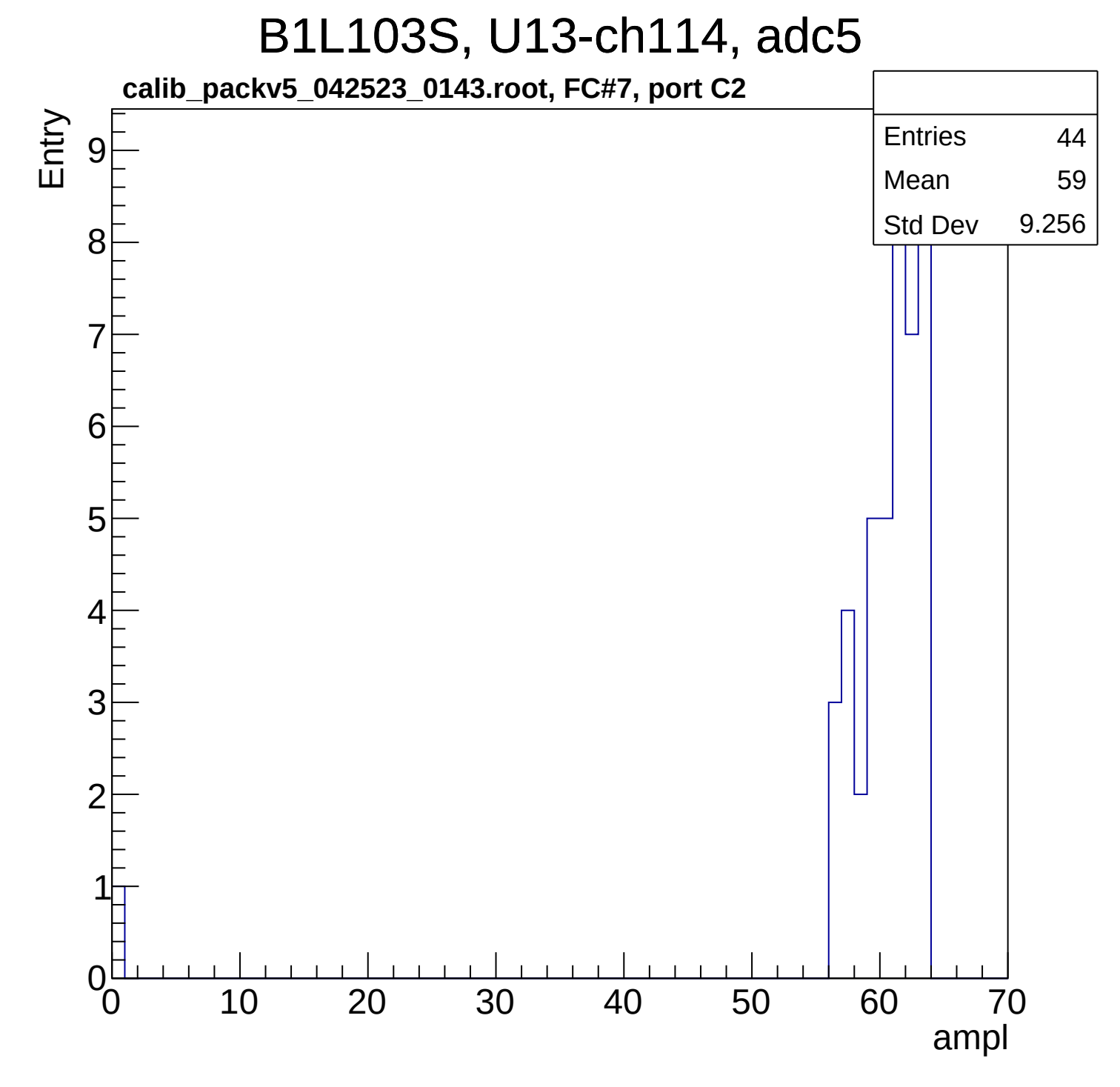
B1L103S, U13-ch114, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59
Std Dev 9.256
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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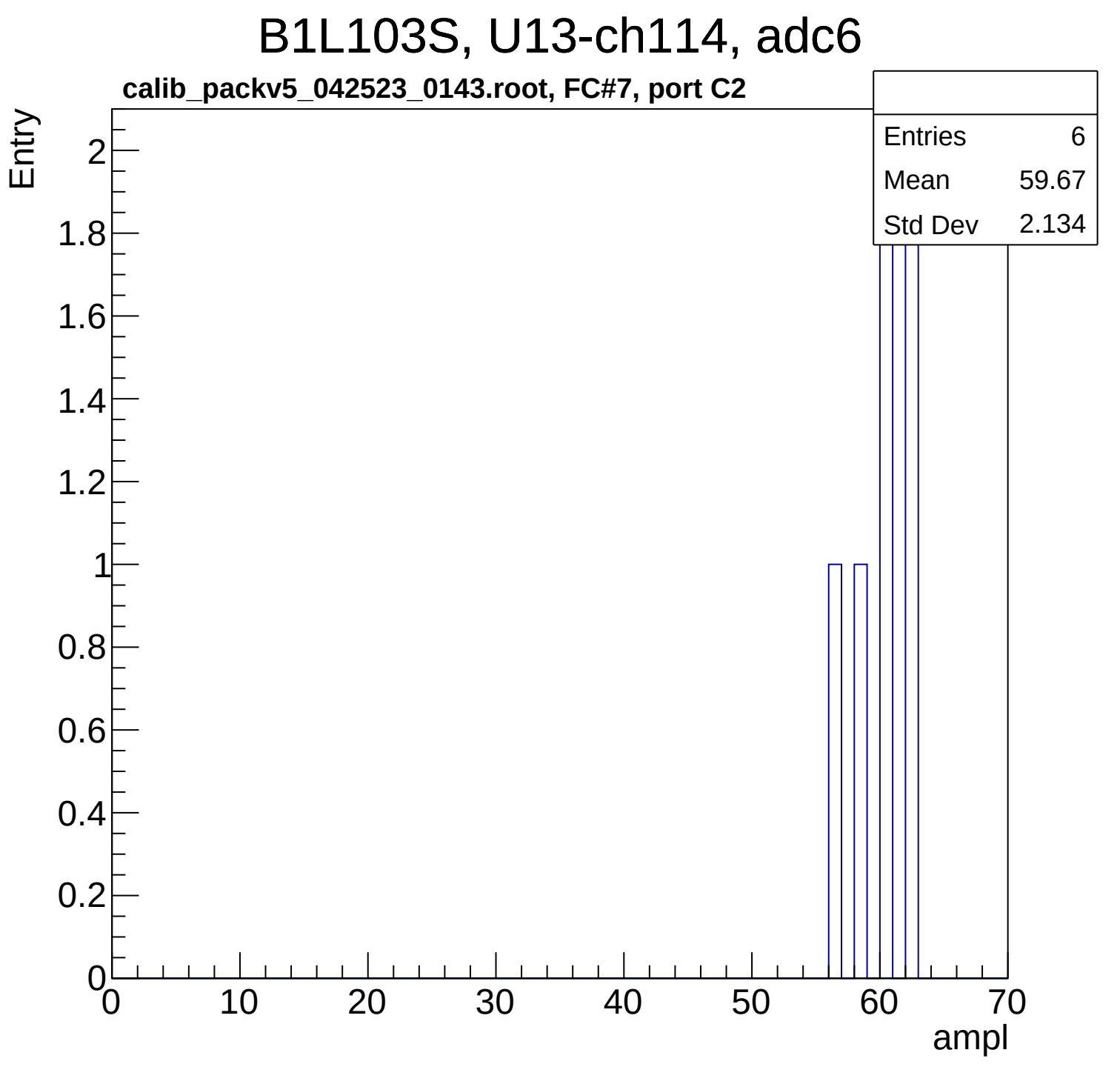
B1L103S, U13-ch114, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 59.67
Std Dev 2.134
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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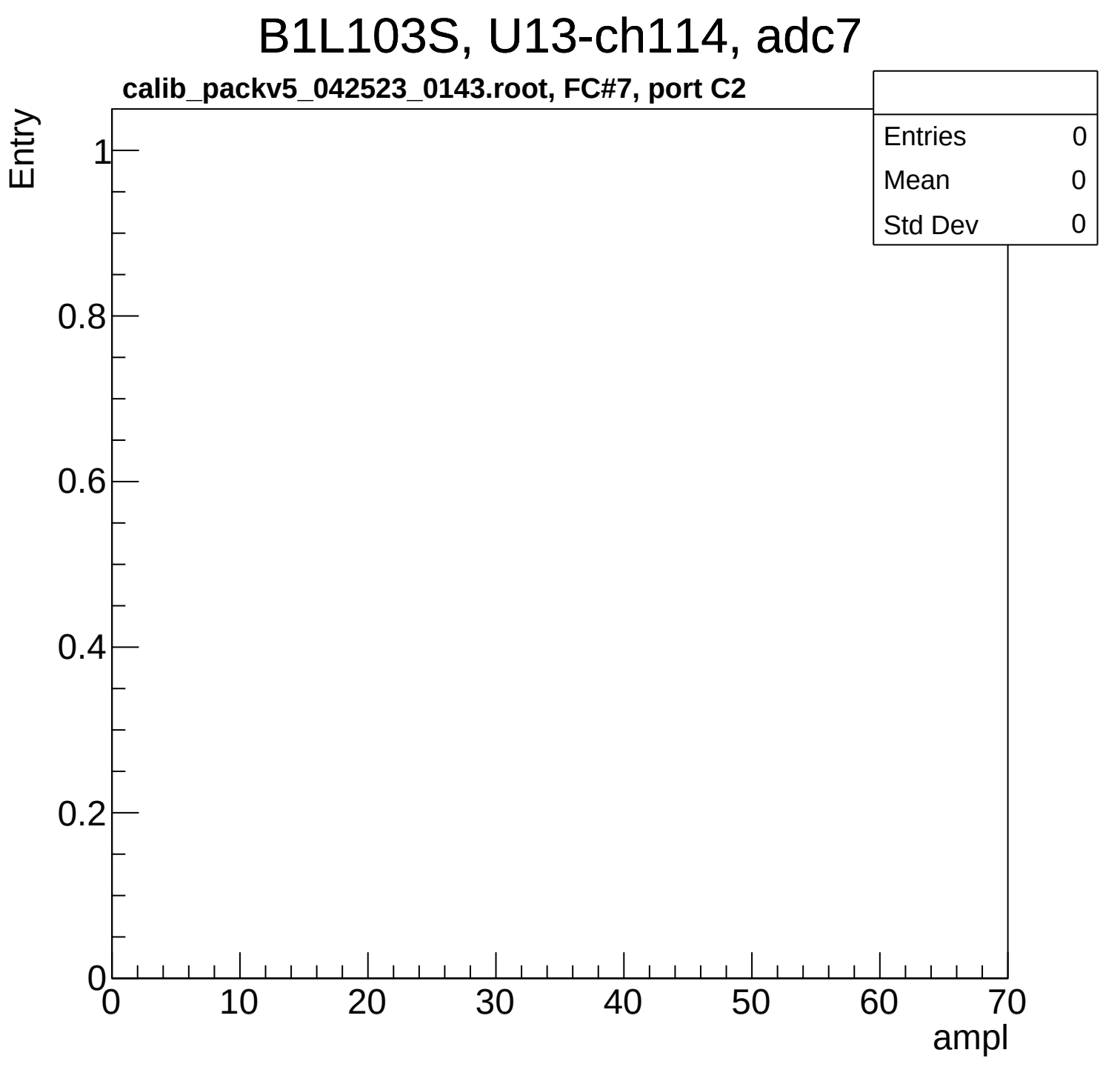
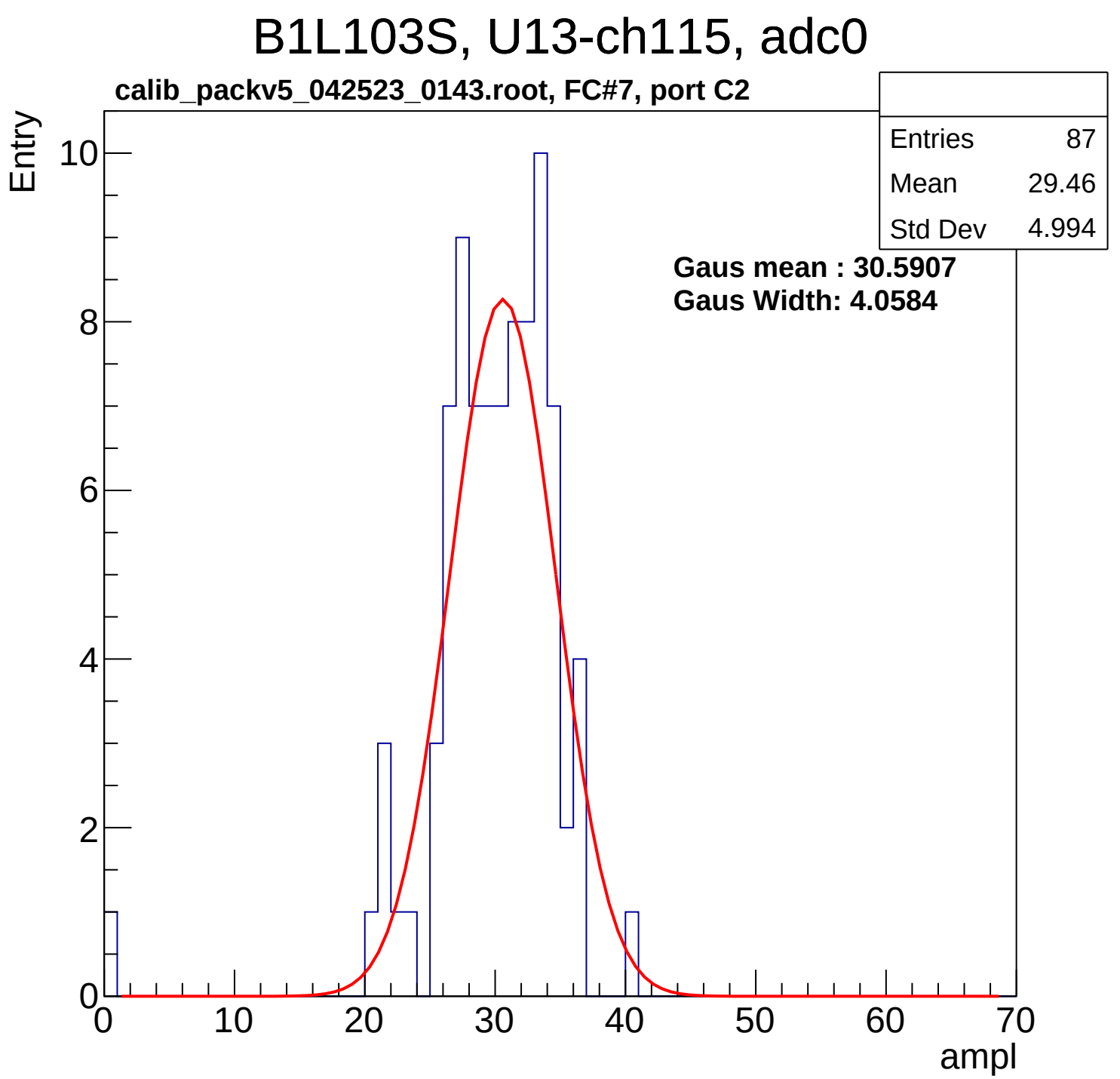
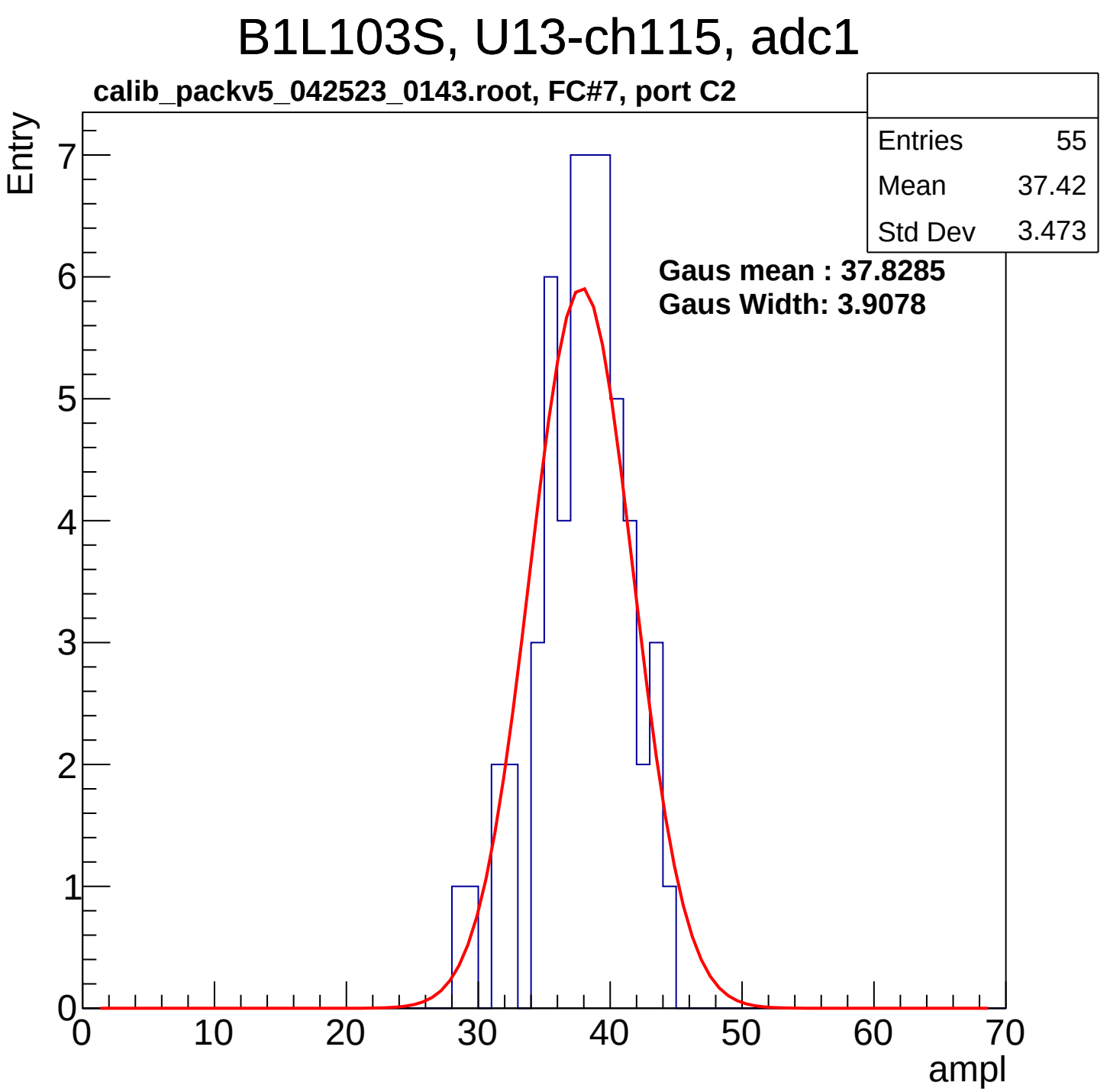
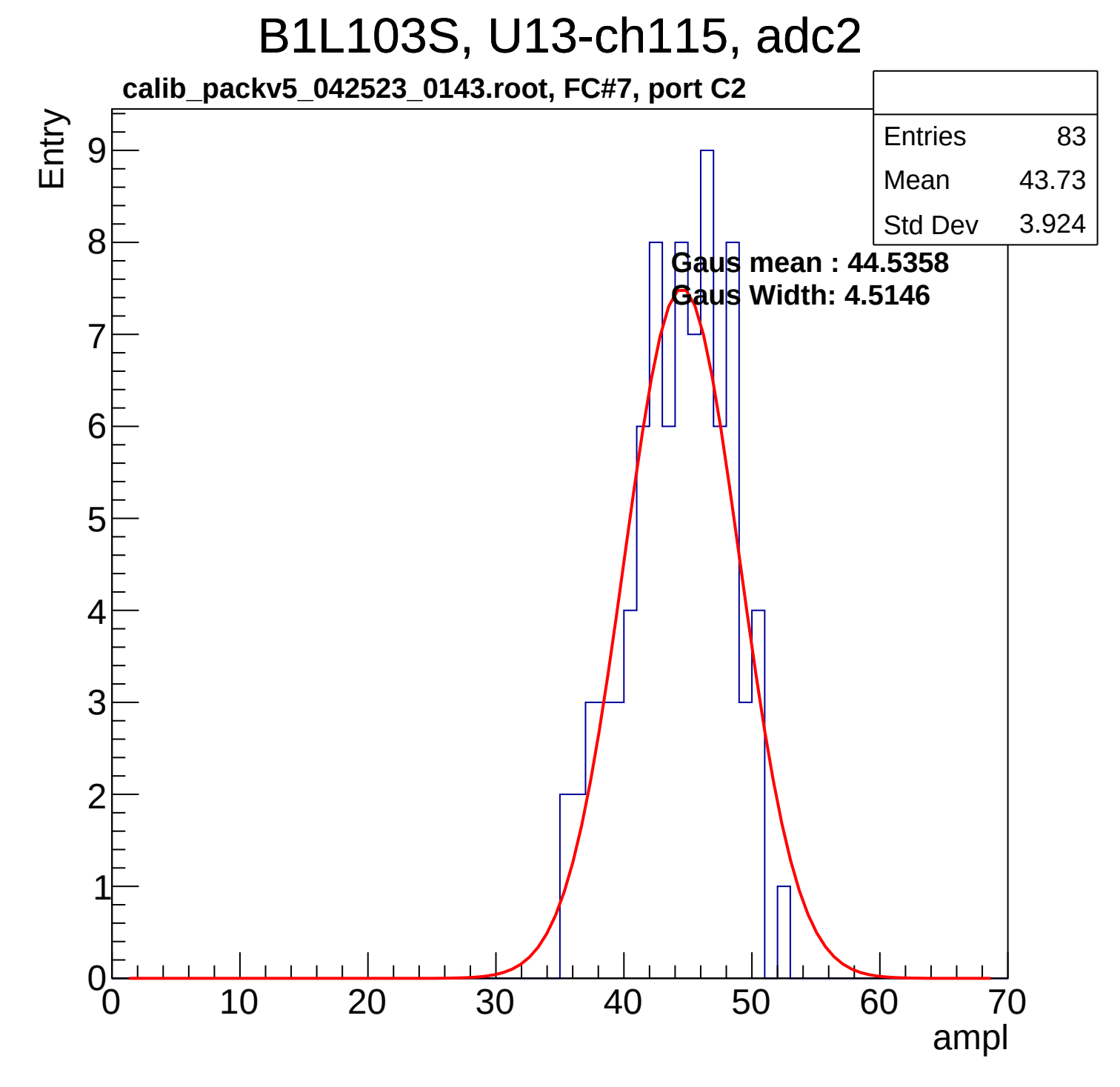

B1L103S, U13-ch114, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch115, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 29.46
Std Dev 4.994
Gaus mean : 30.5907
Gaus Width: 4.0584
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch115, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 37.42
Std Dev 3.473
Gaus mean : 37.8285
Gaus Width: 3.9078
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch115, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 43.73
Std Dev 3.924
Gaus mean : 44.5358
Gaus Width: 4.5146
Entry
ampl
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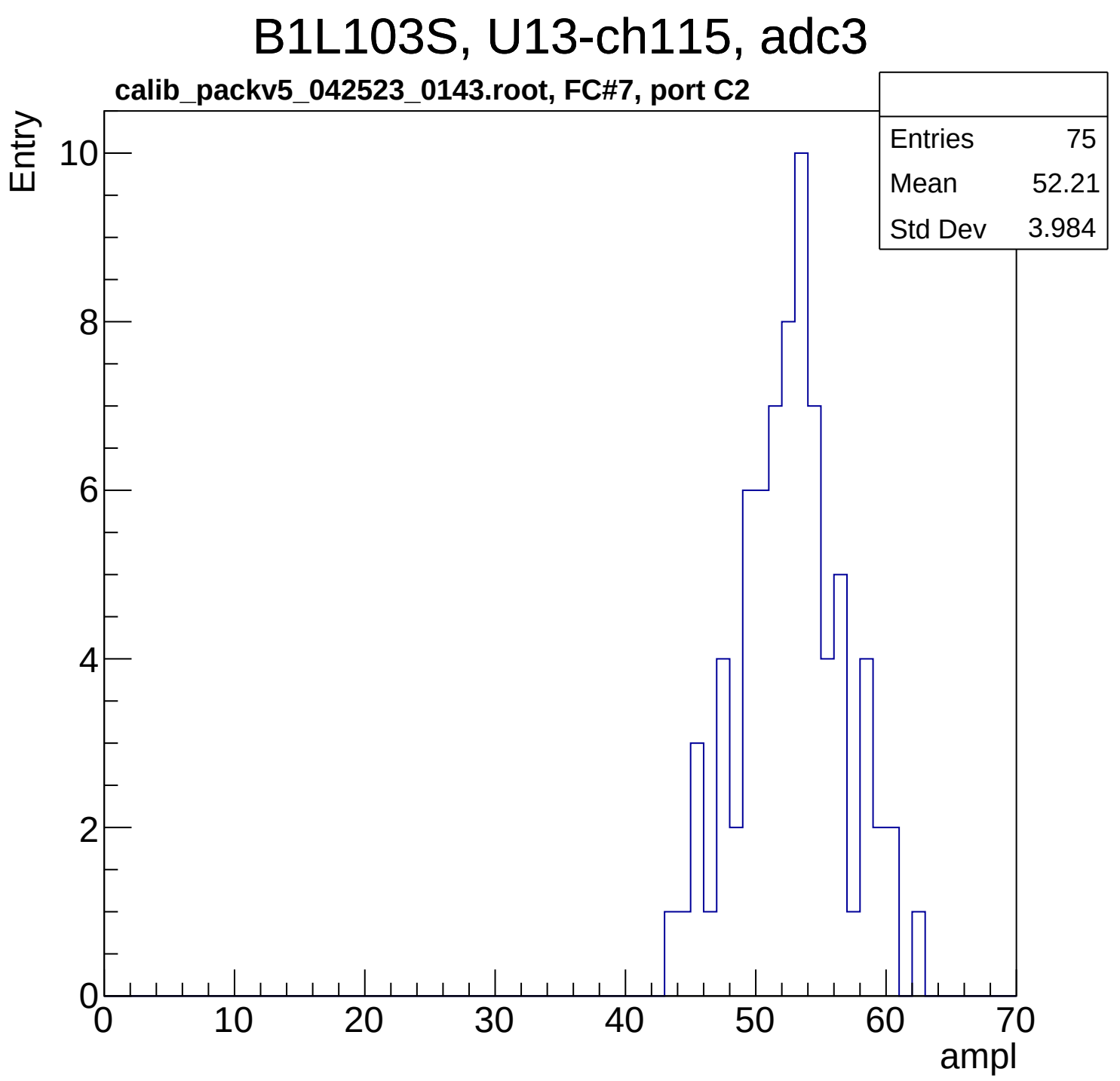
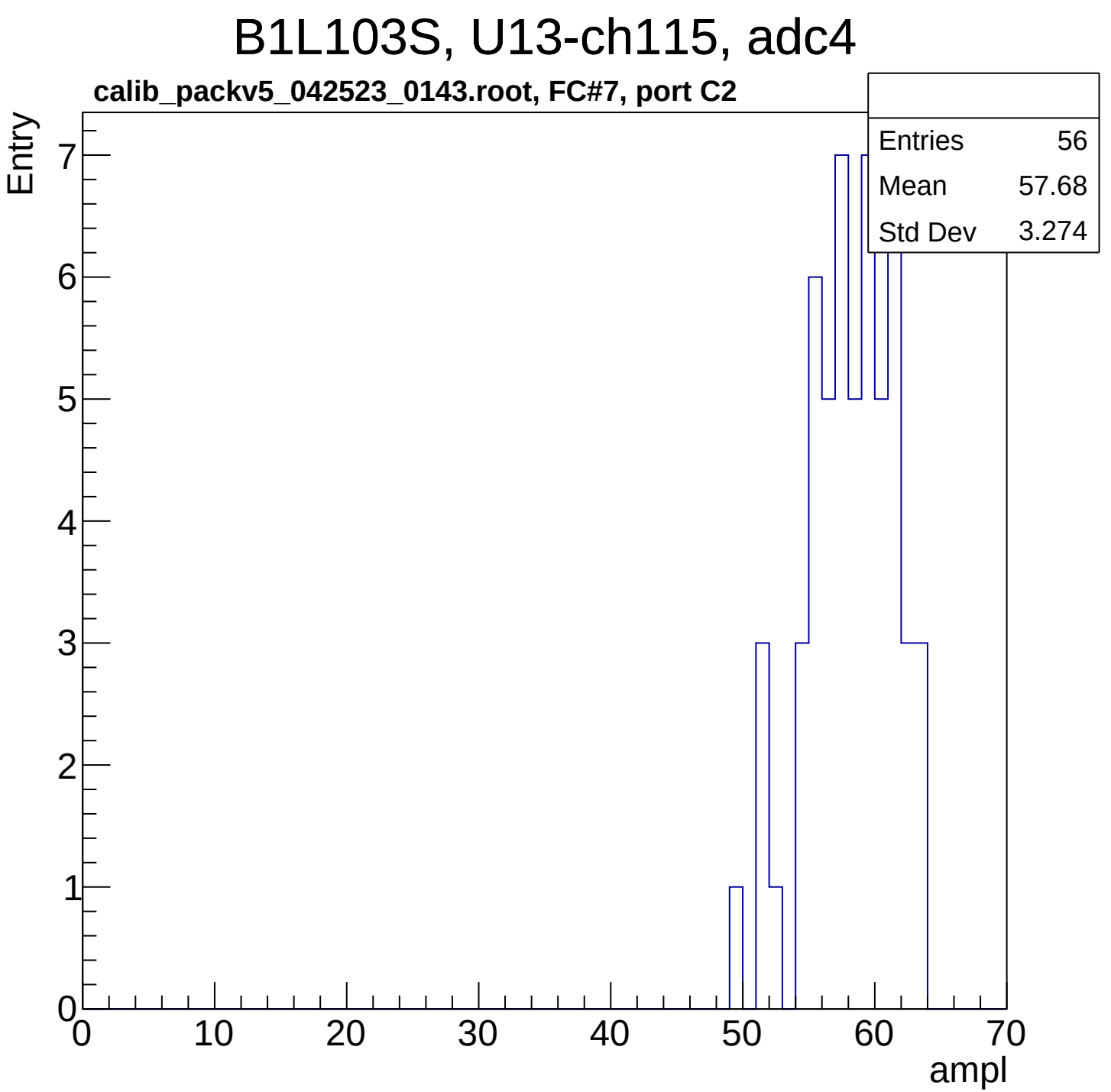
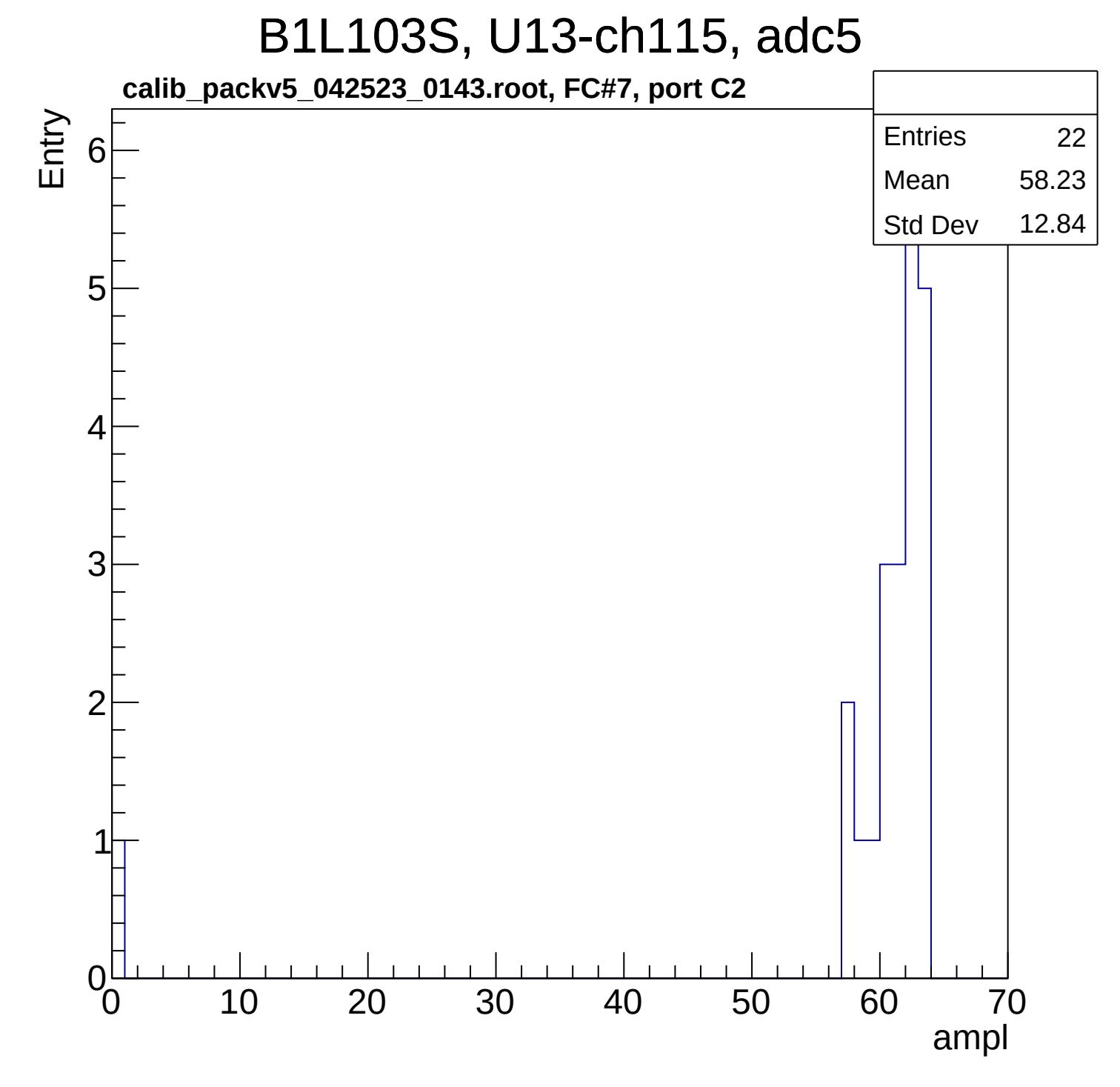
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch115, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 52.21
Std Dev 3.984
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch115, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.68
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch115, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.23
Std Dev 12.84
Entry
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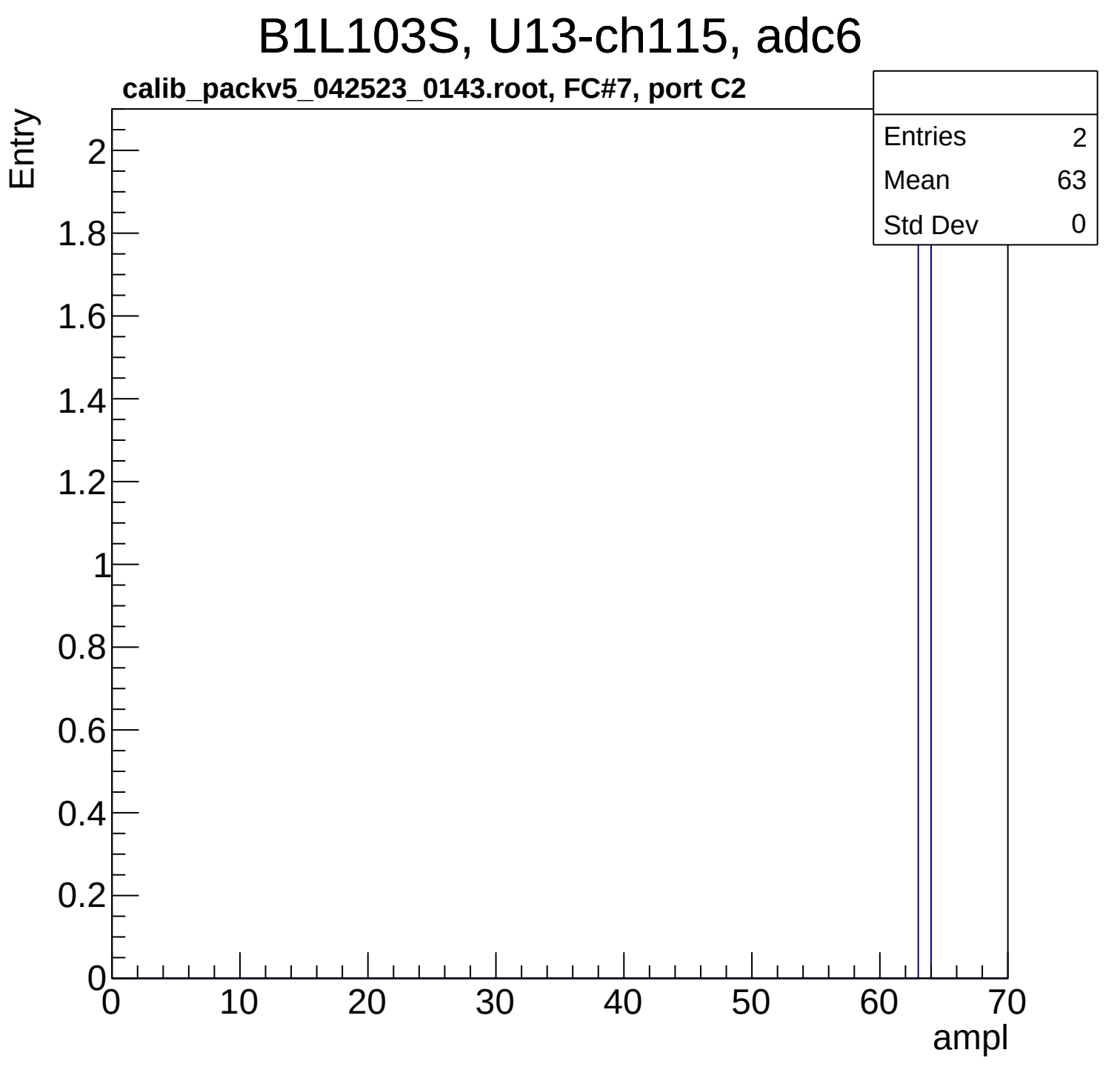
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch115, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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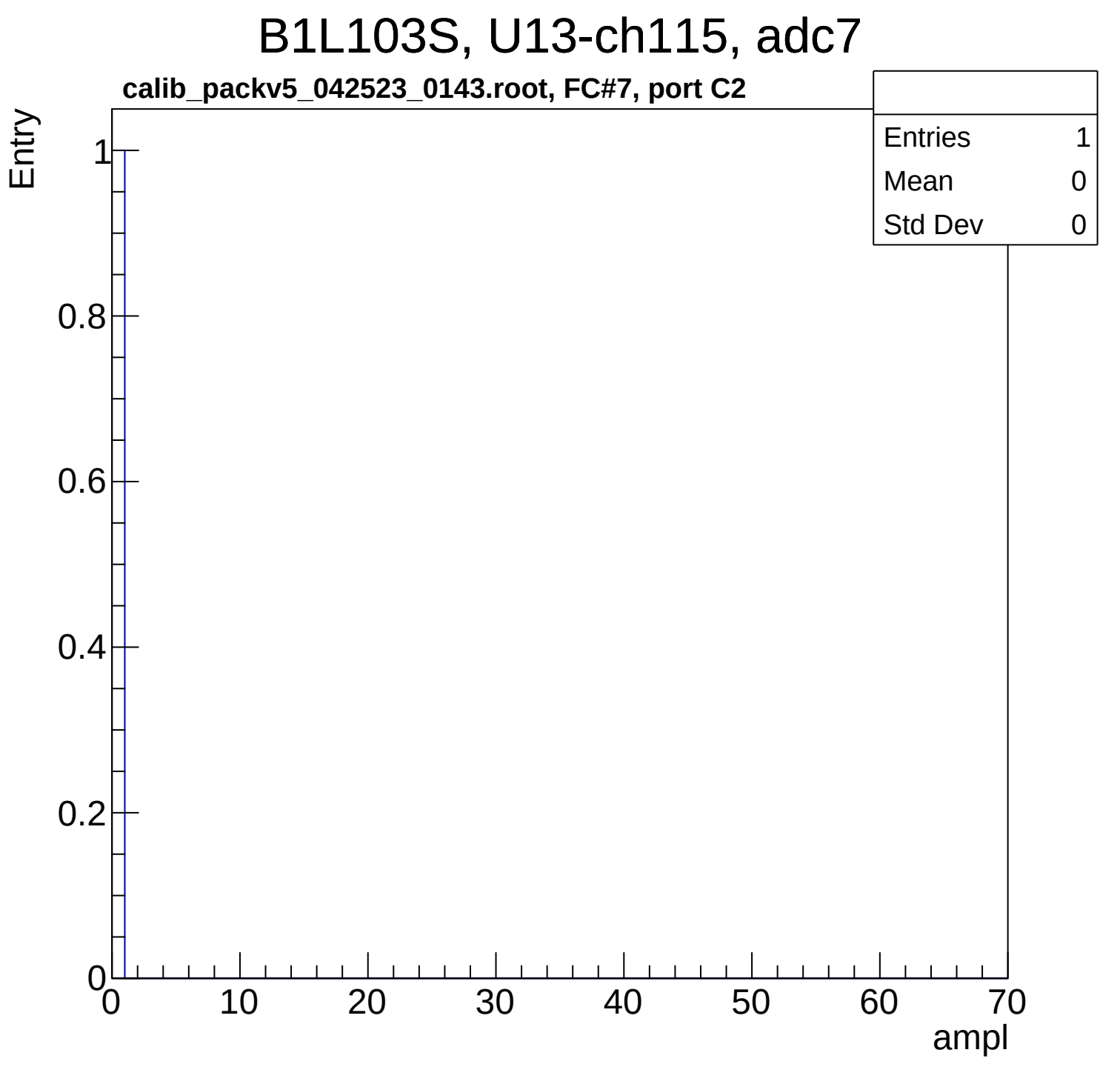
B1L103S, U13-ch115, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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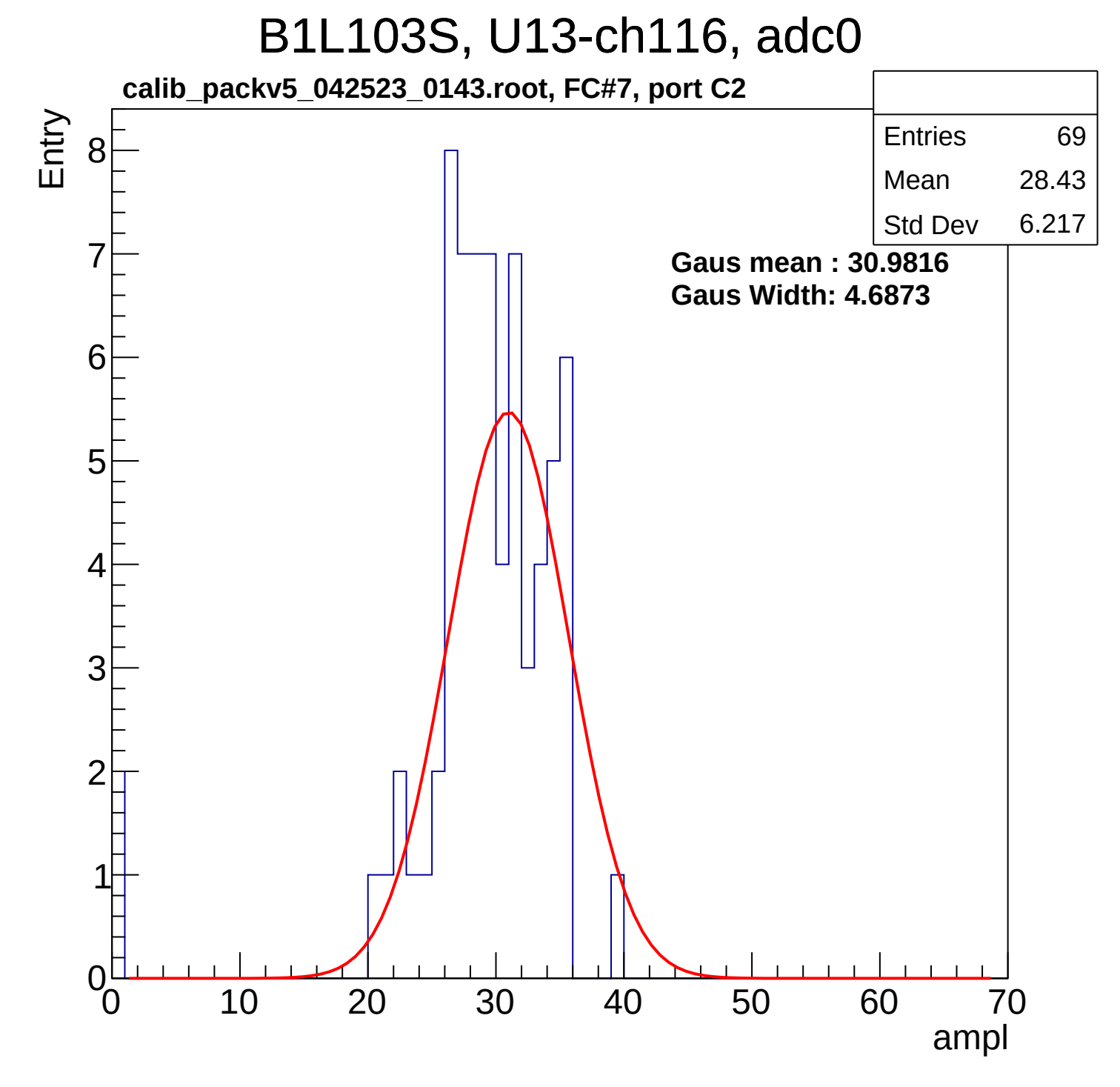
50
60
70
ampl


B1L103S, U13-ch116, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.43
Std Dev 6.217
Gaus mean : 30.9816
Gaus Width: 4.6873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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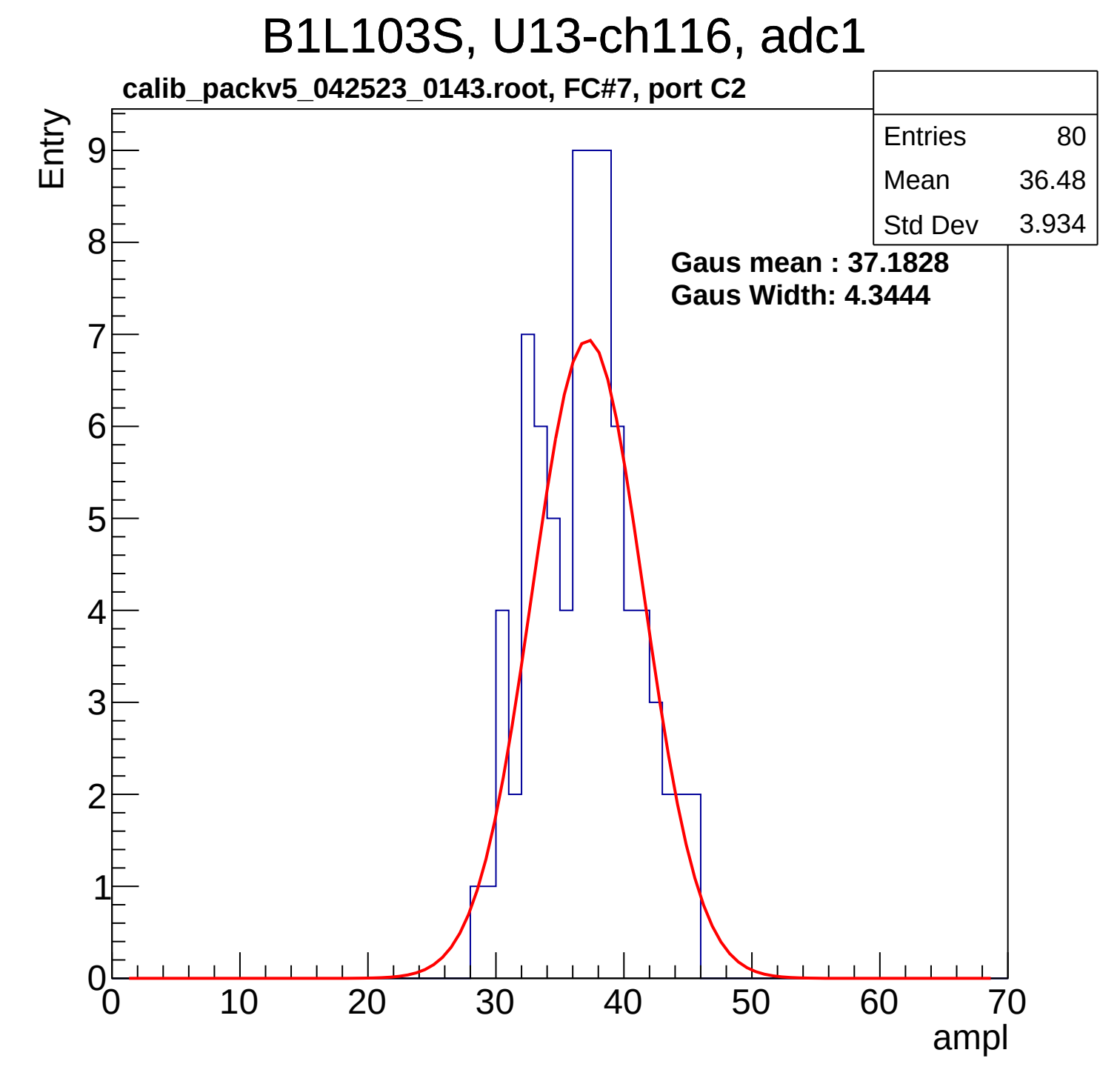
B1L103S, U13-ch116, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 36.48
Std Dev 3.934
Gaus mean : 37.1828
Gaus Width: 4.3444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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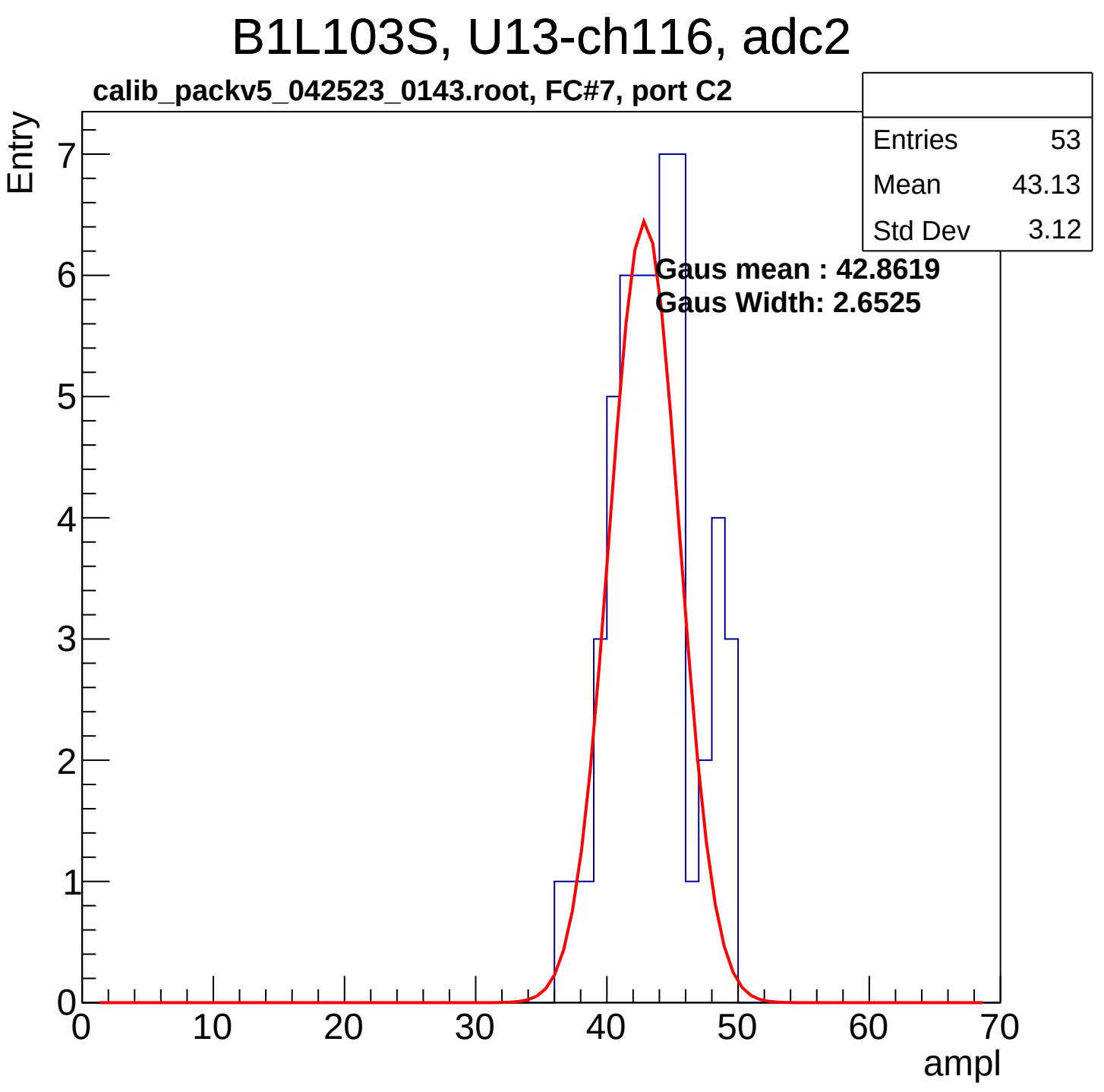
B1L103S, U13-ch116, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 43.13
Std Dev 3.12
Gaus mean : 42.8619
Gaus Width: 2.6525
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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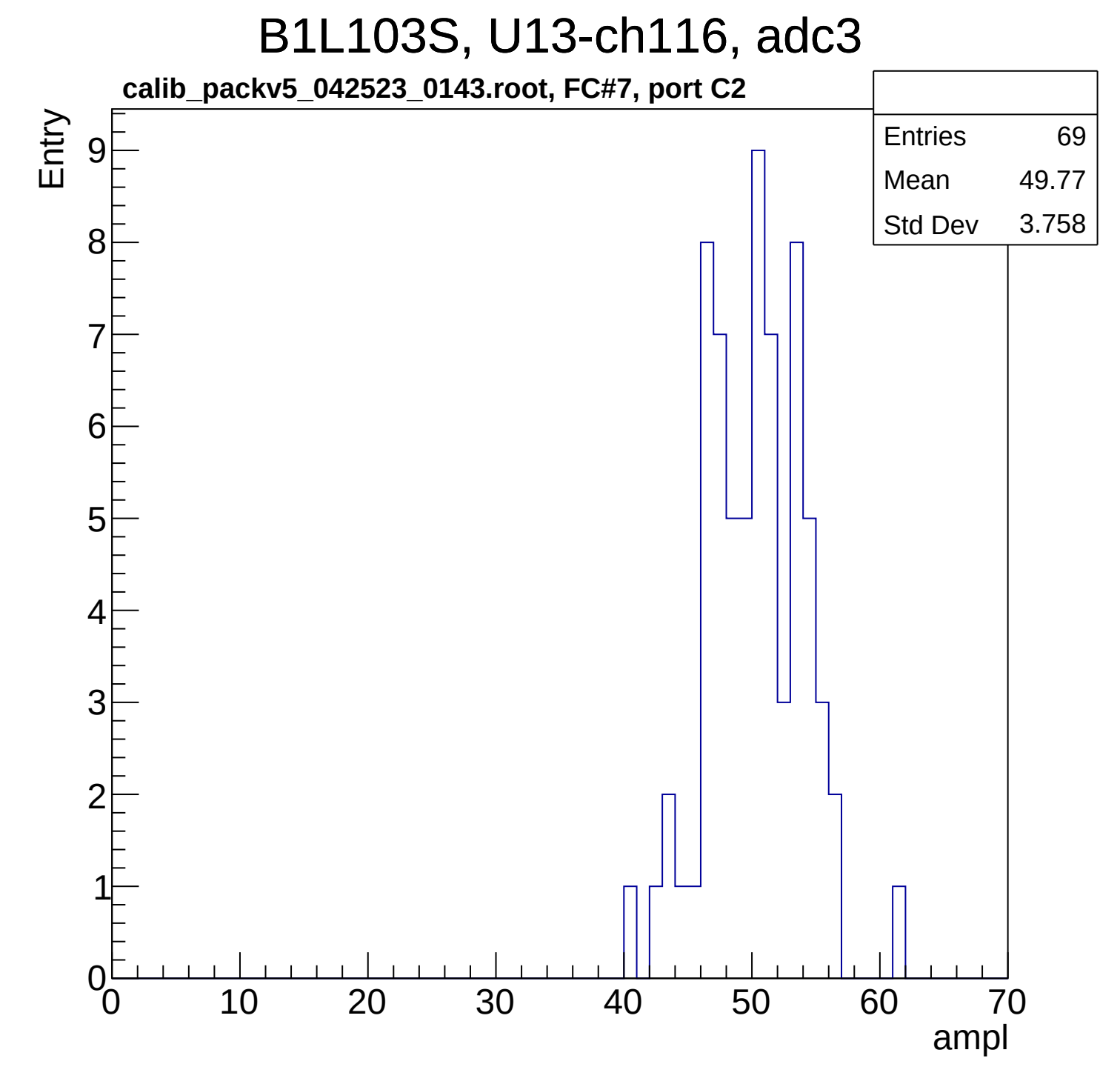
B1L103S, U13-ch116, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.77
Std Dev 3.758
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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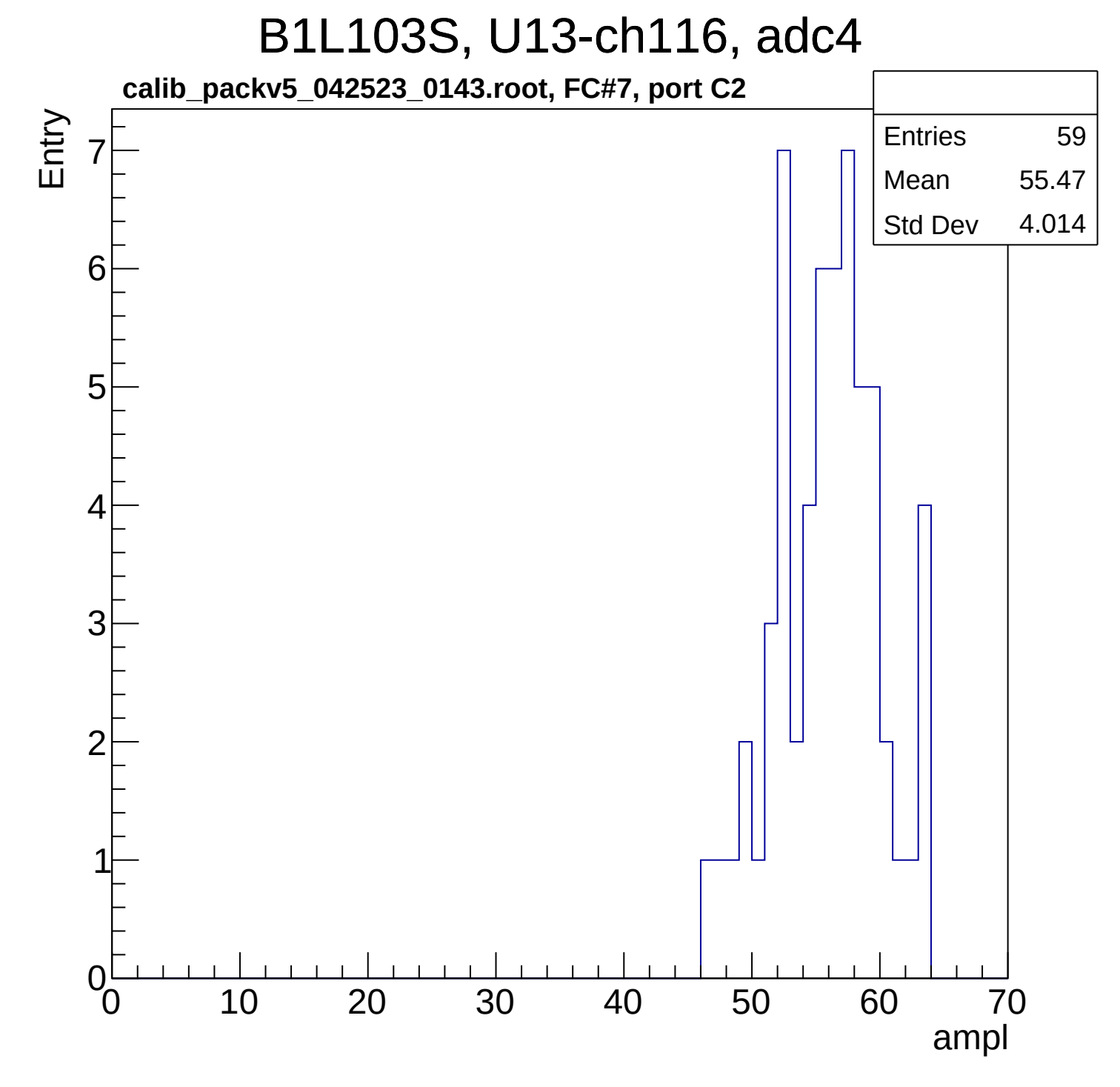
B1L103S, U13-ch116, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.47
Std Dev 4.014
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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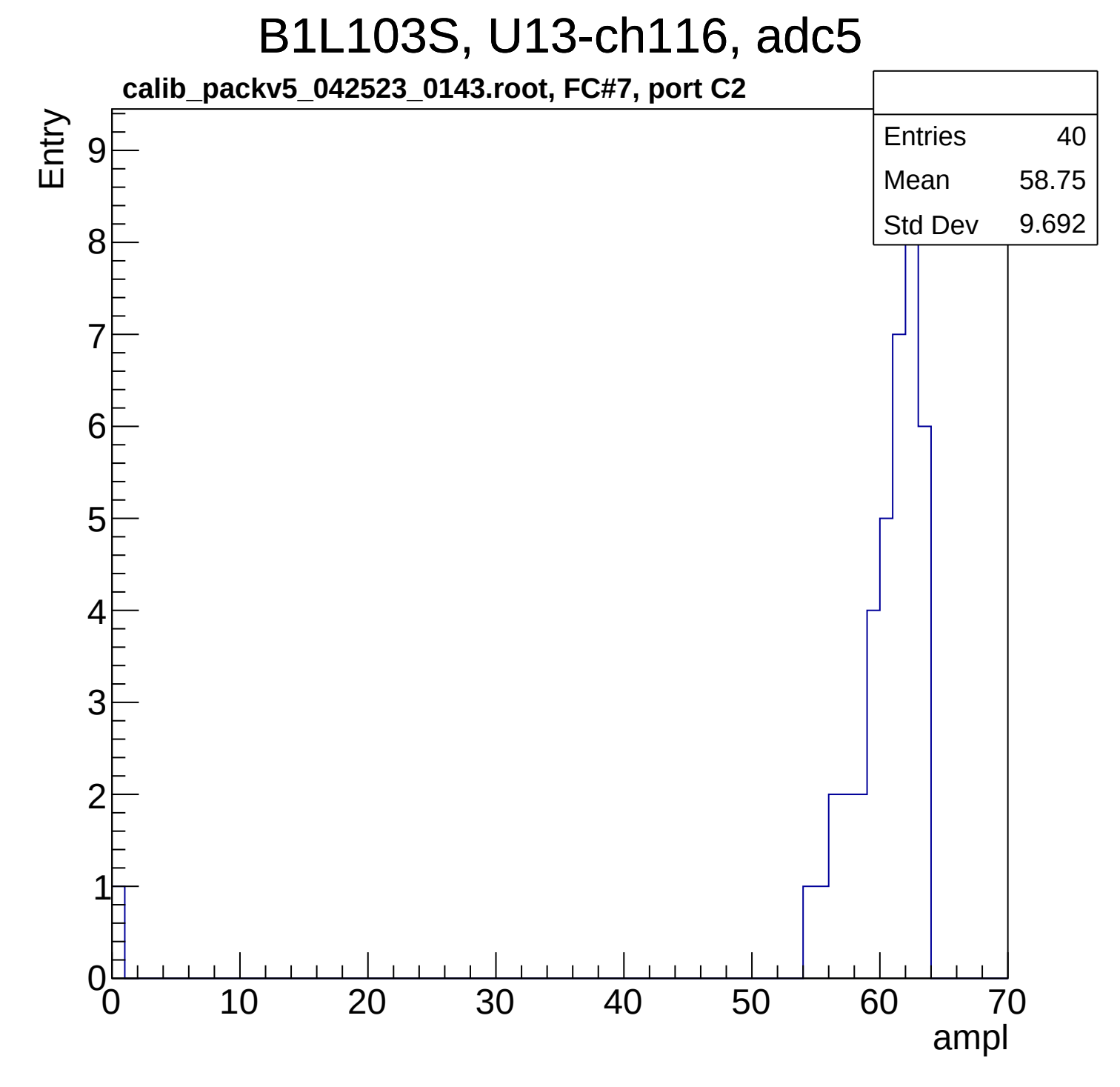
B1L103S, U13-ch116, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.75
Std Dev 9.692
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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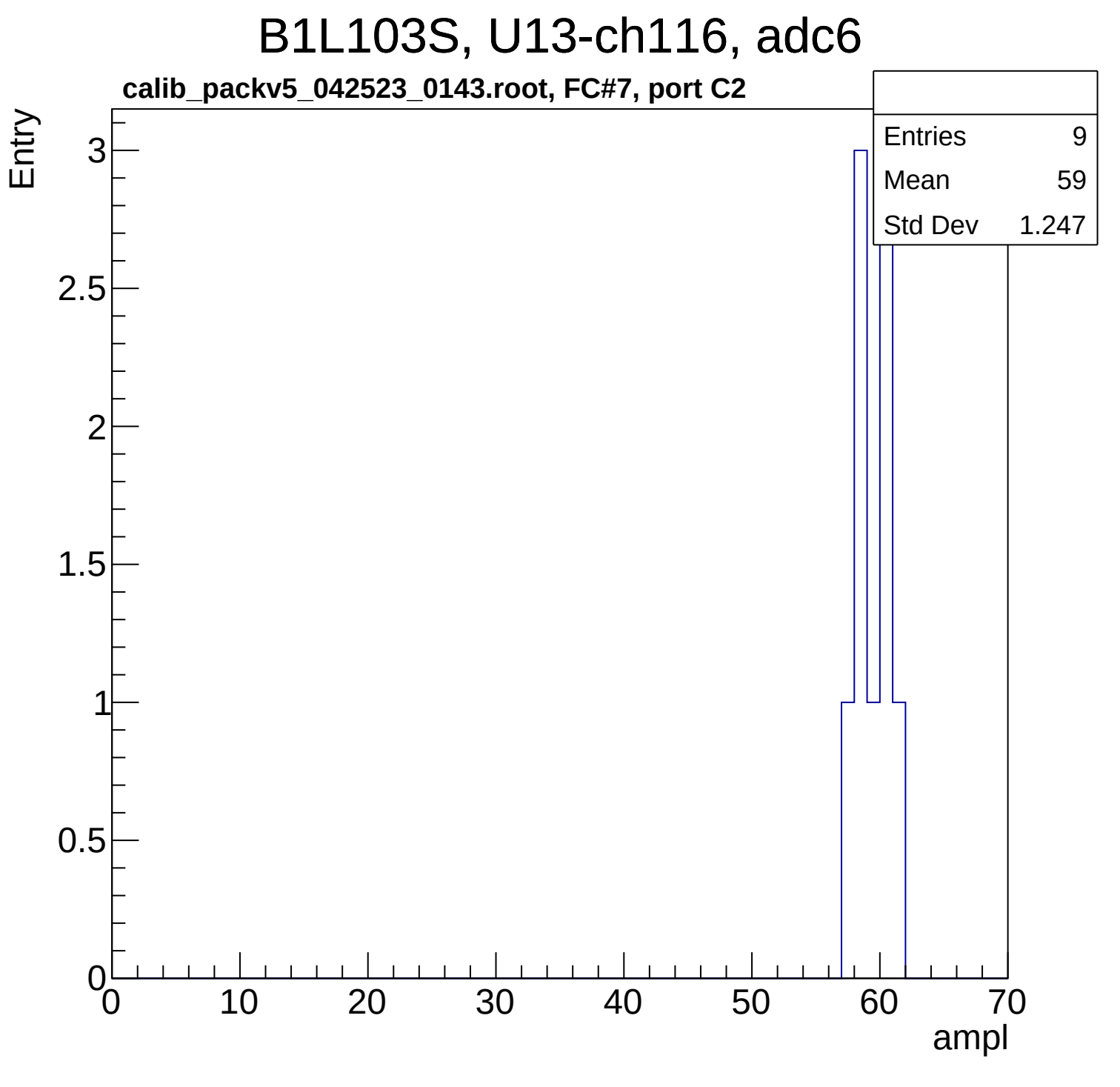
B1L103S, U13-ch116, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 59
Std Dev 1.247
Entry
ampl
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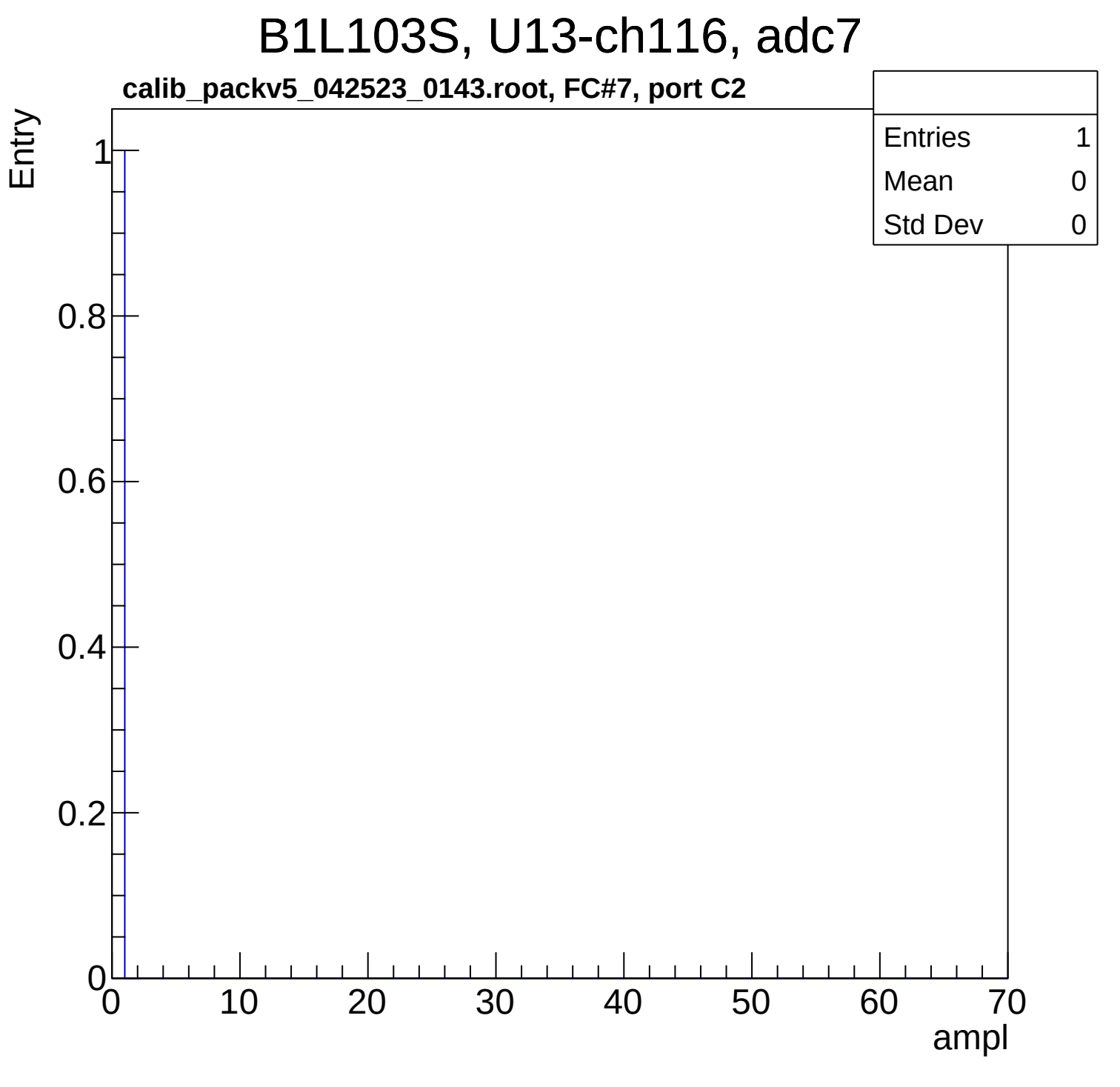
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch116, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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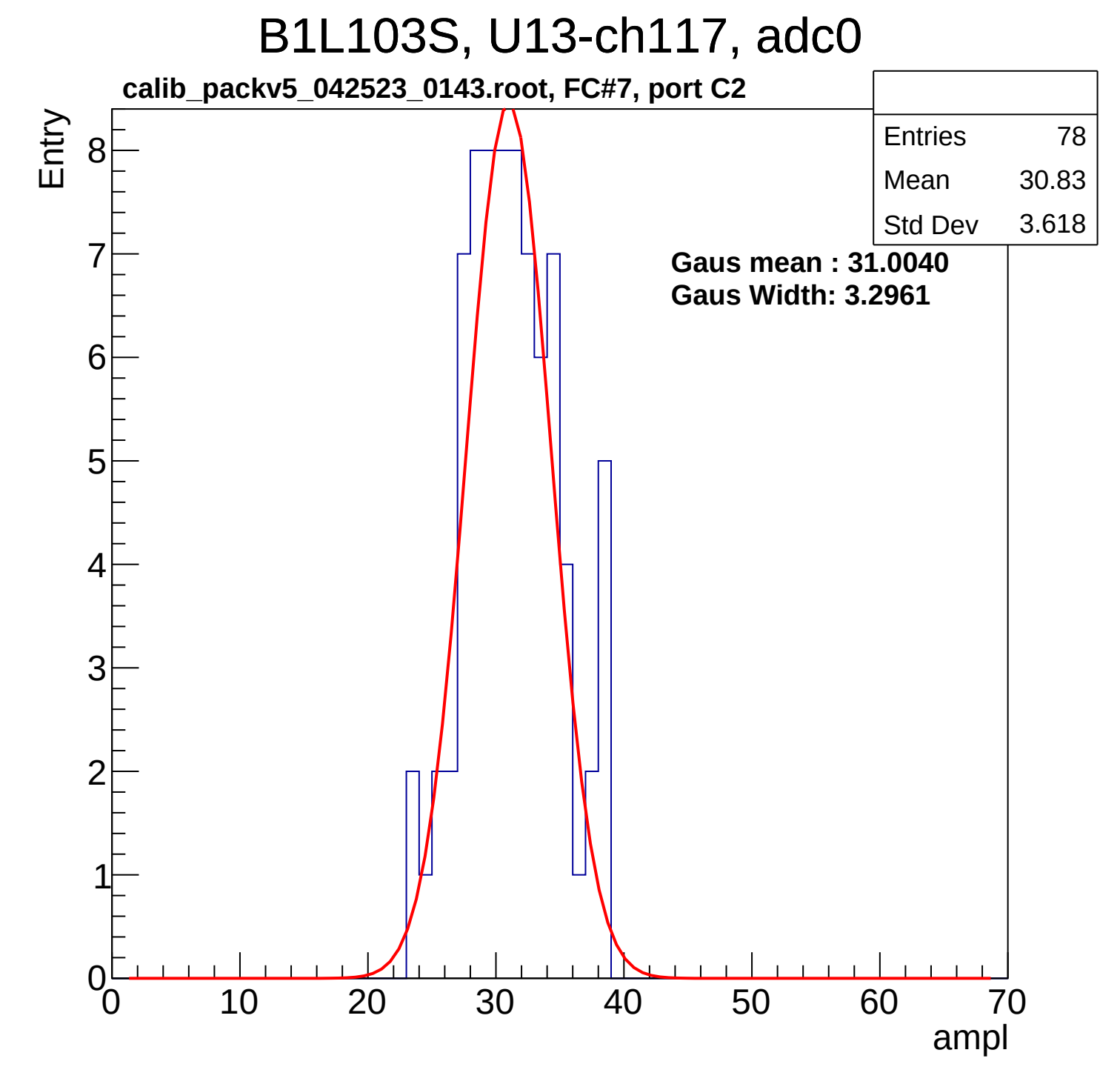
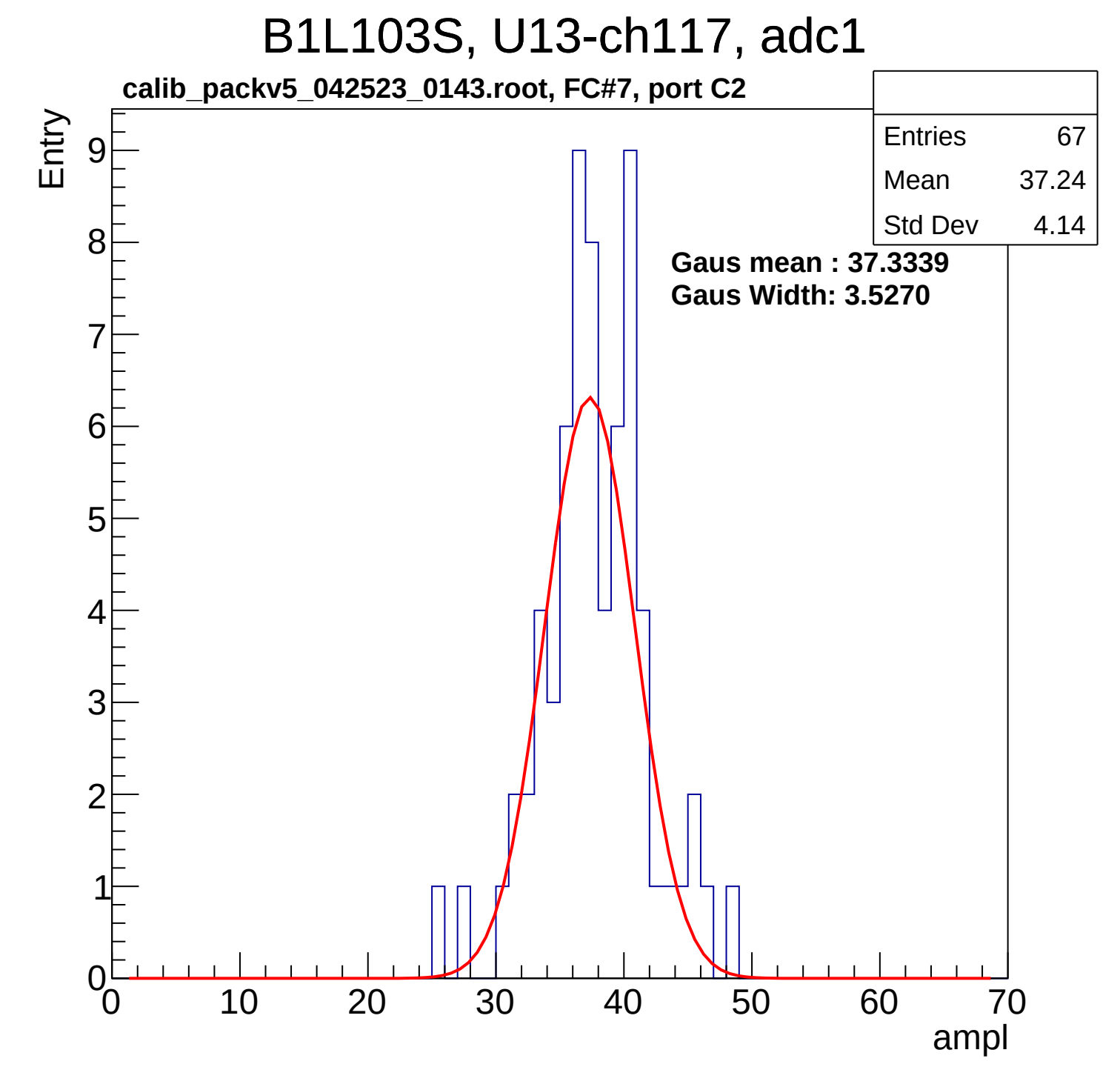
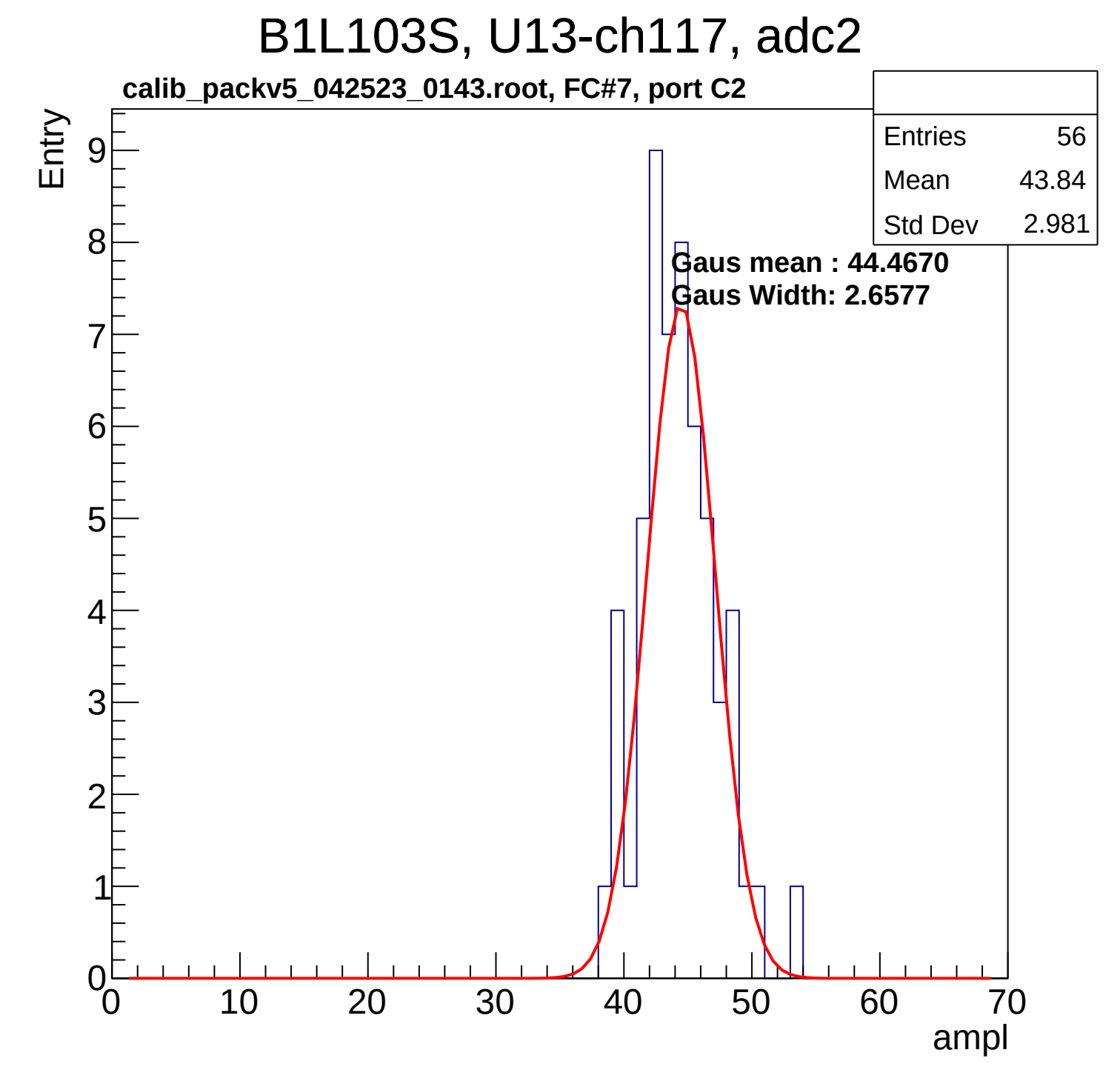
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch117, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 30.83
Std Dev 3.618
Gaus mean : 31.0040
Gaus Width: 3.2961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch117, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.24
Std Dev 4.14
Gaus mean : 37.3339
Gaus Width: 3.5270
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch117, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.84
Std Dev 2.981
Gaus mean : 44.4670
Gaus Width: 2.6577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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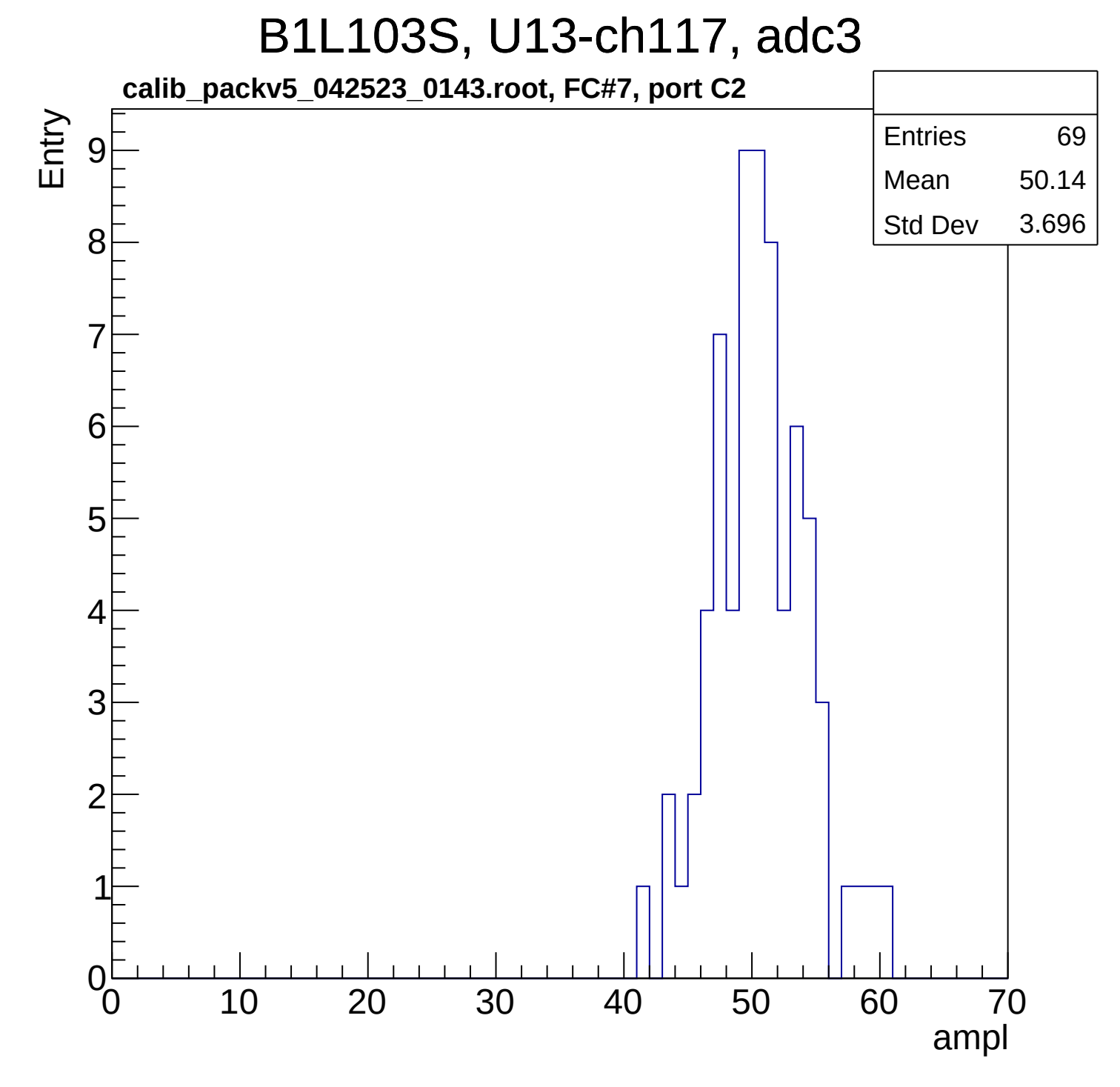
B1L103S, U13-ch117, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.14
Std Dev 3.696
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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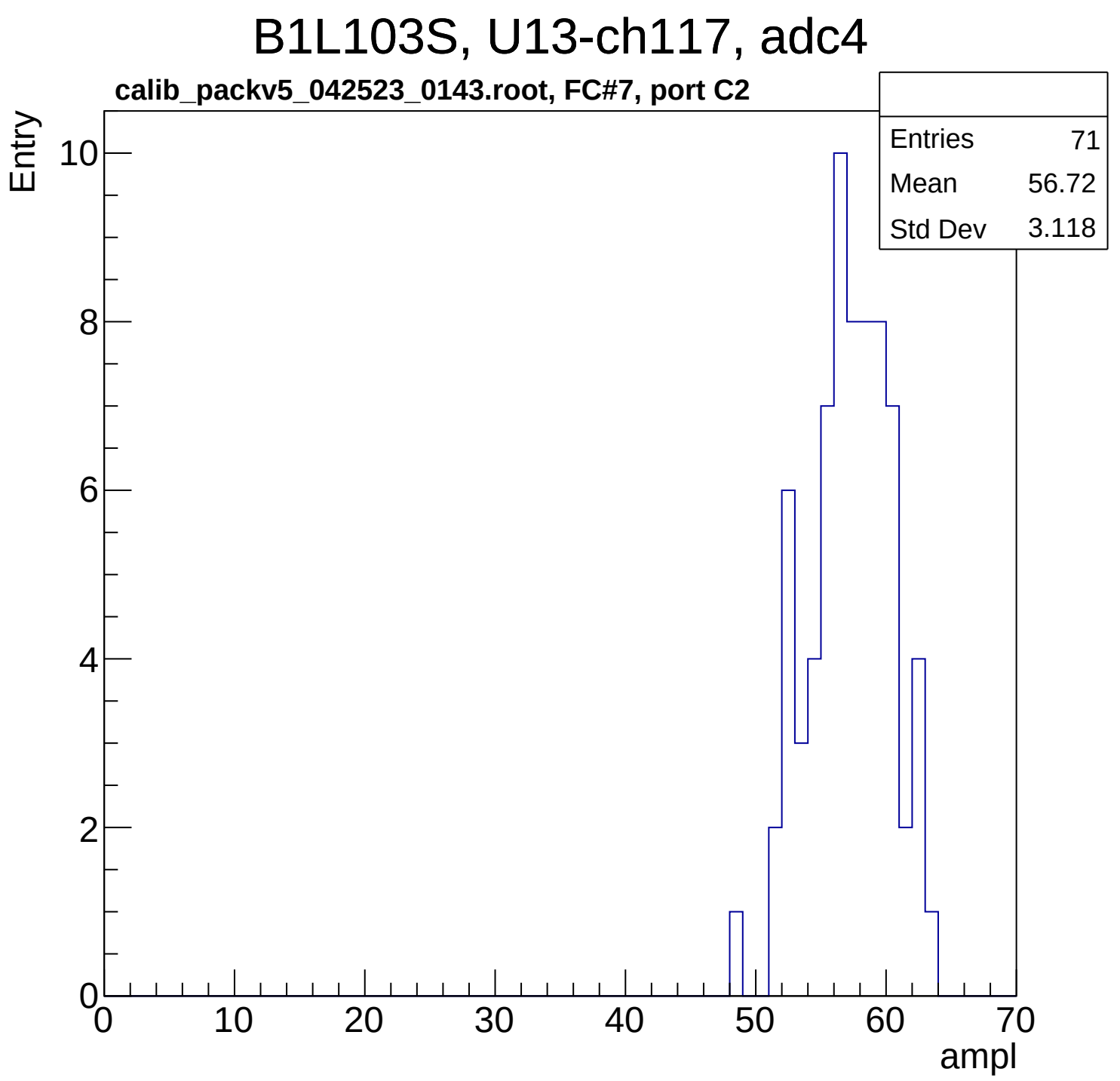
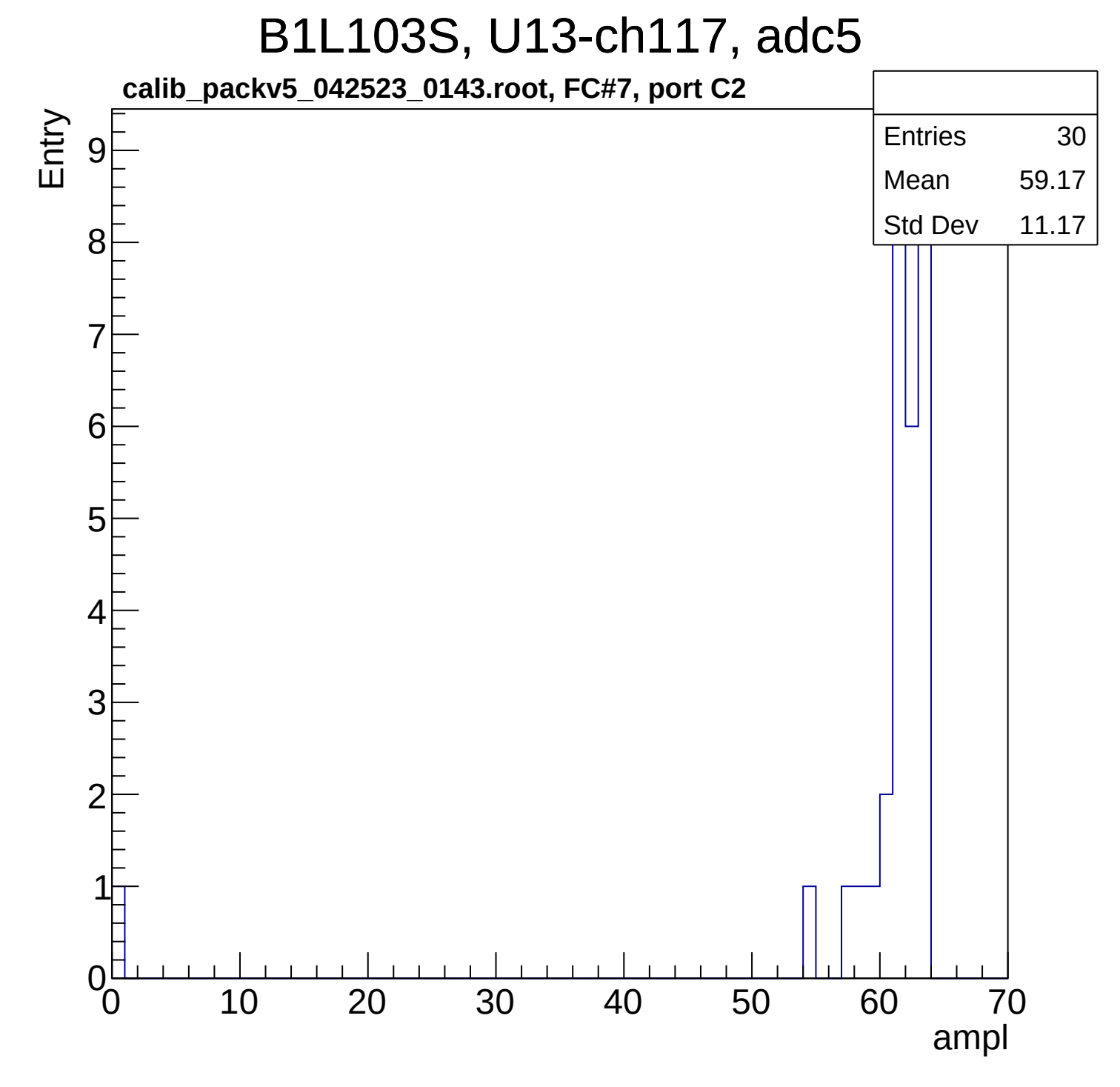

B1L103S, U13-ch117, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 56.72
Std Dev 3.118
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch117, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 59.17
Std Dev 11.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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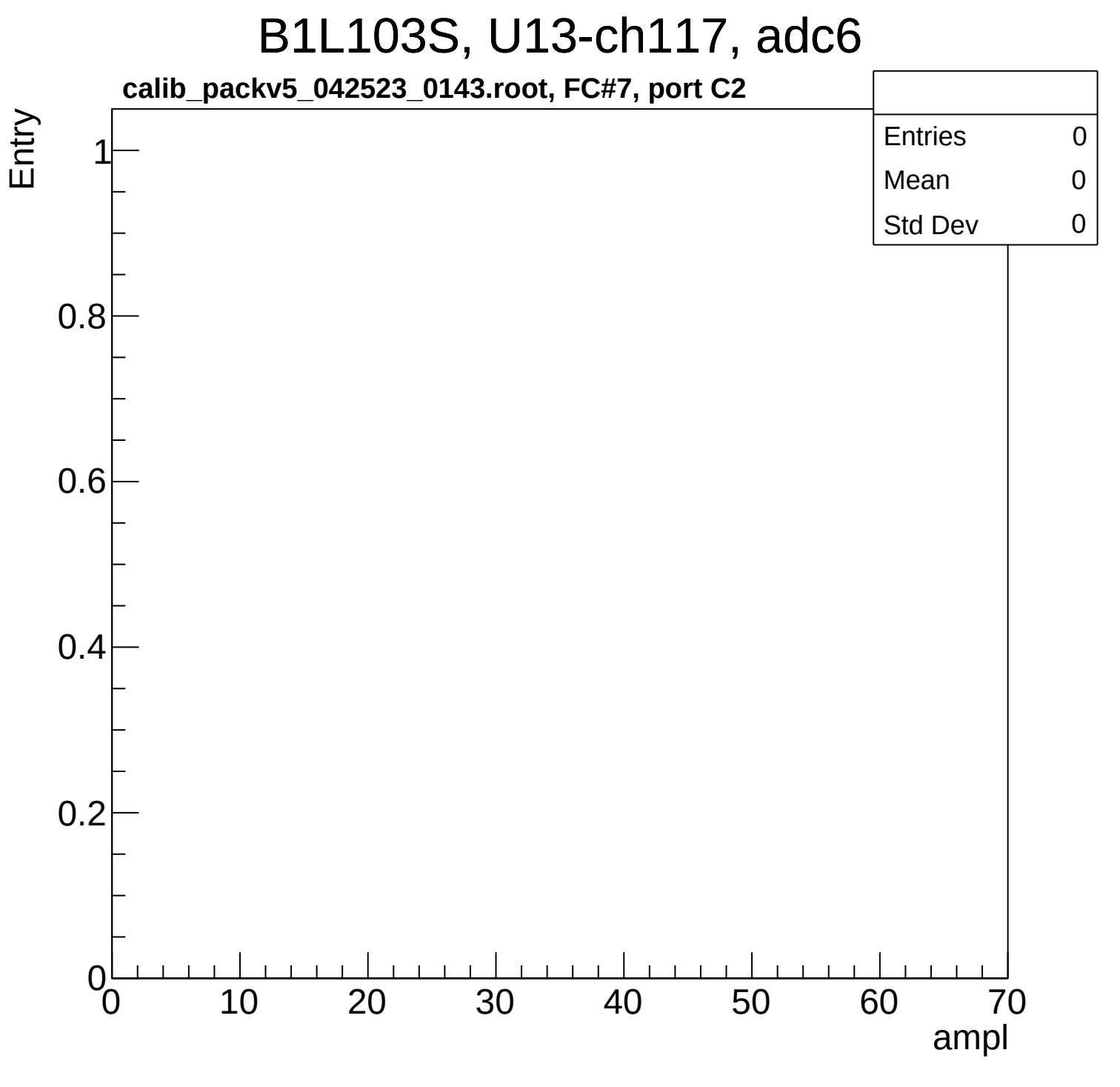
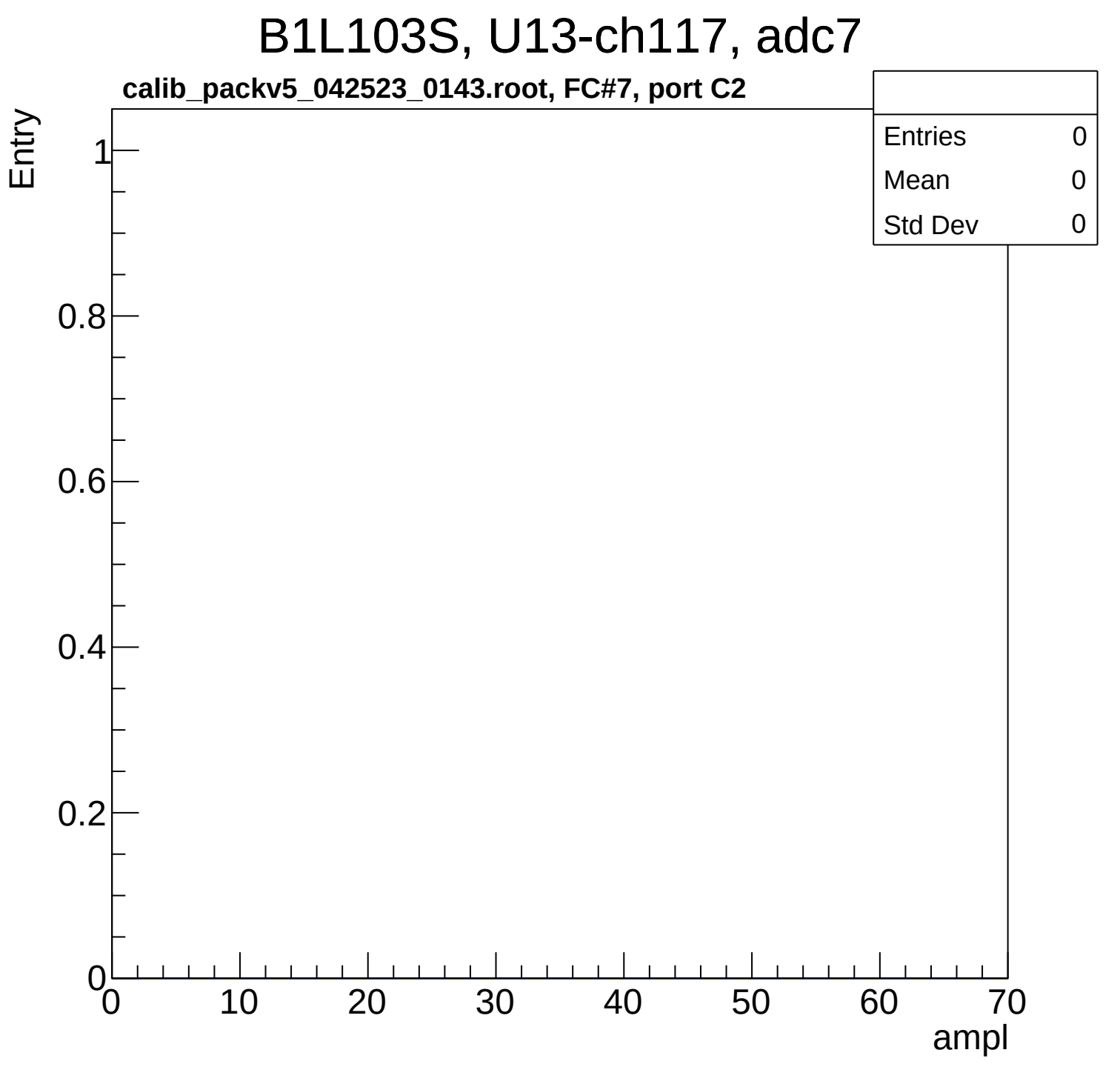
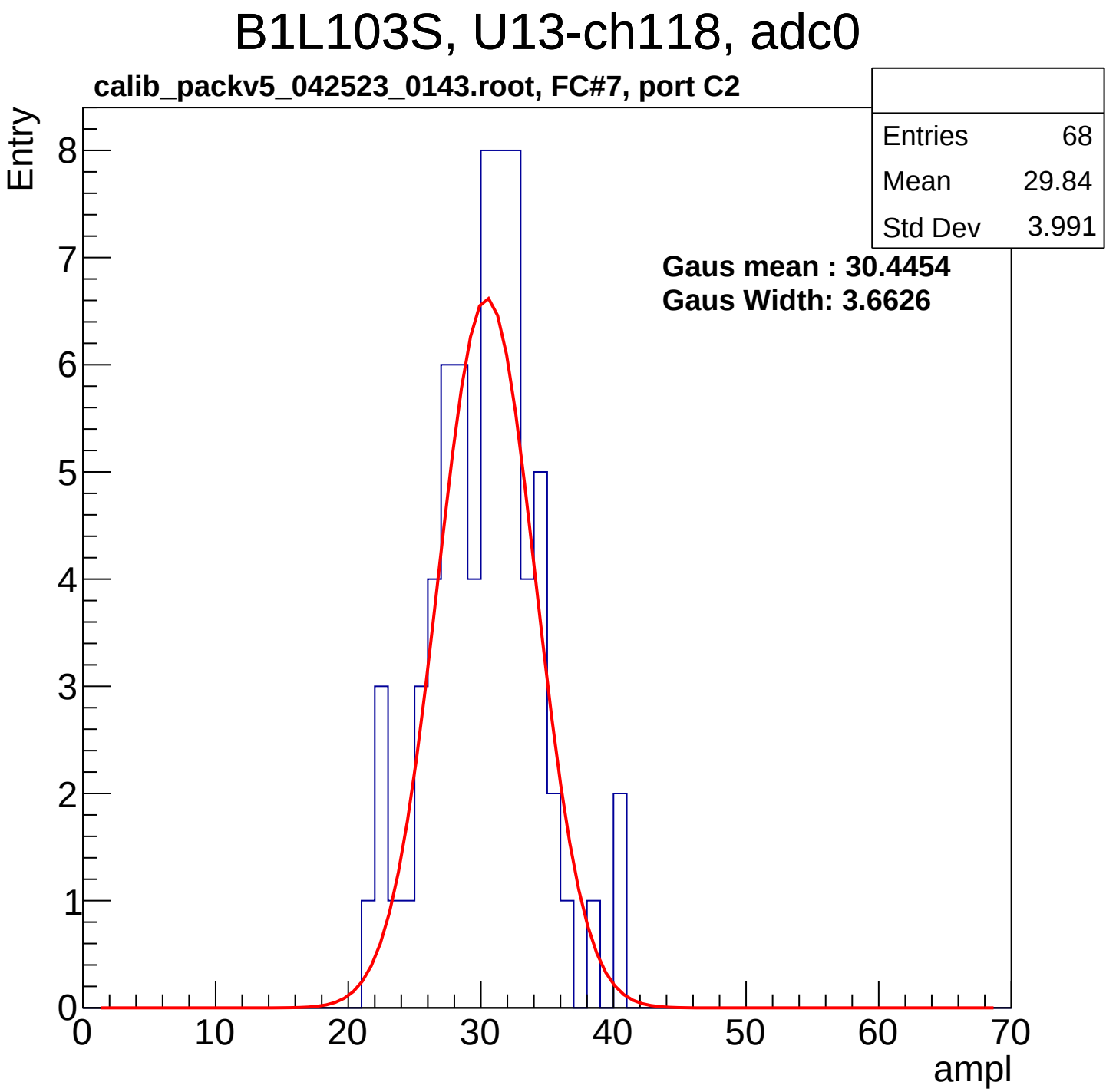
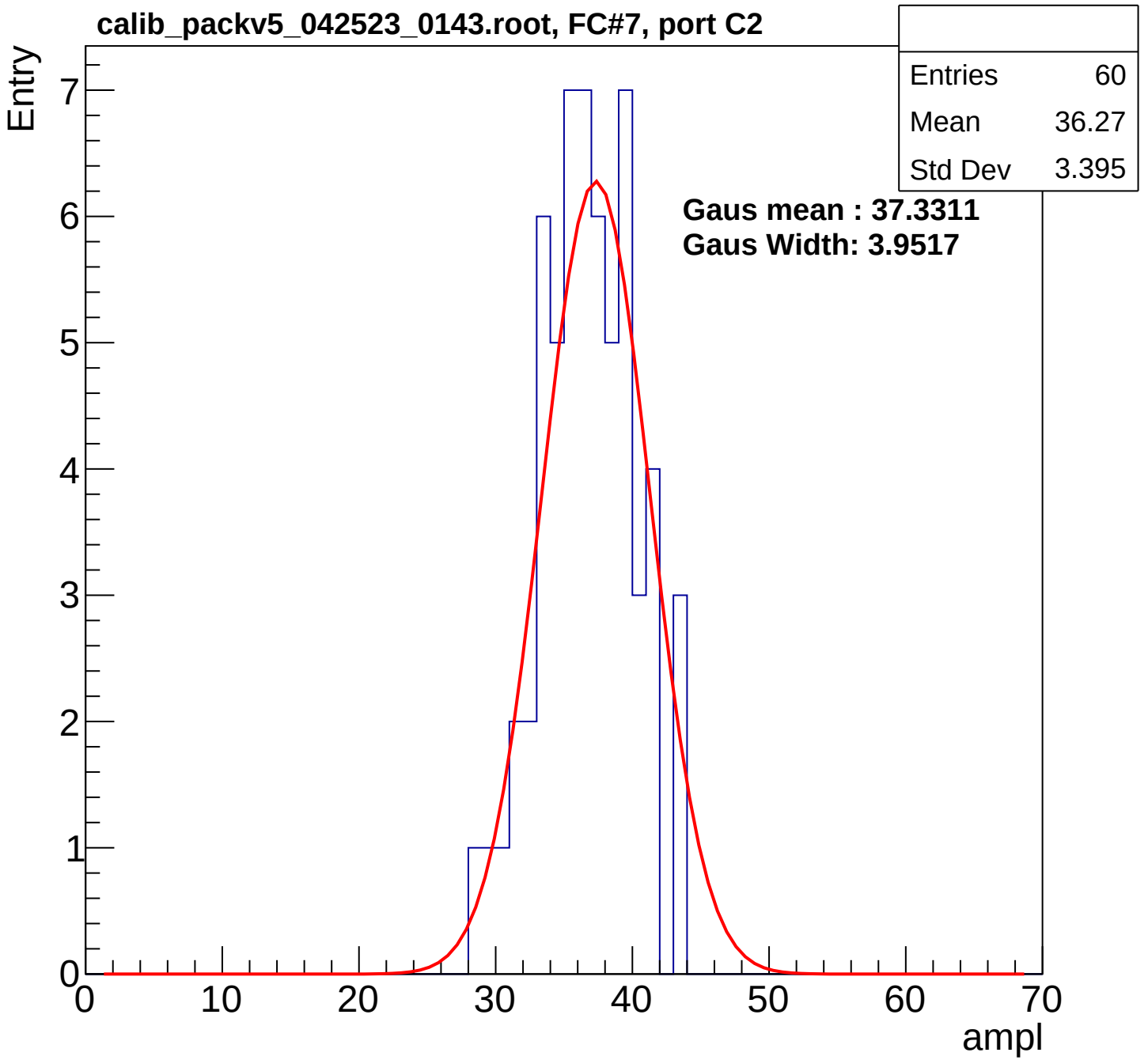

B1L103S, U13-ch117, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch117, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch118, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.84
Std Dev 3.991
Gaus mean : 30.4454
Gaus Width: 3.6626
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U13-ch118, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 36.27
Std Dev 3.395
Gaus mean : 37.3311
Gaus Width: 3.9517
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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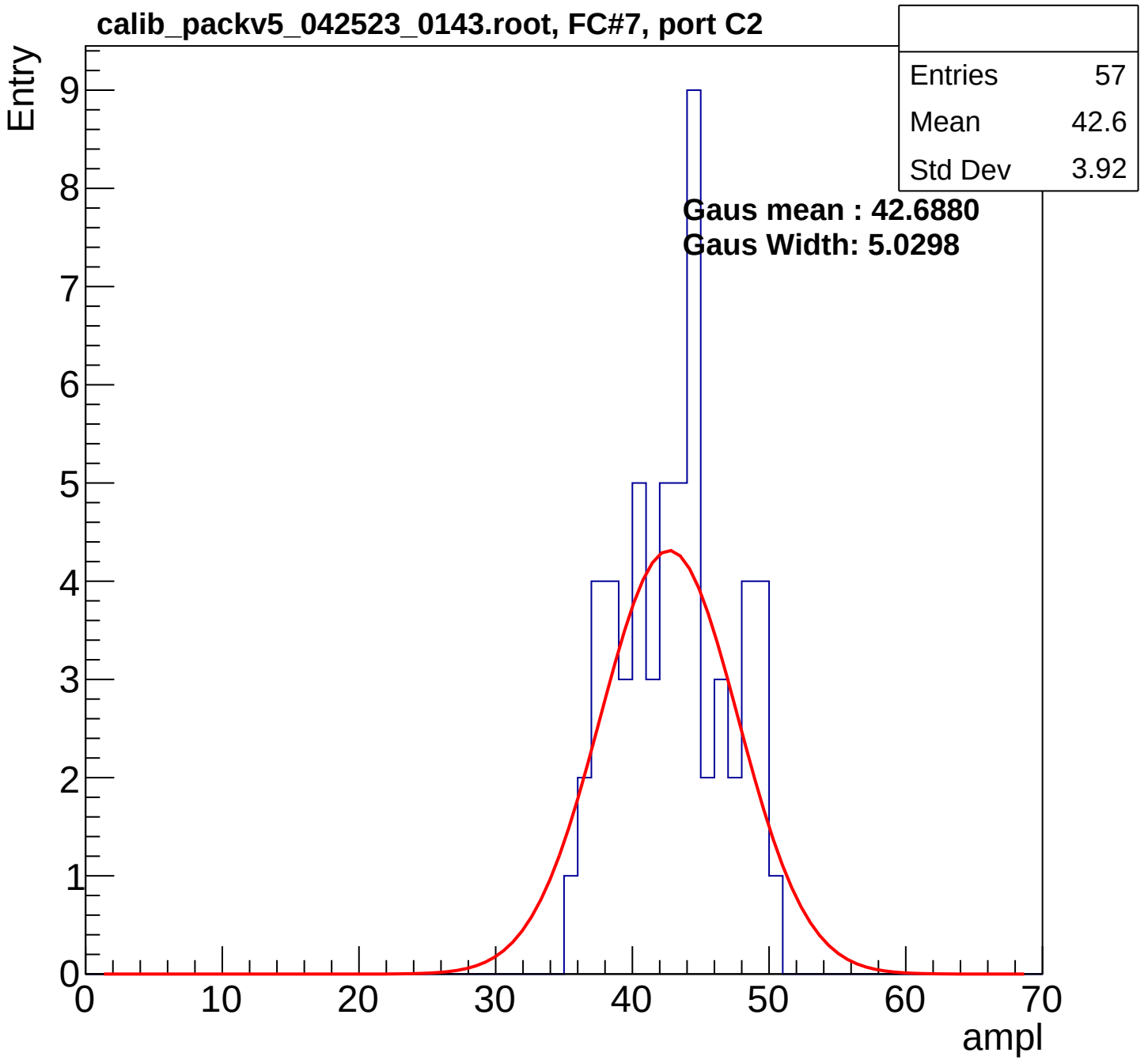
B1L103S, U13-ch118, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.6
Std Dev 3.92
Gaus mean : 42.6880
Gaus Width: 5.0298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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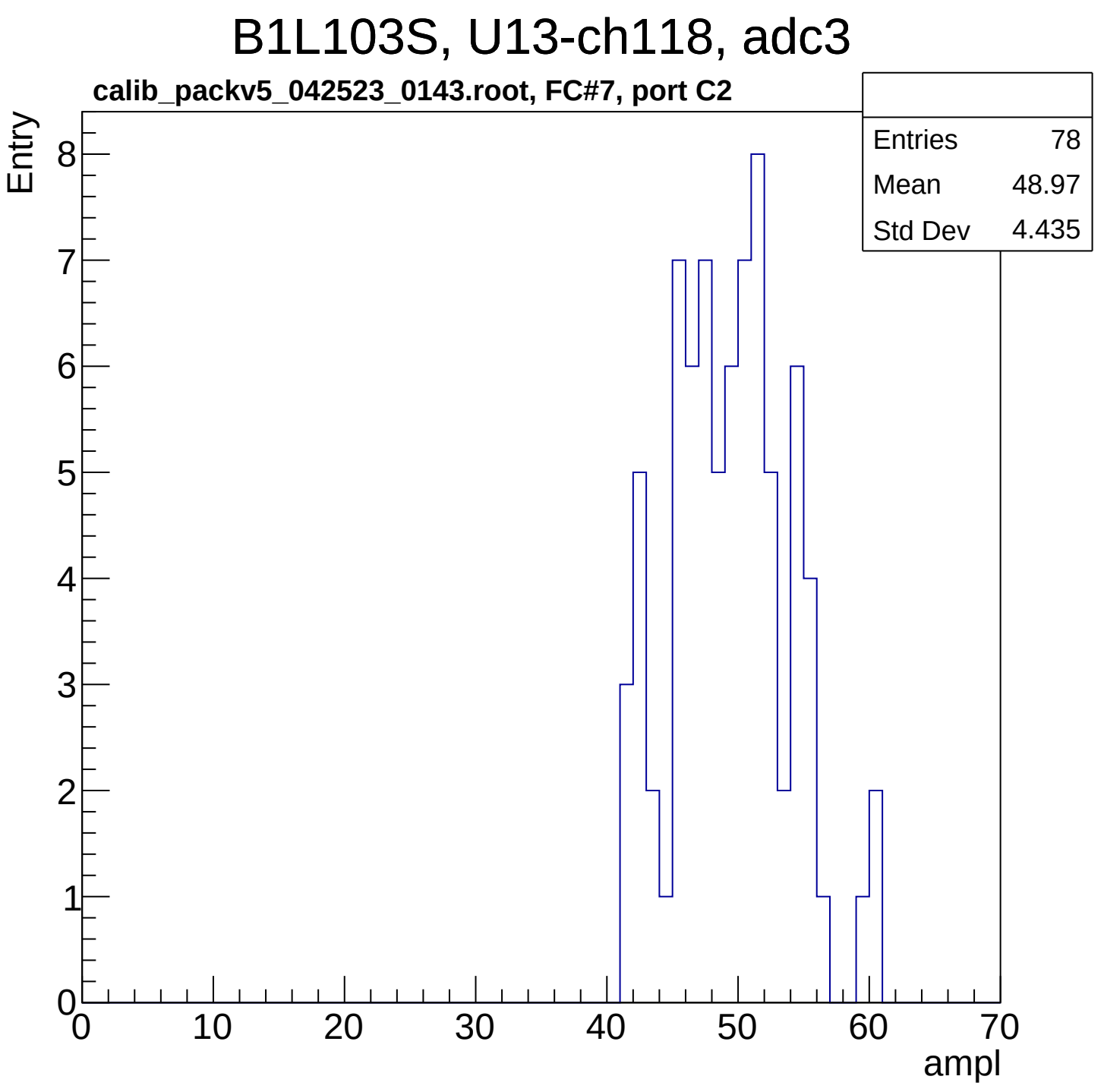
B1L103S, U13-ch118, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 48.97
Std Dev 4.435
Entry
ampl
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1
2
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4
5
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7
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0
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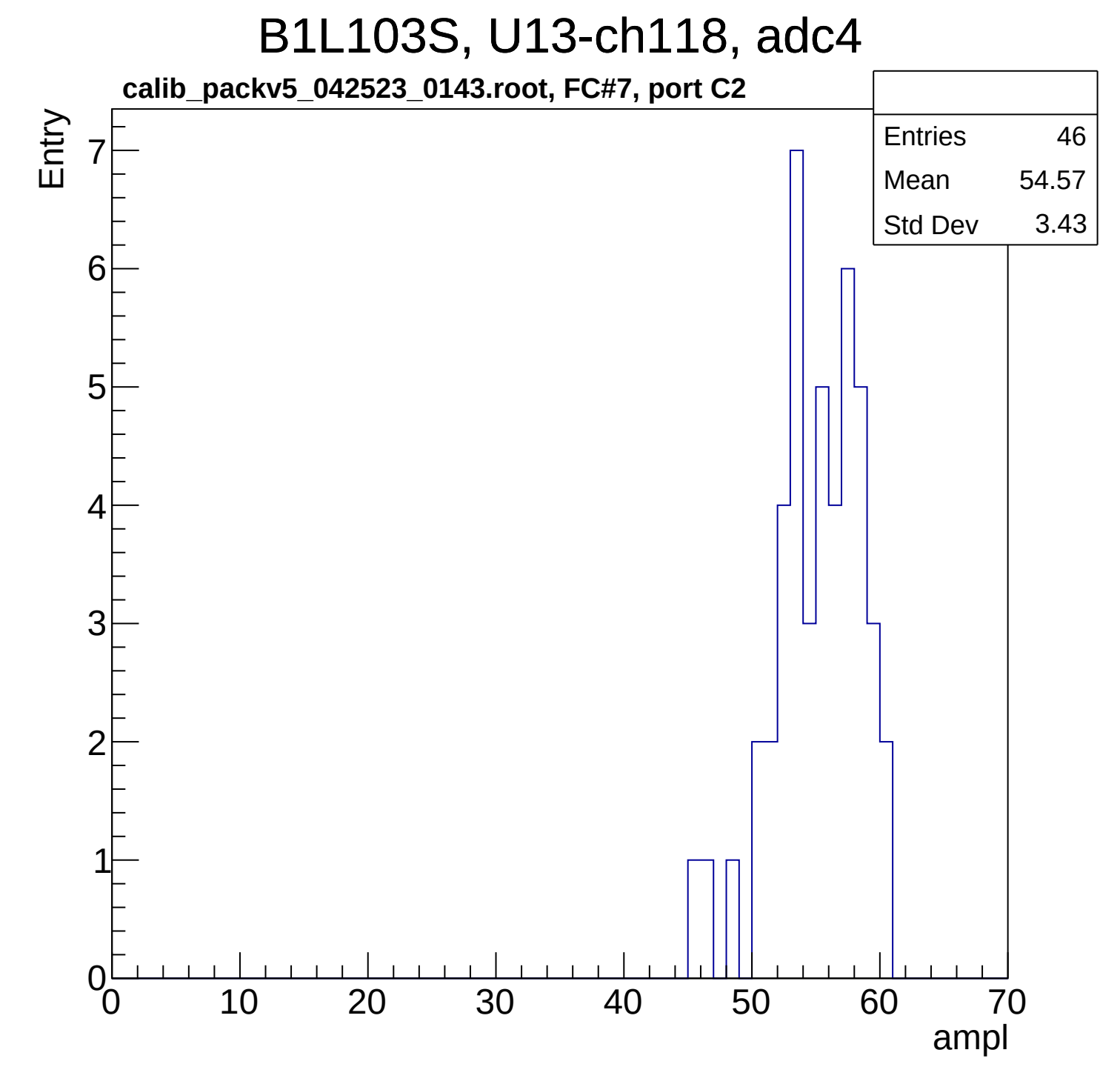
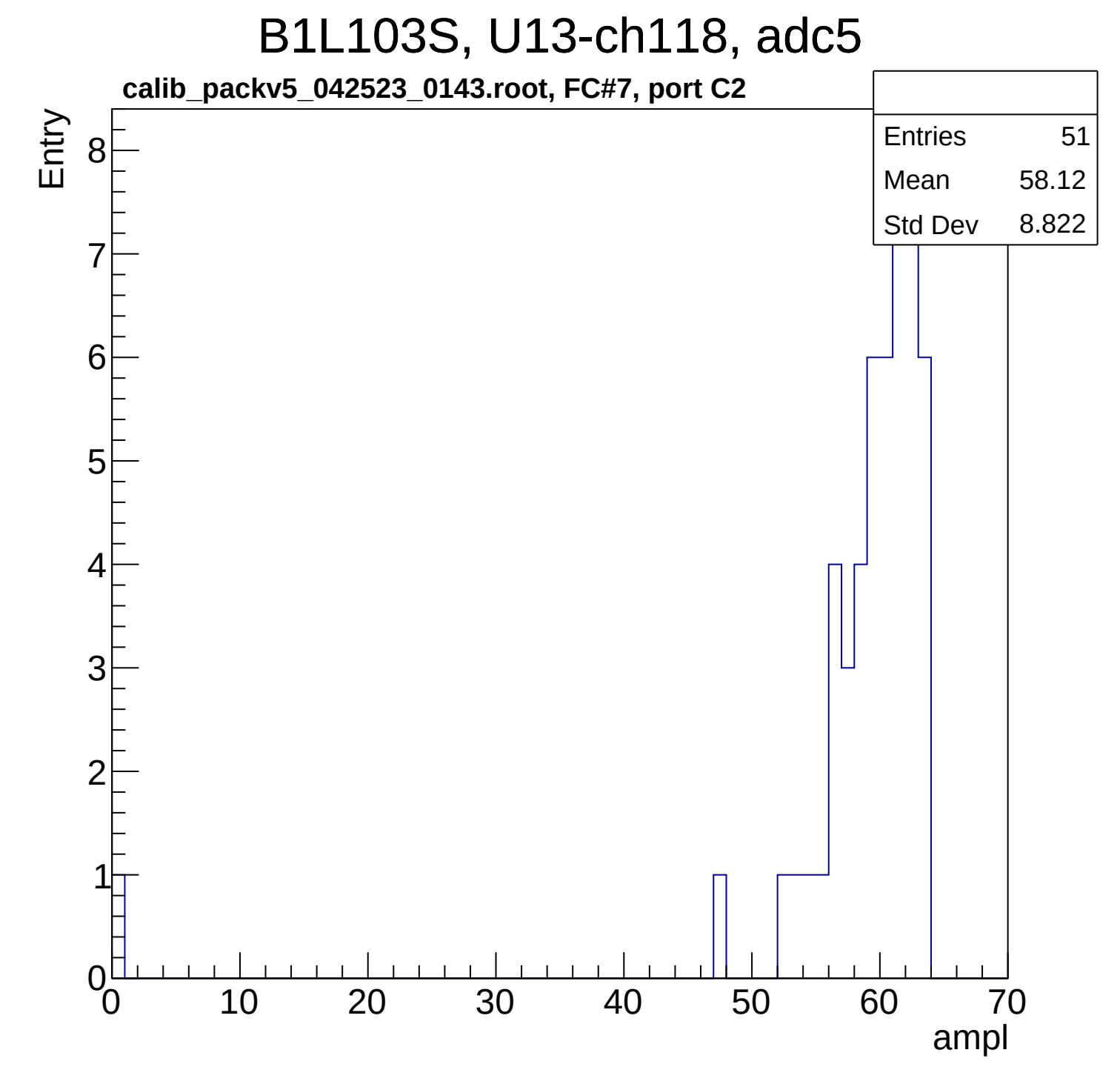

B1L103S, U13-ch118, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 54.57
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U13-ch118, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.12
Std Dev 8.822
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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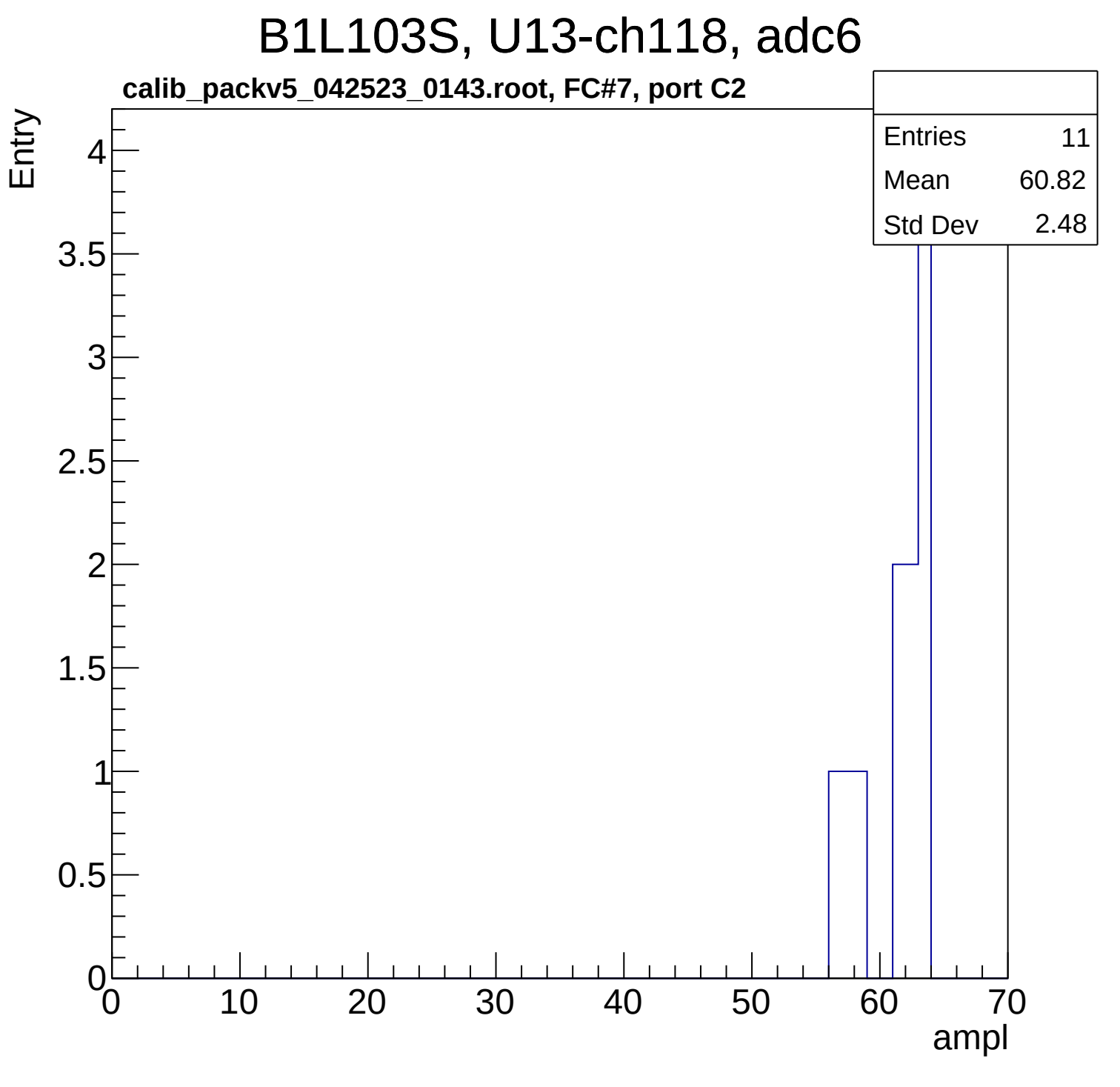
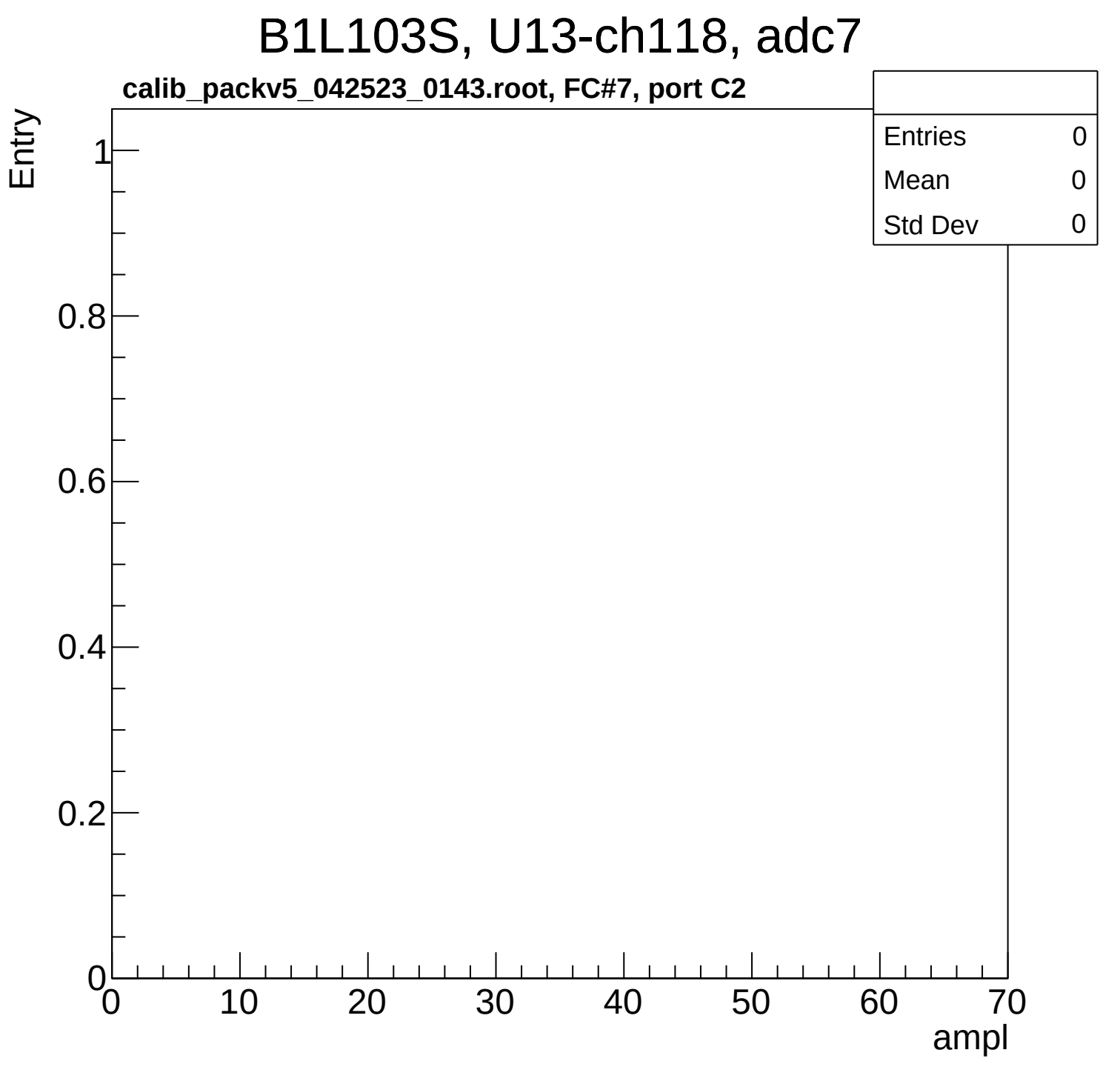

B1L103S, U13-ch118, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 60.82
Std Dev 2.48
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch118, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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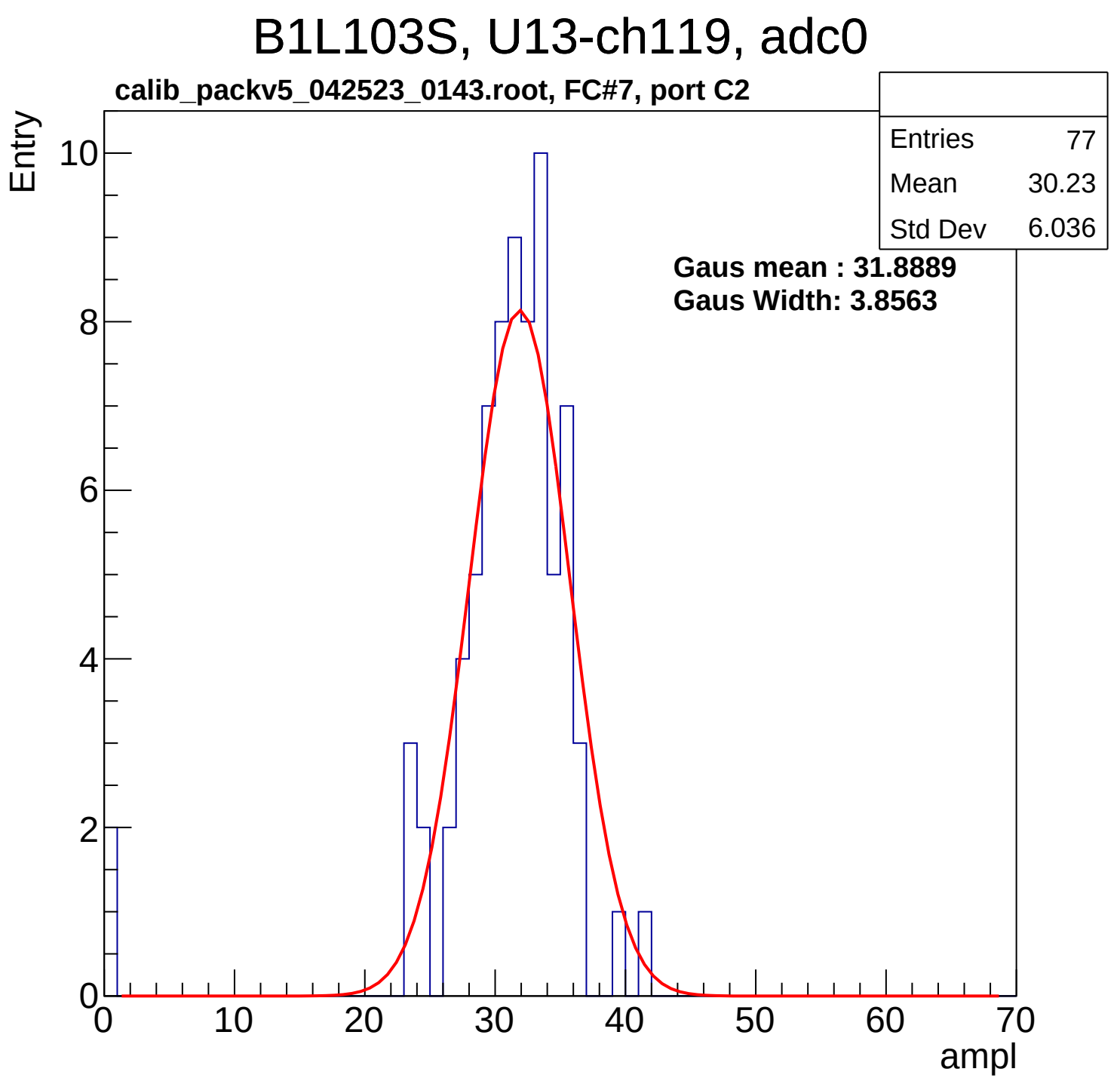
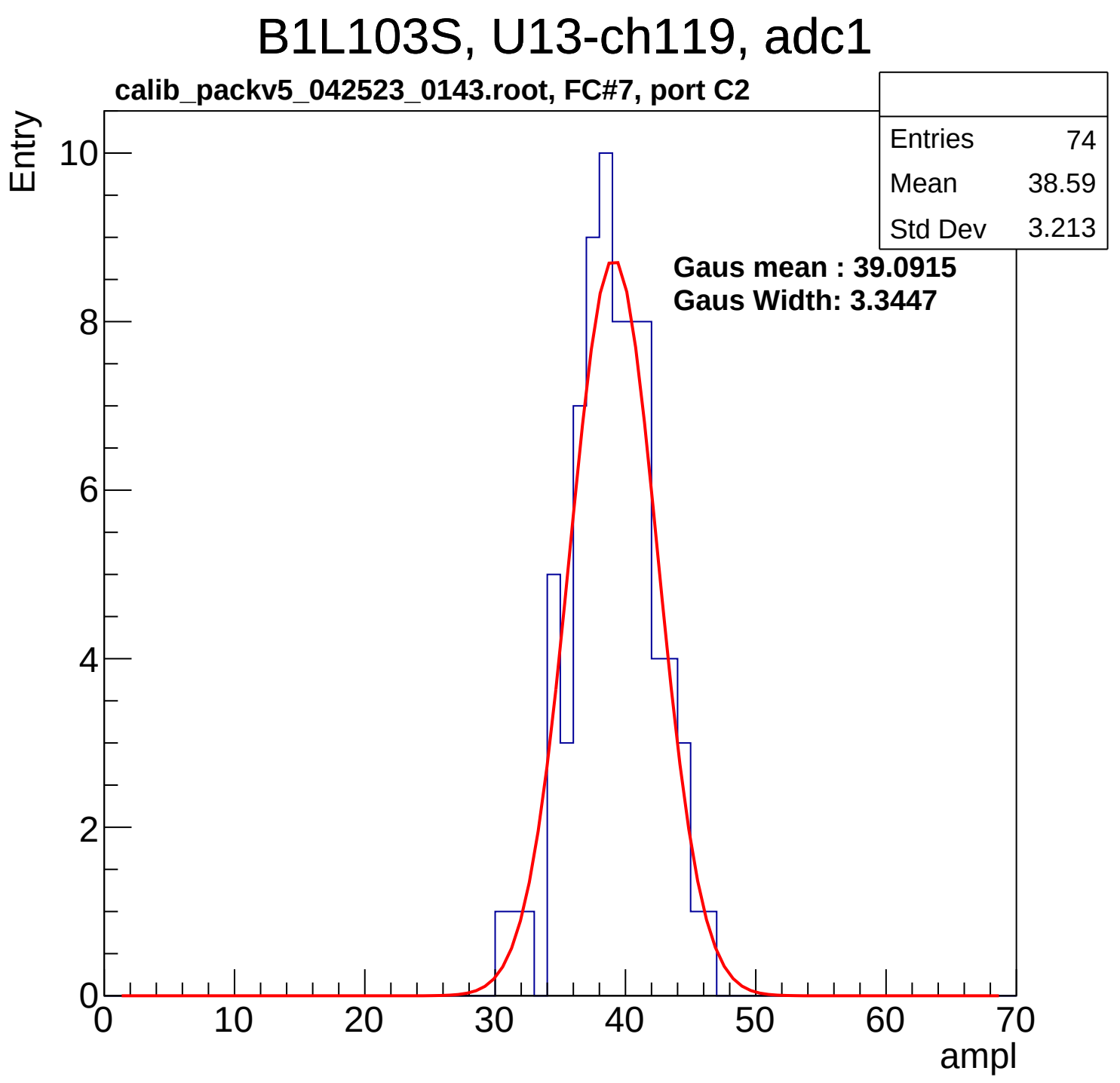
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch119, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.23
Std Dev 6.036
Gaus mean : 31.8889
Gaus Width: 3.8563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch119, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 38.59
Std Dev 3.213
Gaus mean : 39.0915
Gaus Width: 3.3447
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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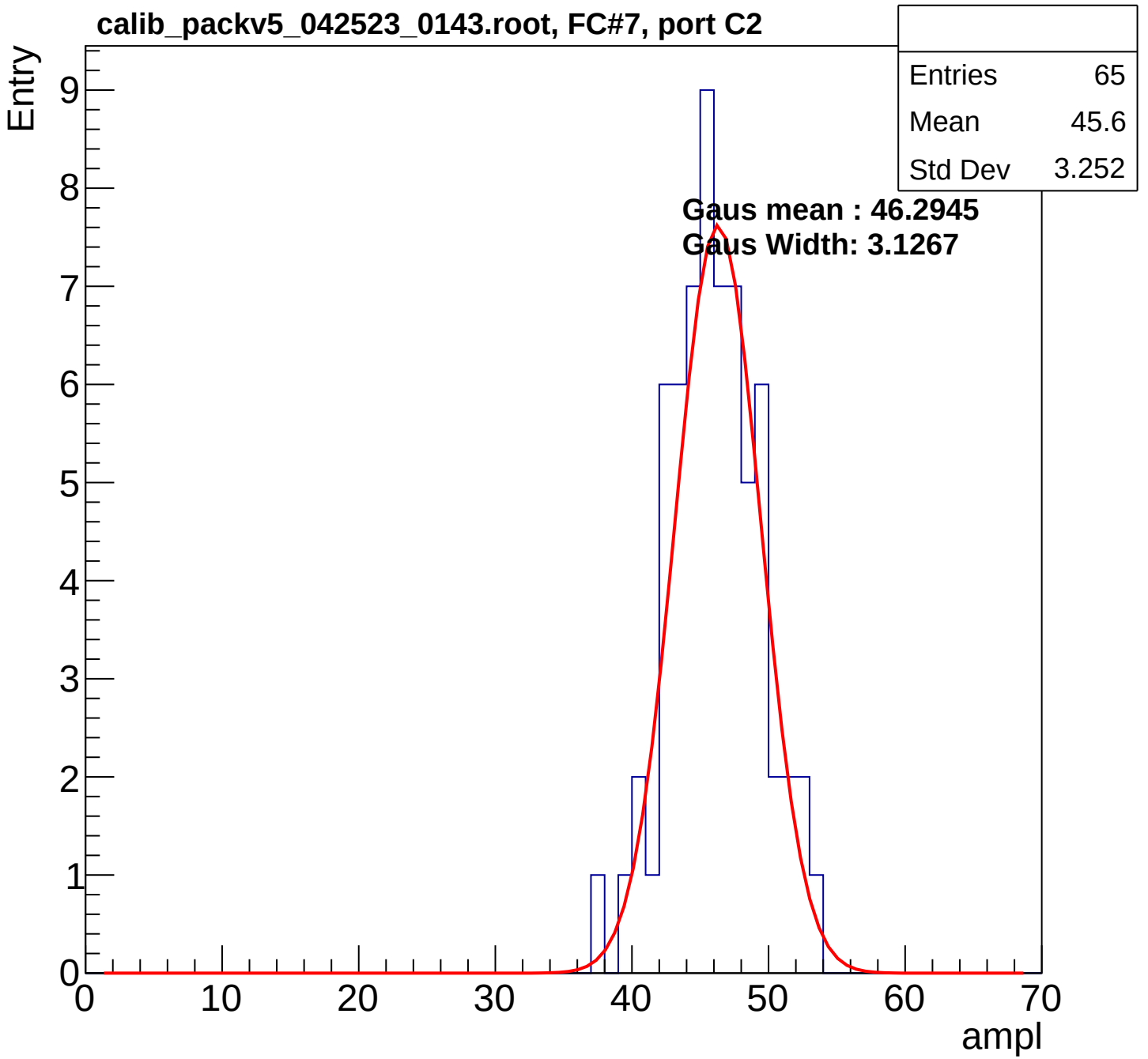
B1L103S, U13-ch119, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 45.6
Std Dev 3.252
Gaus mean : 46.2945
Gaus Width: 3.1267
Entry
ampl
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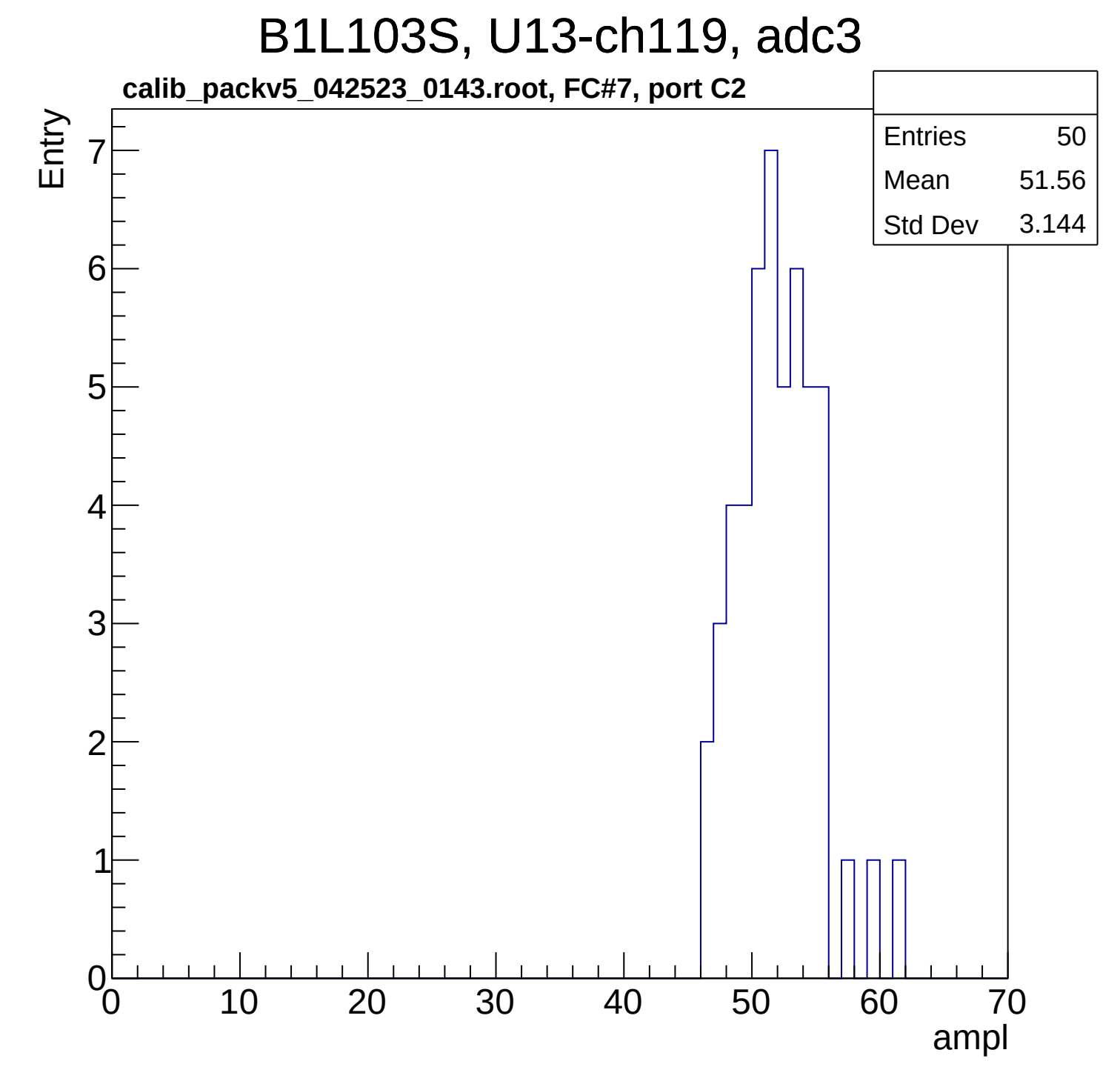
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch119, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 51.56
Std Dev 3.144
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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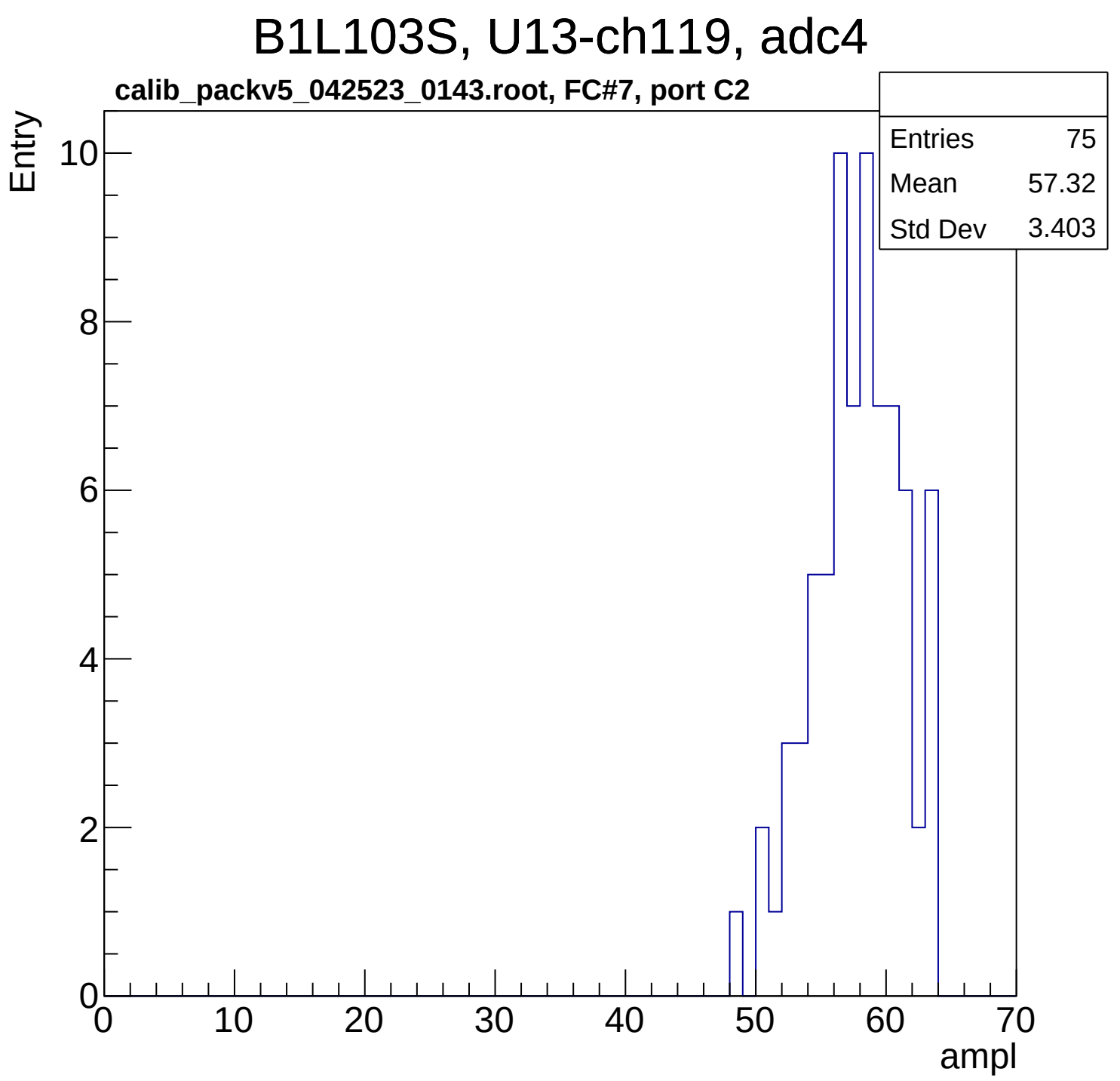
B1L103S, U13-ch119, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 57.32
Std Dev 3.403
Entry
ampl
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4
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8
10
0
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40
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60
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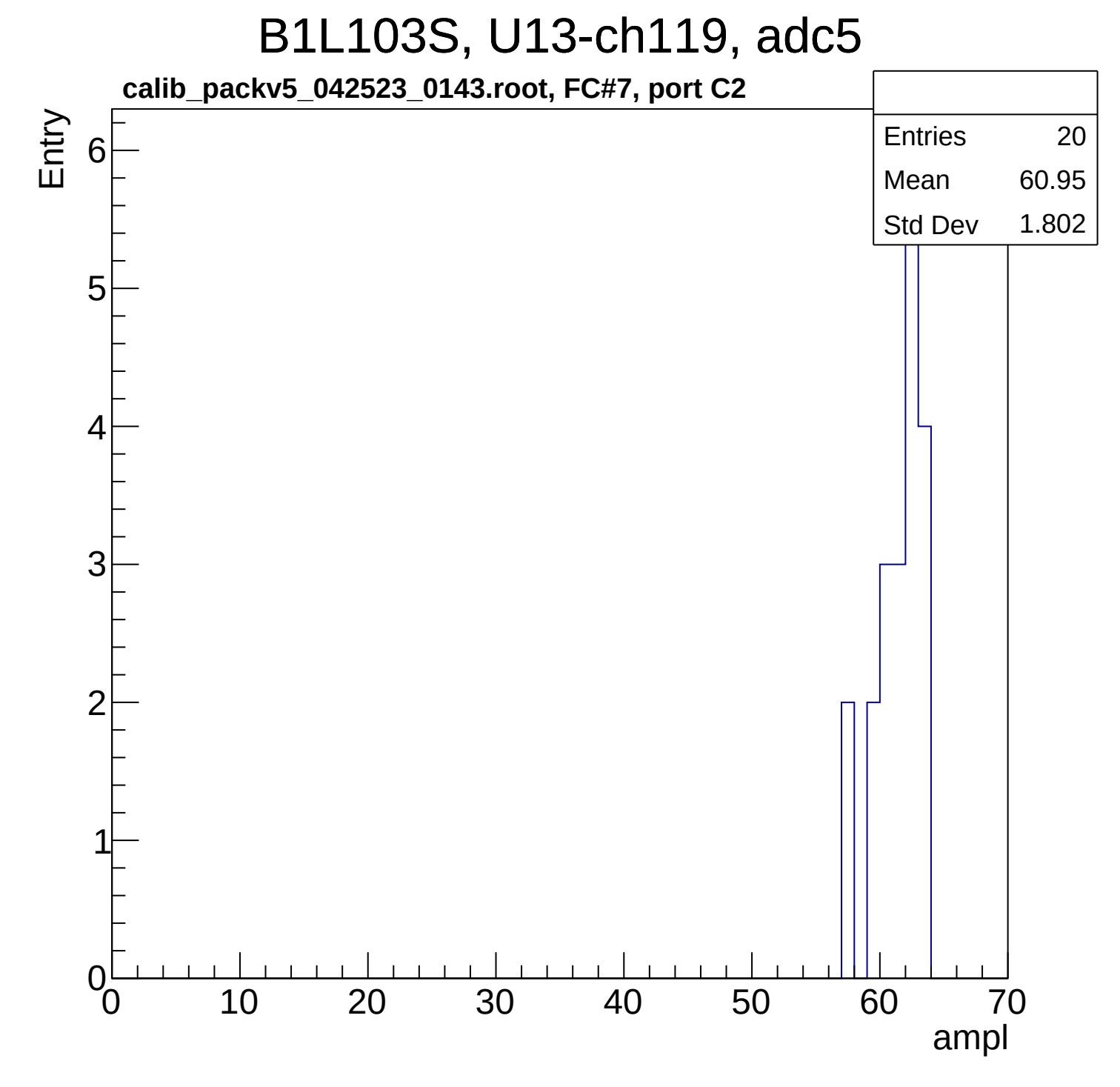
B1L103S, U13-ch119, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 60.95
Std Dev 1.802
Entry
ampl
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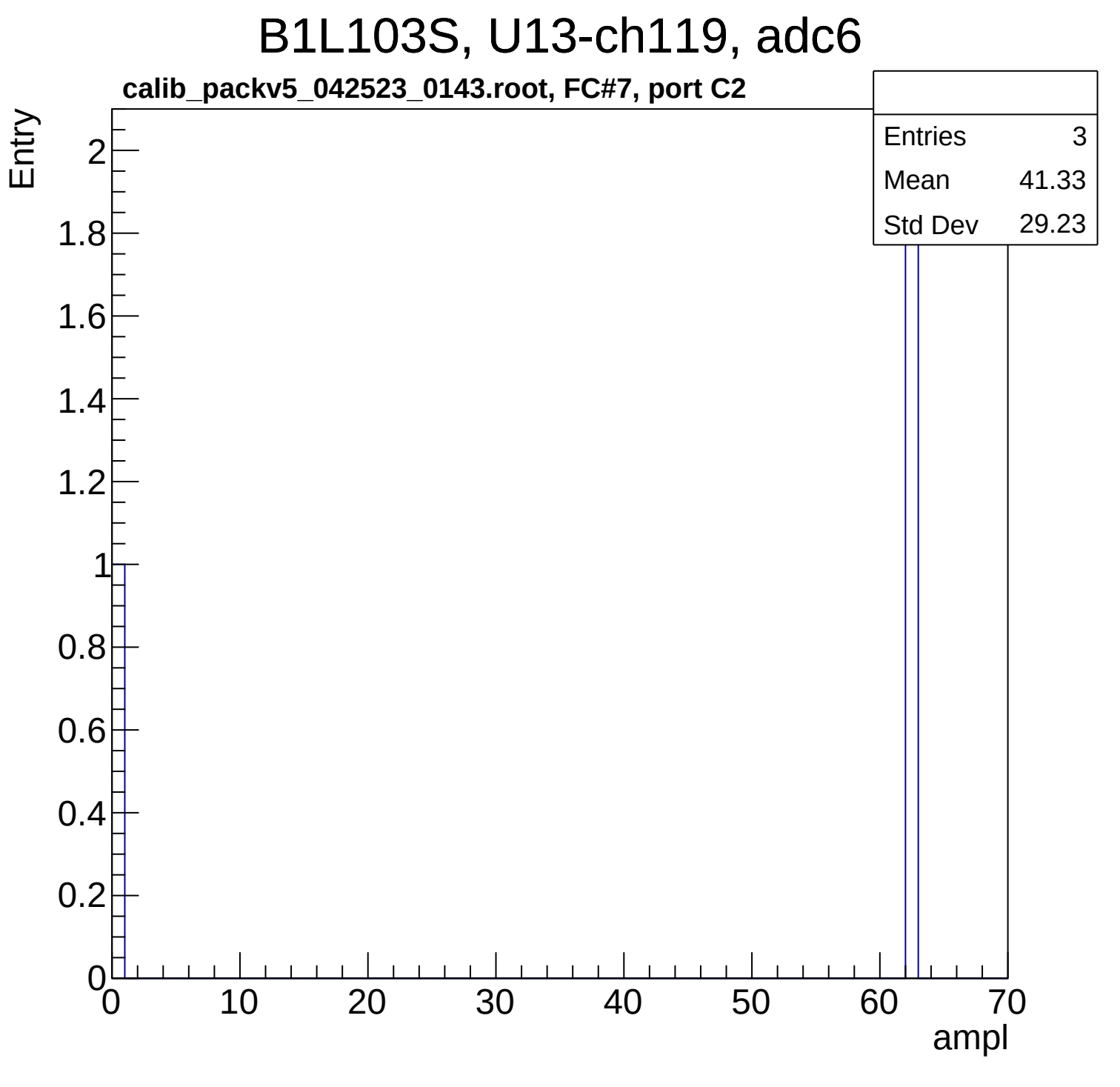
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch119, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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10
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40
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60
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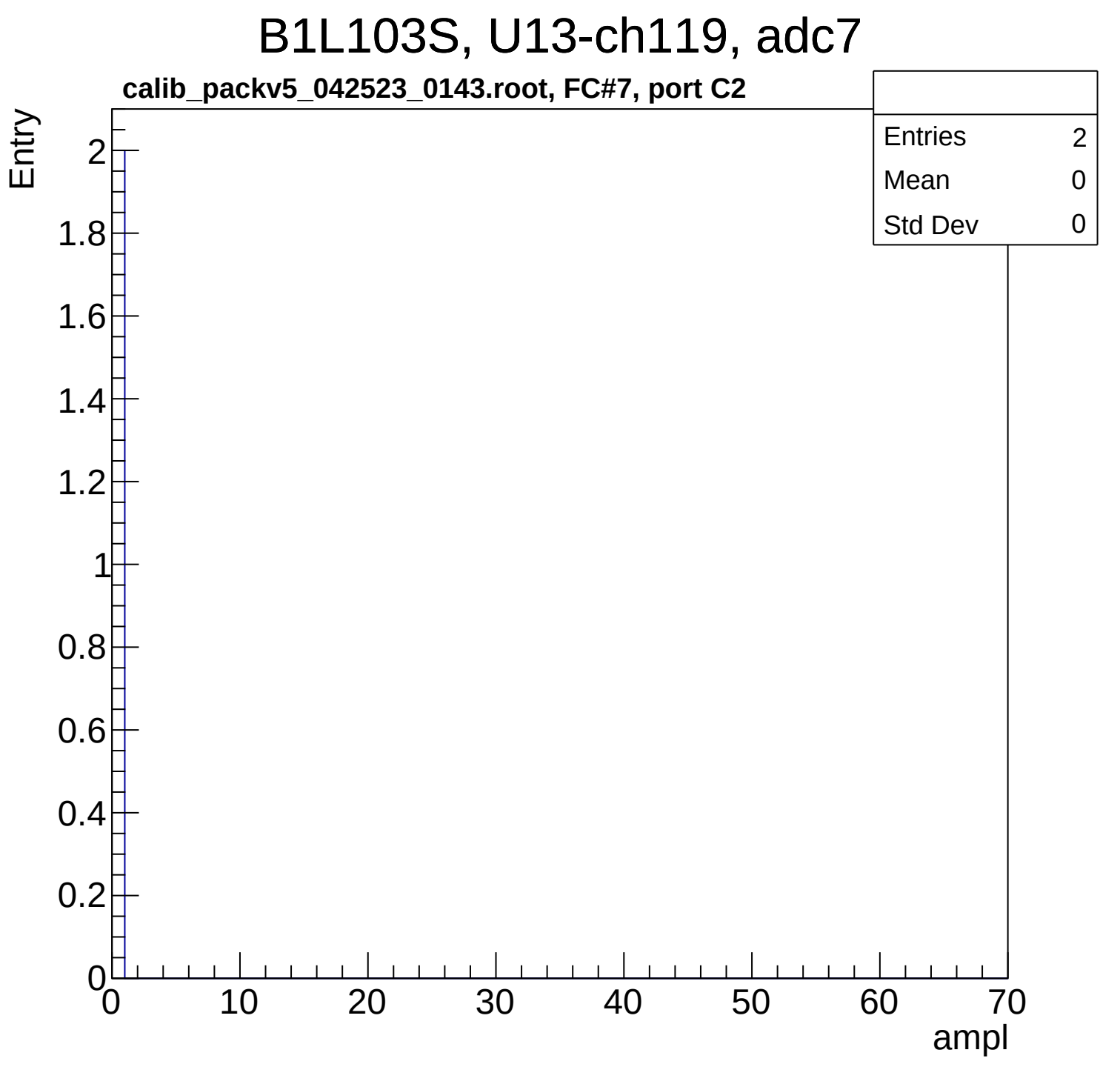
B1L103S, U13-ch119, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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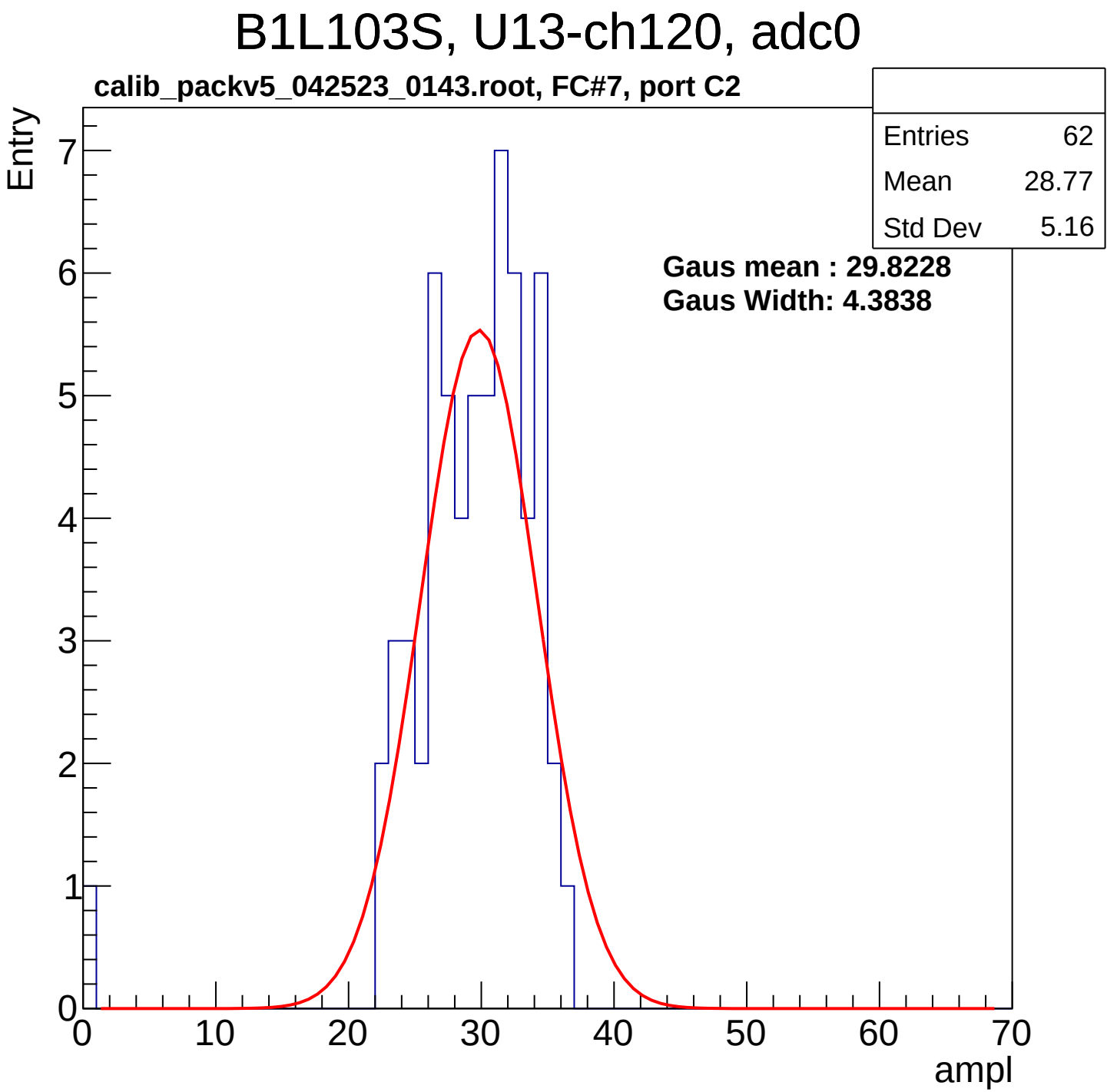
60
70
ampl


B1L103S, U13-ch120, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28.77
Std Dev 5.16
Gaus mean : 29.8228
Gaus Width: 4.3838
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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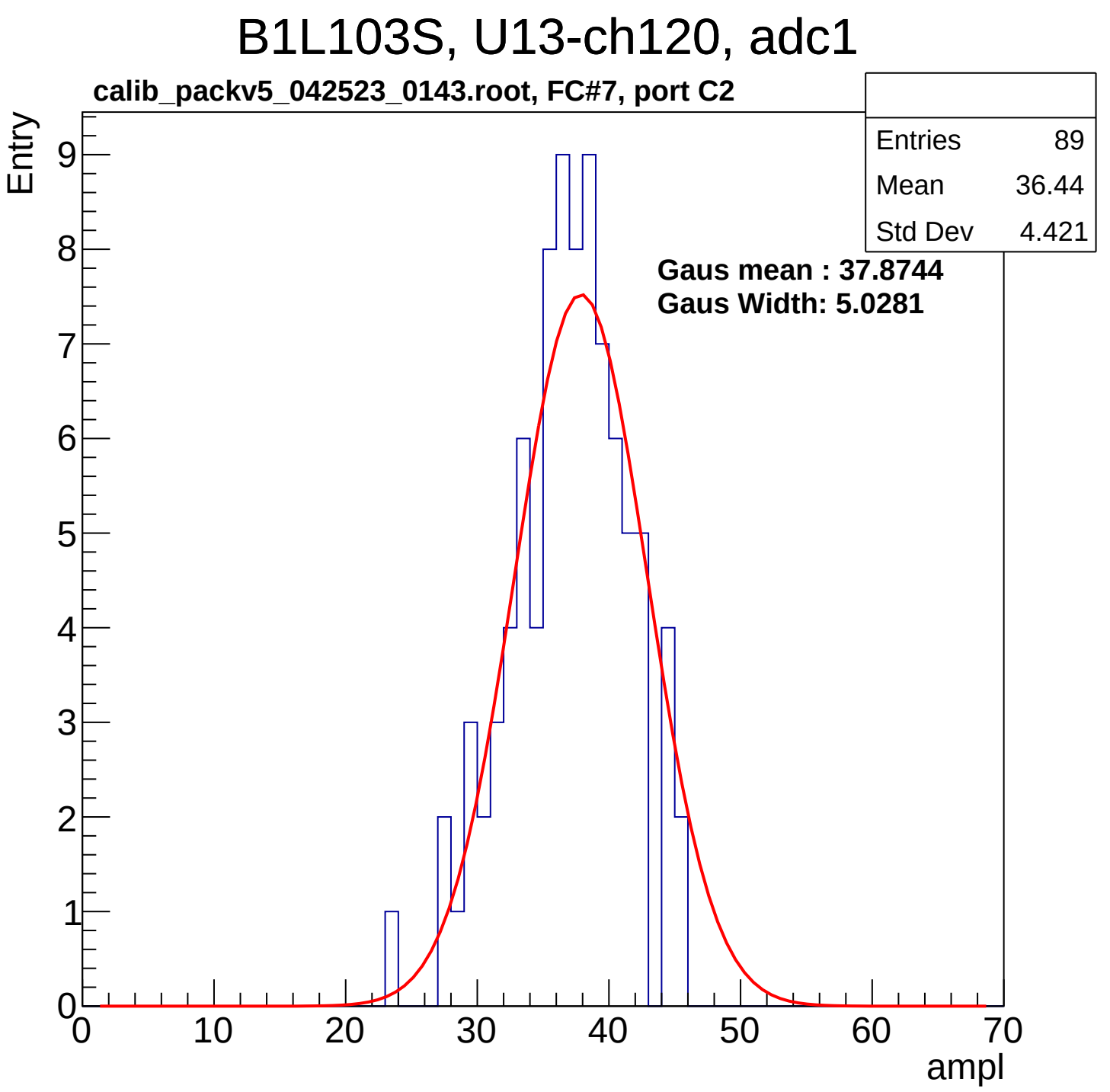
B1L103S, U13-ch120, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 36.44
Std Dev 4.421
Gaus mean : 37.8744
Gaus Width: 5.0281
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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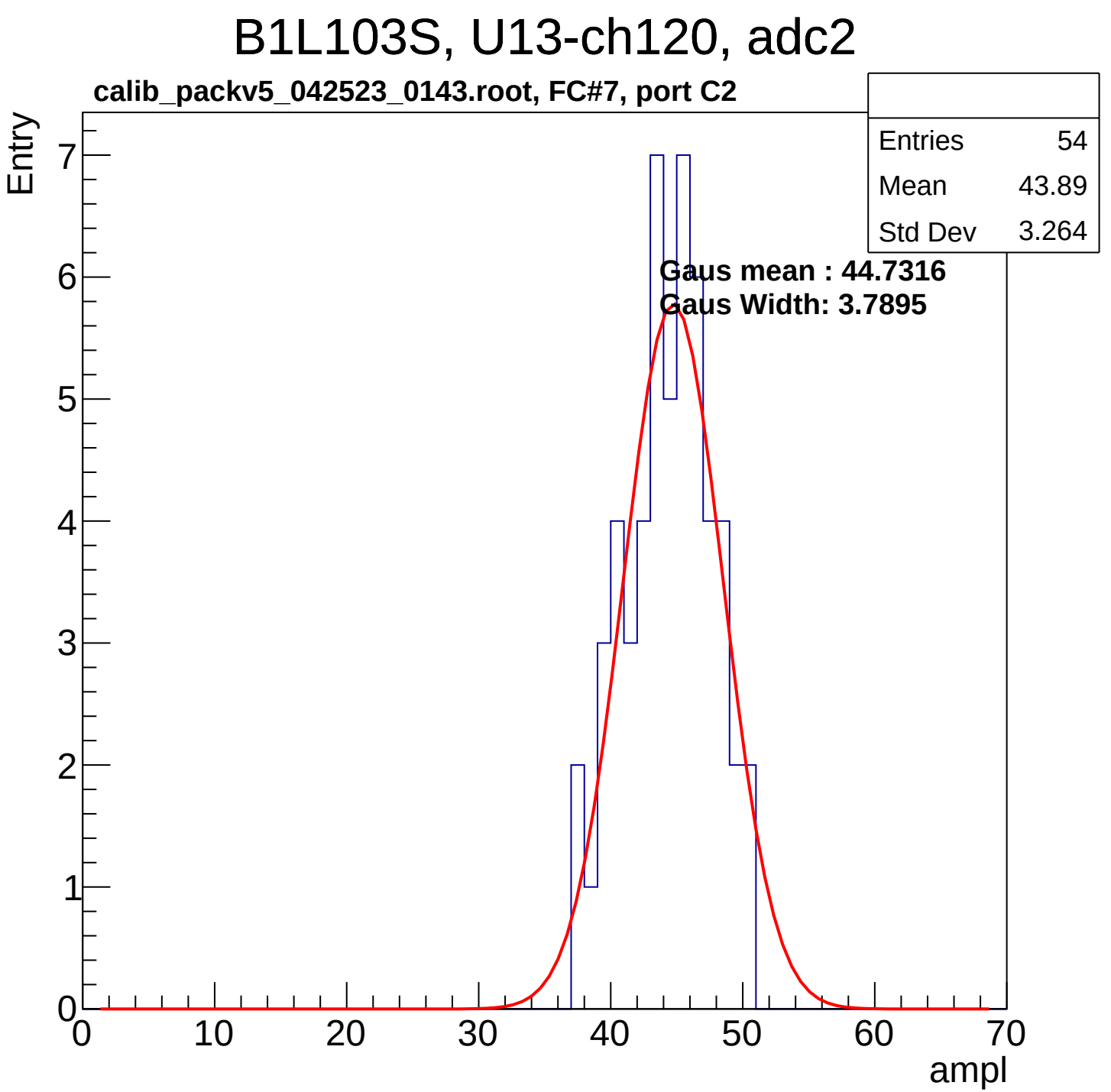
B1L103S, U13-ch120, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 43.89
Std Dev 3.264
Gaus mean : 44.7316
Gaus Width: 3.7895
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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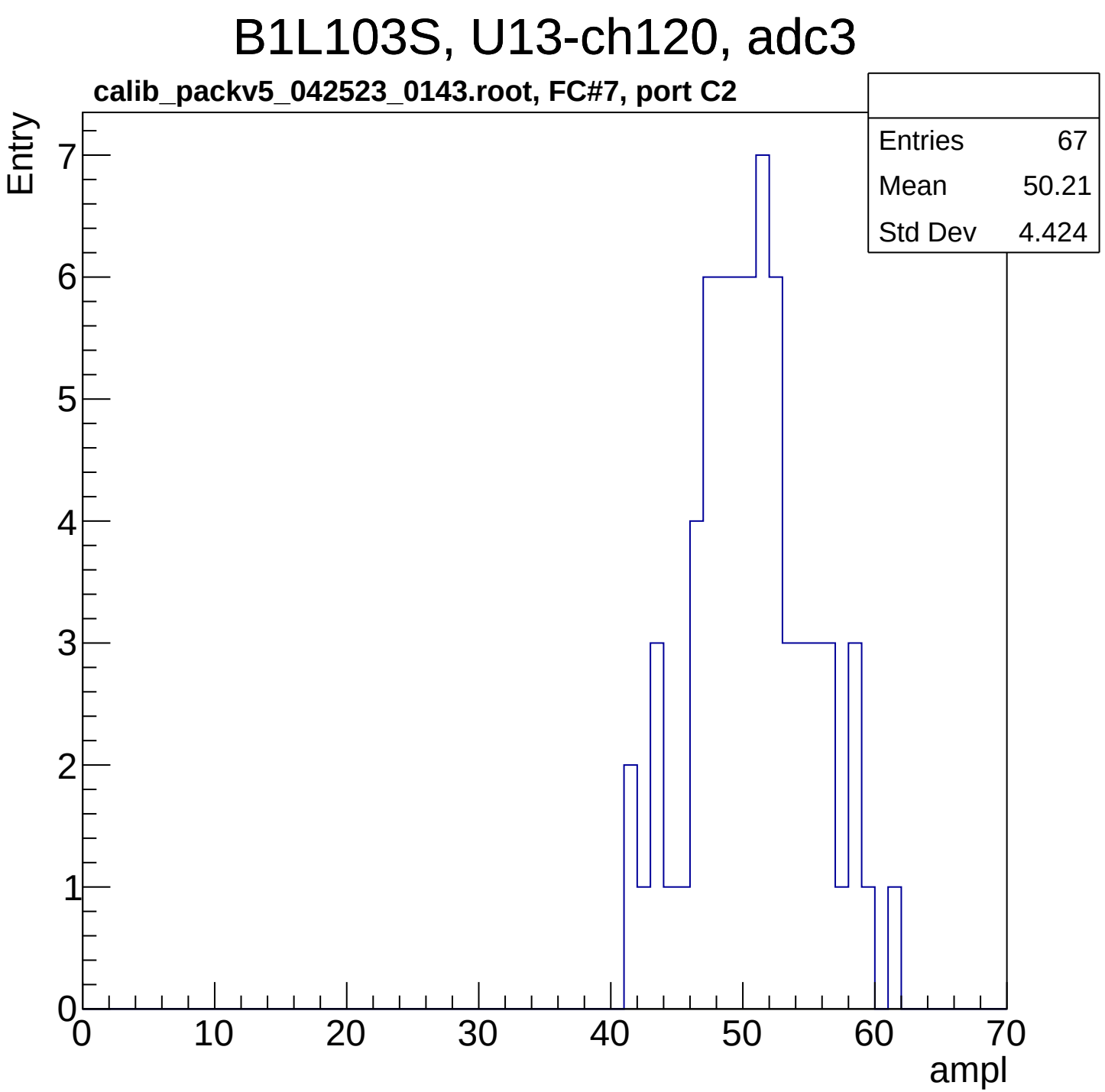
B1L103S, U13-ch120, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 50.21
Std Dev 4.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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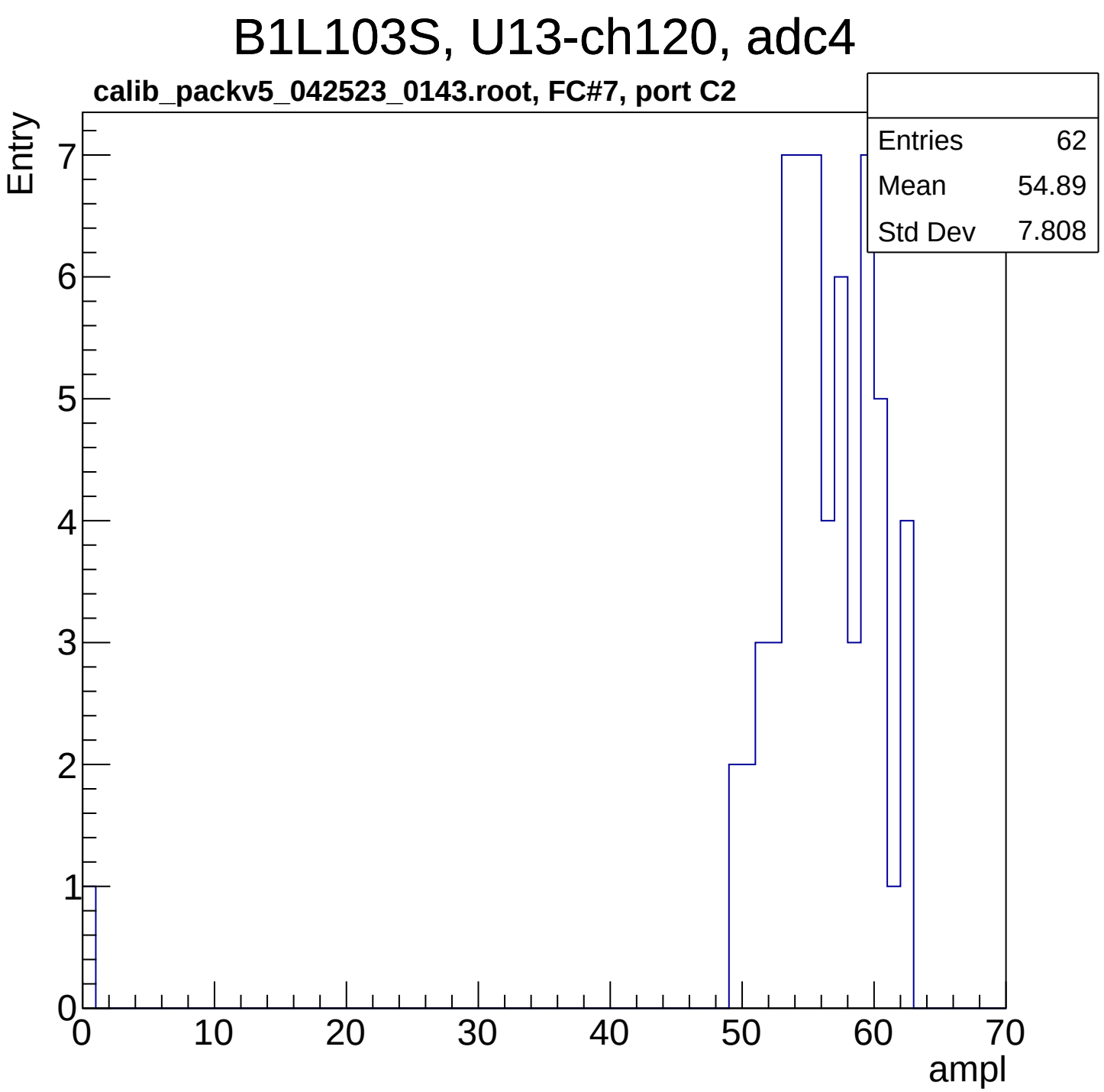
B1L103S, U13-ch120, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 54.89
Std Dev 7.808
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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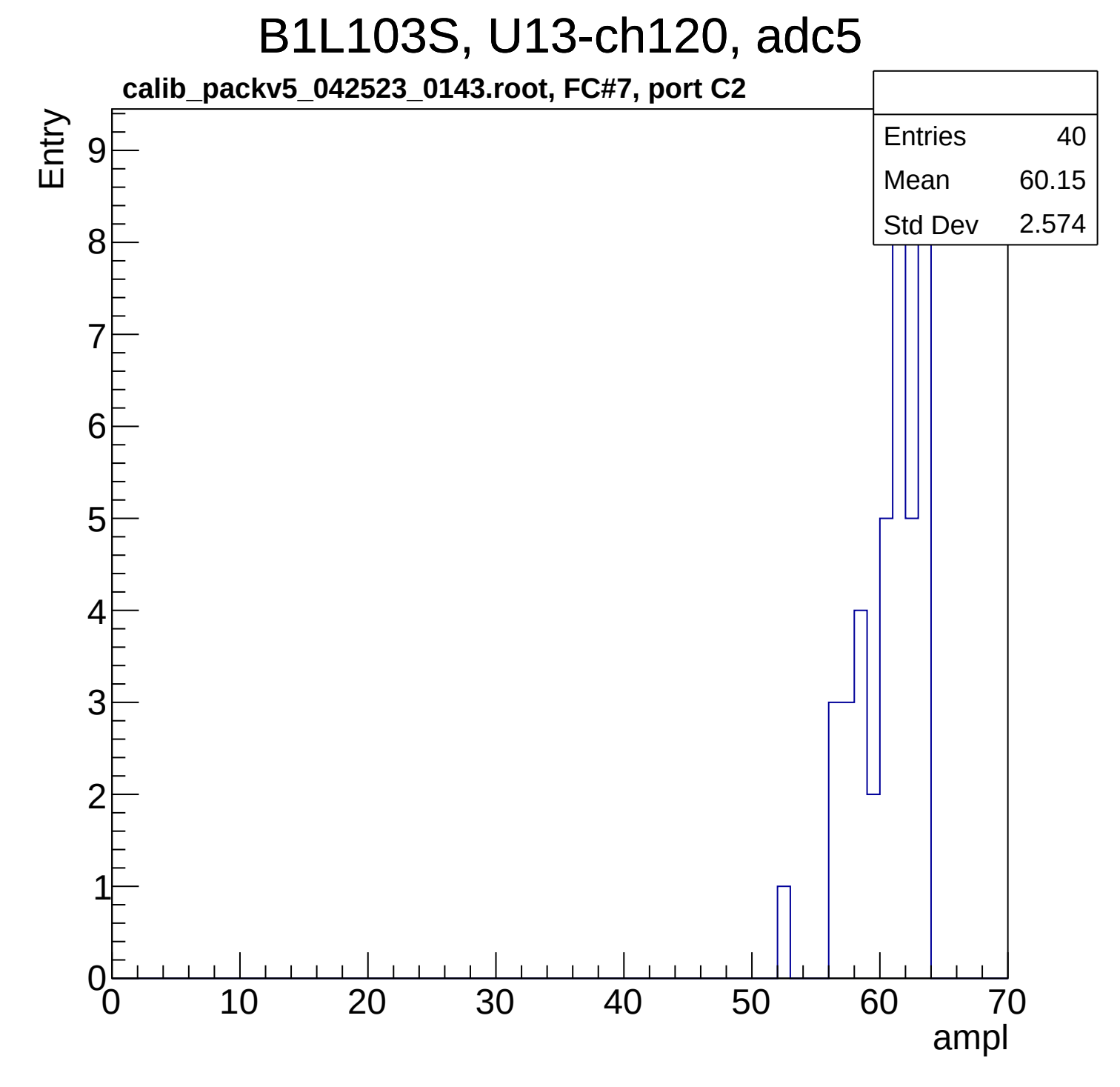
B1L103S, U13-ch120, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 60.15
Std Dev 2.574
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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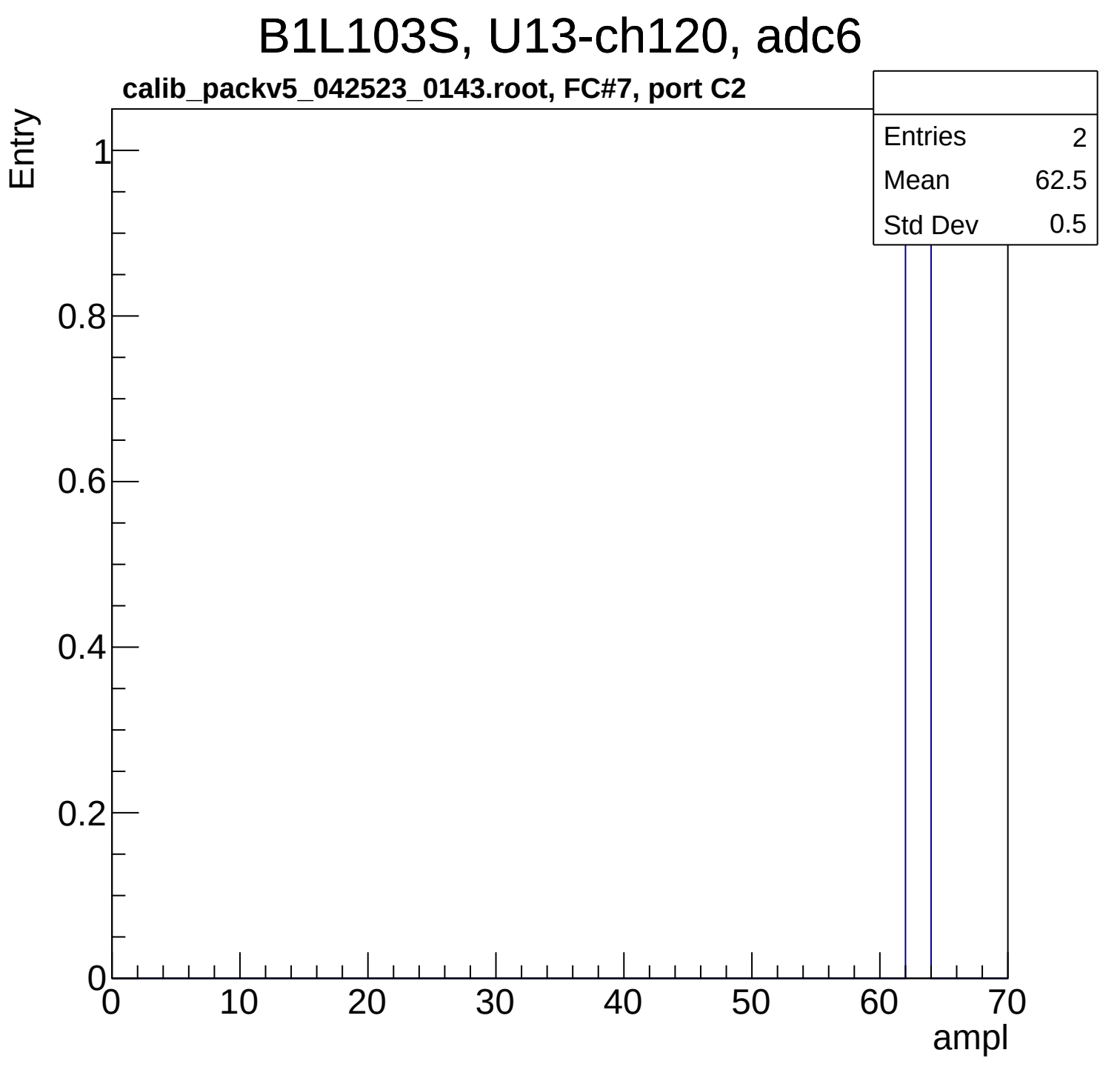
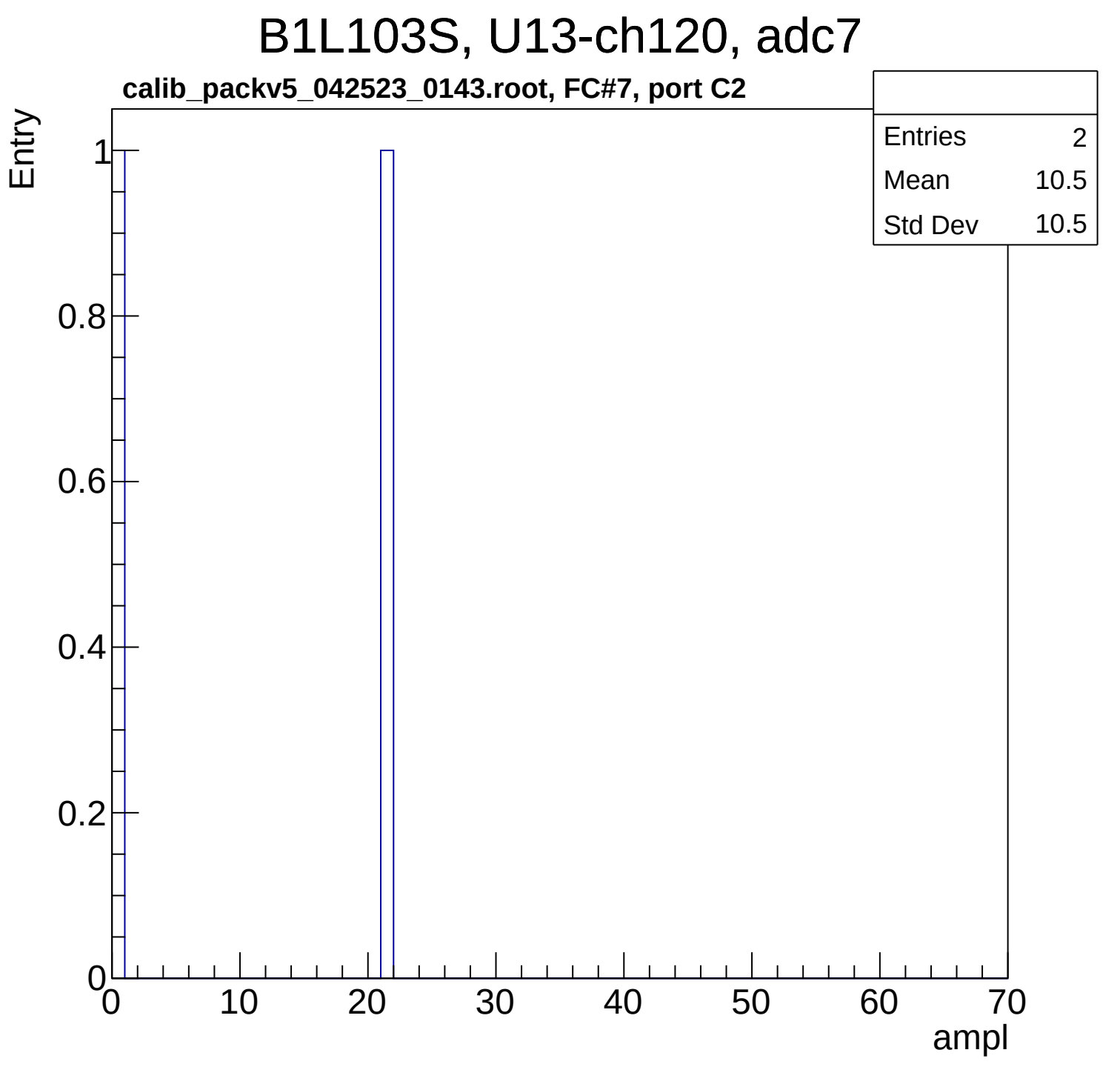
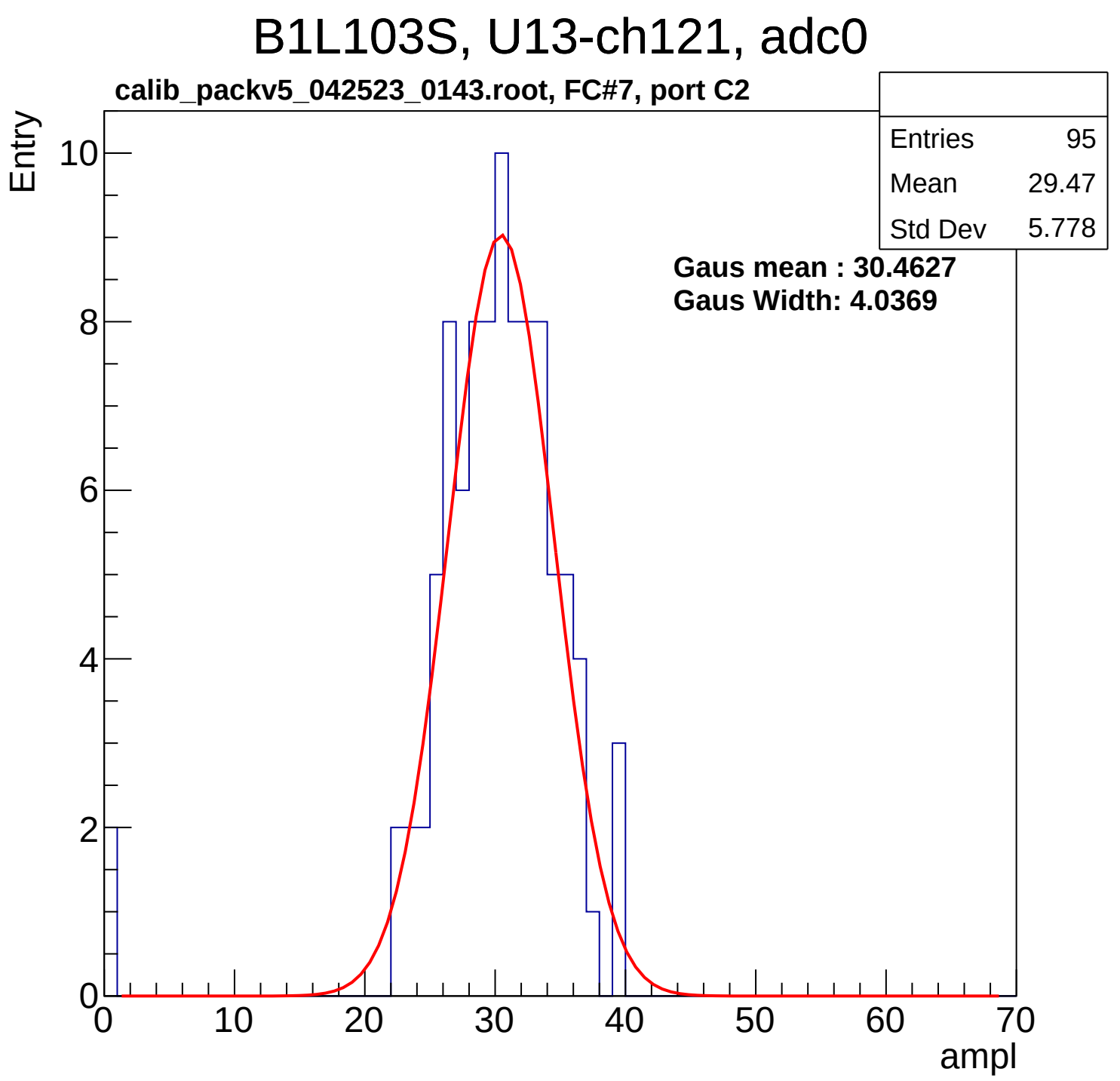
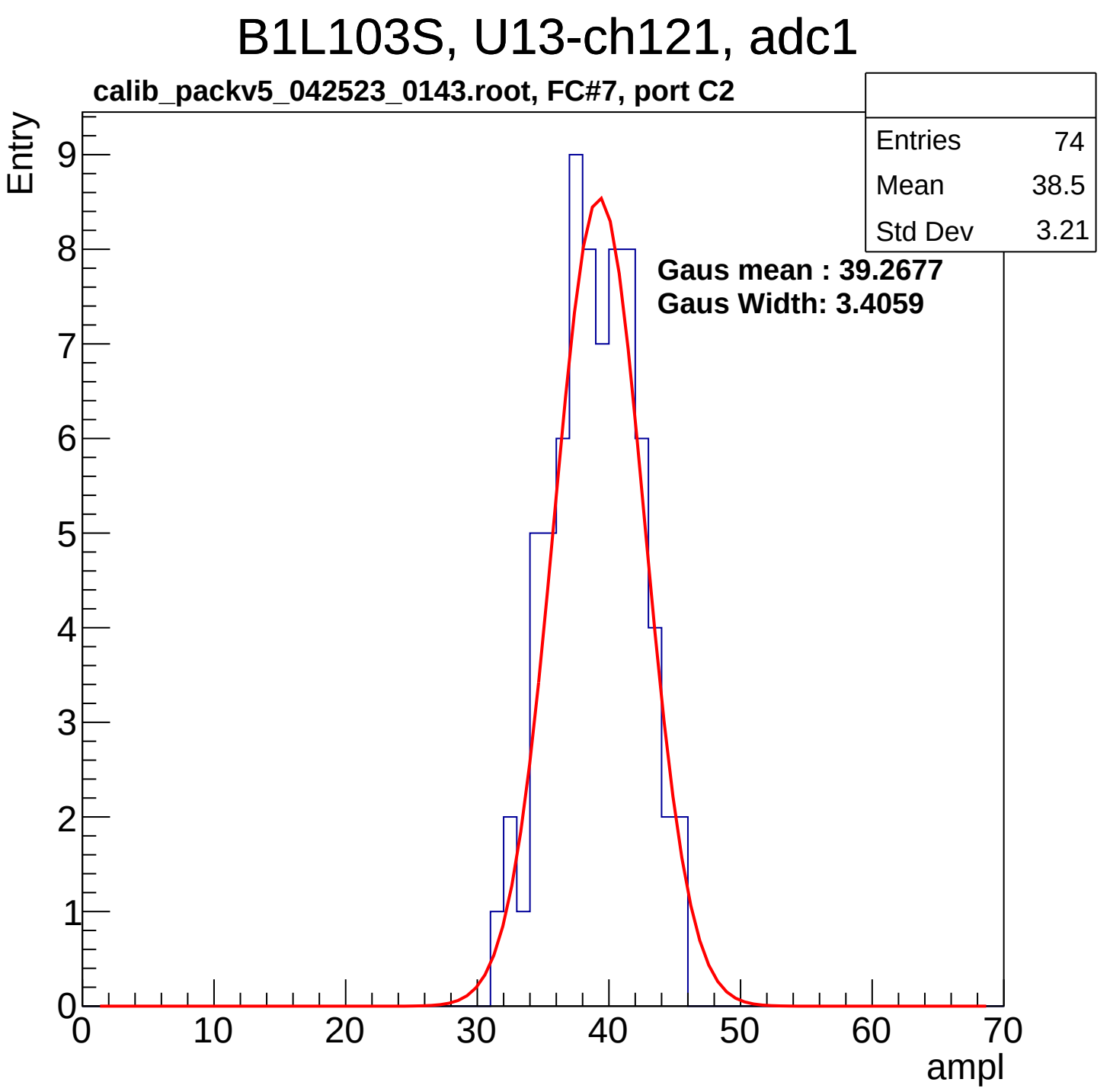
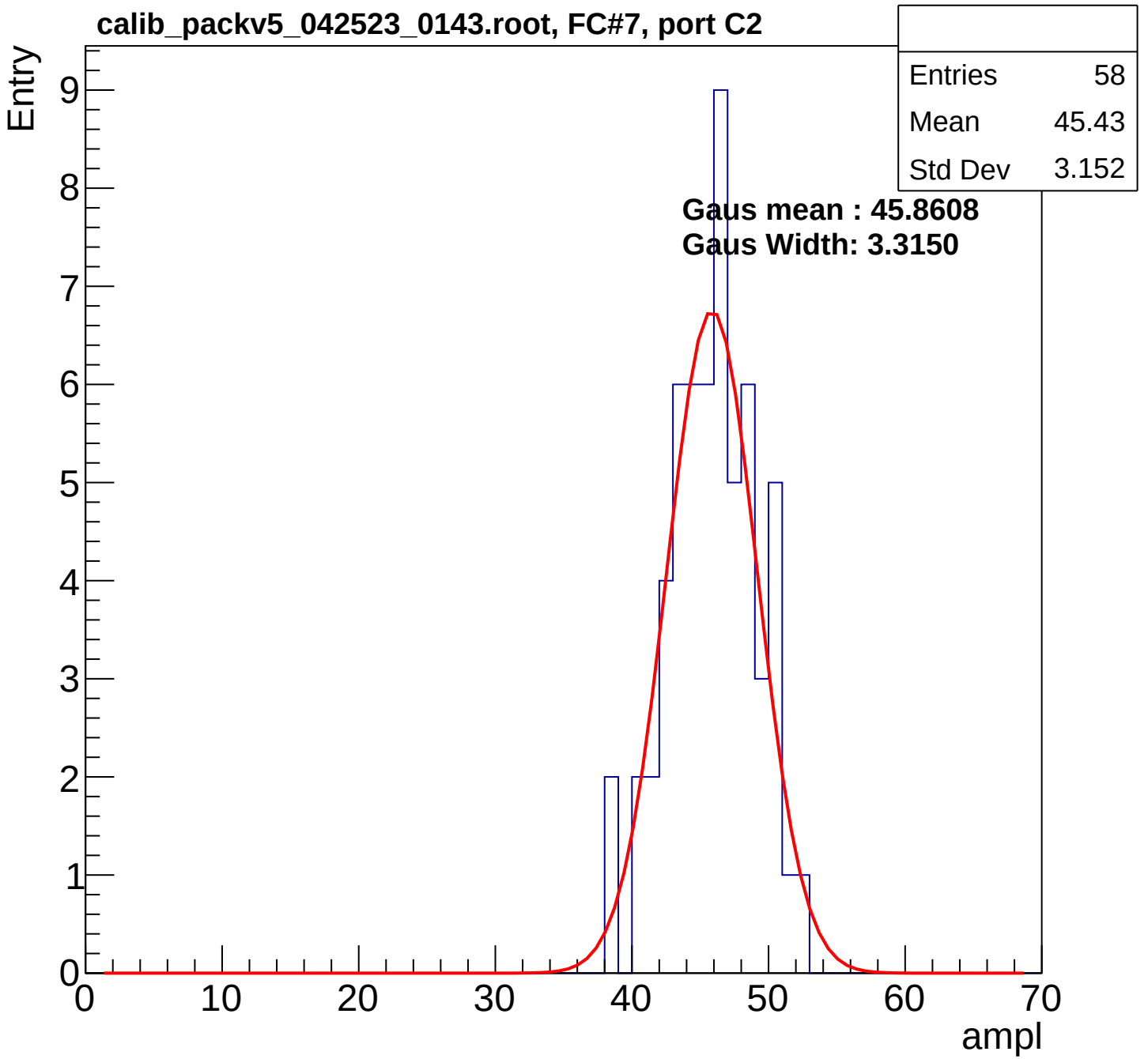

B1L103S, U13-ch120, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch120, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch121, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 95
Mean 29.47
Std Dev 5.778
Gaus mean : 30.4627
Gaus Width: 4.0369
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch121, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 38.5
Std Dev 3.21
Gaus mean : 39.2677
Gaus Width: 3.4059
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U13-ch121, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 45.43
Std Dev 3.152
Gaus mean : 45.8608
Gaus Width: 3.3150
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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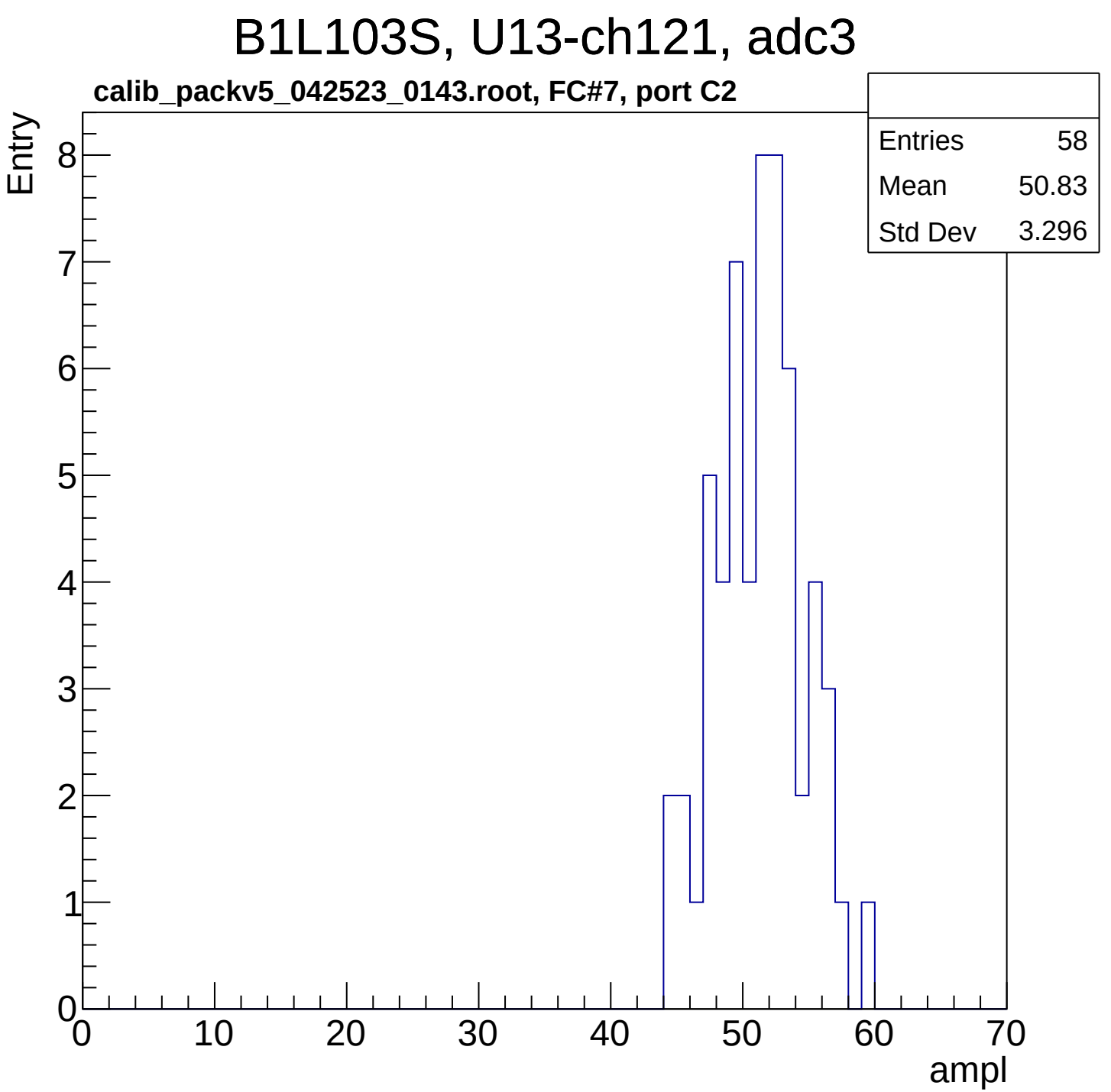
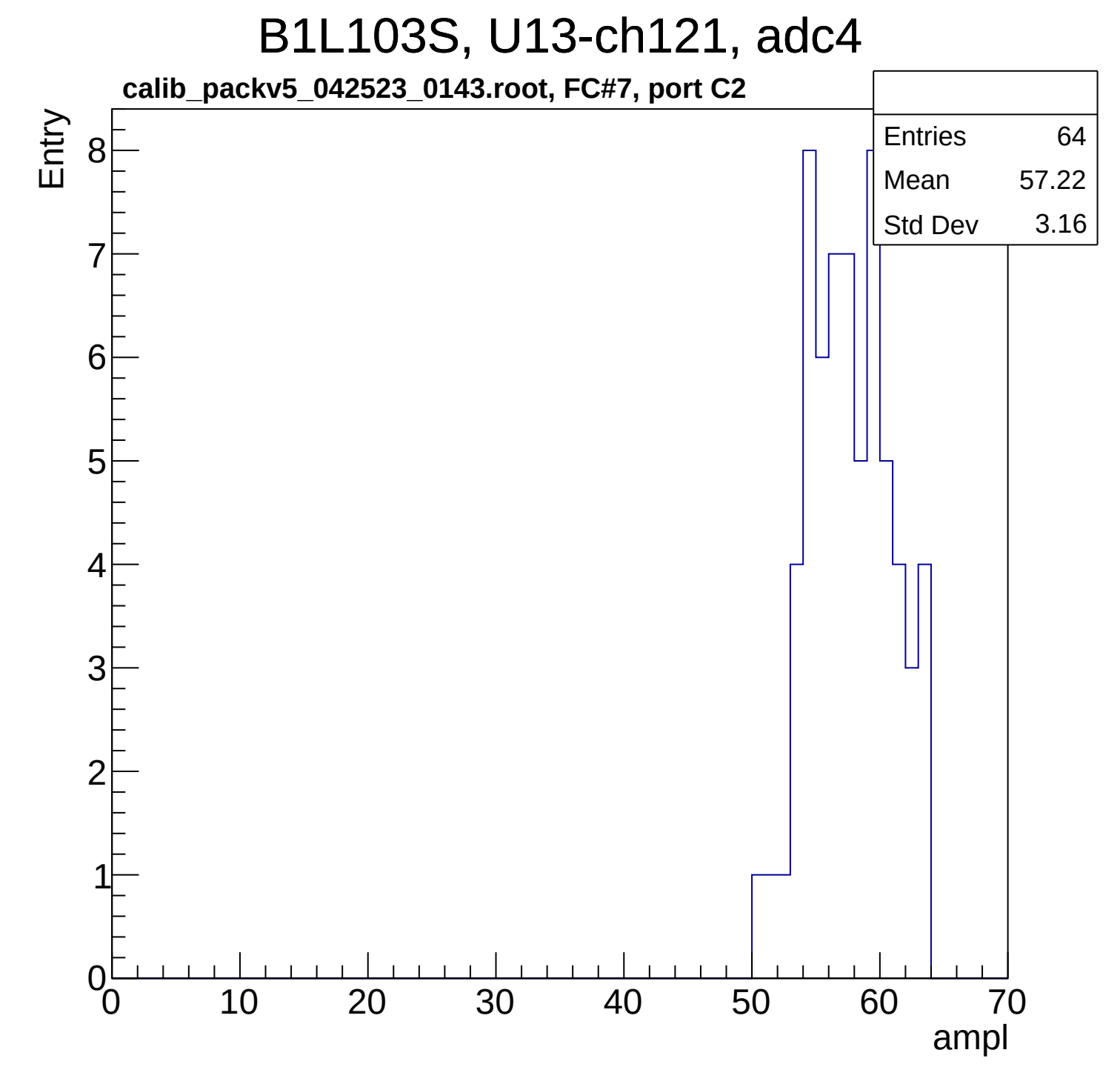

B1L103S, U13-ch121, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 50.83
Std Dev 3.296
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch121, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 57.22
Std Dev 3.16
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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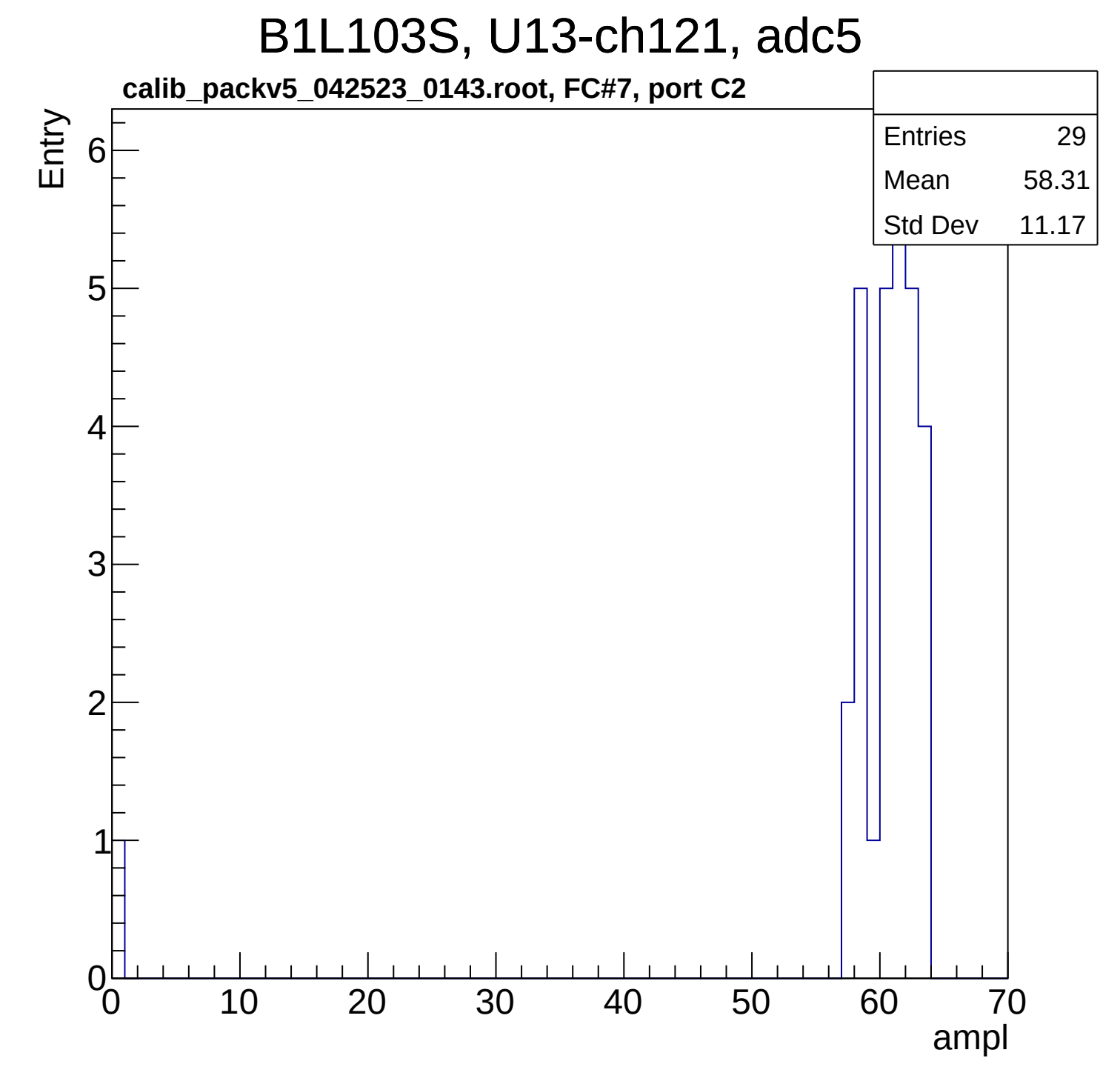
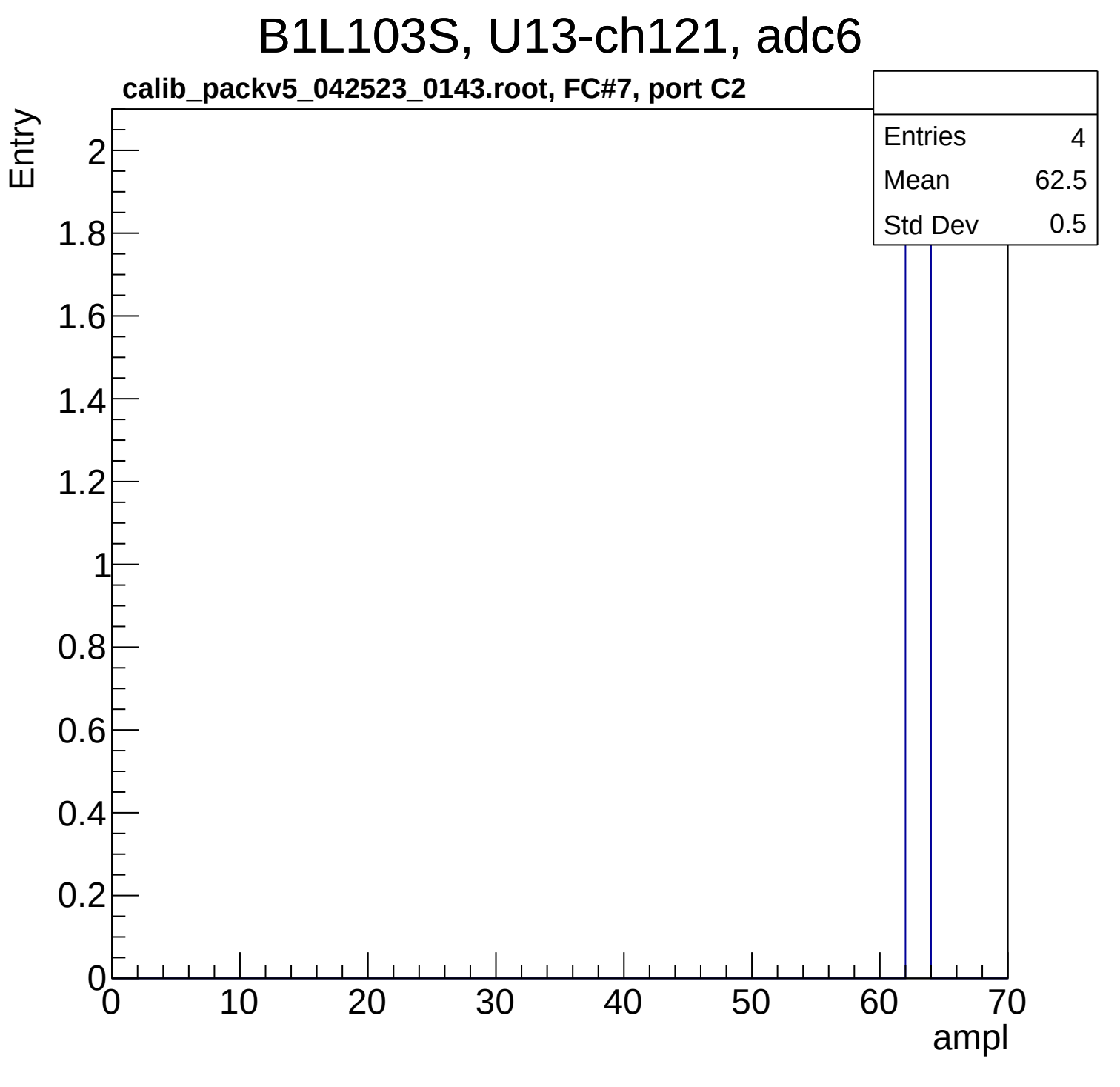
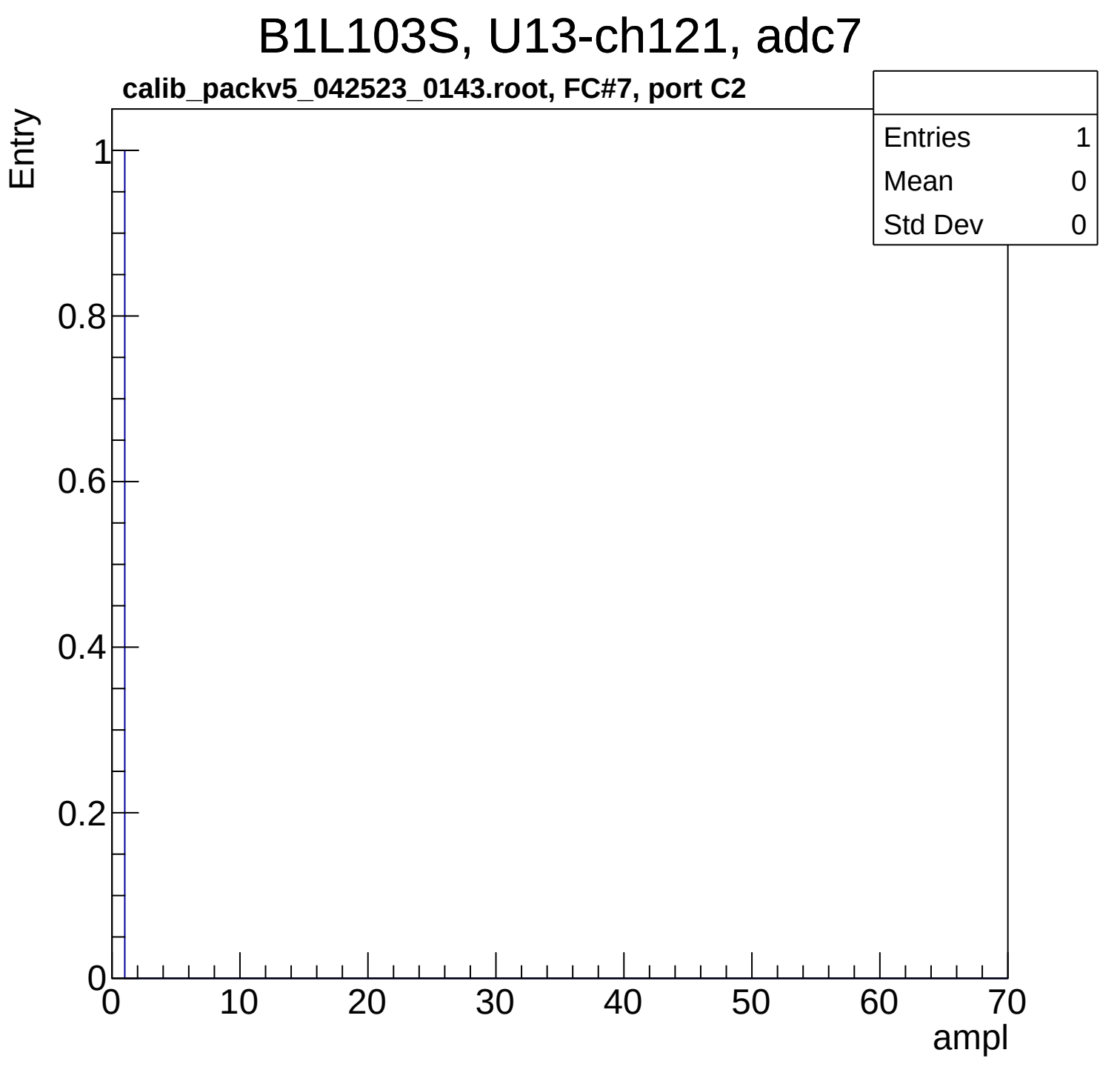
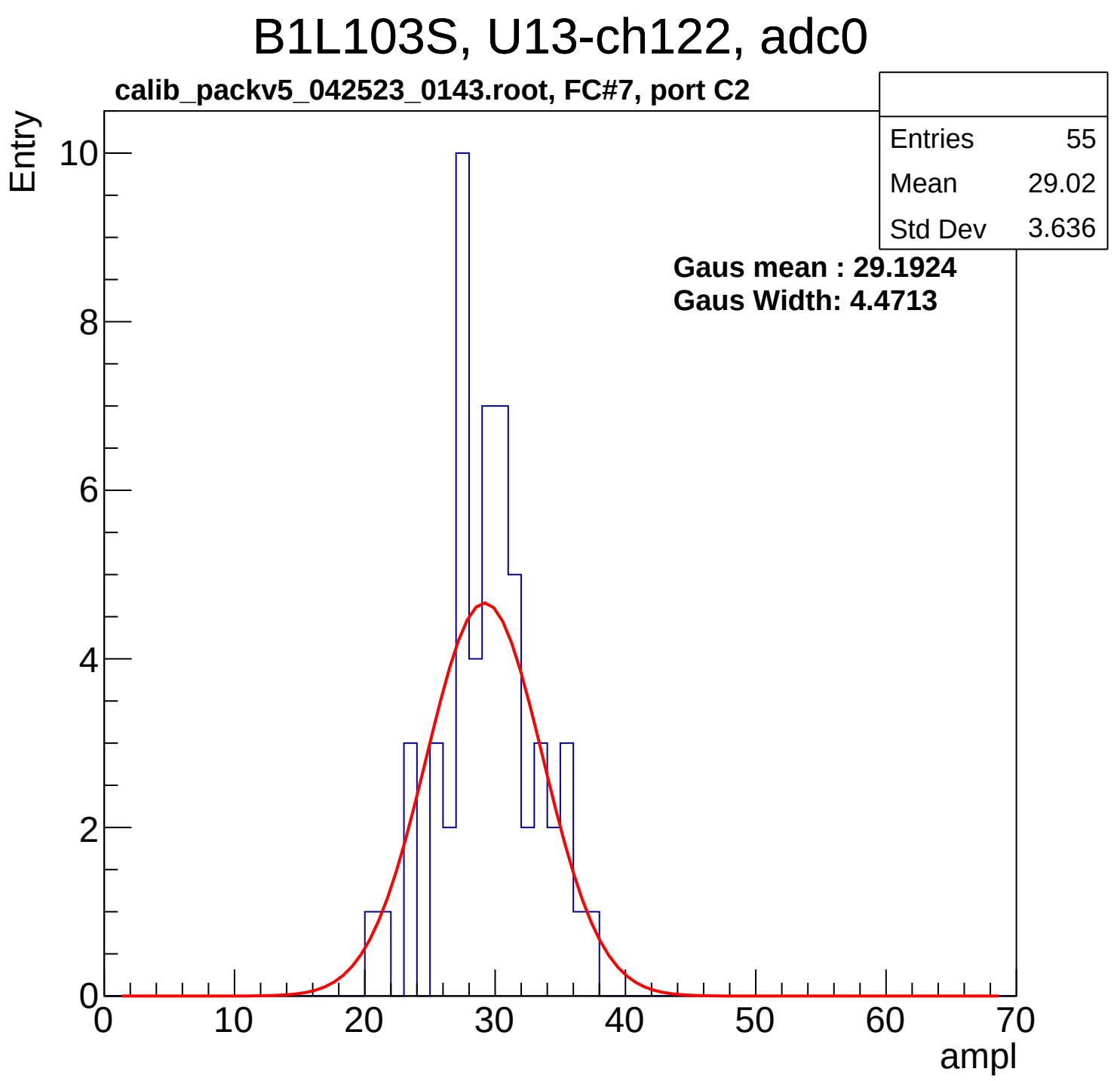

B1L103S, U13-ch121, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 58.31
Std Dev 11.17
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch121, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U13-ch121, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch122, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 29.02
Std Dev 3.636
Gaus mean : 29.1924
Gaus Width: 4.4713
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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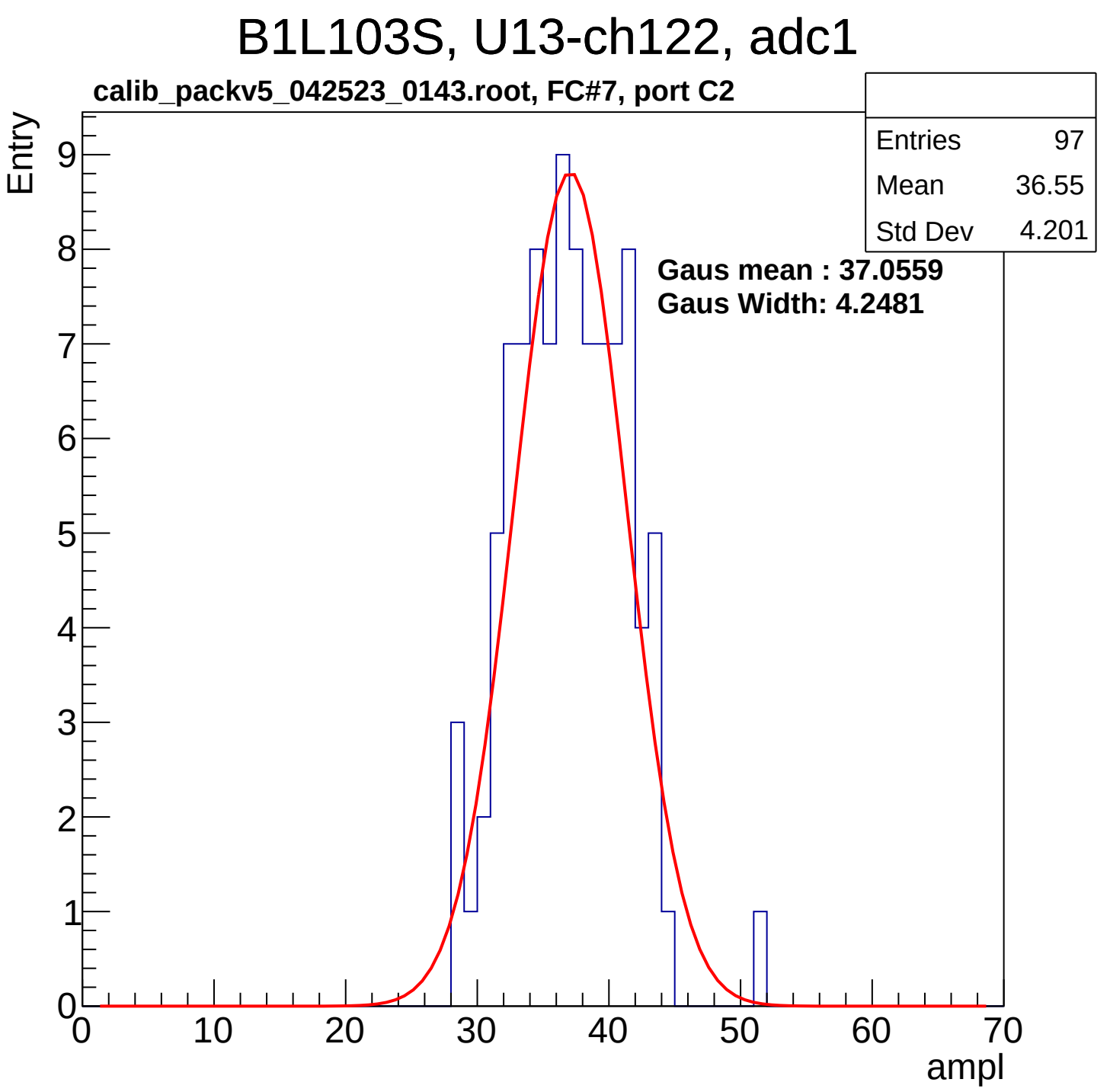
B1L103S, U13-ch122, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 97
Mean 36.55
Std Dev 4.201
Gaus mean : 37.0559
Gaus Width: 4.2481
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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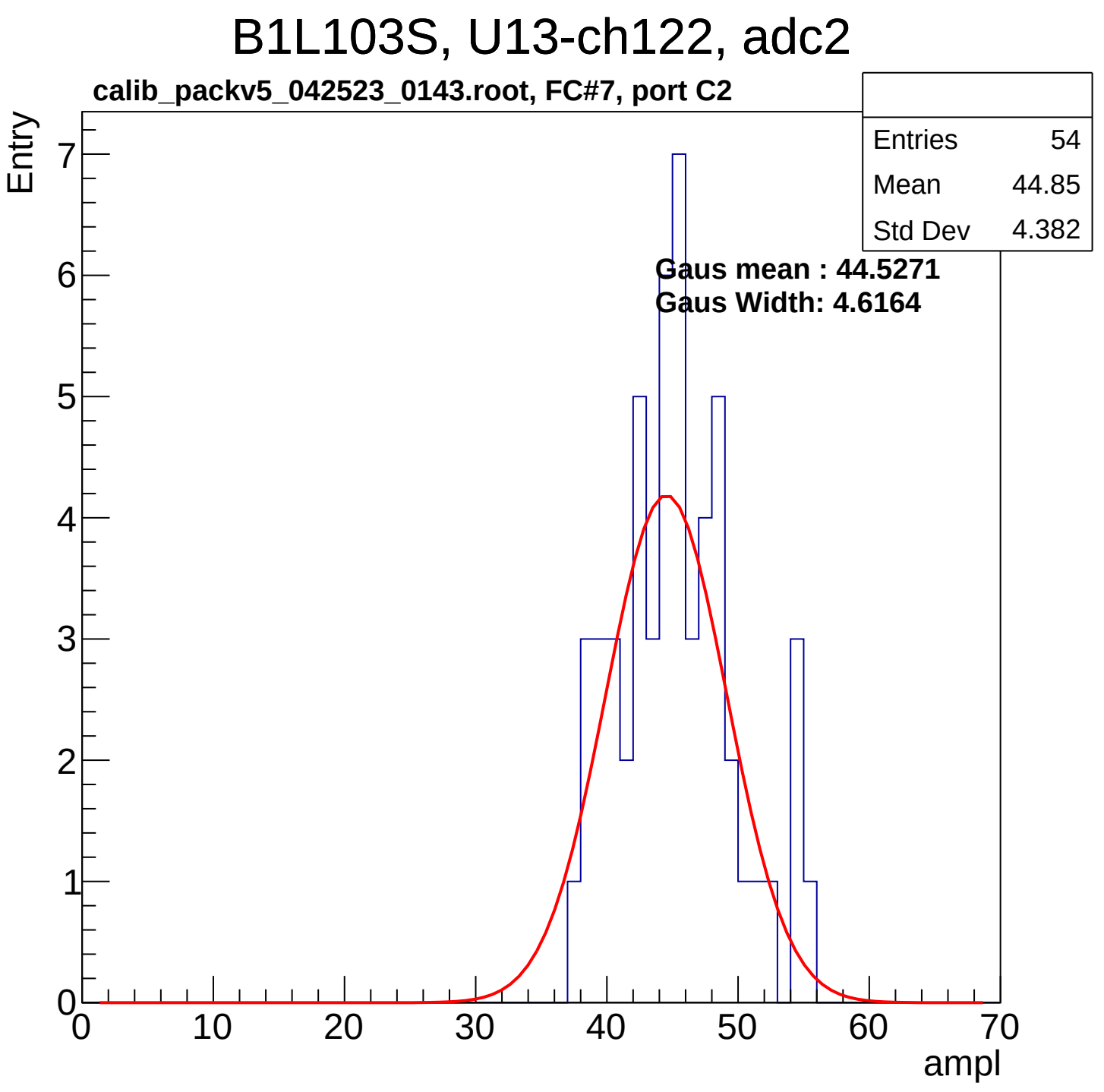
B1L103S, U13-ch122, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 44.85
Std Dev 4.382
Gaus mean : 44.5271
Gaus Width: 4.6164
Entry
ampl
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1
2
3
4
5
6
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0
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60
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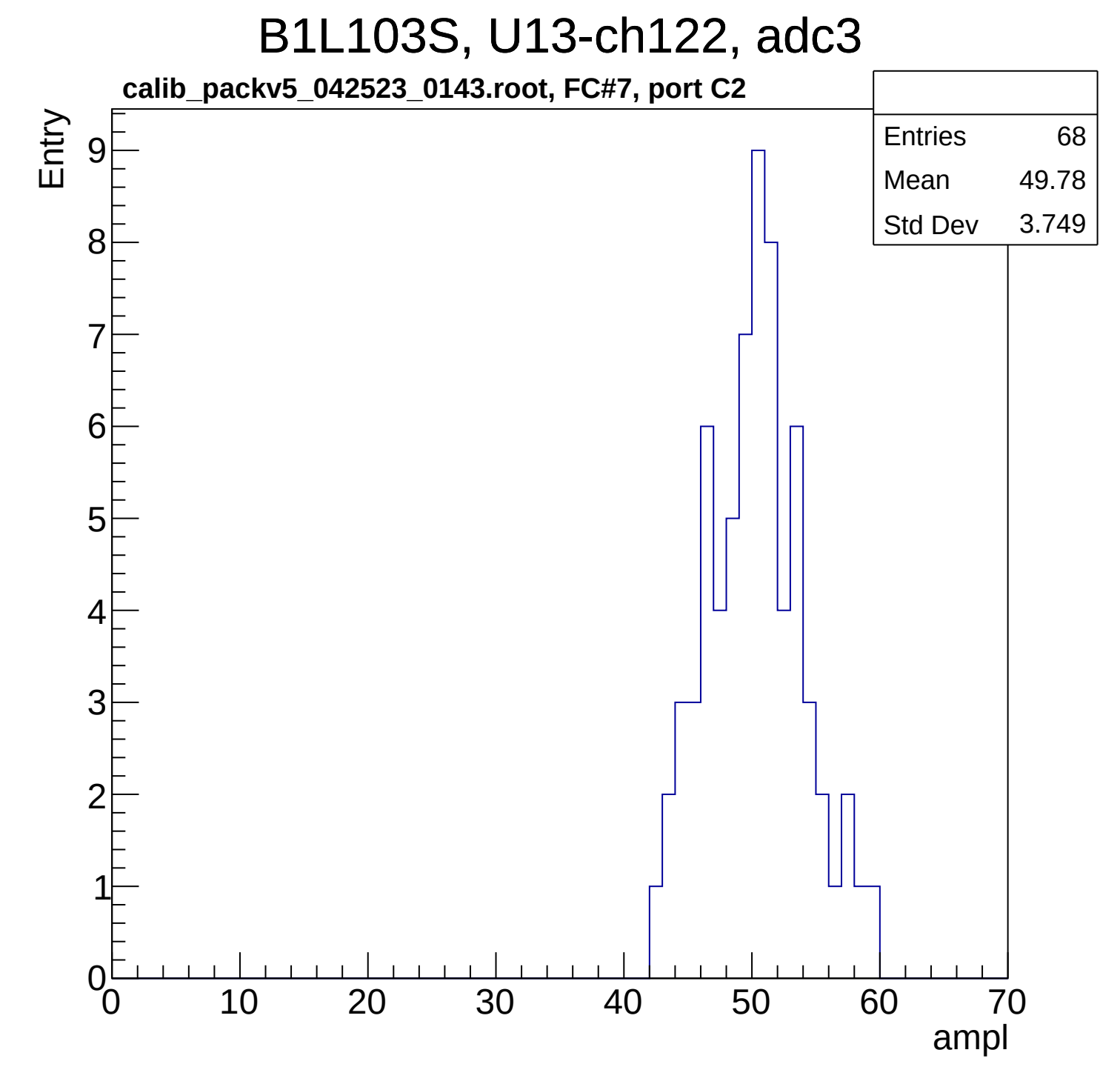
B1L103S, U13-ch122, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 49.78
Std Dev 3.749
Entry
ampl
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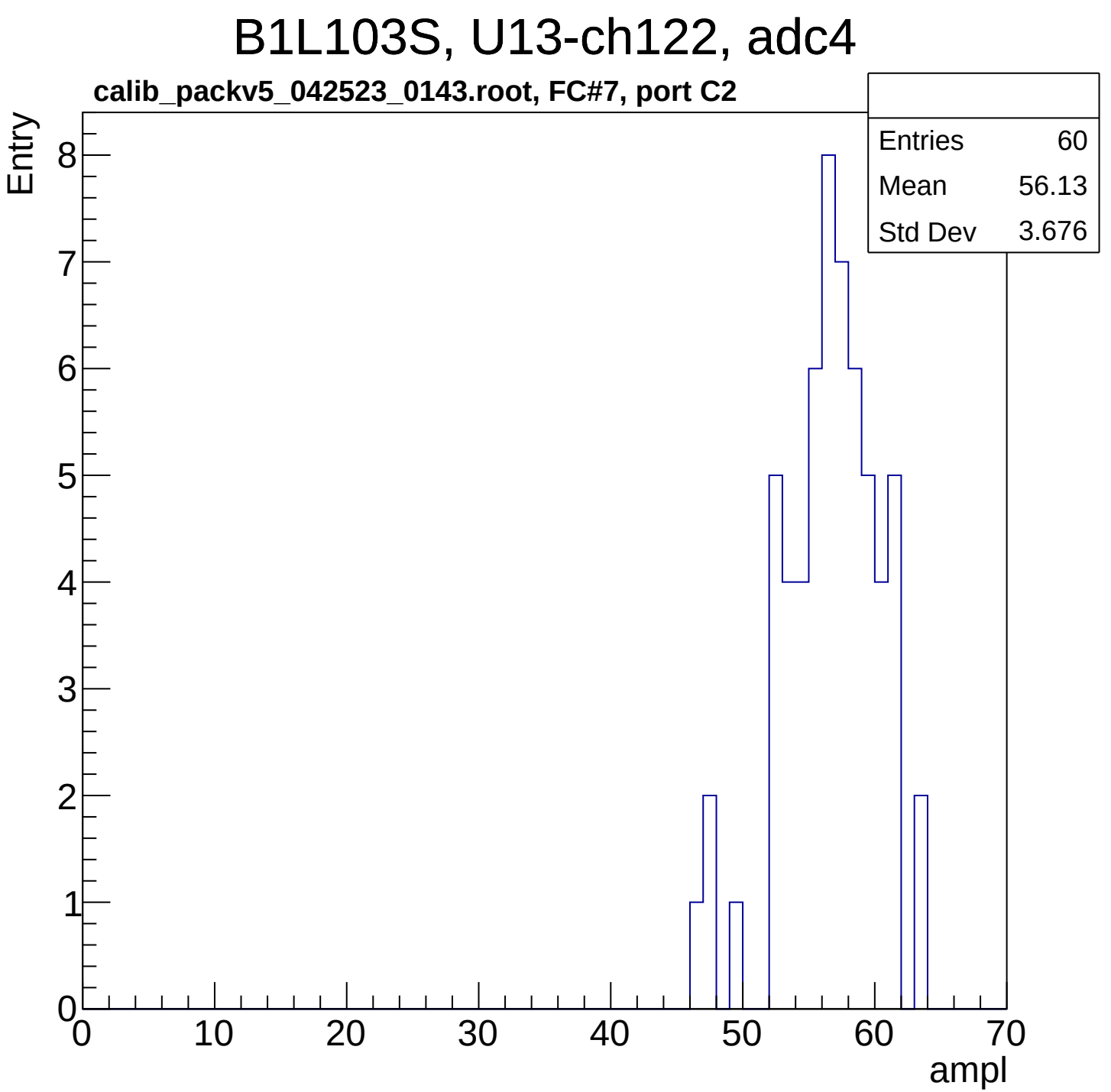
B1L103S, U13-ch122, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.13
Std Dev 3.676
Entry
ampl
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4
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0
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60
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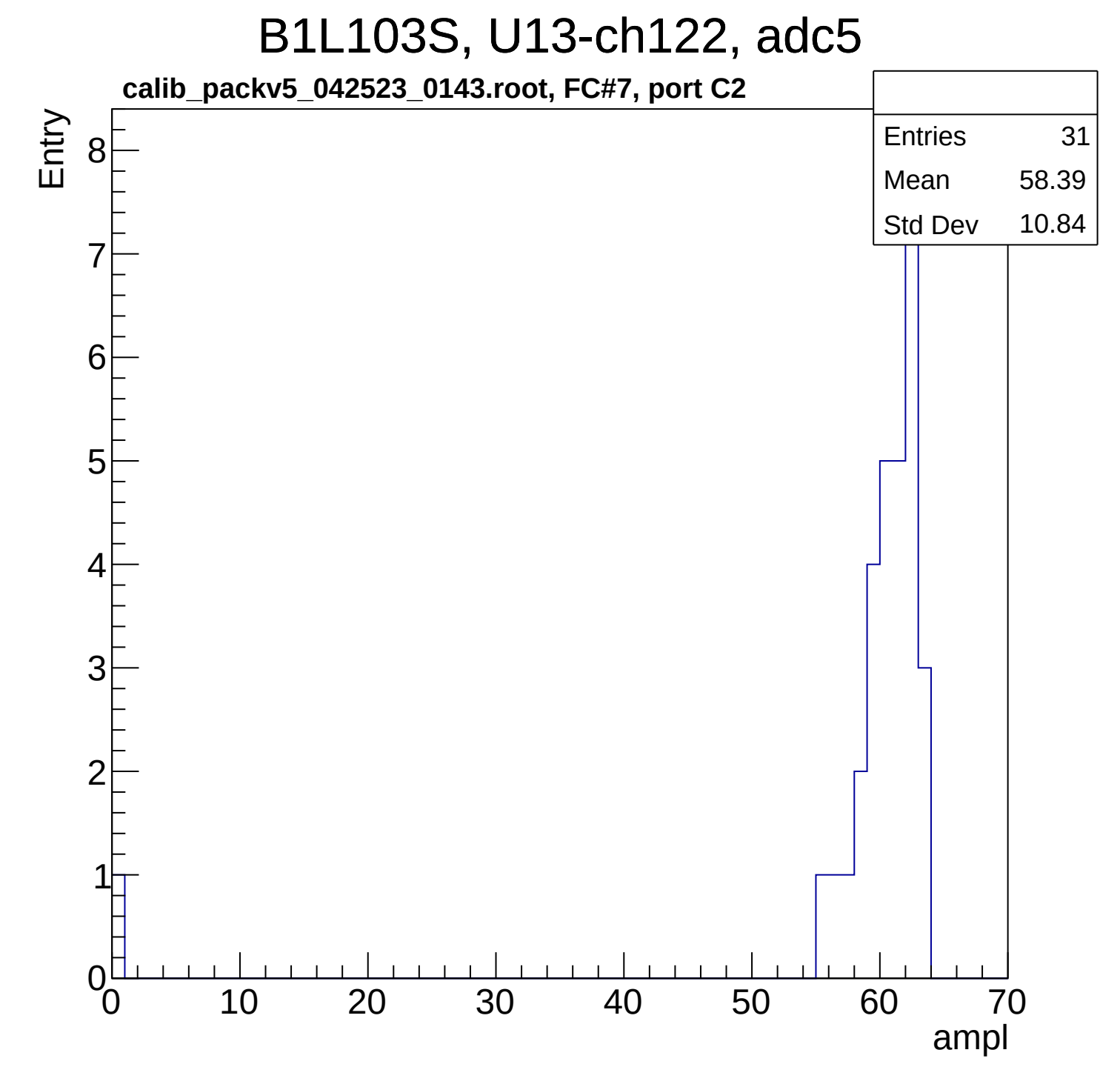
B1L103S, U13-ch122, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 58.39
Std Dev 10.84
Entry
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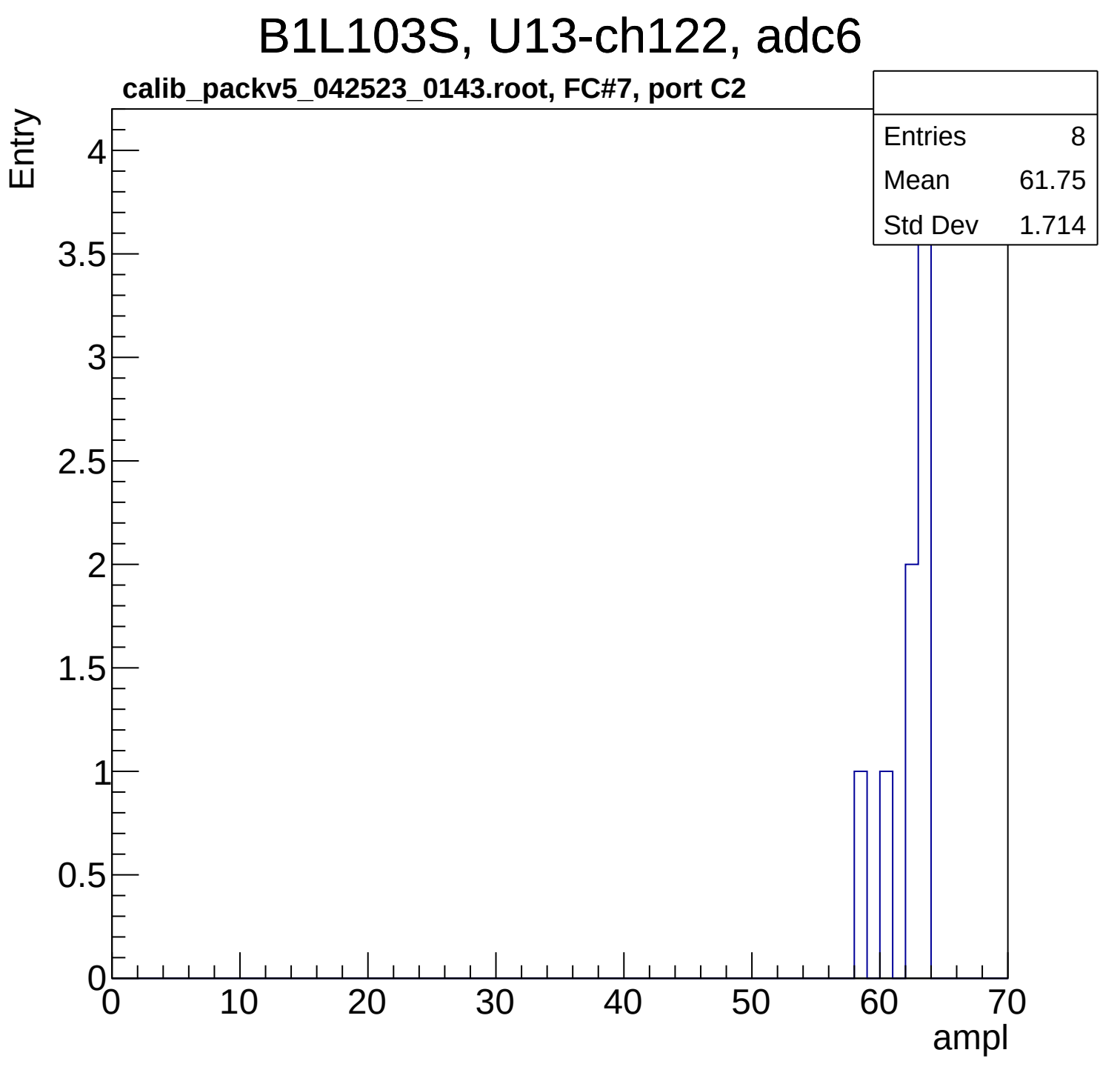
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch122, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.75
Std Dev 1.714
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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60
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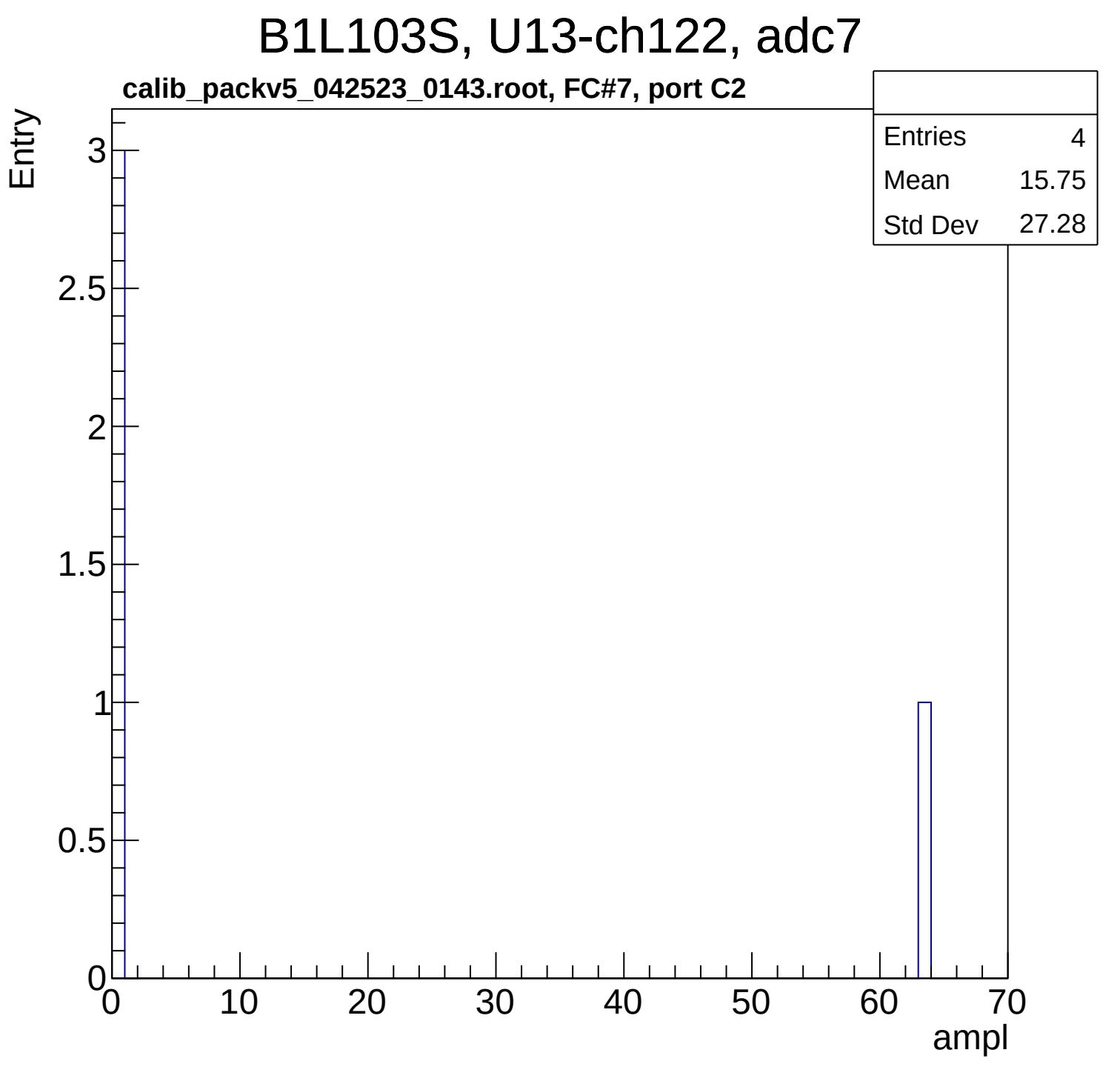
B1L103S, U13-ch122, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
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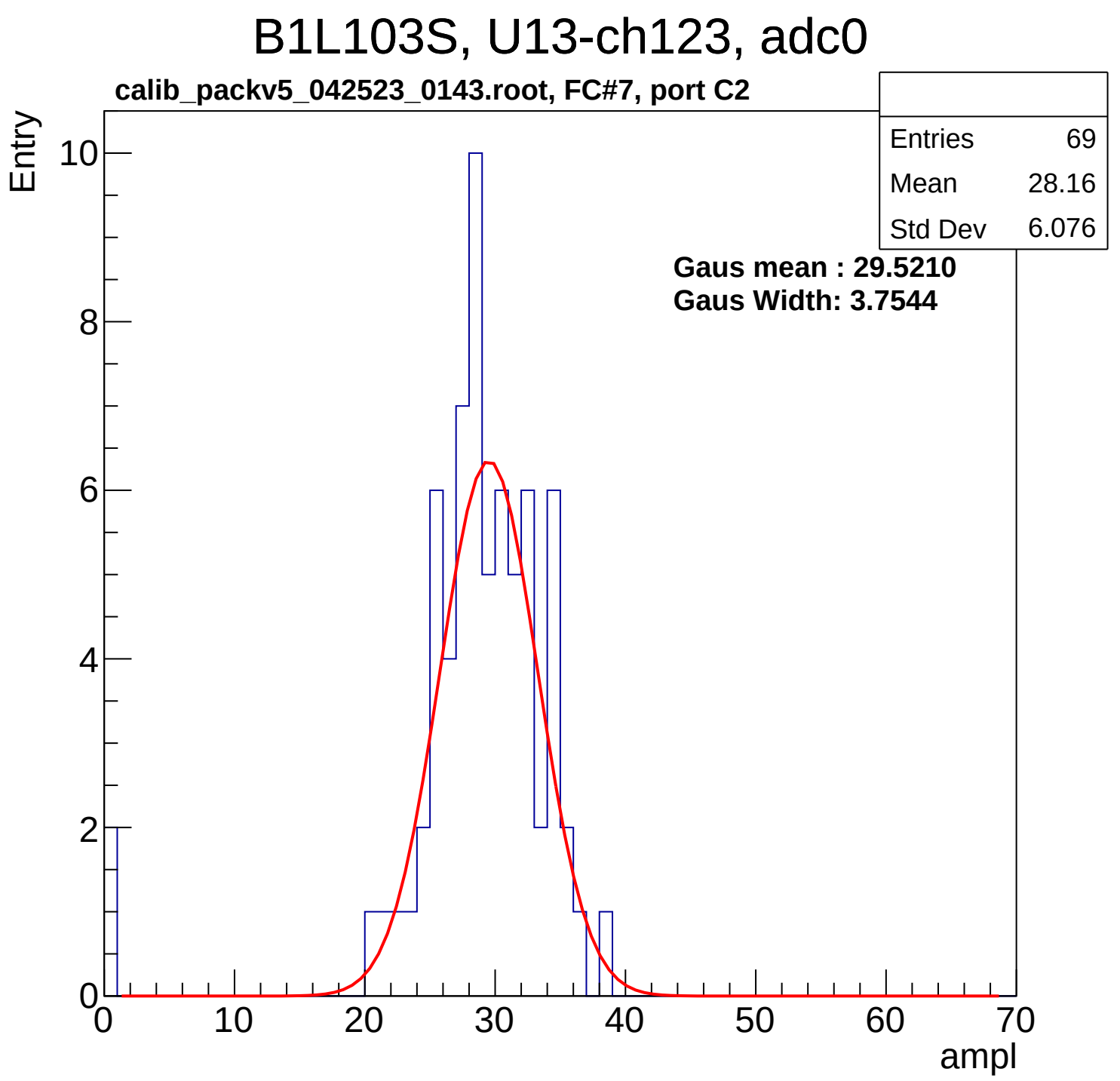
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch123, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.16
Std Dev 6.076
Gaus mean : 29.5210
Gaus Width: 3.7544
Entry
ampl
0
2
4
6
8
10
0
10
20
30
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60
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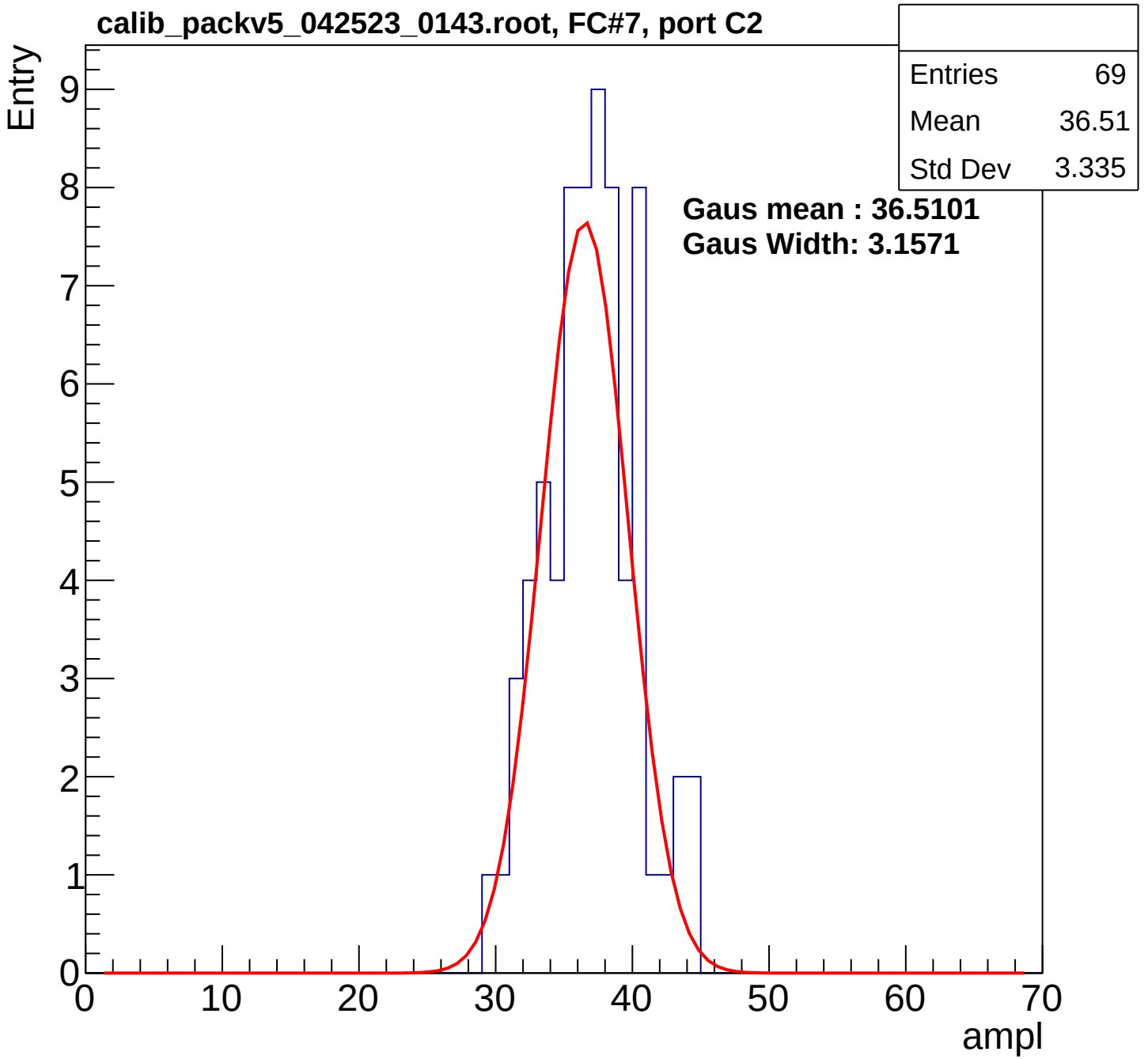
B1L103S, U13-ch123, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.51
Std Dev 3.335
Gaus mean : 36.5101
Gaus Width: 3.1571
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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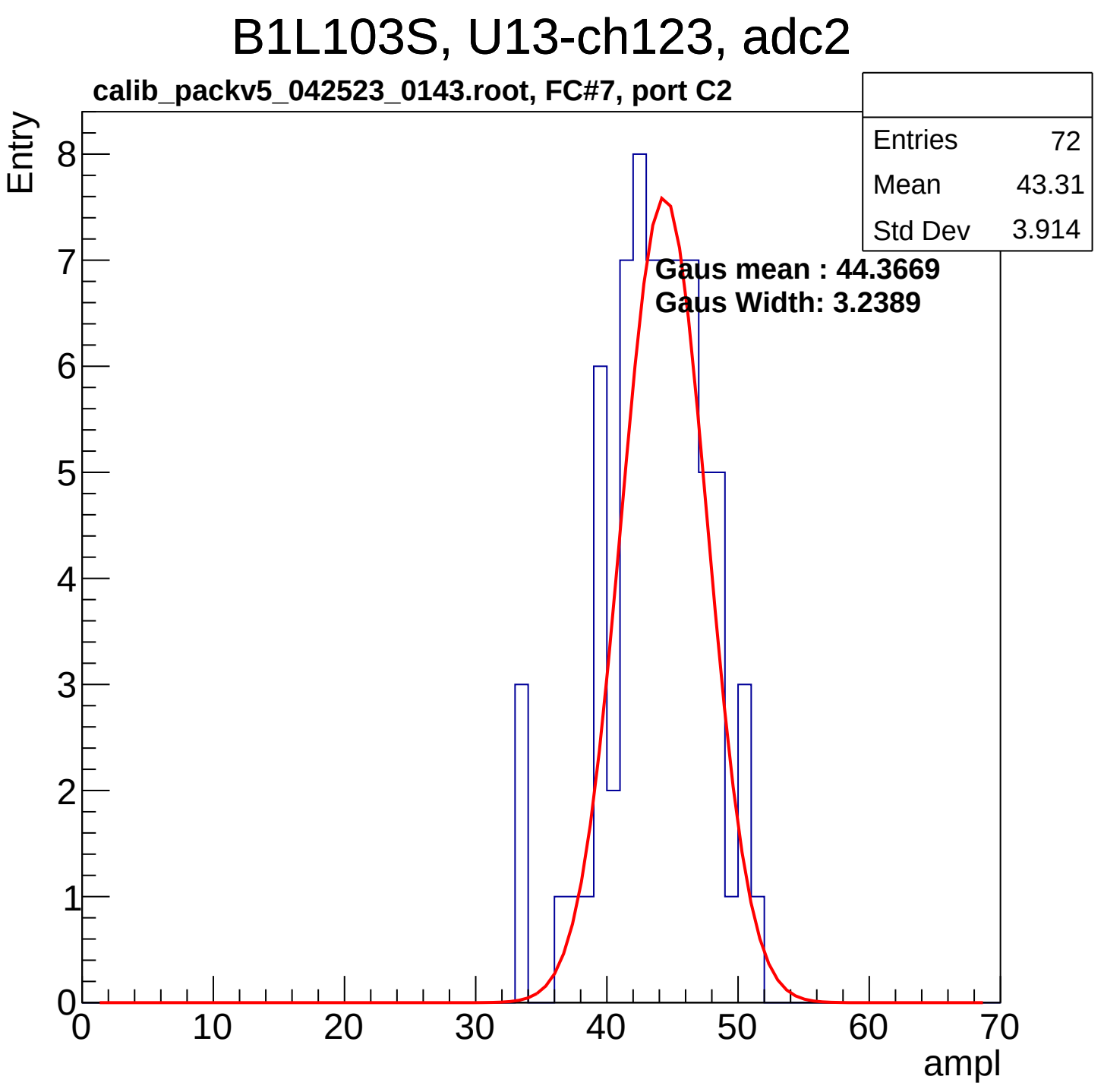
B1L103S, U13-ch123, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.31
Std Dev 3.914
Gaus mean : 44.3669
Gaus Width: 3.2389
Entry
ampl
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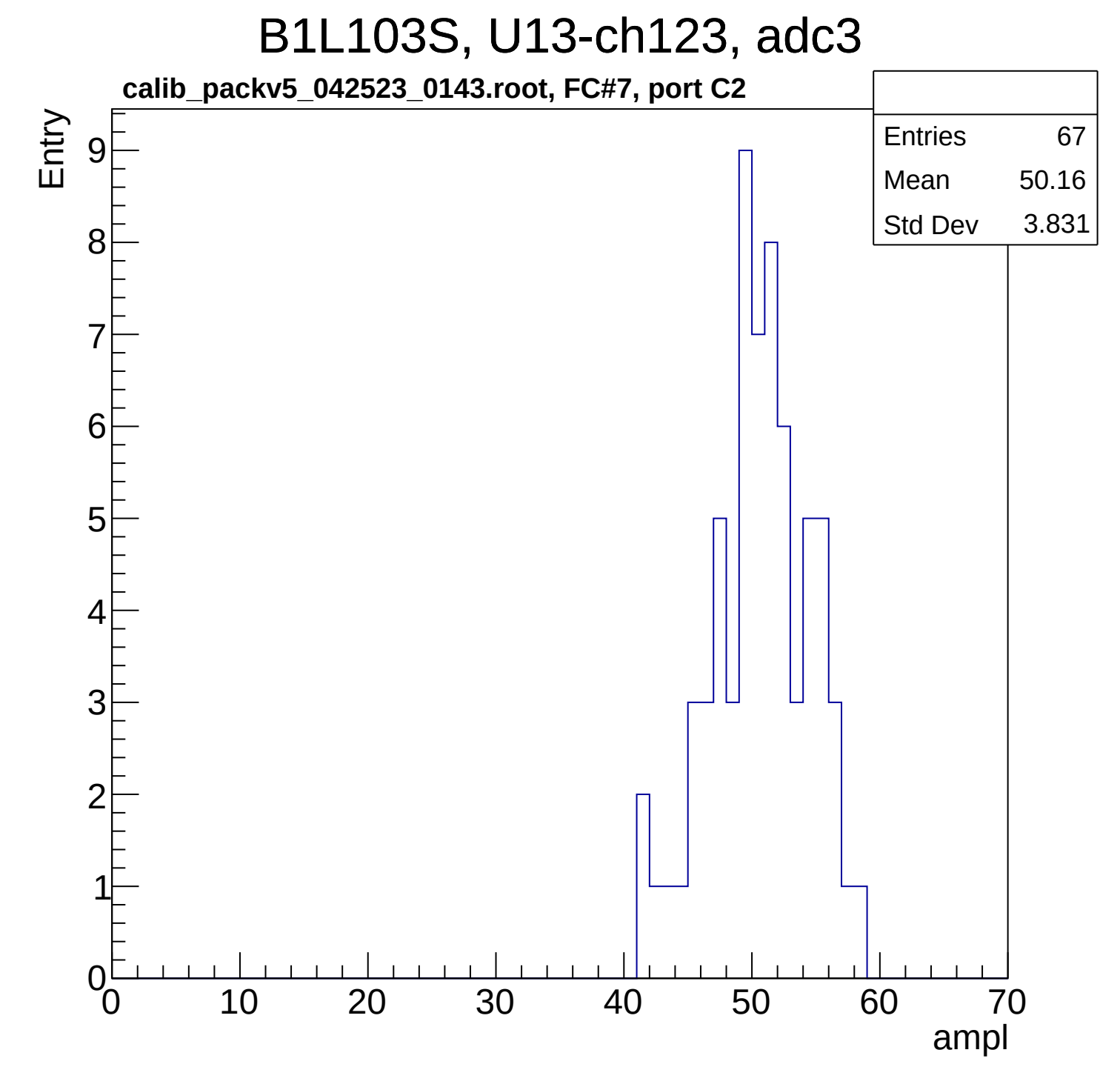
B1L103S, U13-ch123, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 50.16
Std Dev 3.831
Entry
ampl
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4
5
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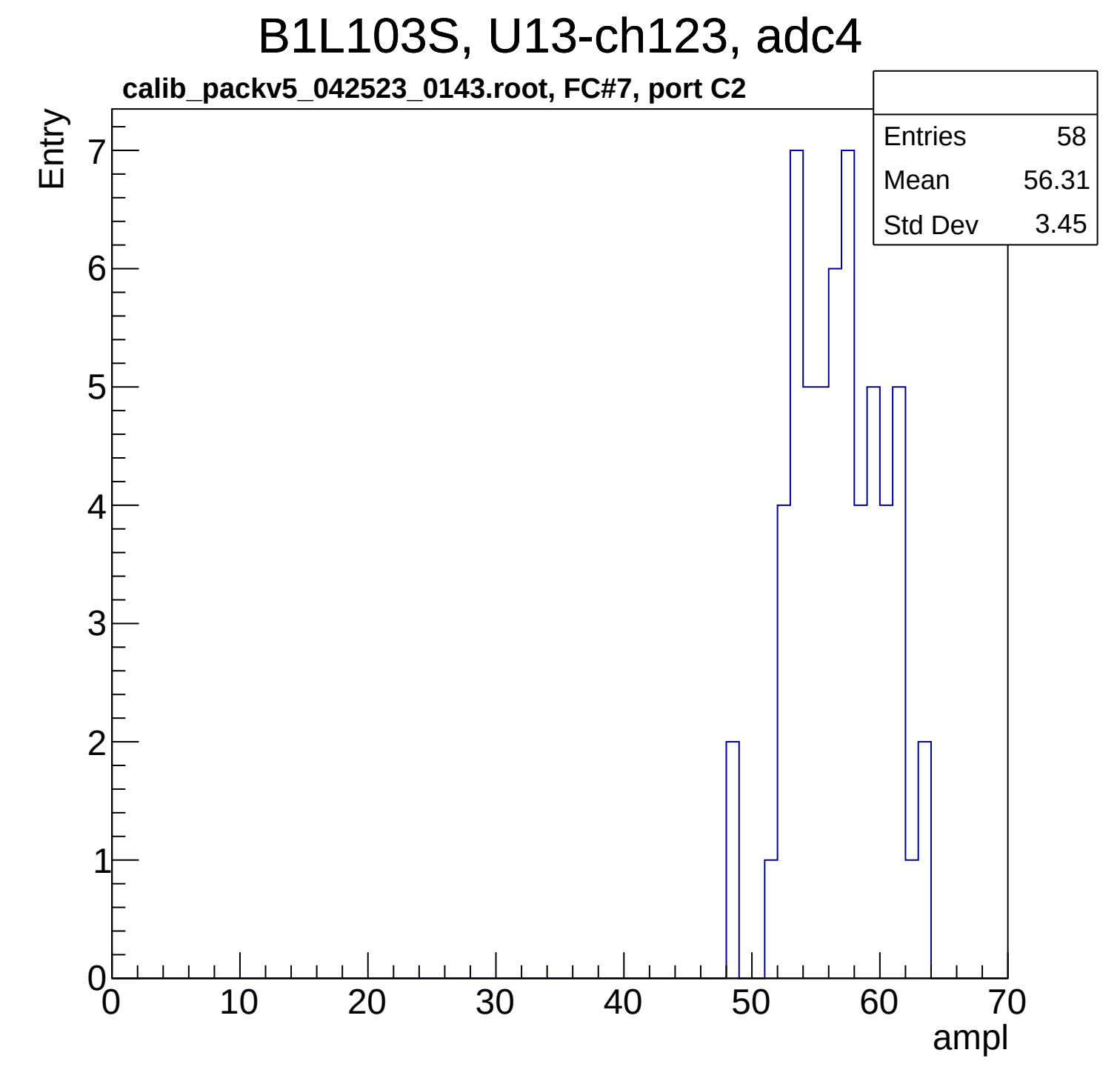
B1L103S, U13-ch123, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.31
Std Dev 3.45
Entry
ampl
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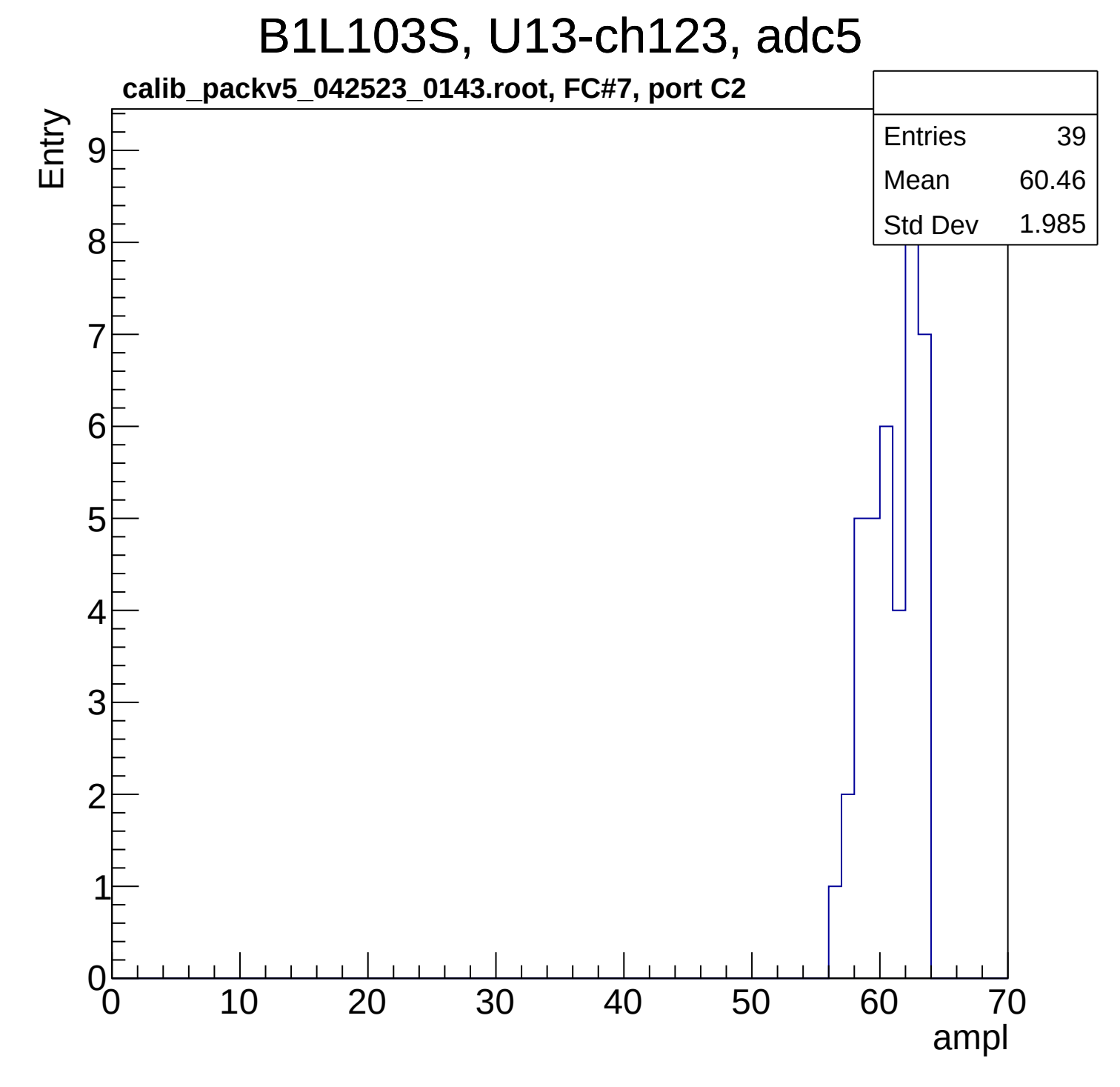
B1L103S, U13-ch123, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 60.46
Std Dev 1.985
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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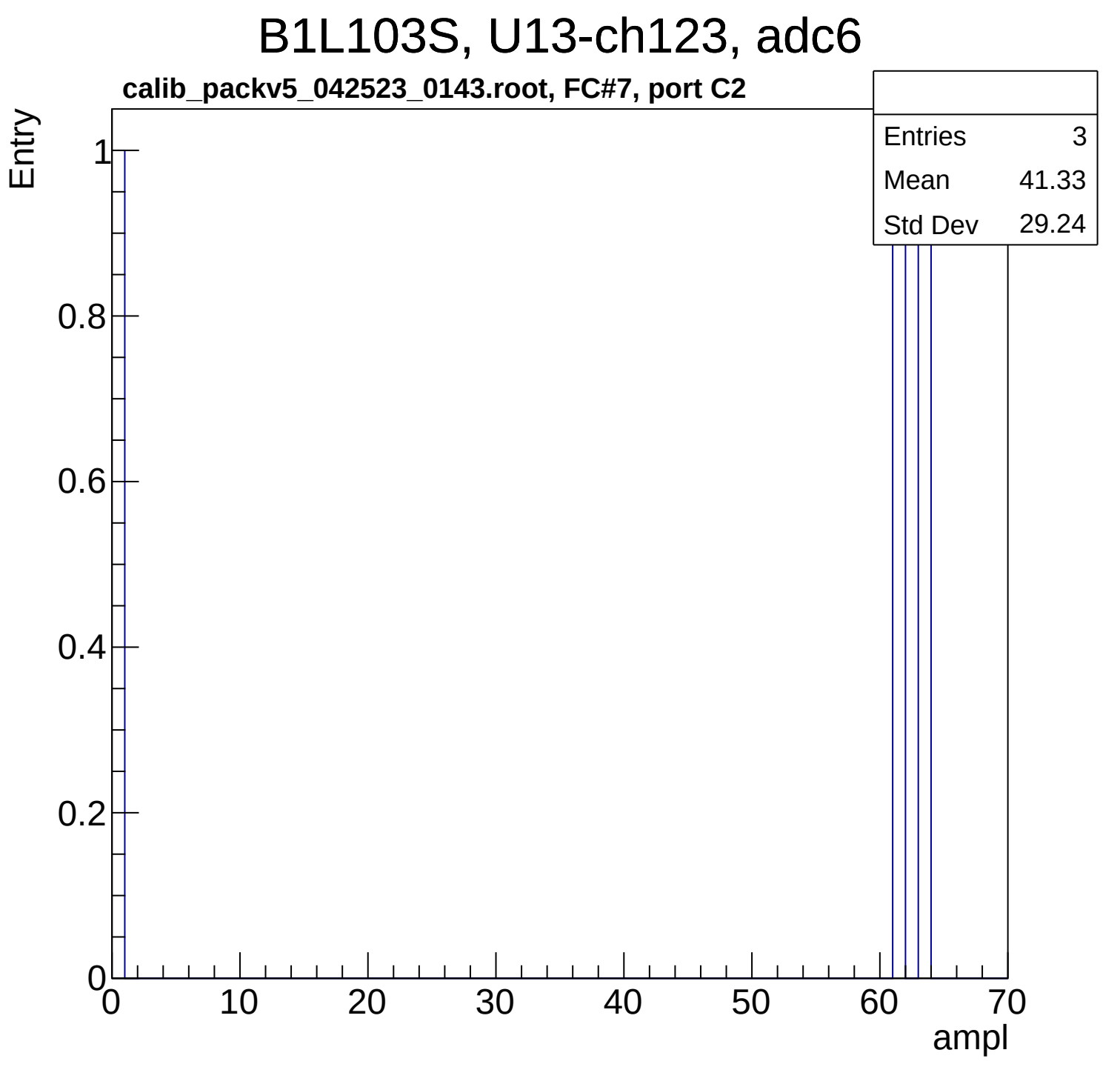
B1L103S, U13-ch123, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
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0.6
0.4
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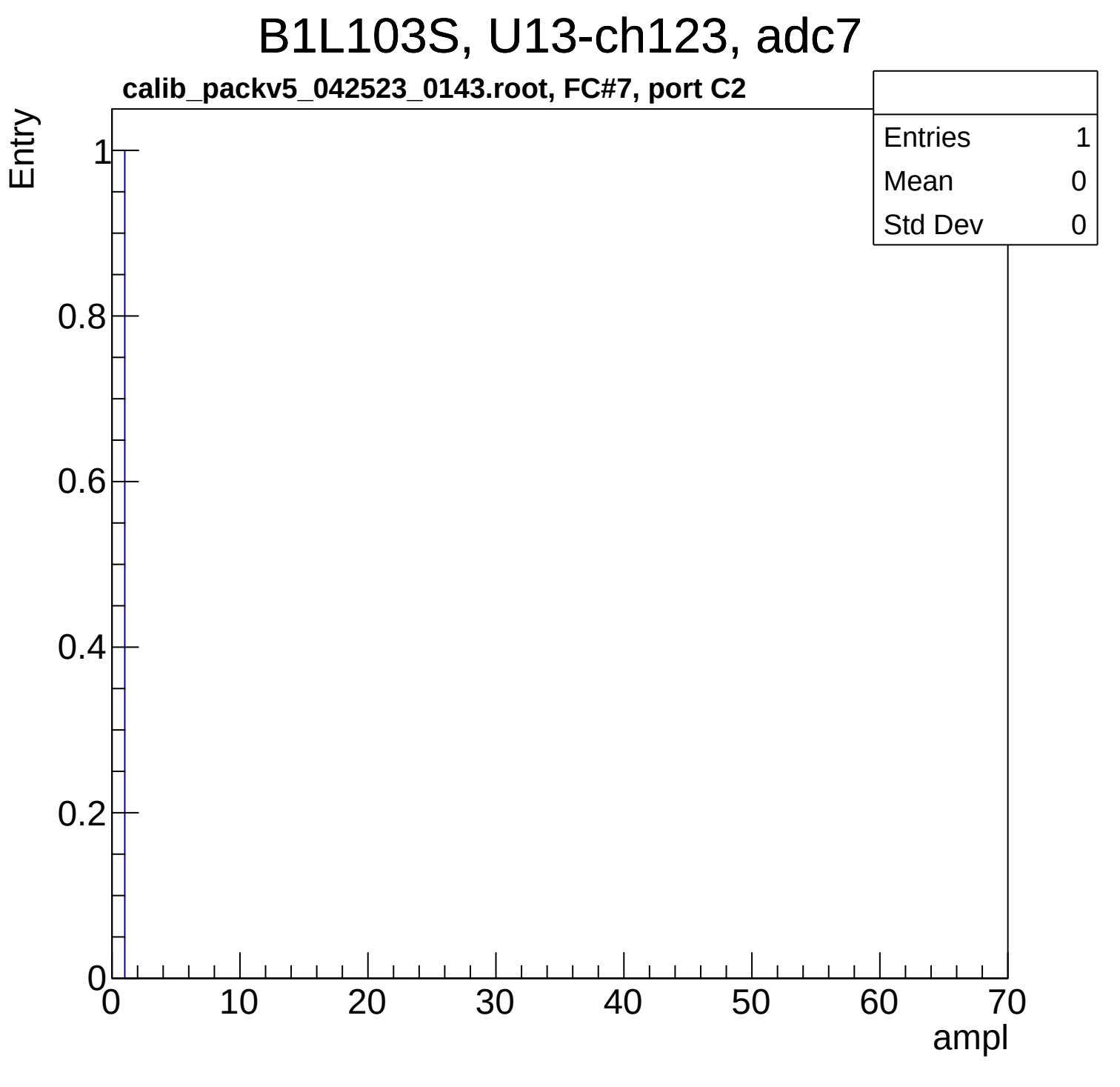
ampl


B1L103S, U13-ch123, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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10
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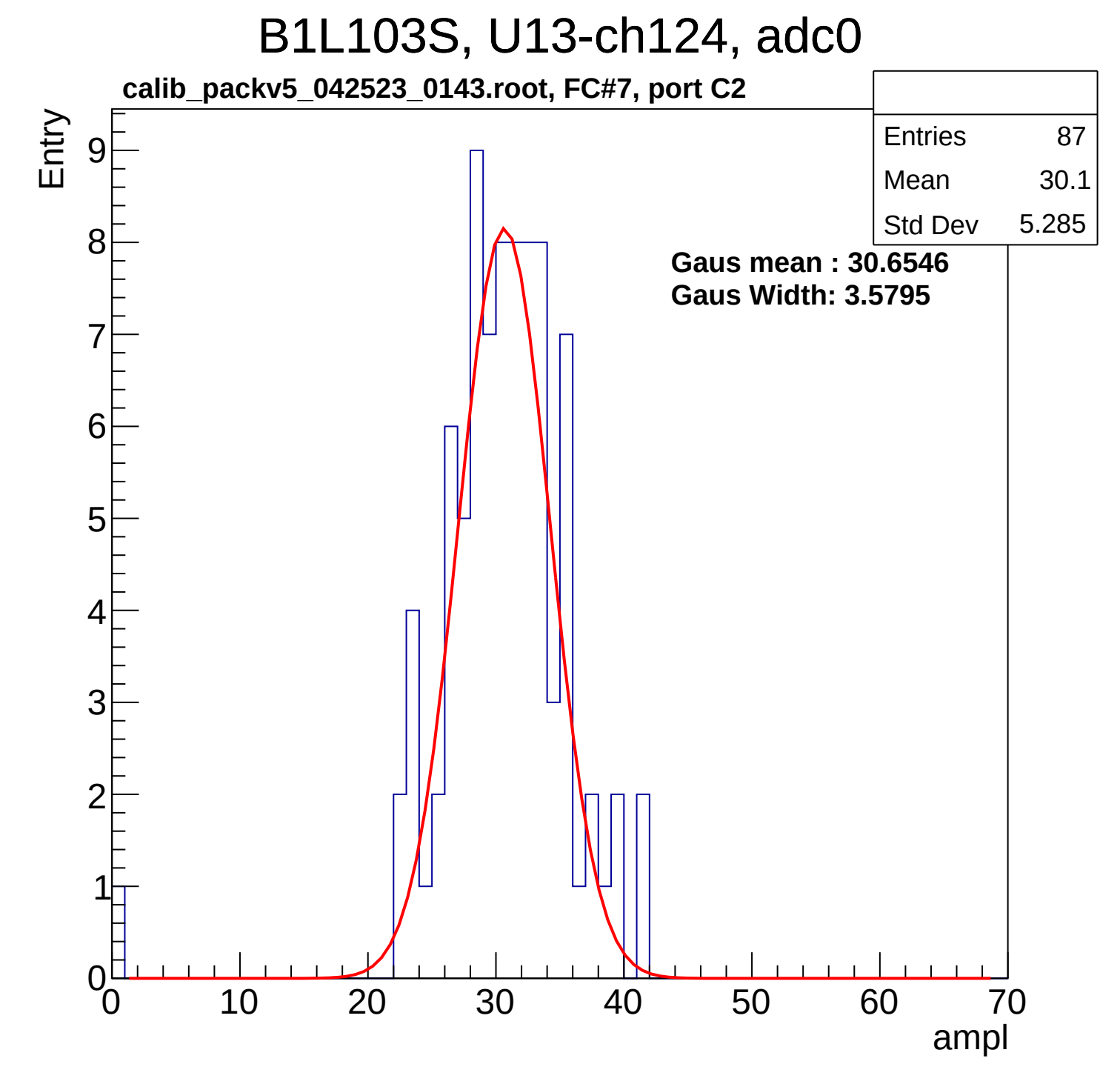
50
60
70
ampl


B1L103S, U13-ch124, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 30.1
Std Dev 5.285
Gaus mean : 30.6546
Gaus Width: 3.5795
Entry
ampl
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2
3
4
5
6
7
8
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0
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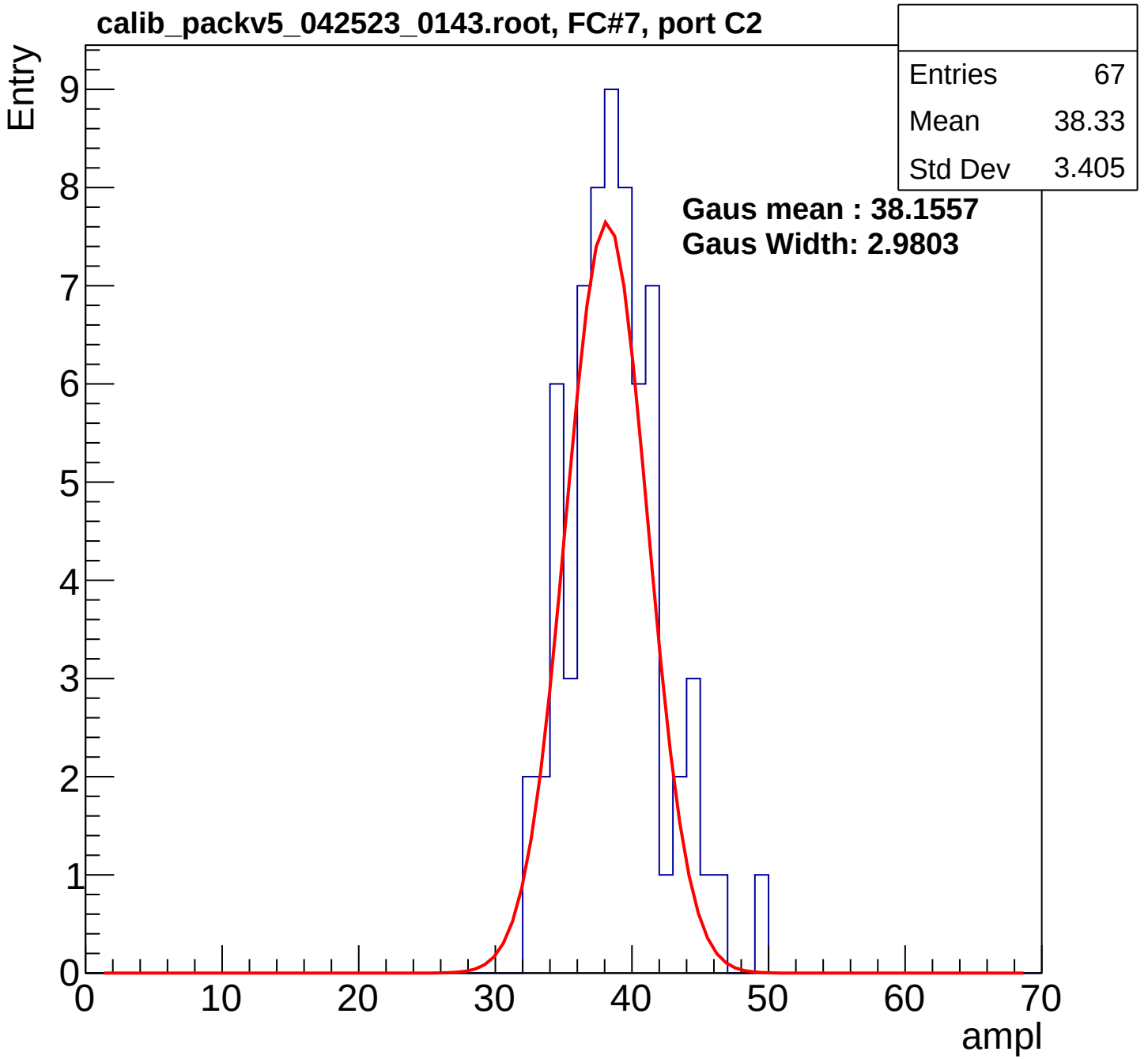
B1L103S, U13-ch124, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 38.33
Std Dev 3.405
Gaus mean : 38.1557
Gaus Width: 2.9803
Entry
ampl
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2
3
4
5
6
7
8
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0
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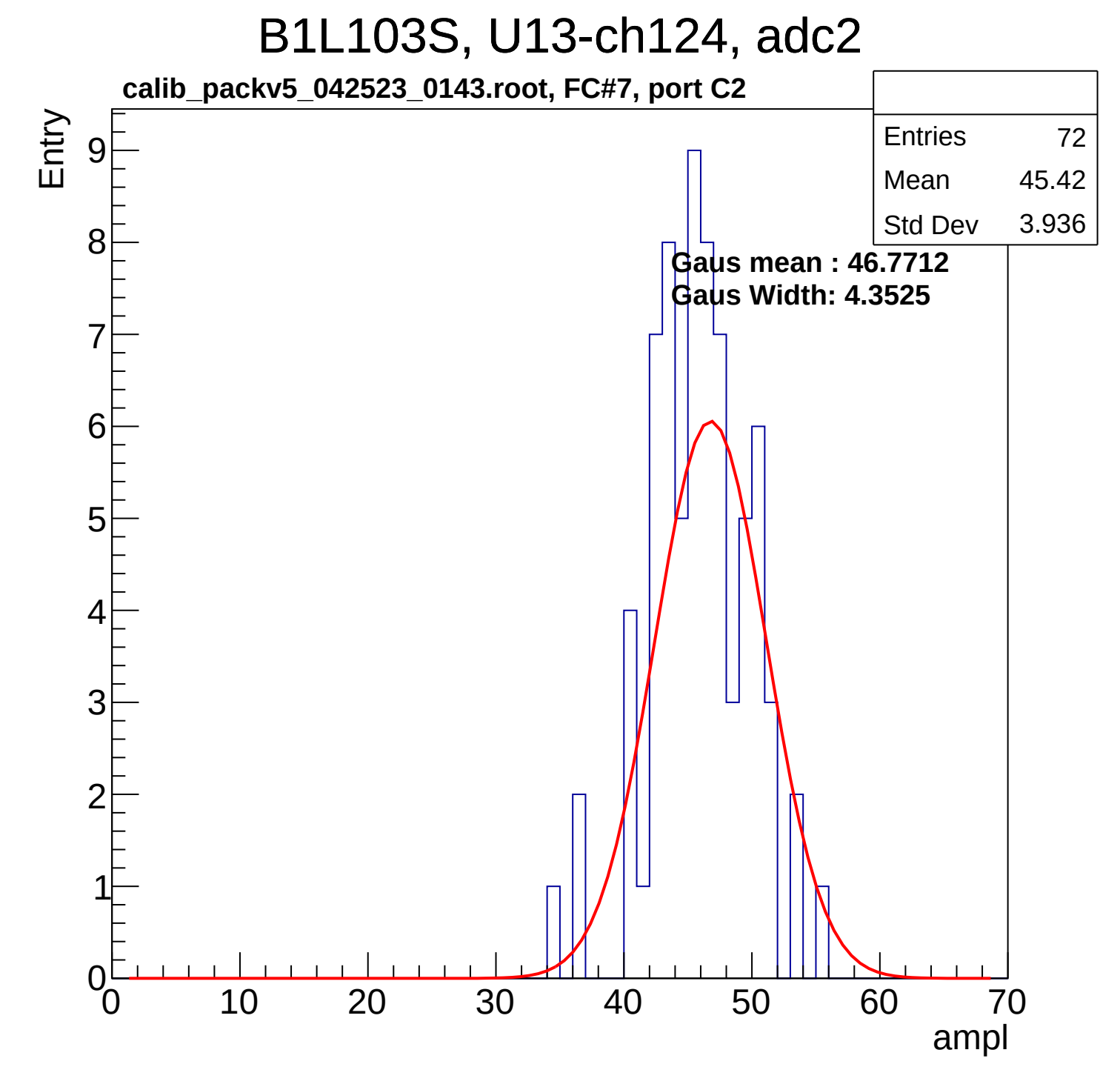
B1L103S, U13-ch124, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 45.42
Std Dev 3.936
Gaus mean : 46.7712
Gaus Width: 4.3525
Entry
ampl
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3
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5
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0
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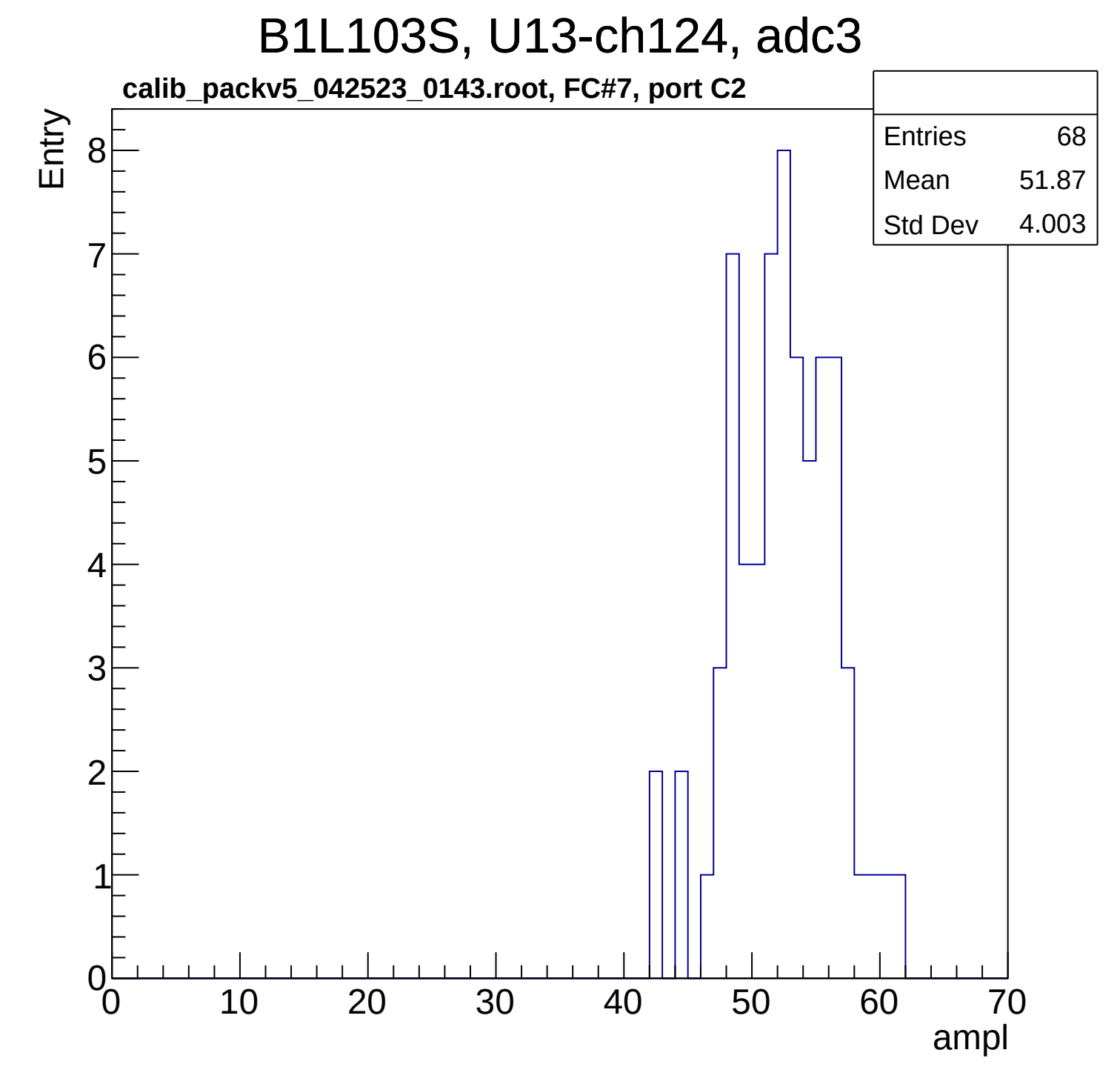
B1L103S, U13-ch124, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 51.87
Std Dev 4.003
Entry
ampl
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2
3
4
5
6
7
8
0
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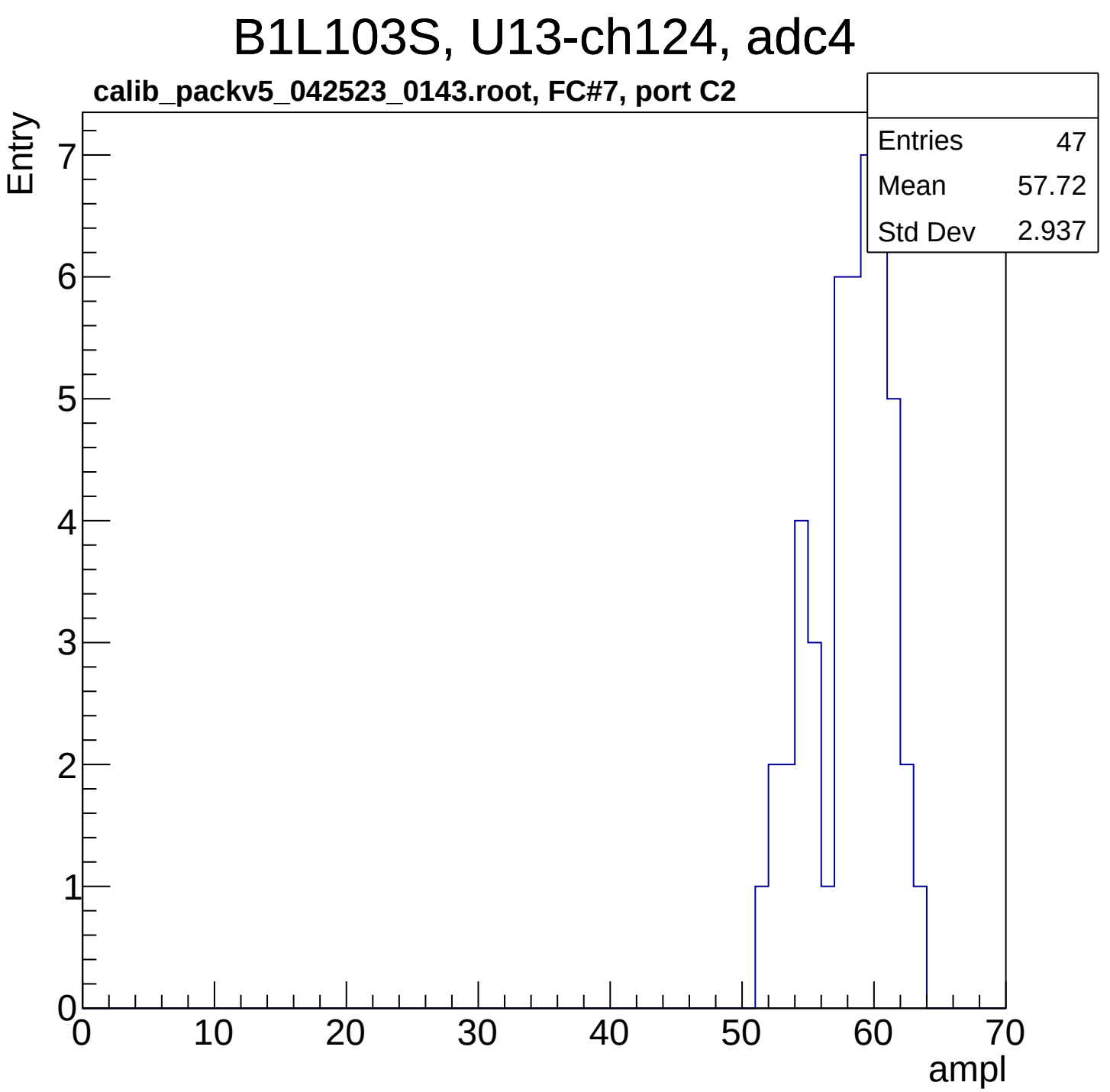
B1L103S, U13-ch124, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.72
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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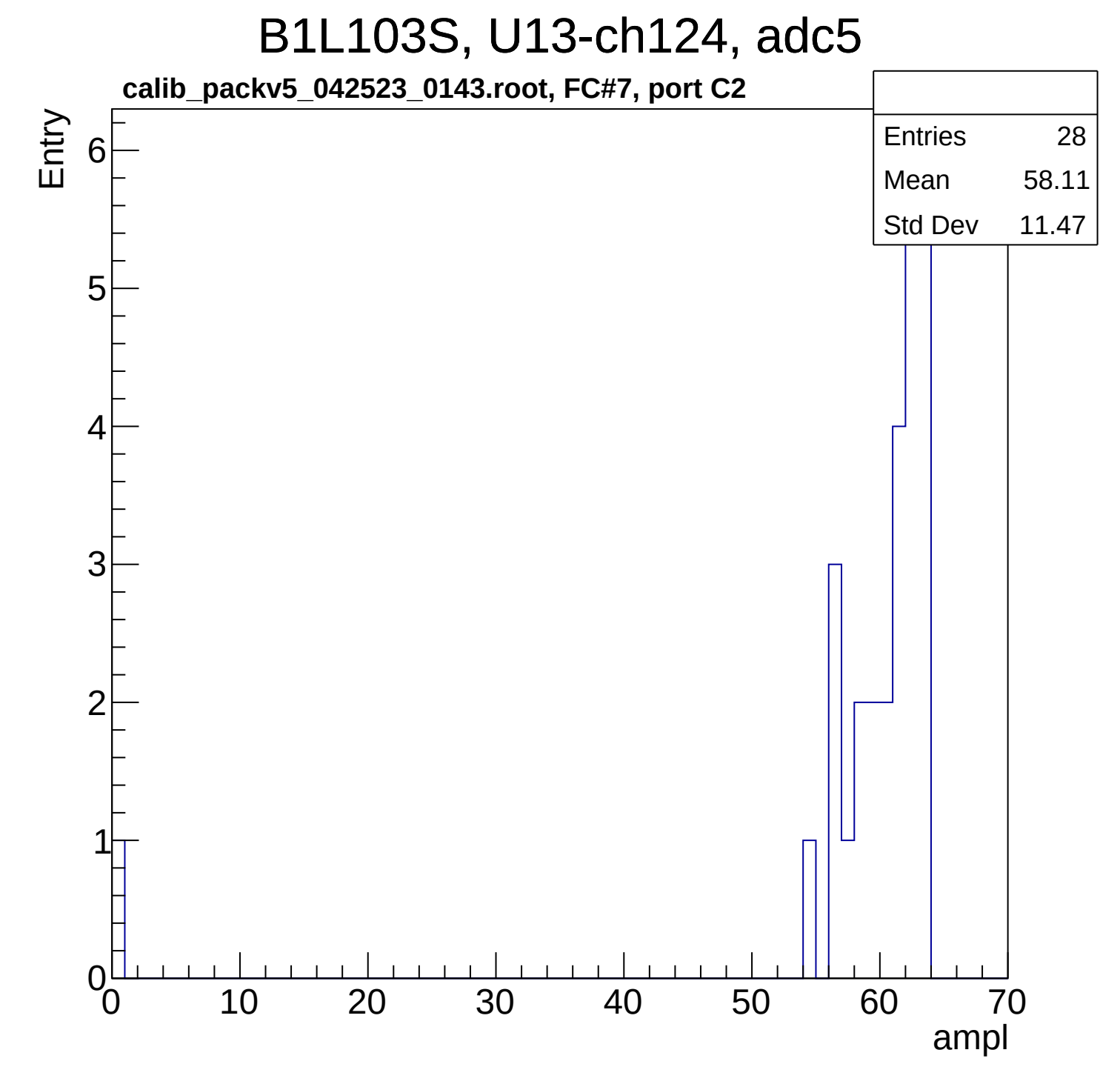
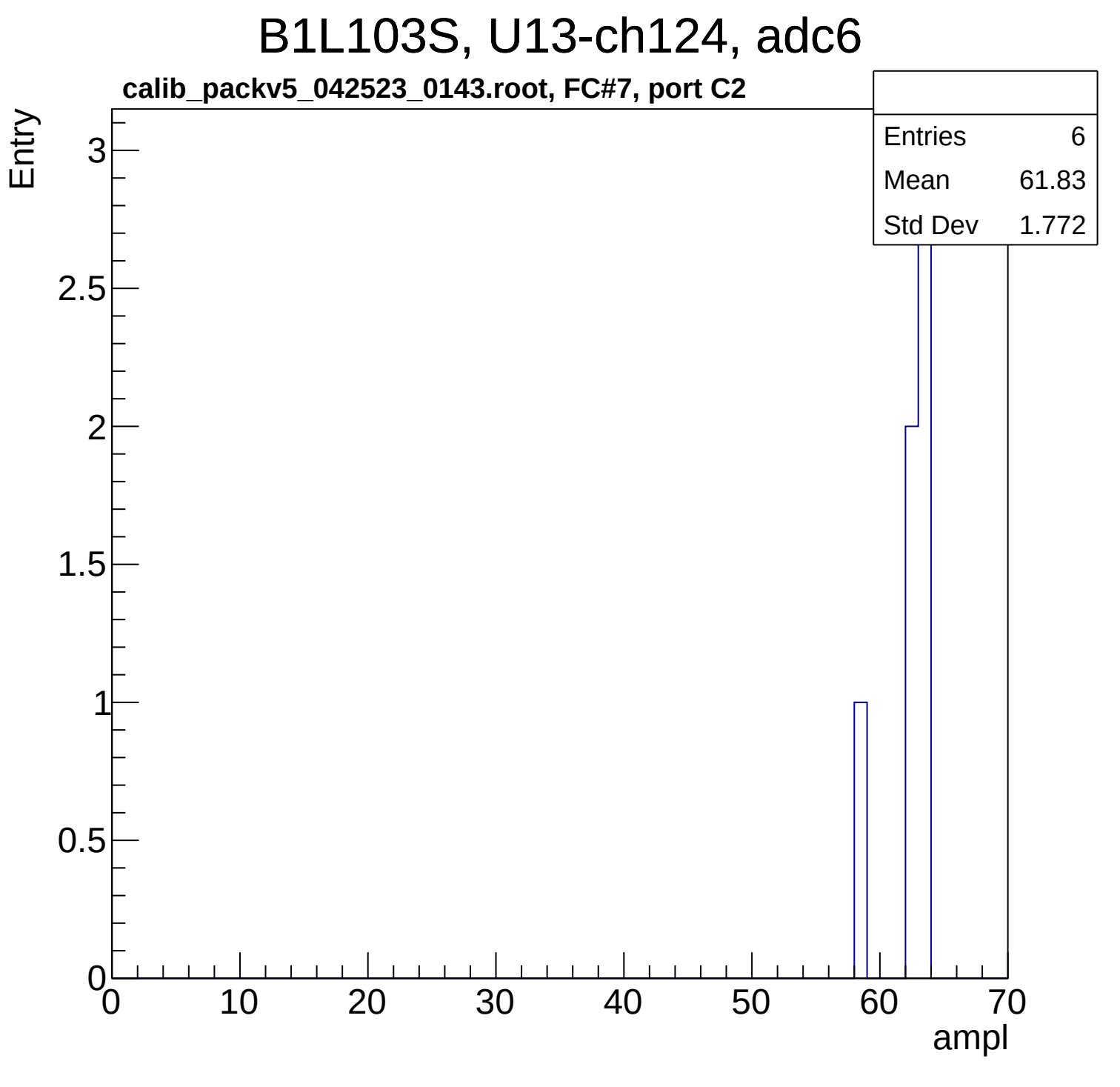

B1L103S, U13-ch124, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 28
Mean 58.11
Std Dev 11.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch124, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.83
Std Dev 1.772
Entry
ampl
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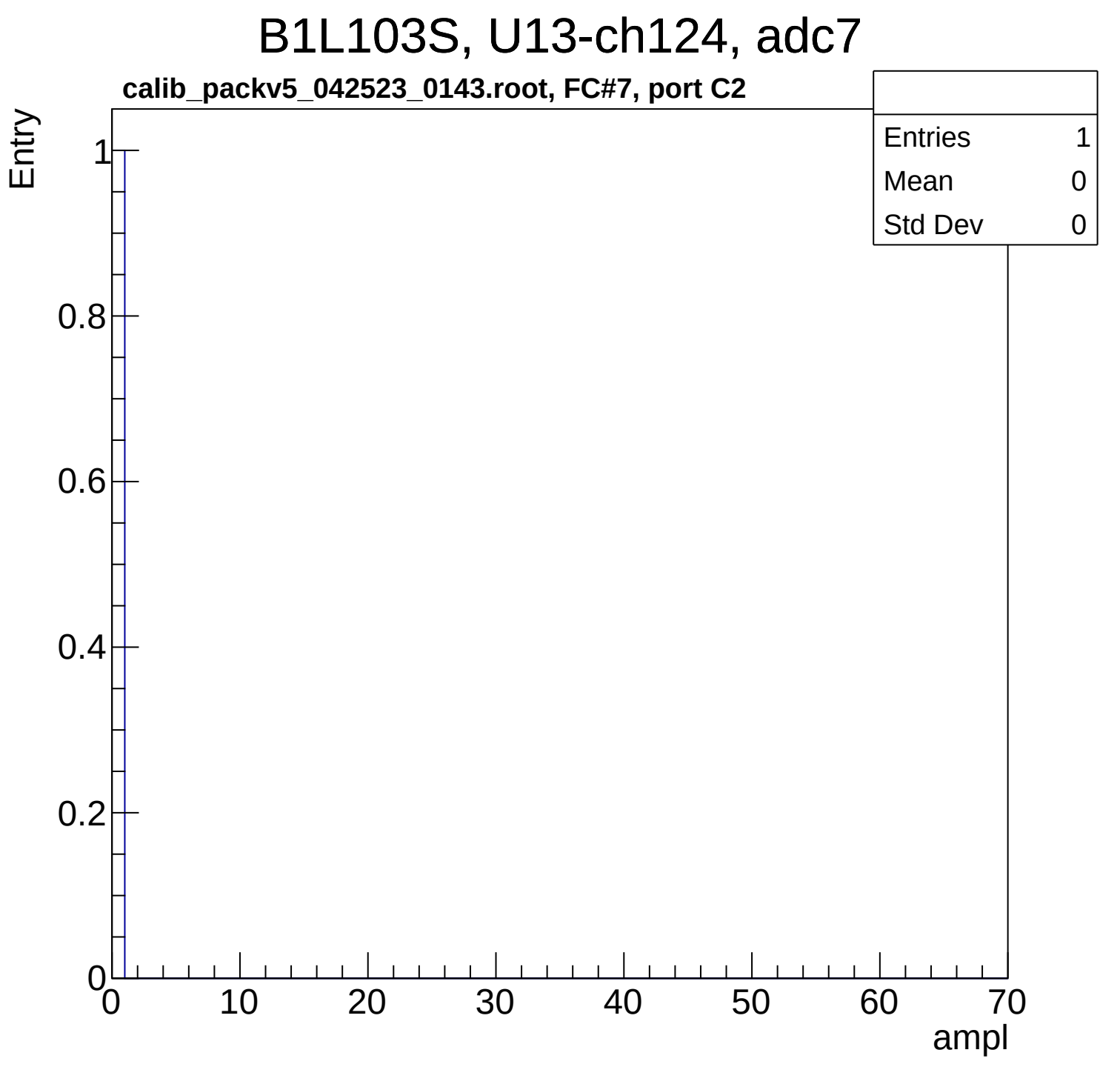
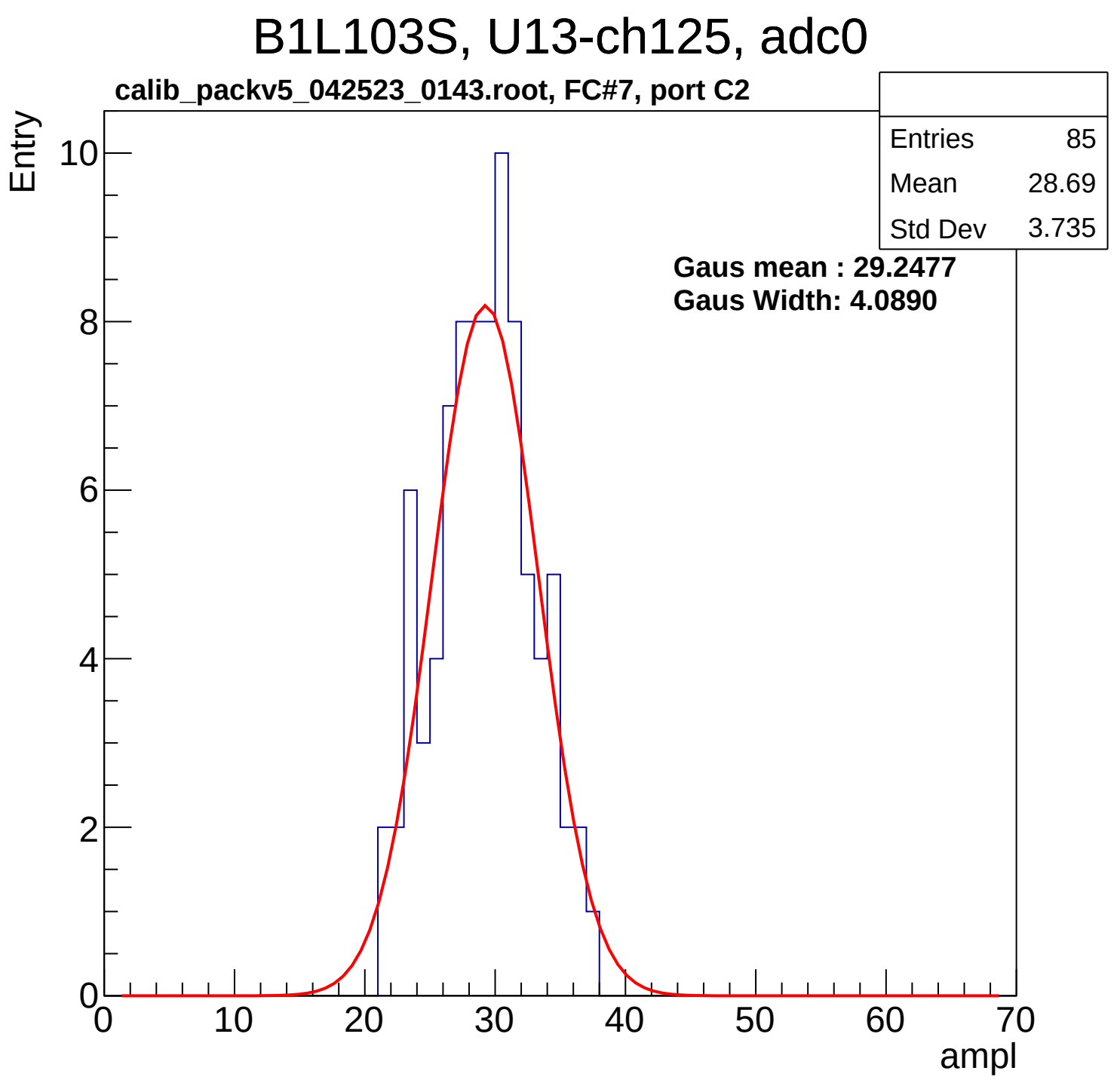
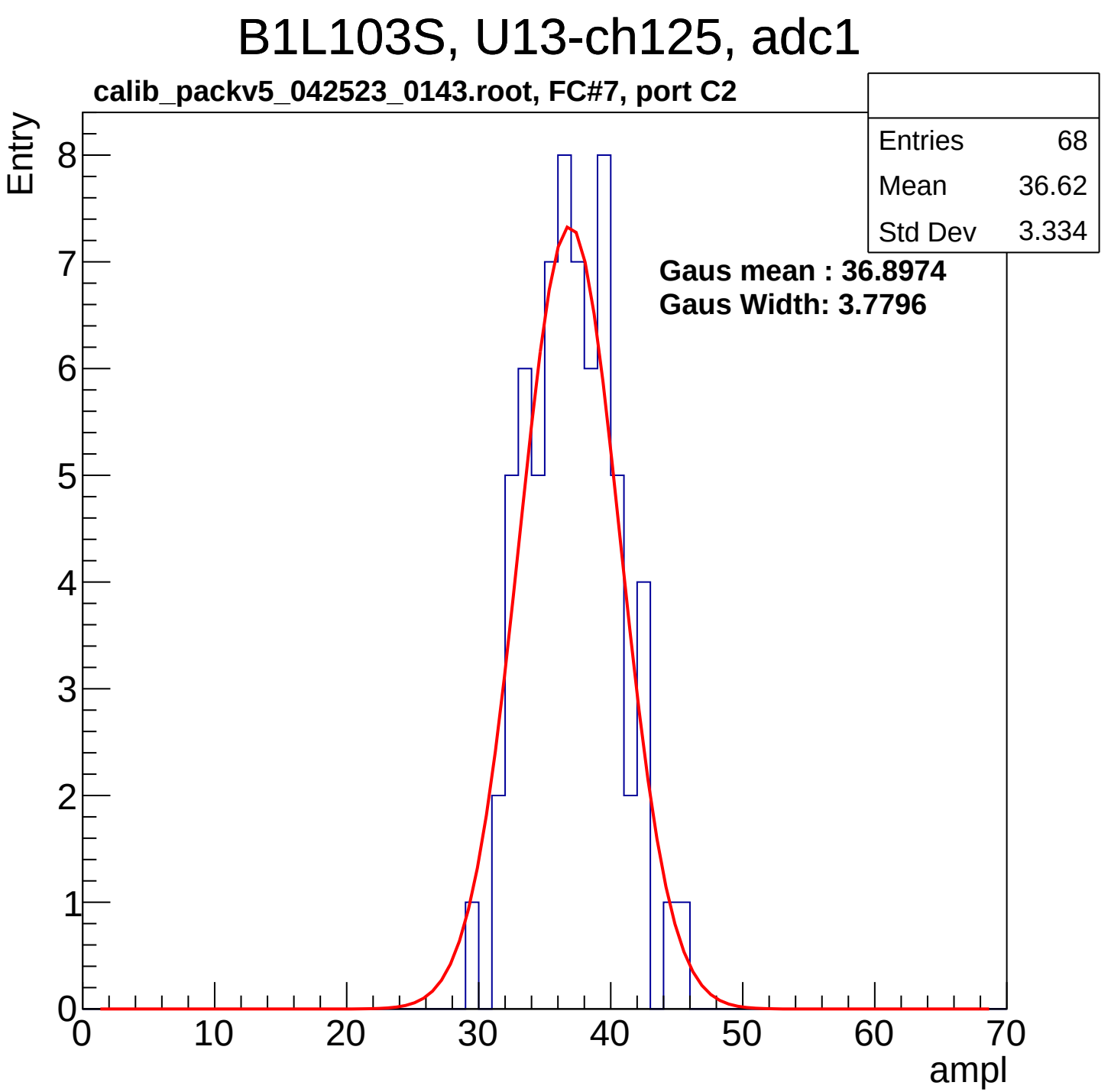
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch124, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch125, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 28.69
Std Dev 3.735
Gaus mean : 29.2477
Gaus Width: 4.0890
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch125, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.62
Std Dev 3.334
Gaus mean : 36.8974
Gaus Width: 3.7796
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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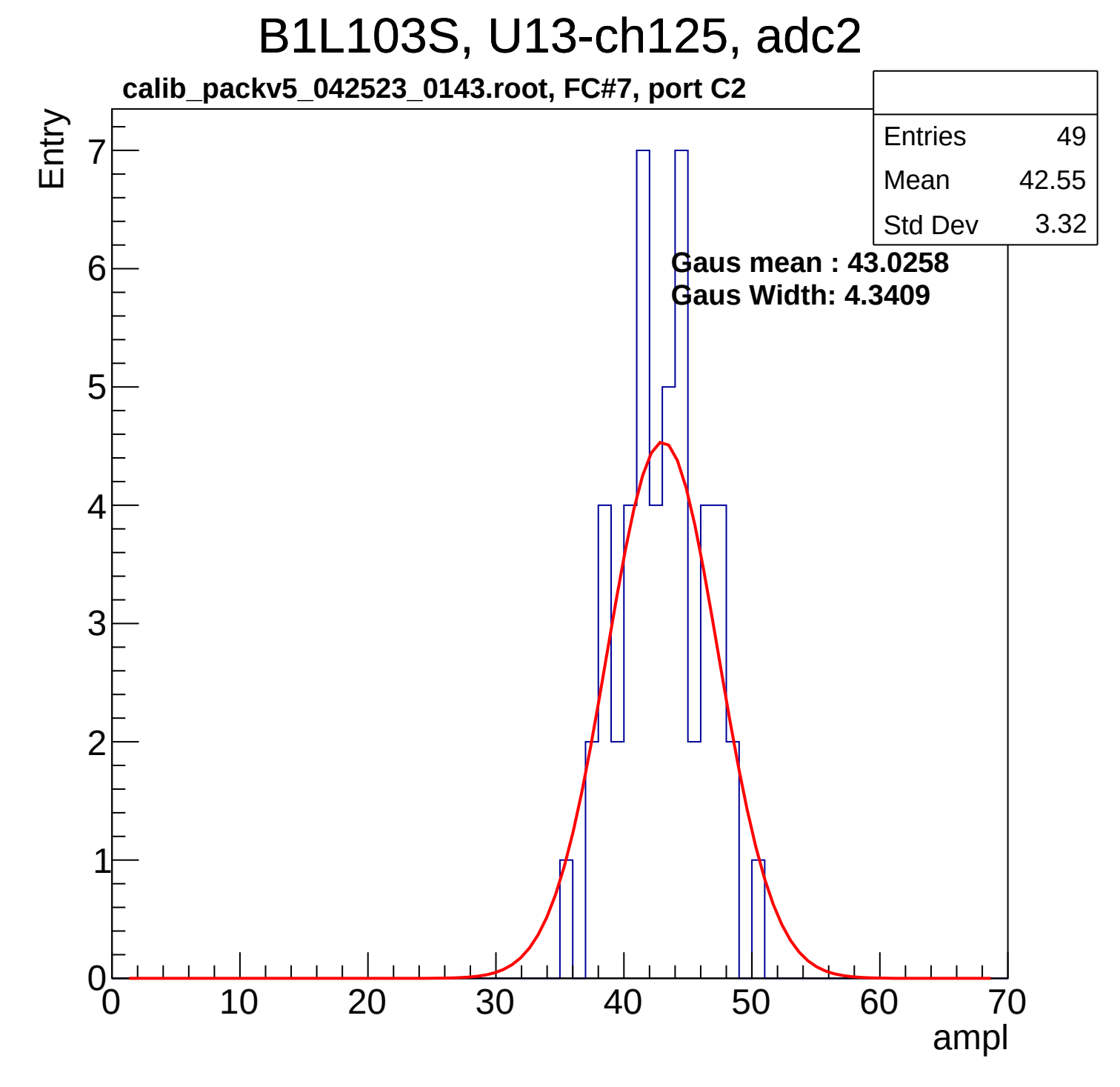
B1L103S, U13-ch125, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 42.55
Std Dev 3.32
Gaus mean : 43.0258
Gaus Width: 4.3409
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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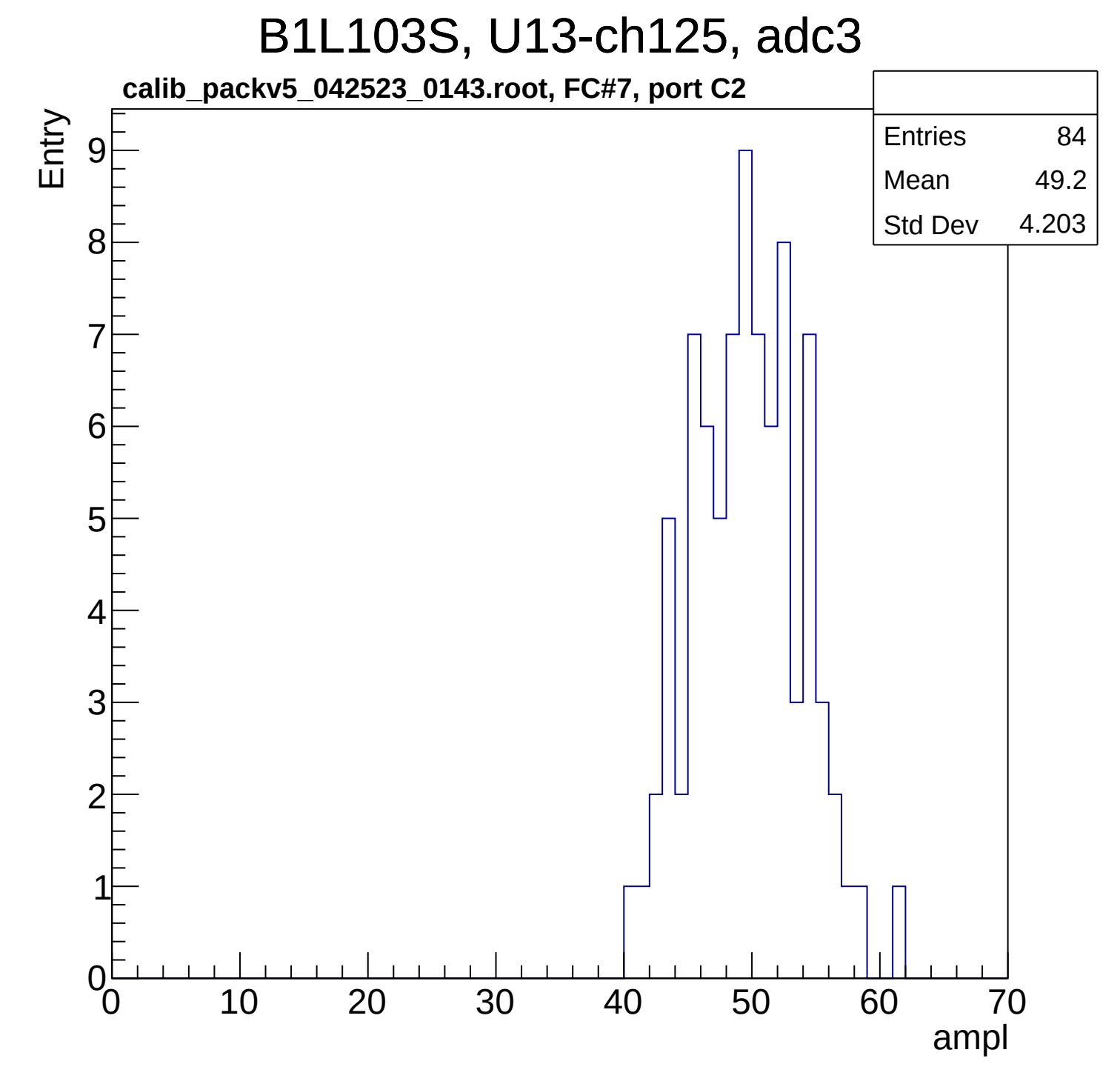
B1L103S, U13-ch125, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 49.2
Std Dev 4.203
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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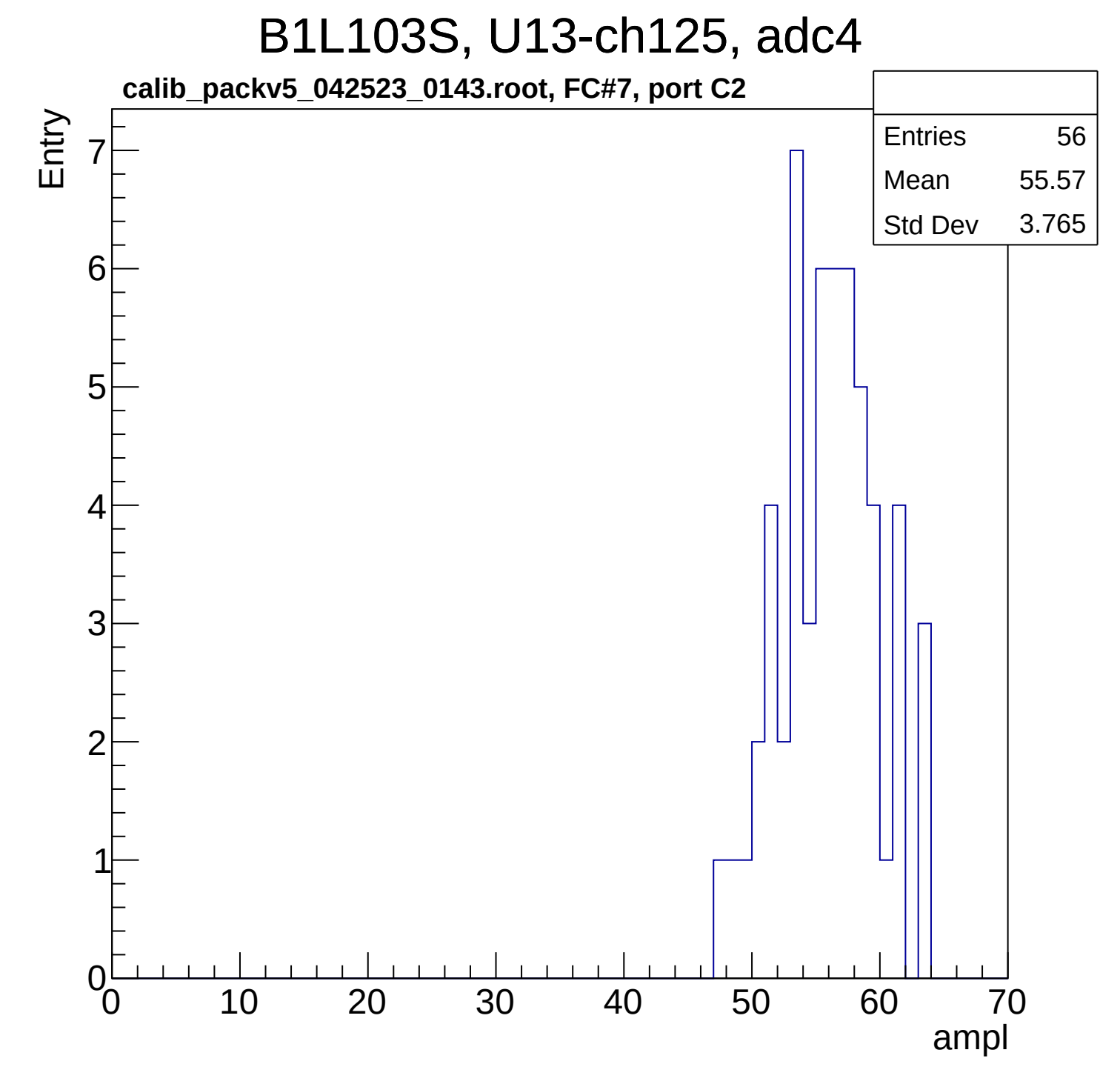
B1L103S, U13-ch125, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55.57
Std Dev 3.765
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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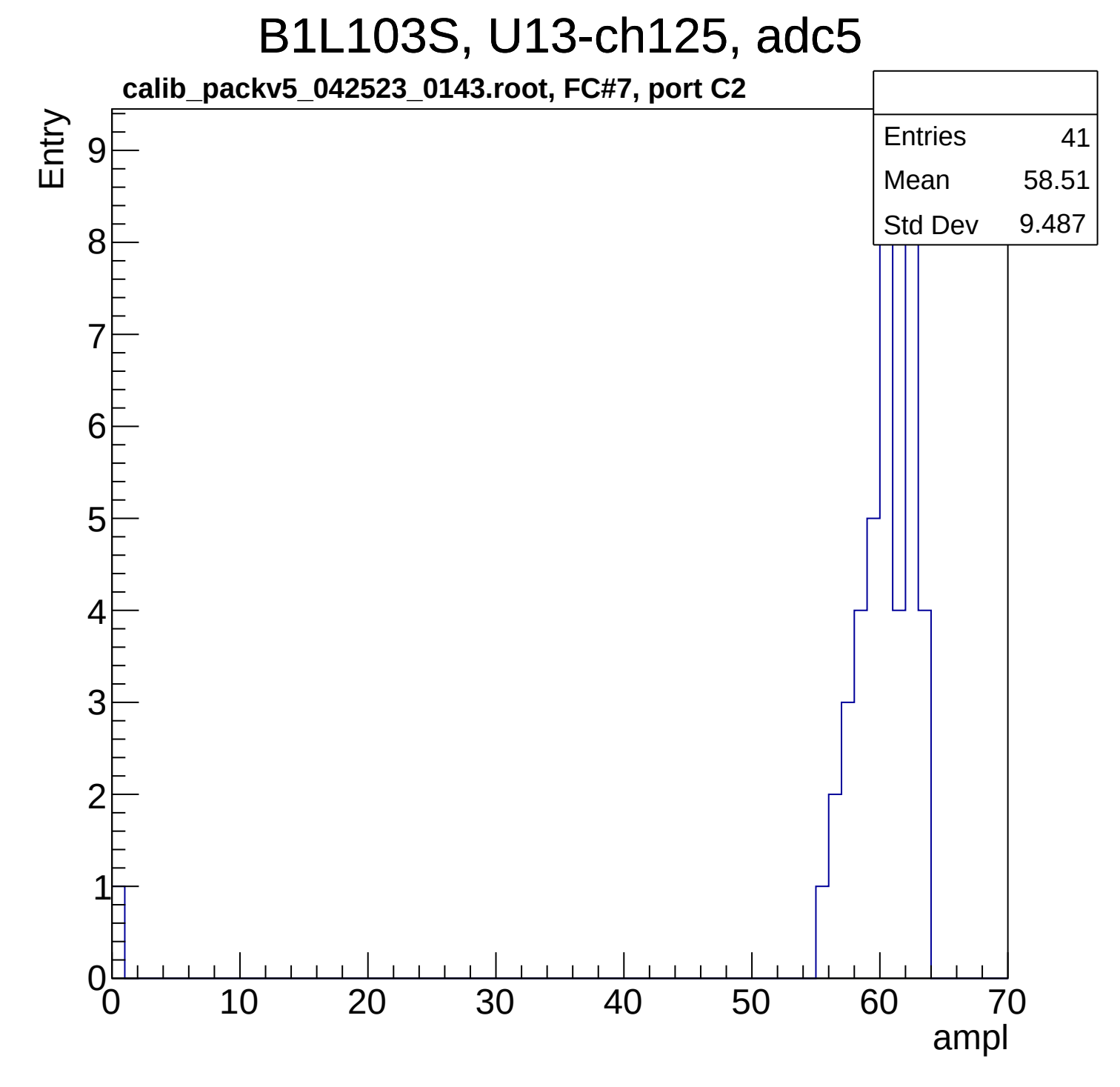
B1L103S, U13-ch125, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 58.51
Std Dev 9.487
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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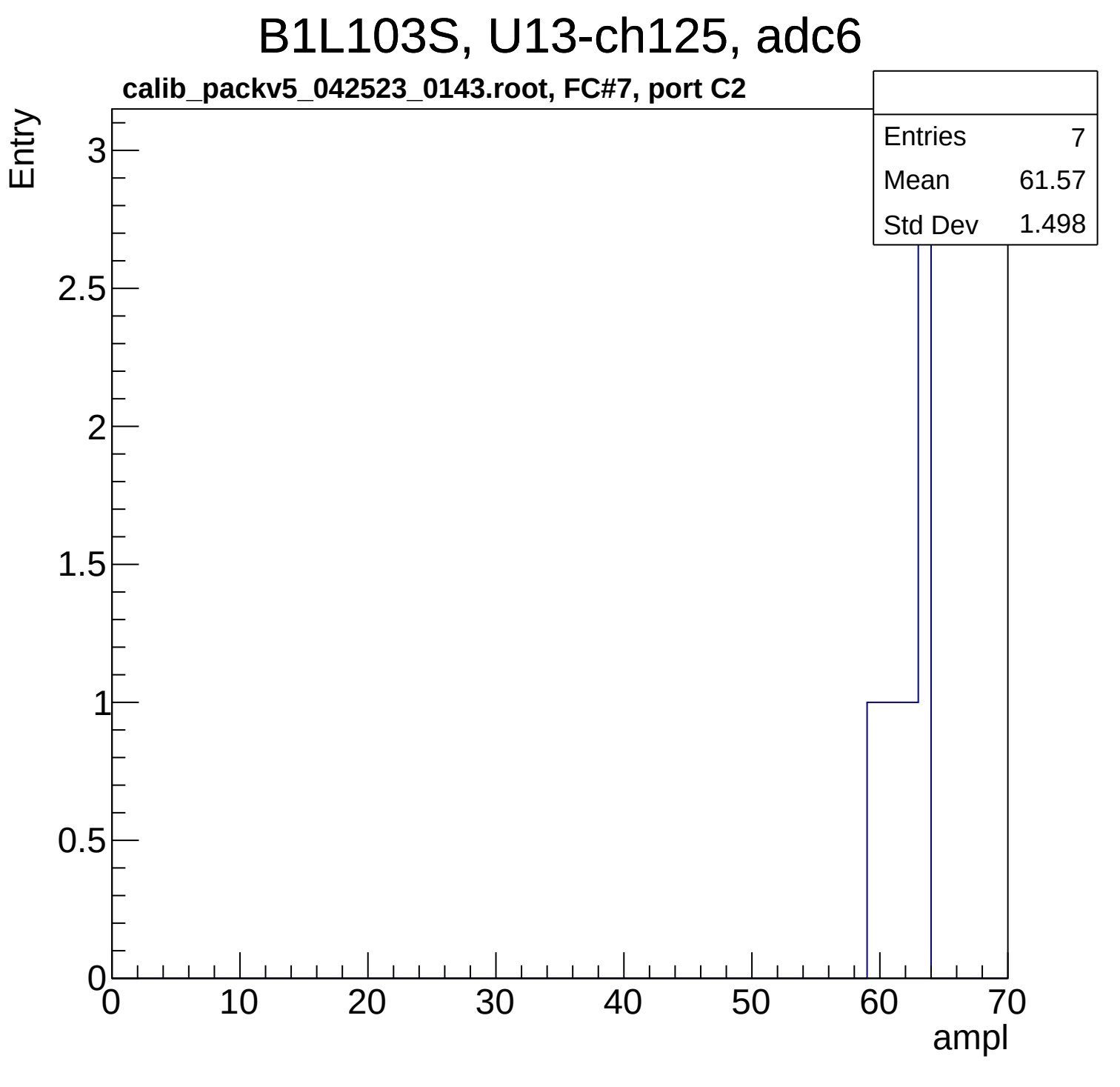
B1L103S, U13-ch125, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.57
Std Dev 1.498
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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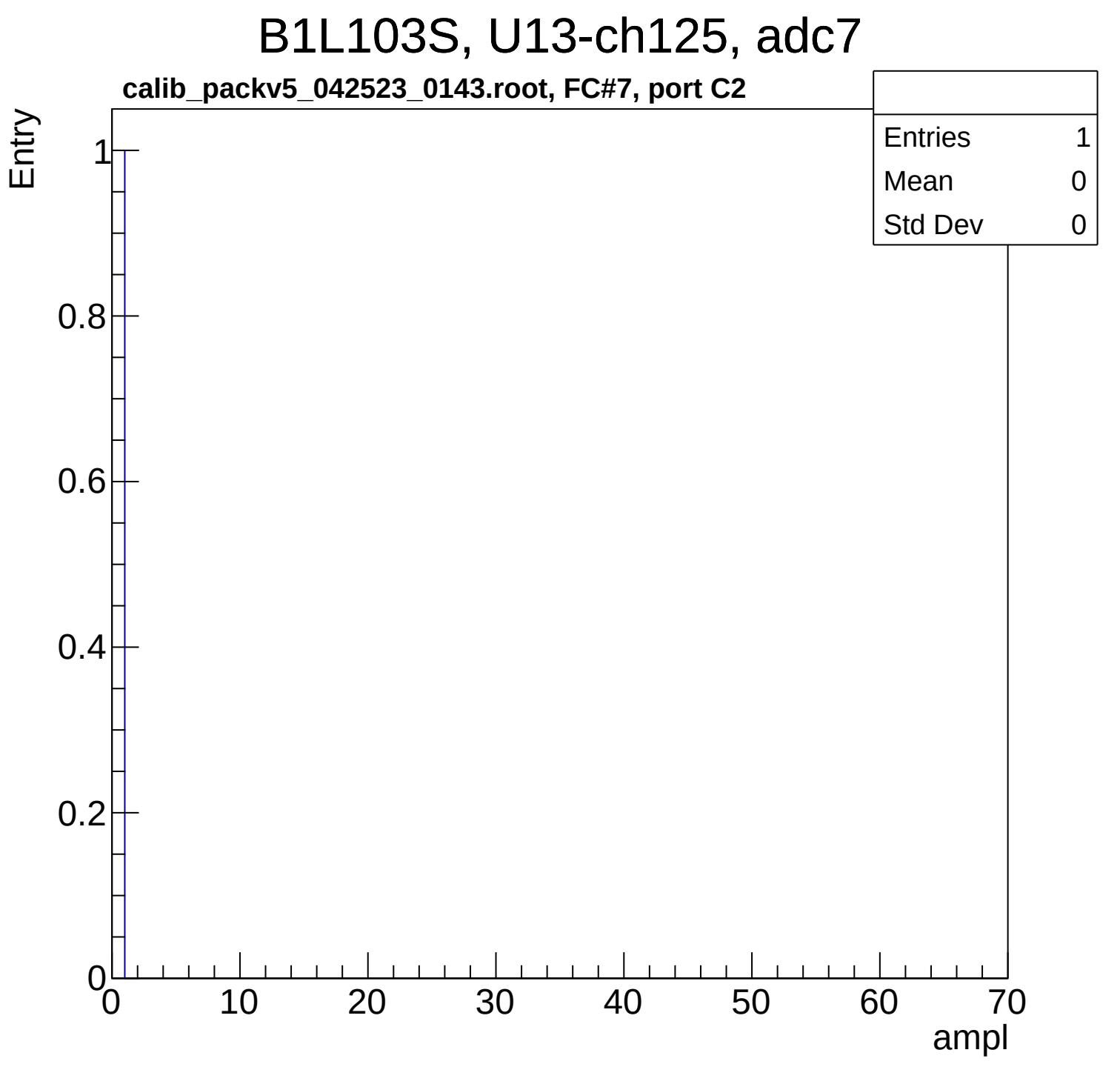
40
50
60
70


B1L103S, U13-ch125, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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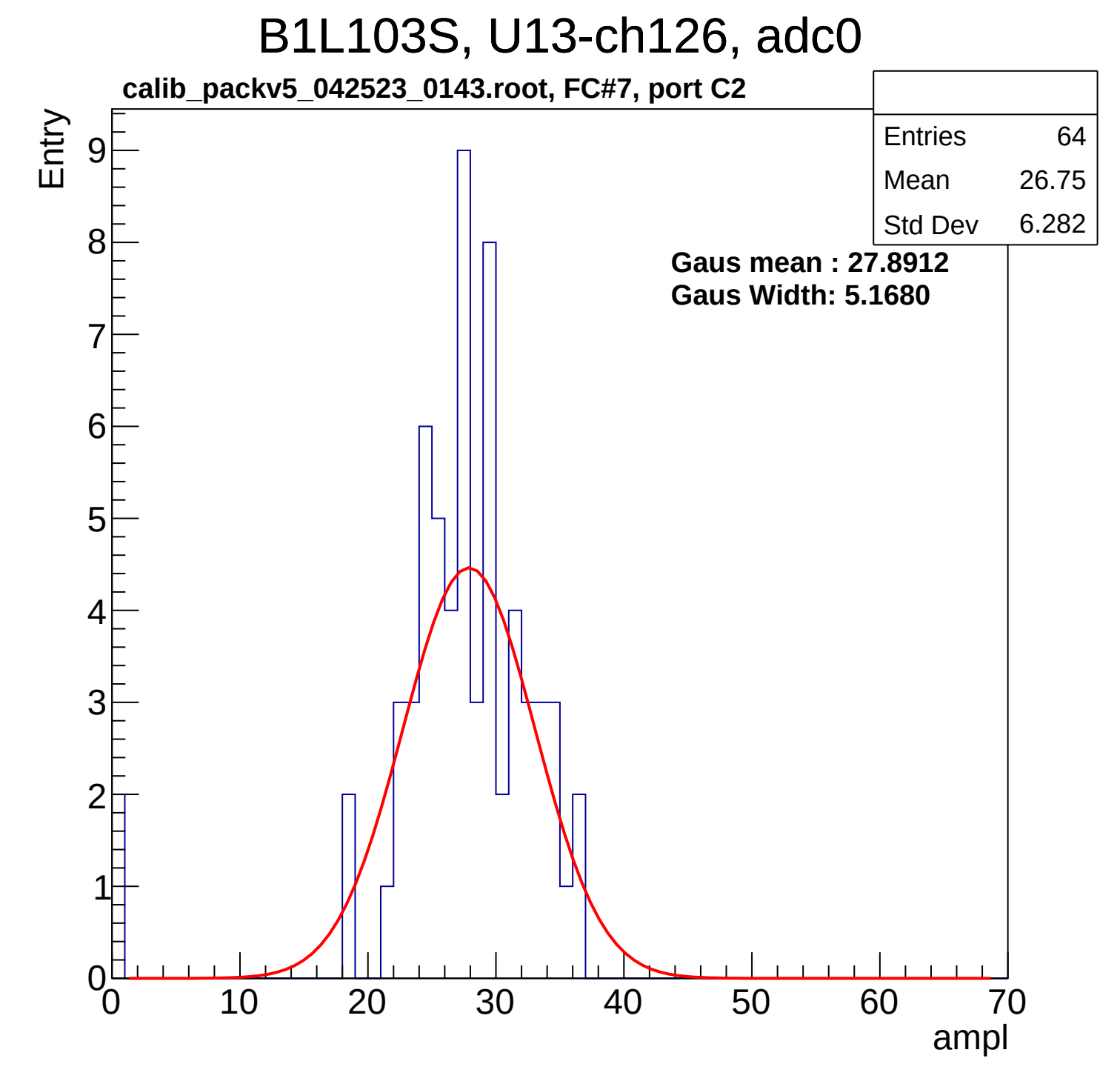
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U13-ch126, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 26.75
Std Dev 6.282
Gaus mean : 27.8912
Gaus Width: 5.1680
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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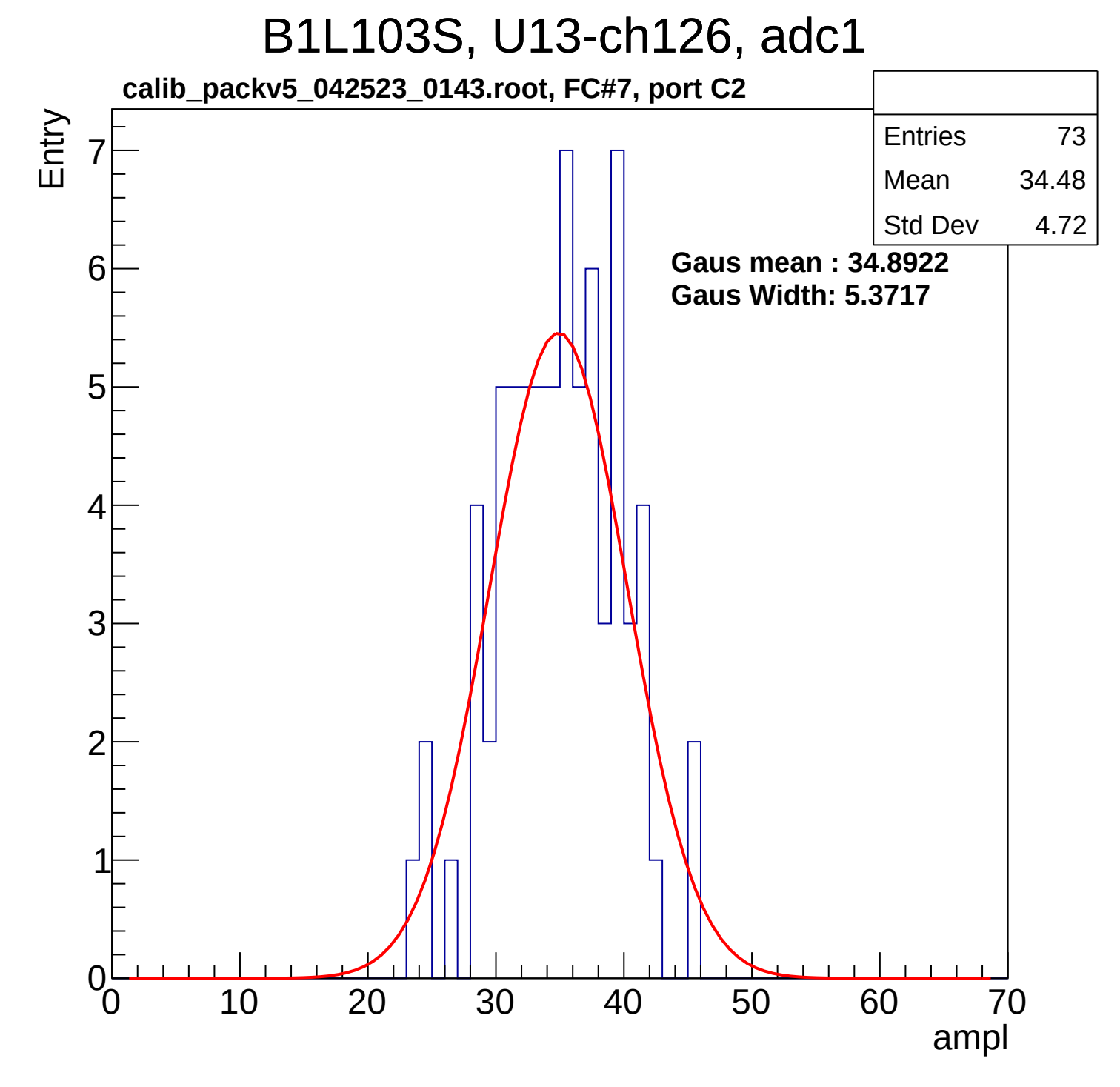
60
70


B1L103S, U13-ch126, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 34.48
Std Dev 4.72
Gaus mean : 34.8922
Gaus Width: 5.3717
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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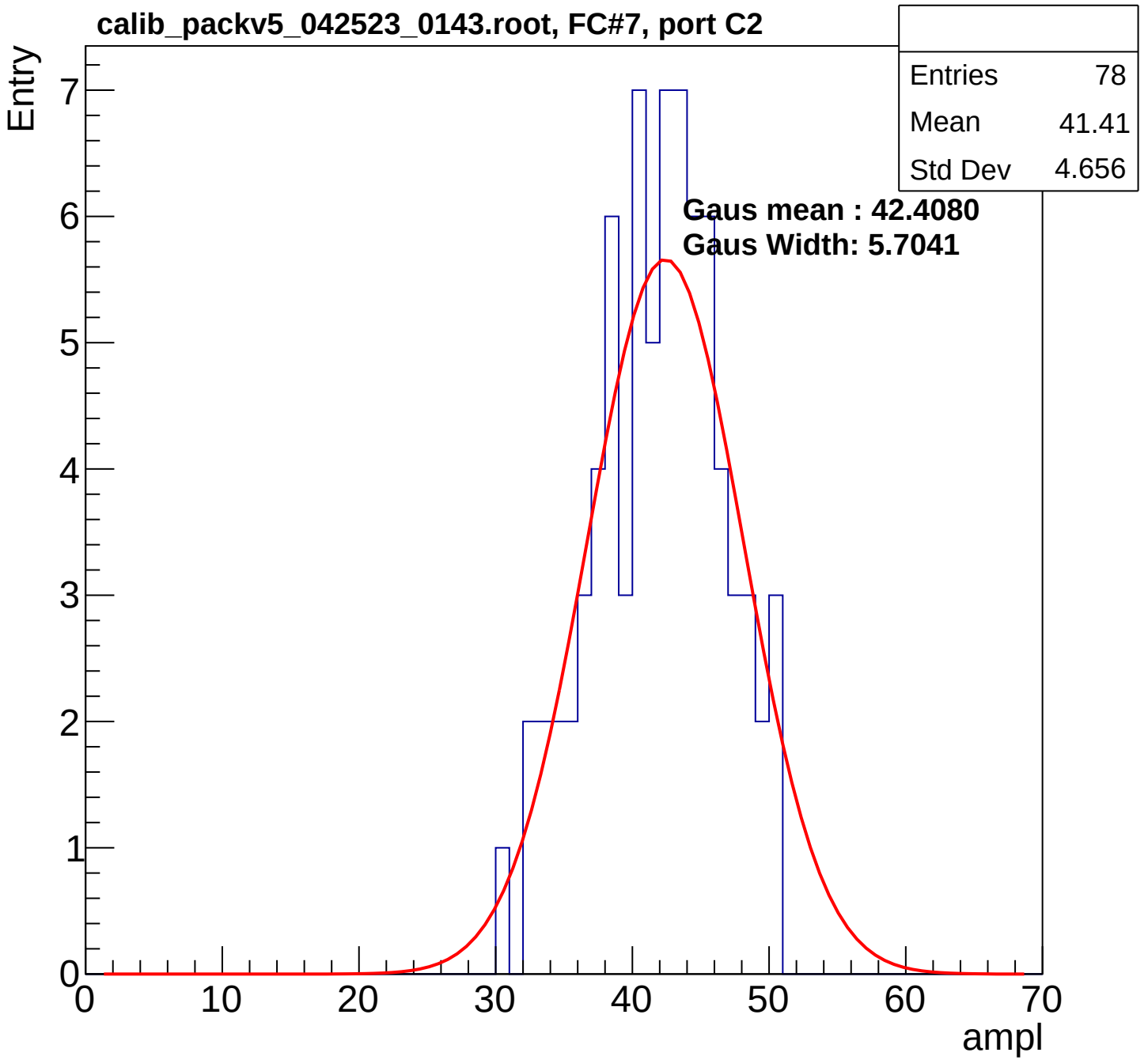
60
70


B1L103S, U13-ch126, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 41.41
Std Dev 4.656
Gaus mean : 42.4080
Gaus Width: 5.7041
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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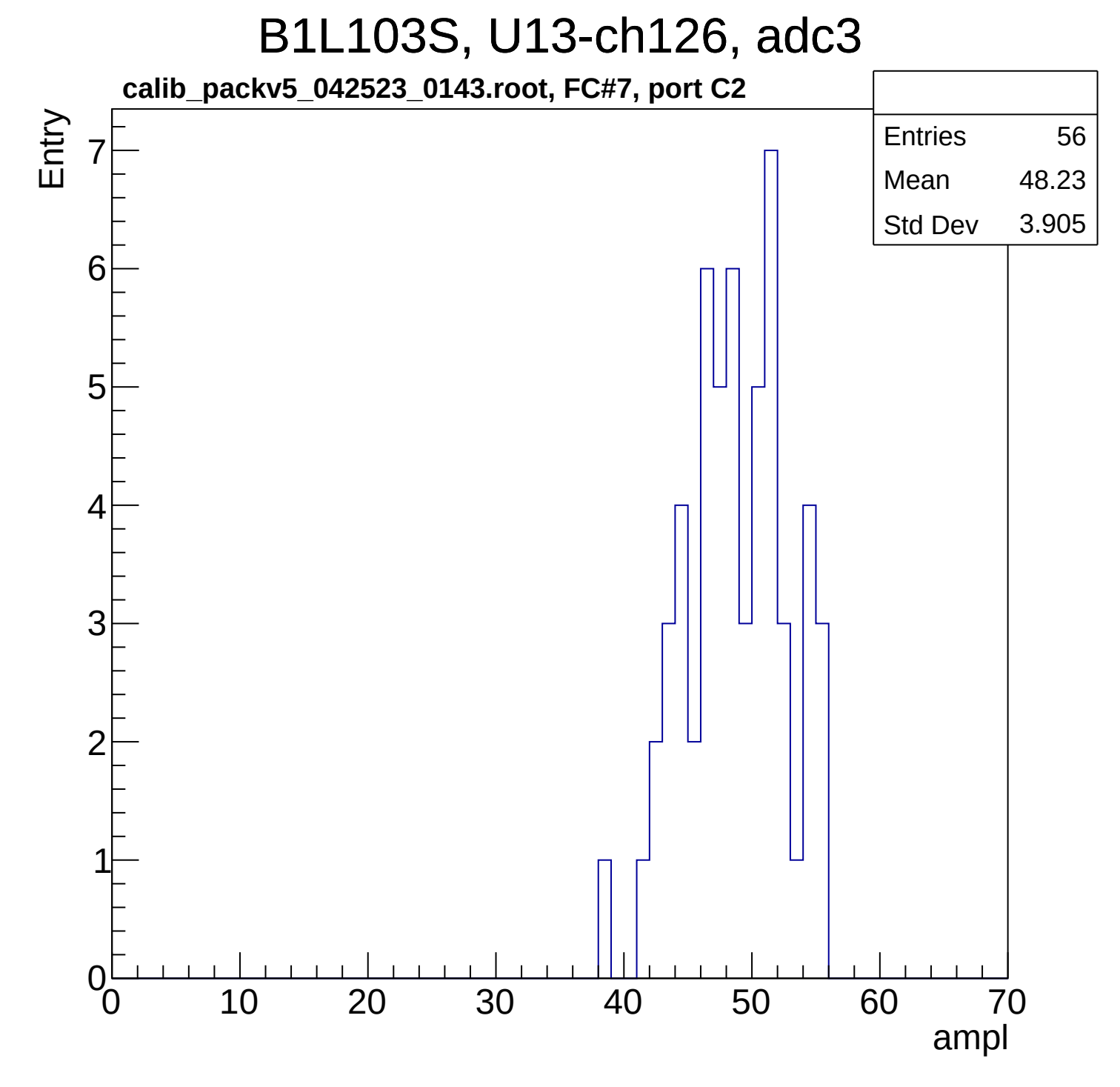
B1L103S, U13-ch126, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 48.23
Std Dev 3.905
Entry
ampl
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3
4
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60
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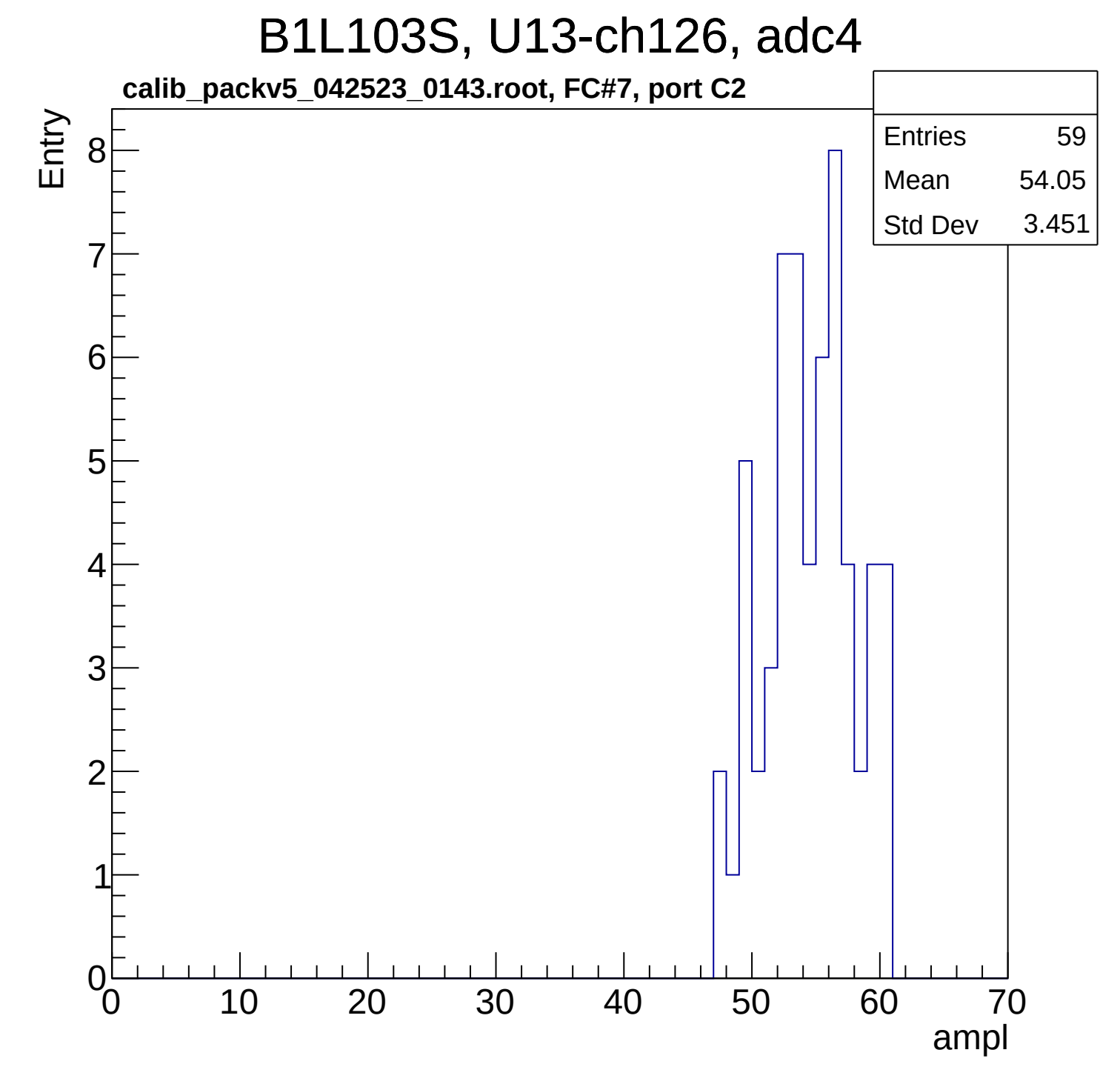
B1L103S, U13-ch126, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.05
Std Dev 3.451
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
50
60
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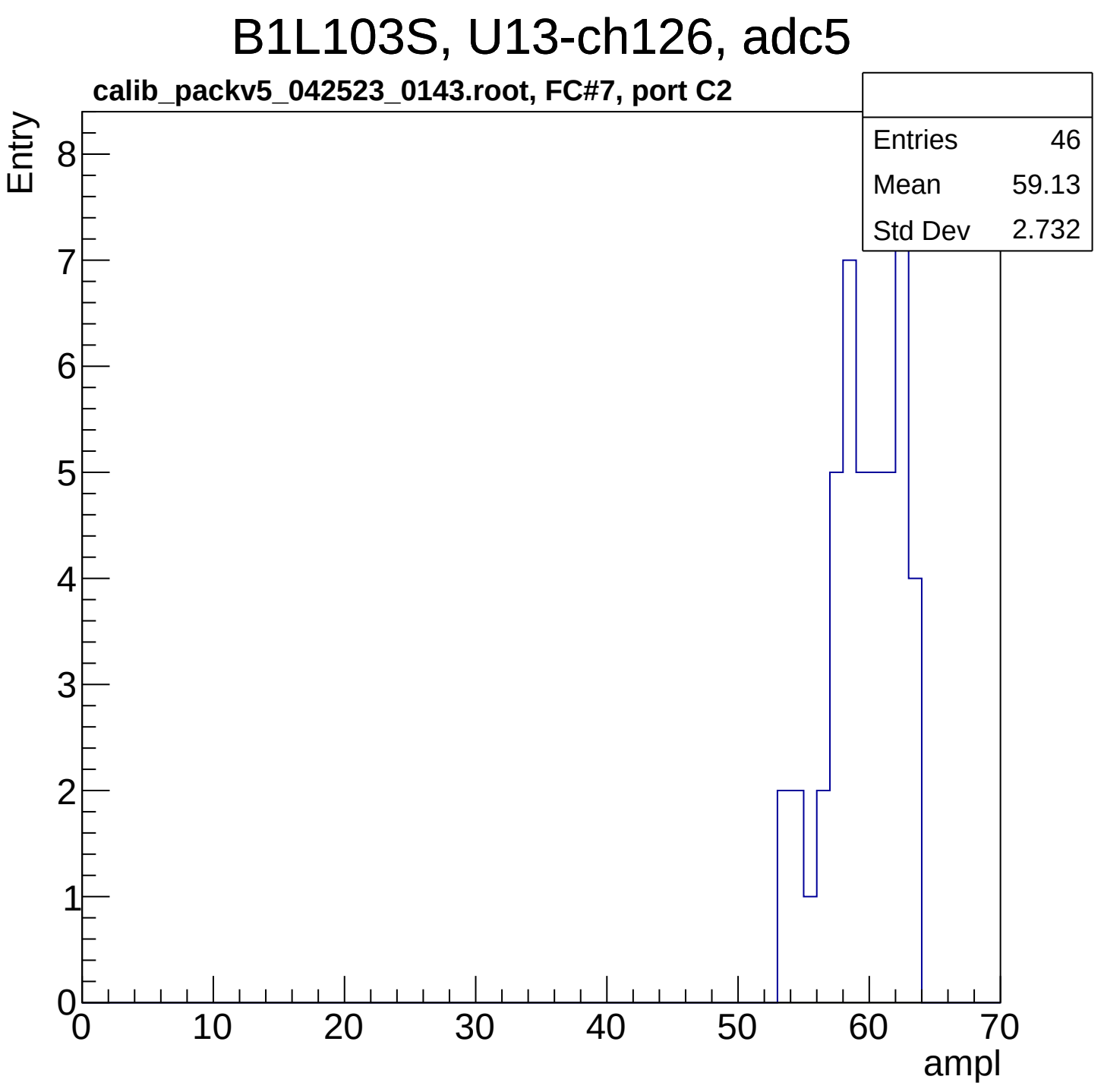
B1L103S, U13-ch126, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.13
Std Dev 2.732
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
50
60
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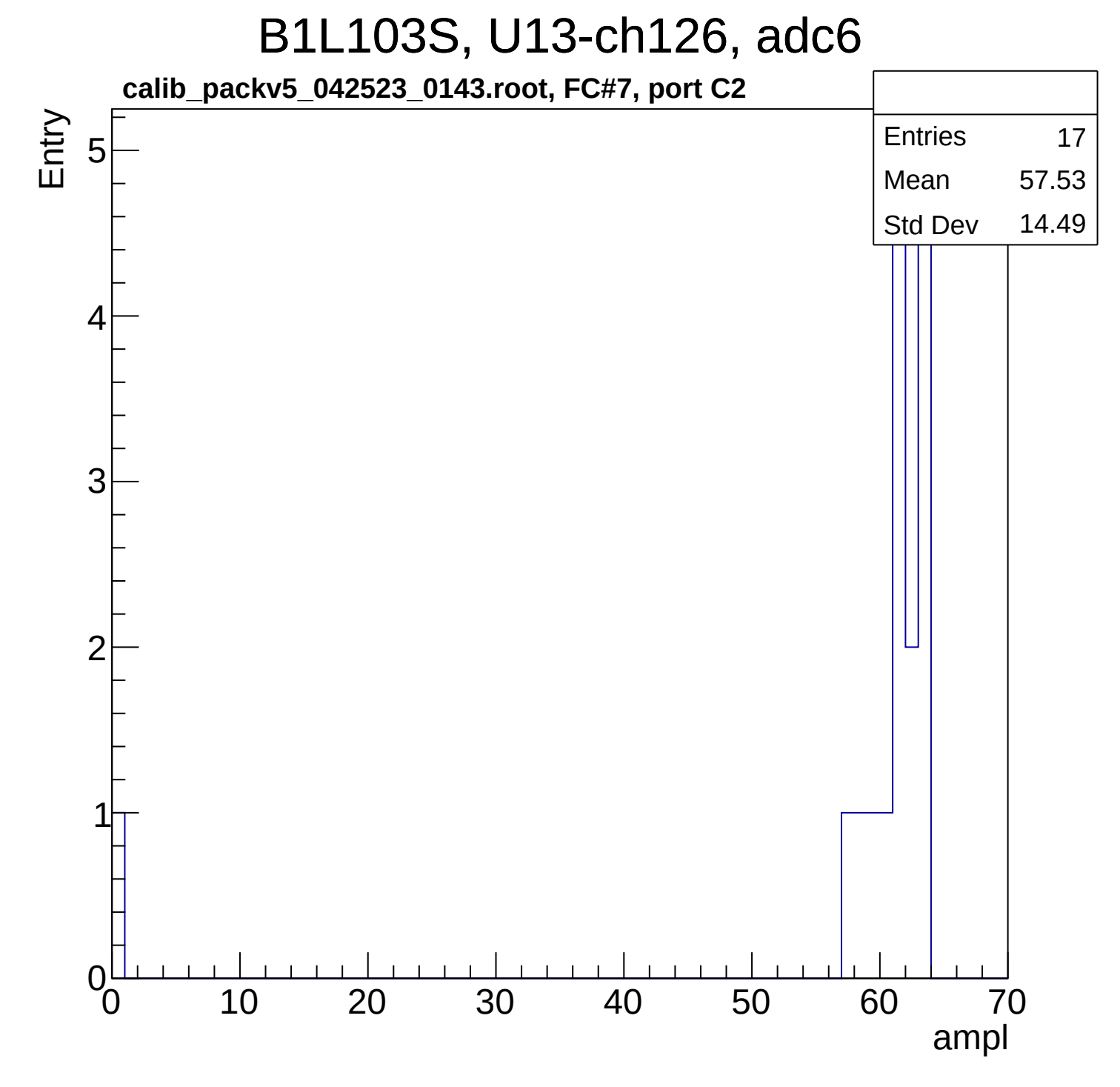
B1L103S, U13-ch126, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 57.53
Std Dev 14.49
Entry
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10
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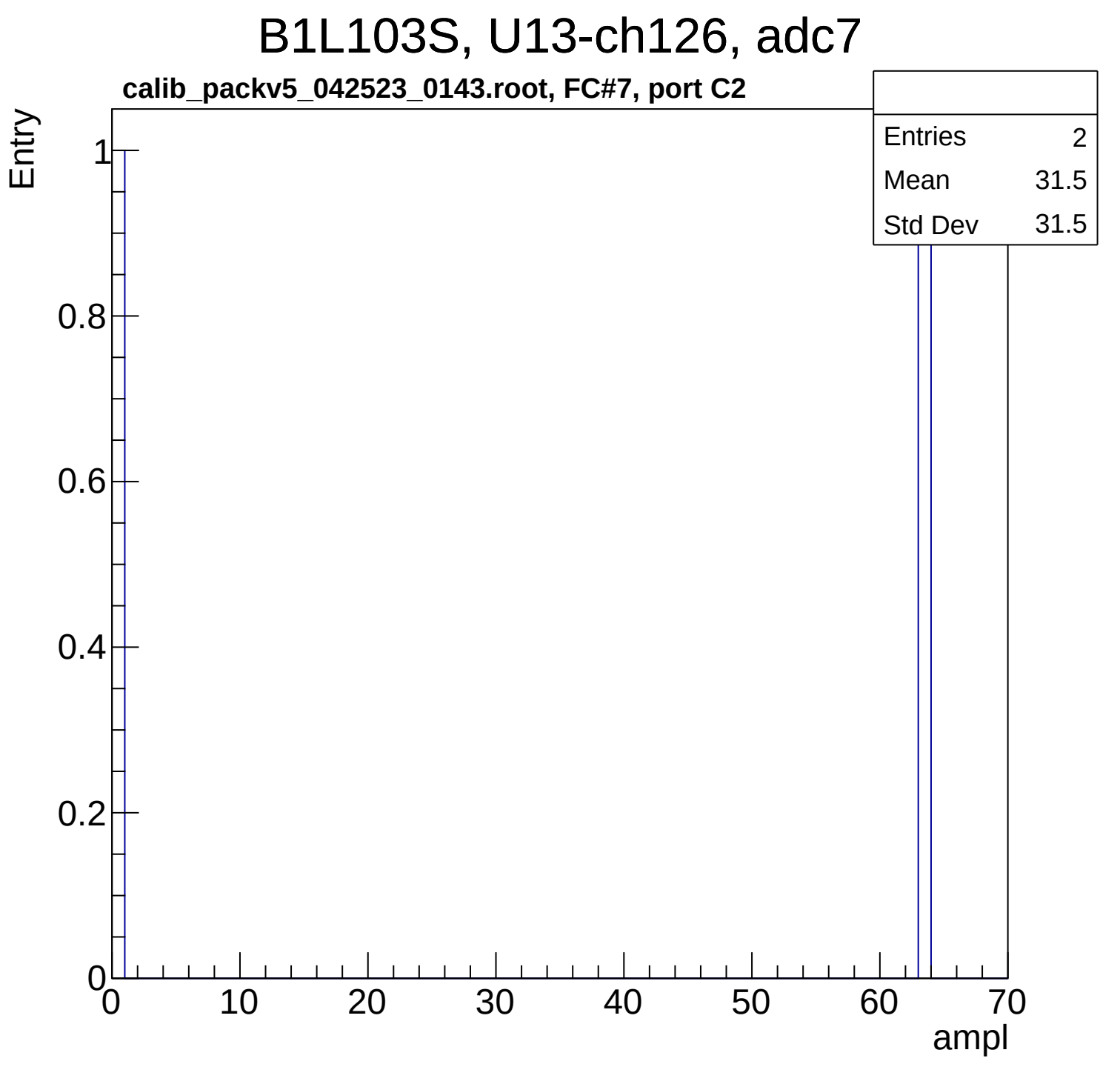
30
40
50
60
70
ampl


B1L103S, U13-ch126, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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10
20
30
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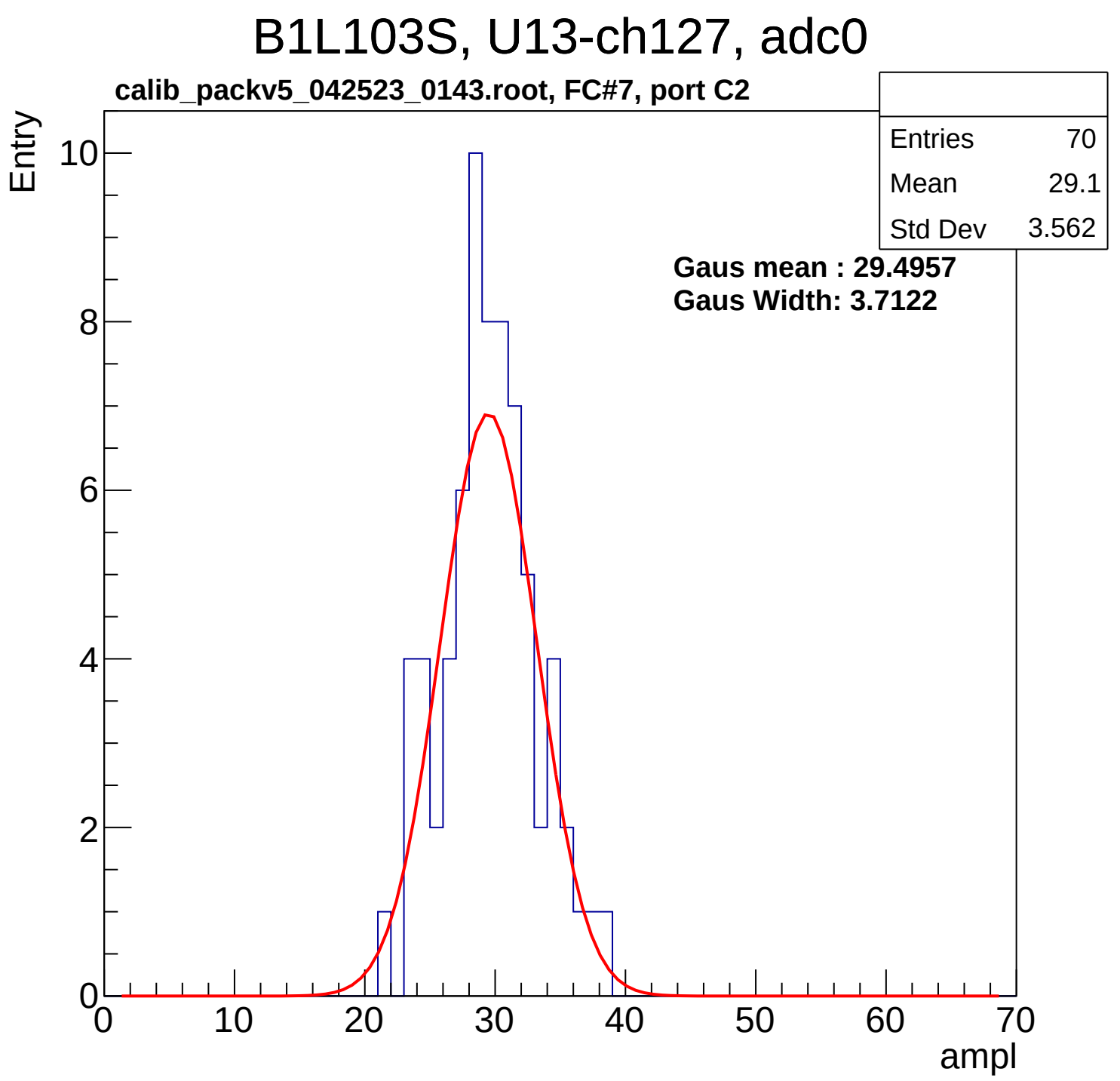
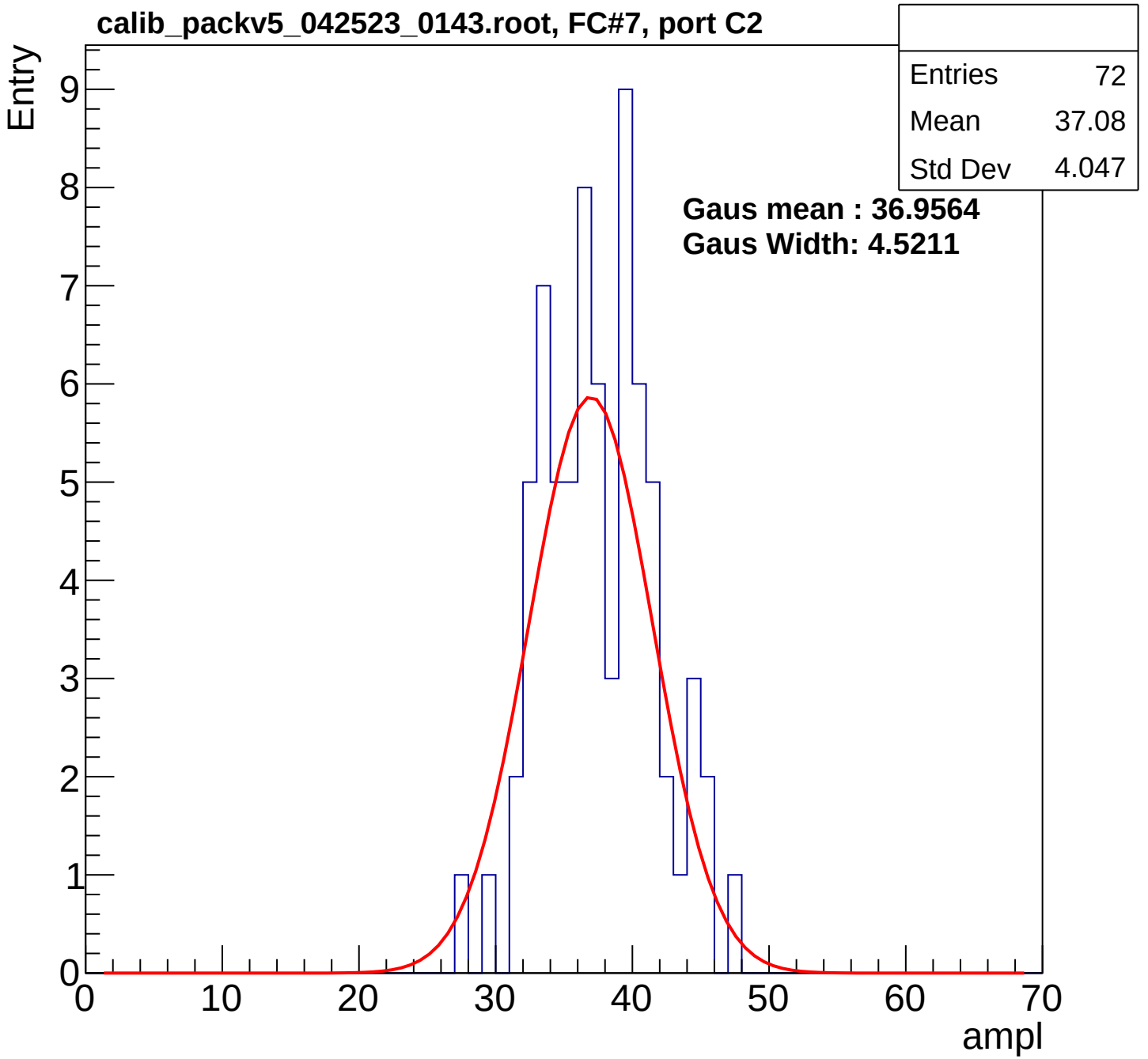
50
60
70
ampl


B1L103S, U13-ch127, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.1
Std Dev 3.562
Gaus mean : 29.4957
Gaus Width: 3.7122
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U13-ch127, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.08
Std Dev 4.047
Gaus mean : 36.9564
Gaus Width: 4.5211
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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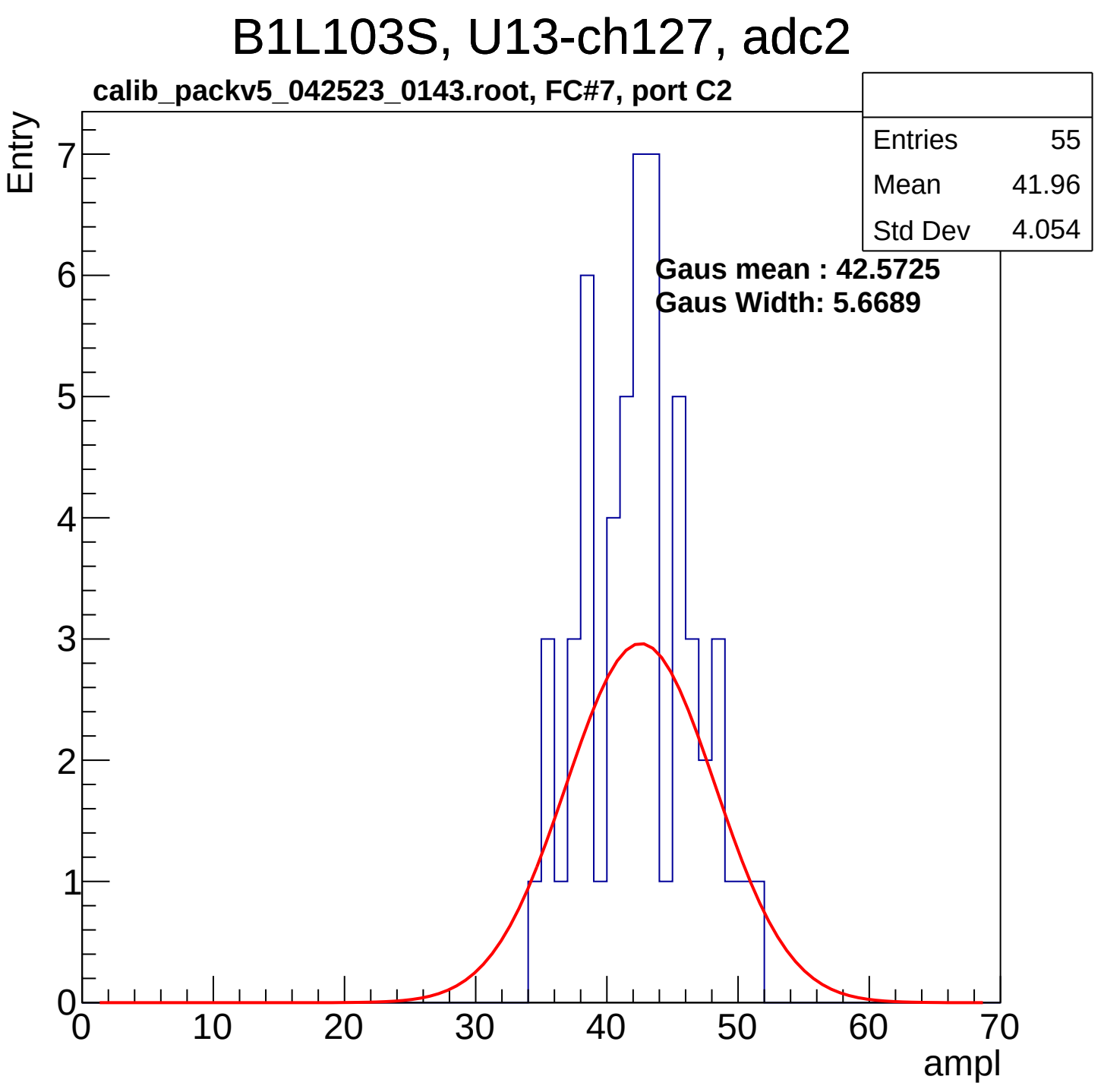
B1L103S, U13-ch127, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 41.96
Std Dev 4.054
Gaus mean : 42.5725
Gaus Width: 5.6689
Entry
ampl
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1
2
3
4
5
6
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0
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60
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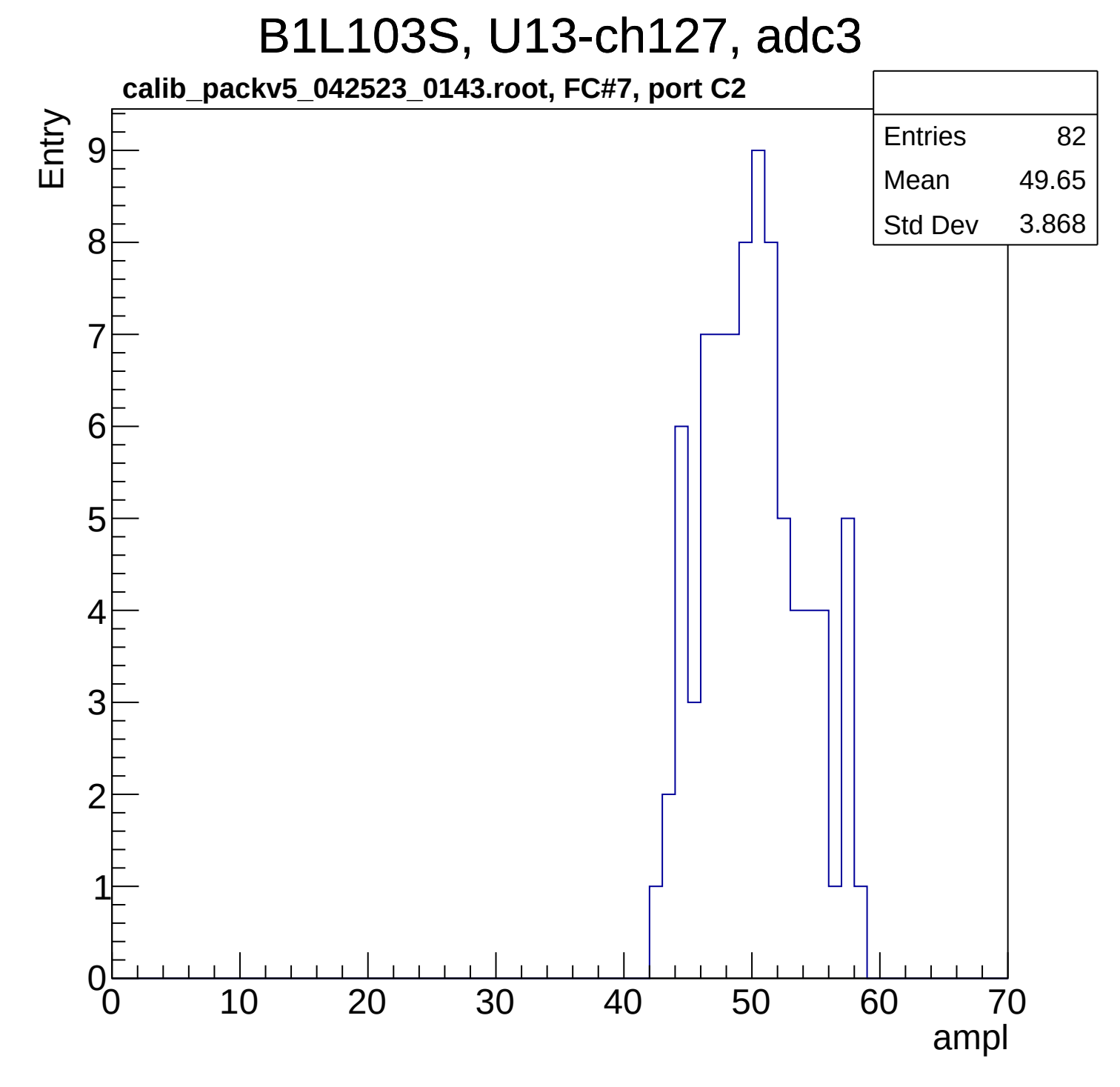
B1L103S, U13-ch127, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 49.65
Std Dev 3.868
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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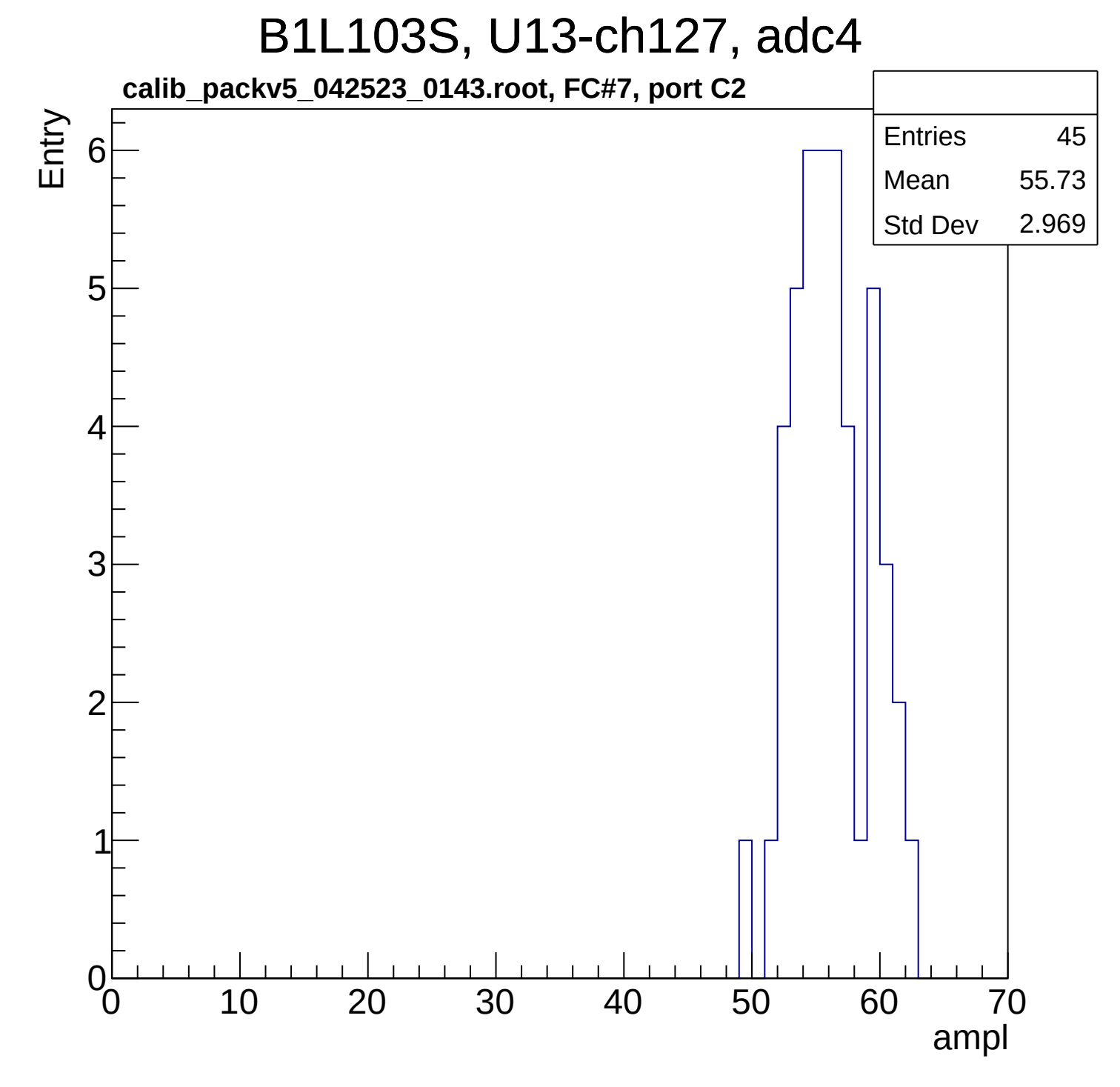
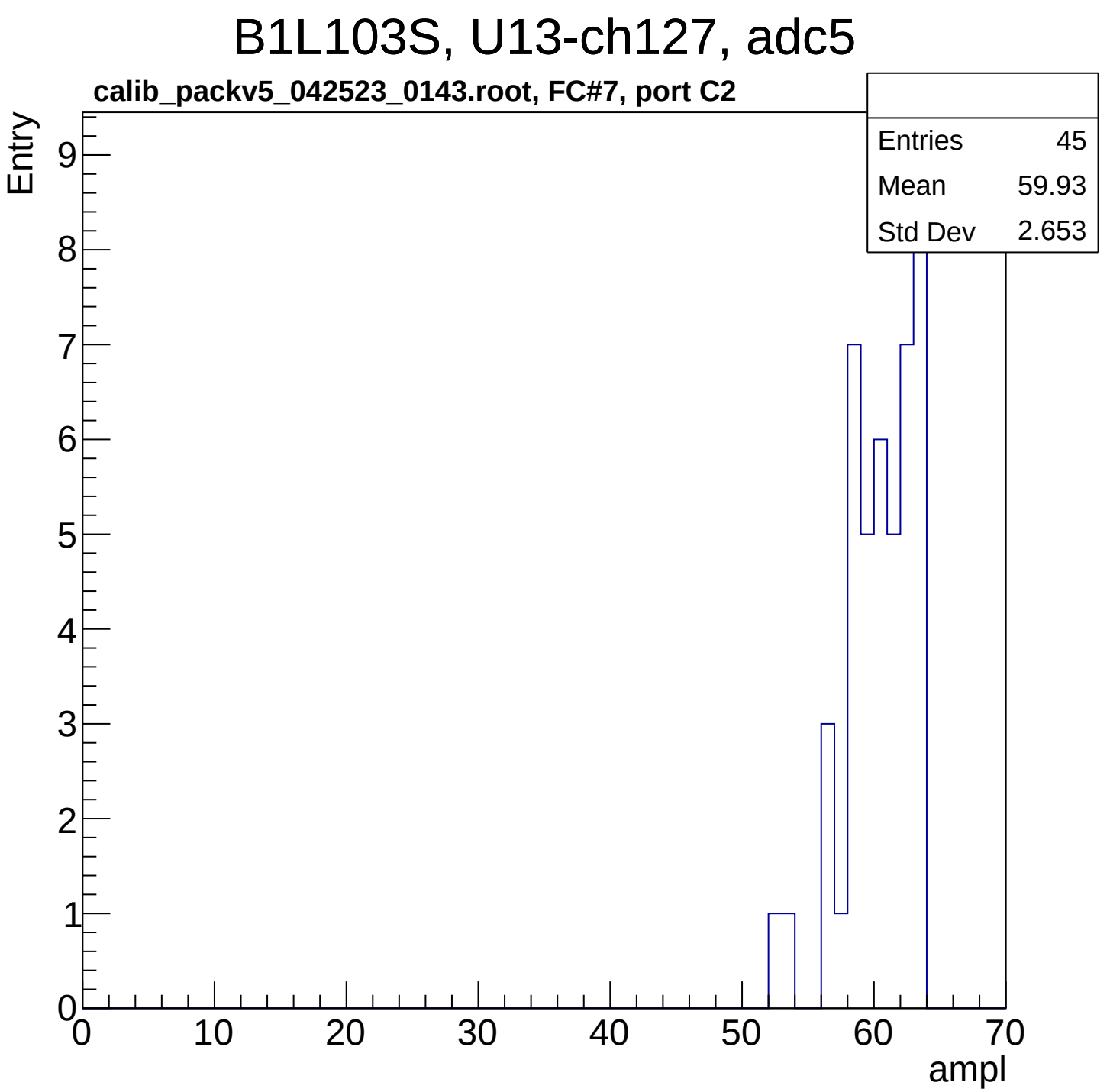
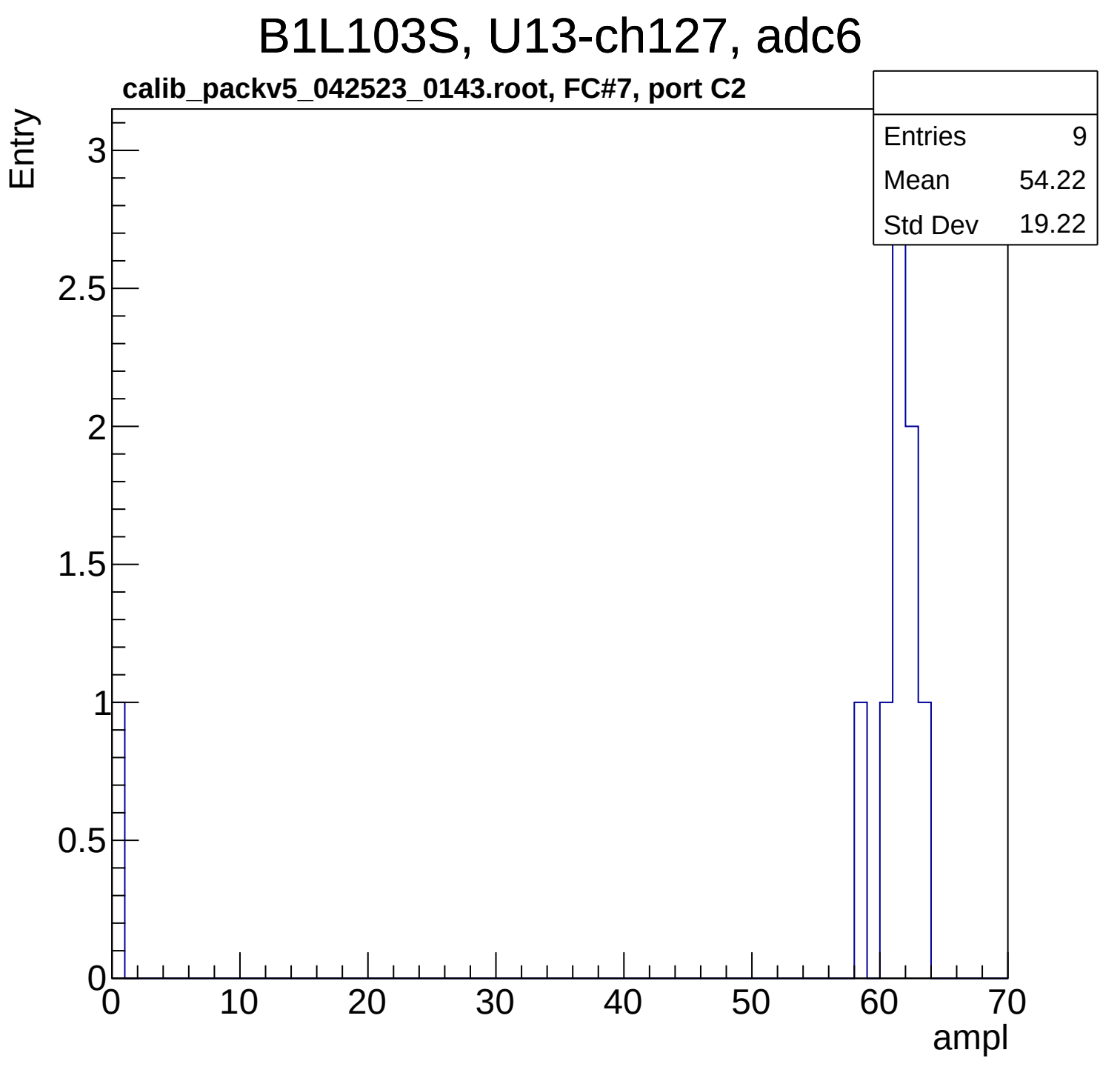
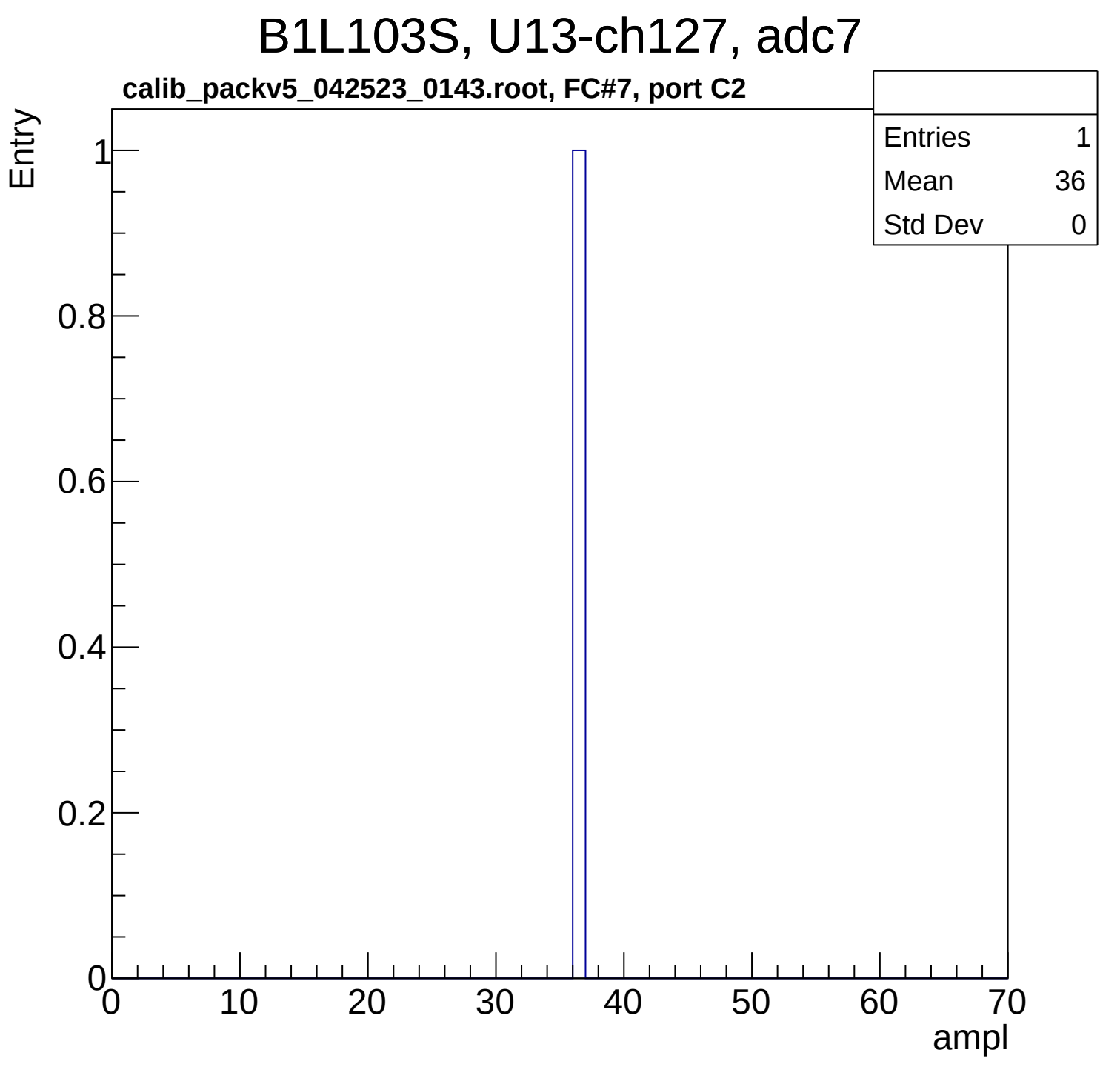
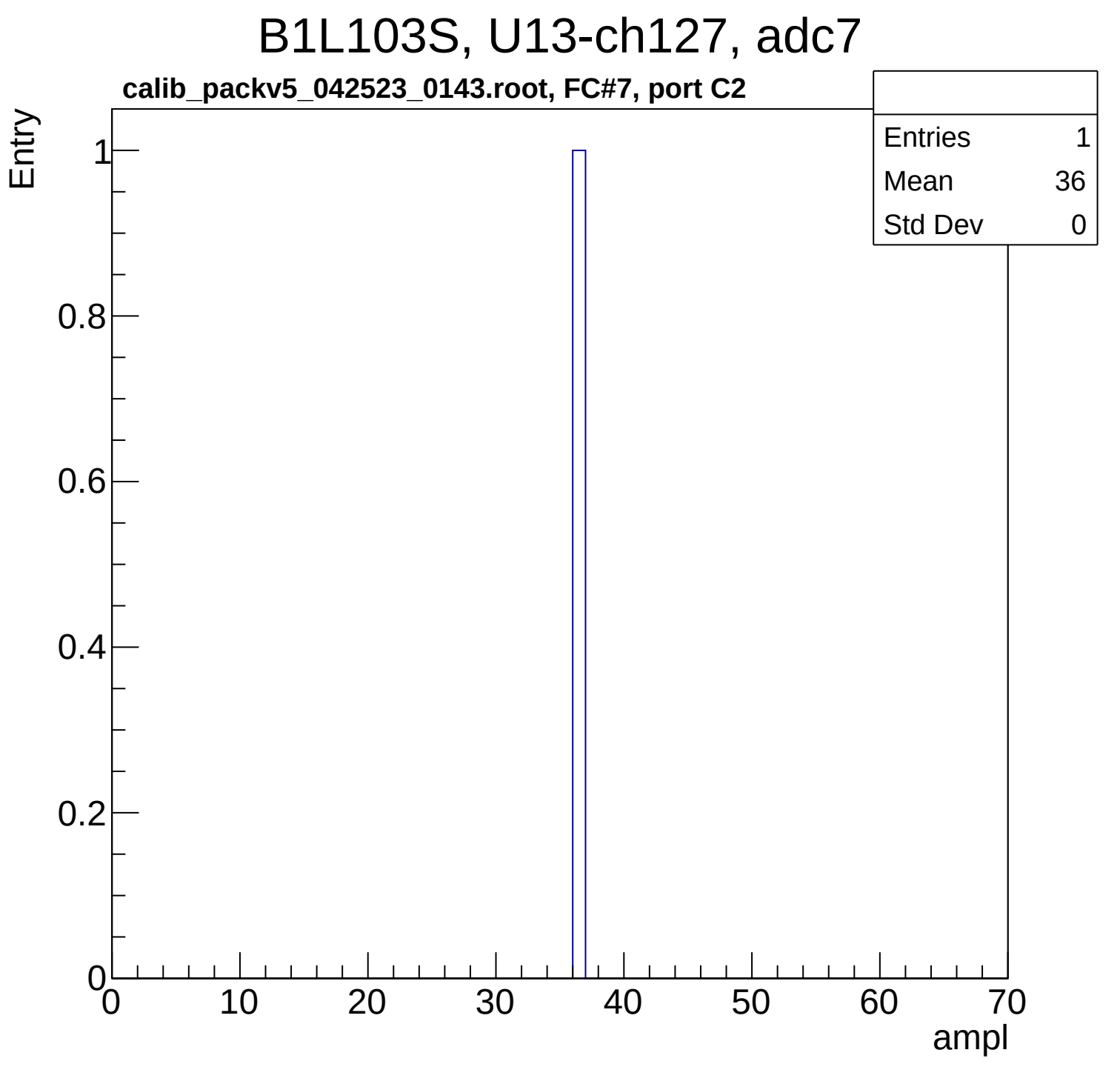

B1L103S, U13-ch127, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 55.73
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U13-ch127, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.93
Std Dev 2.653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U13-ch127, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 54.22
Std Dev 19.22
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 36
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U13-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 36
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl