

B1L003S, U23-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.16
Std Dev	11.69

Turn on : 26.5982

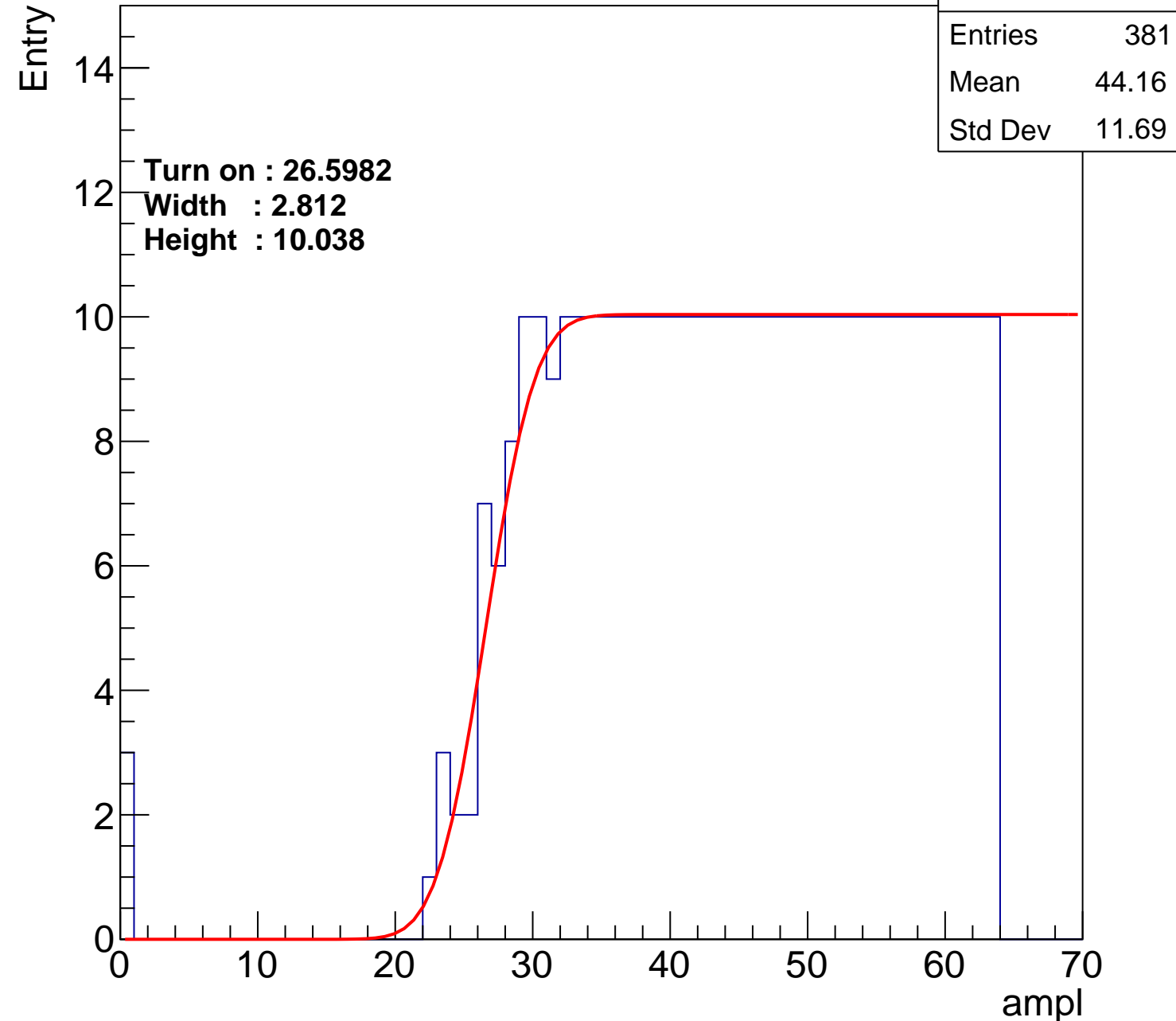
Width : 2.812

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.6880

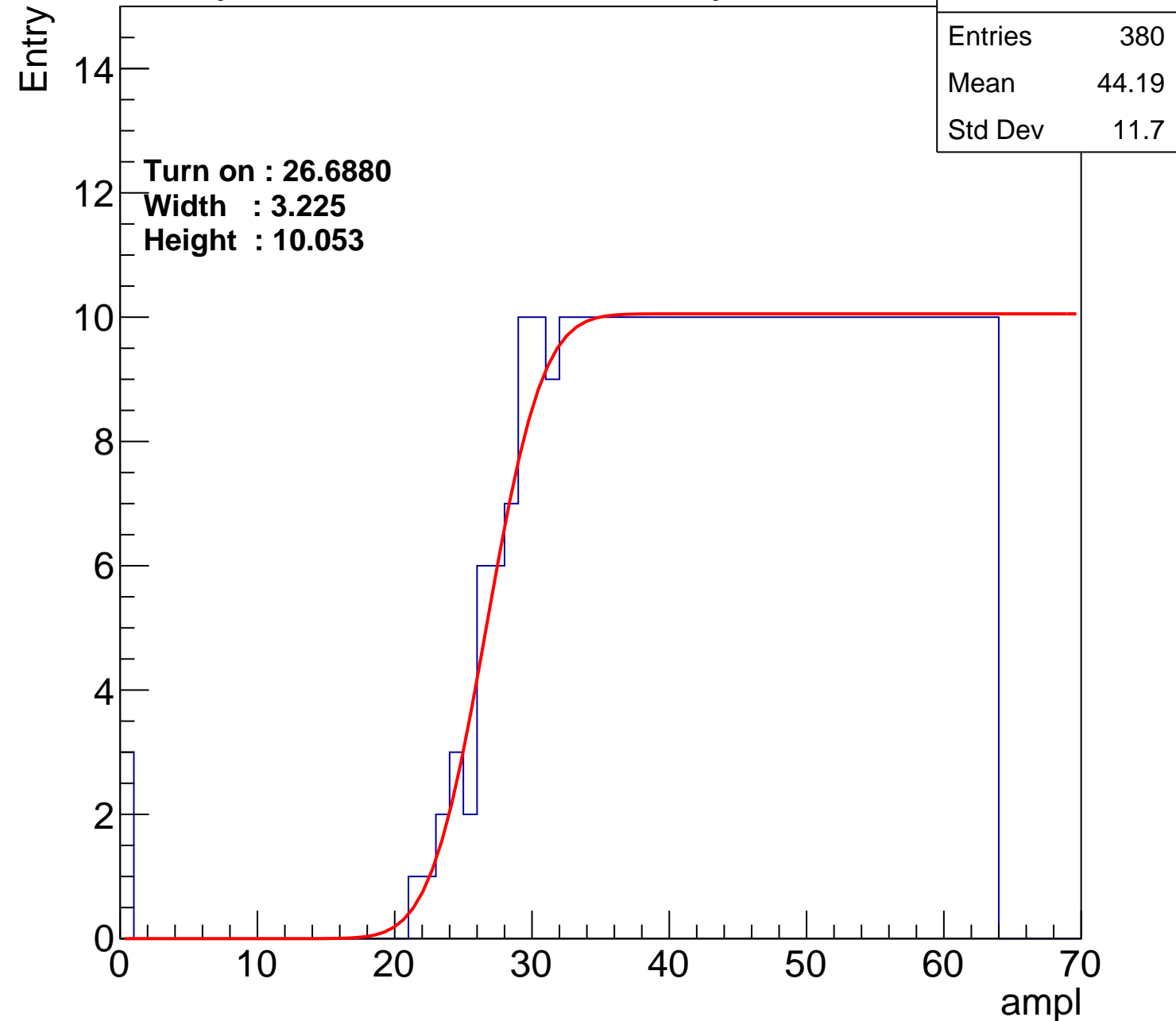
Width : 3.225

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.0511

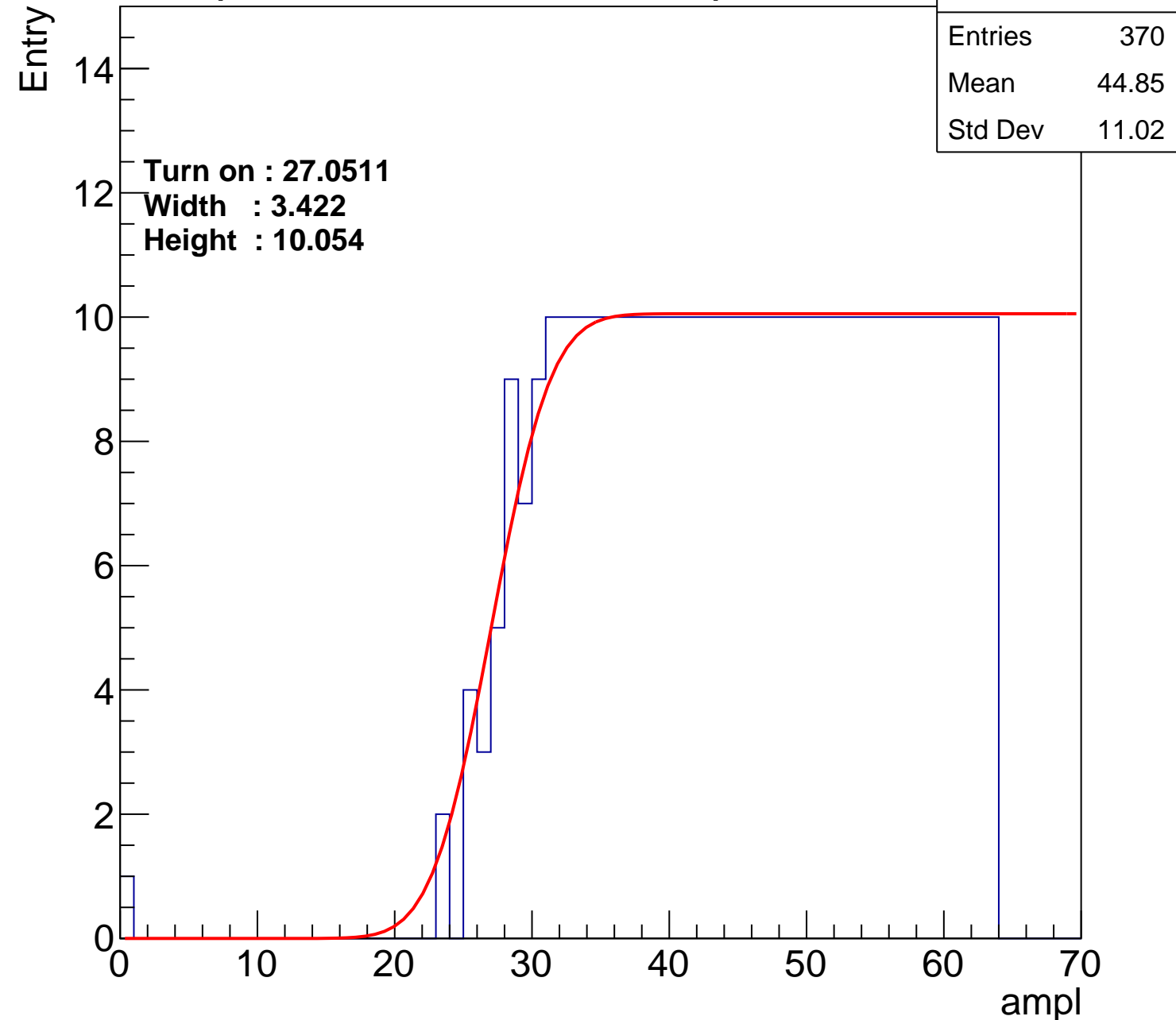
Width : 3.422

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.32
Std Dev	10.74

Turn on : 28.3926

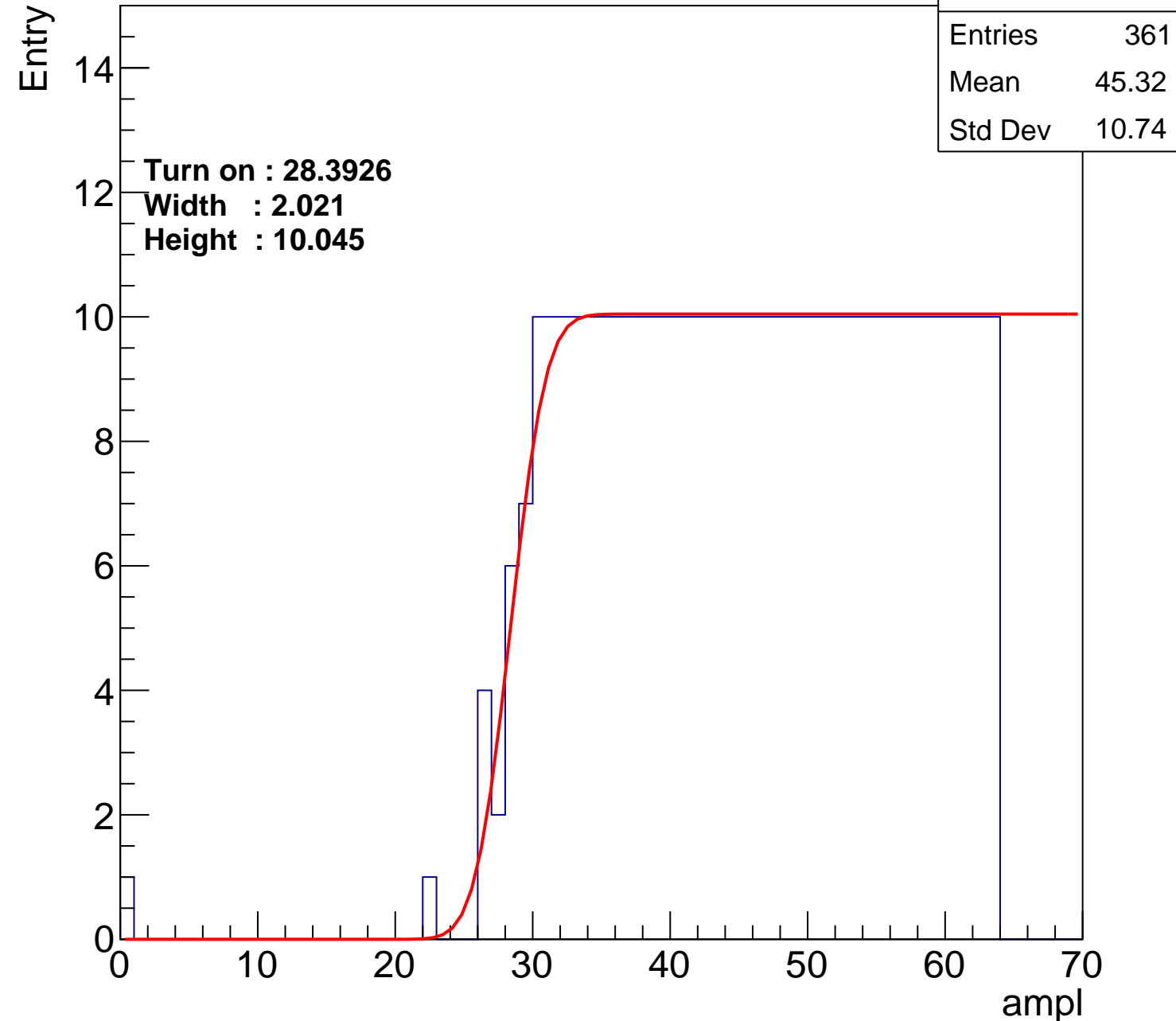
Width : 2.021

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch4

calib_packv5_042523_0143.root, FC#13, port D2

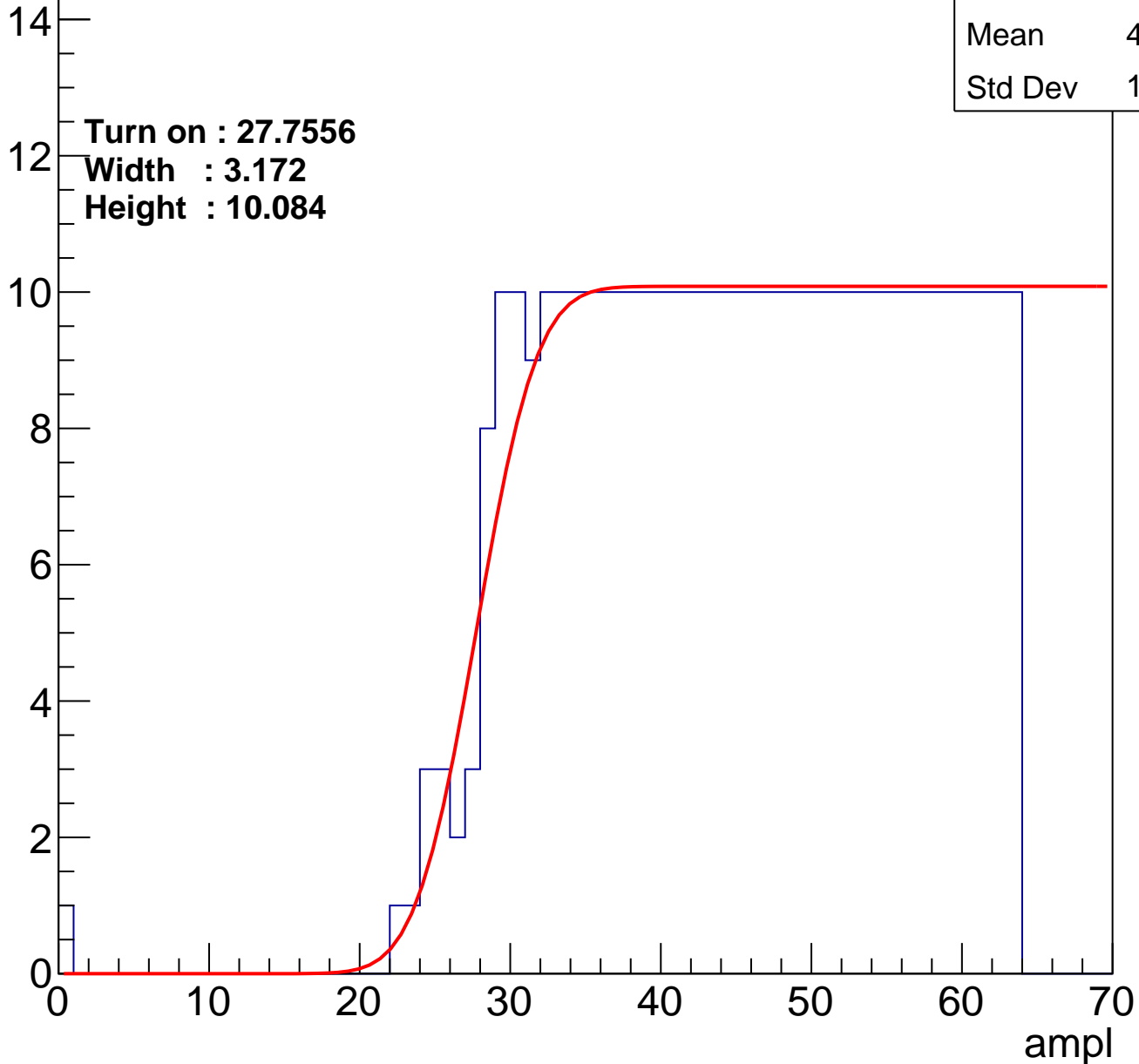
Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.7556

Width : 3.172

Height : 10.084

Entry



B1L003S, U23-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.28
Std Dev	11.79

Turn on : 26.8443

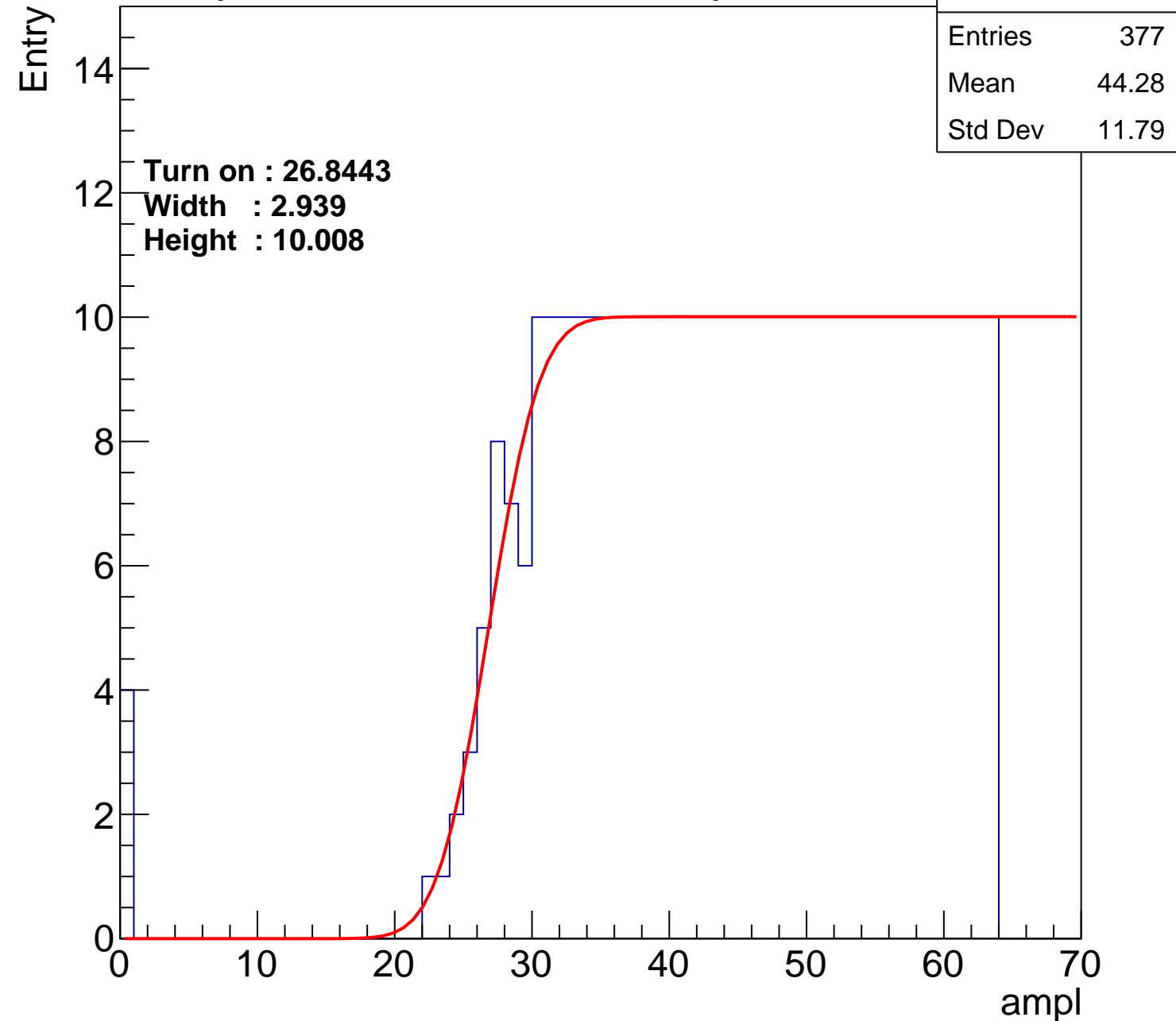
Width : 2.939

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.63
Std Dev	11.13

Turn on : 27.1276

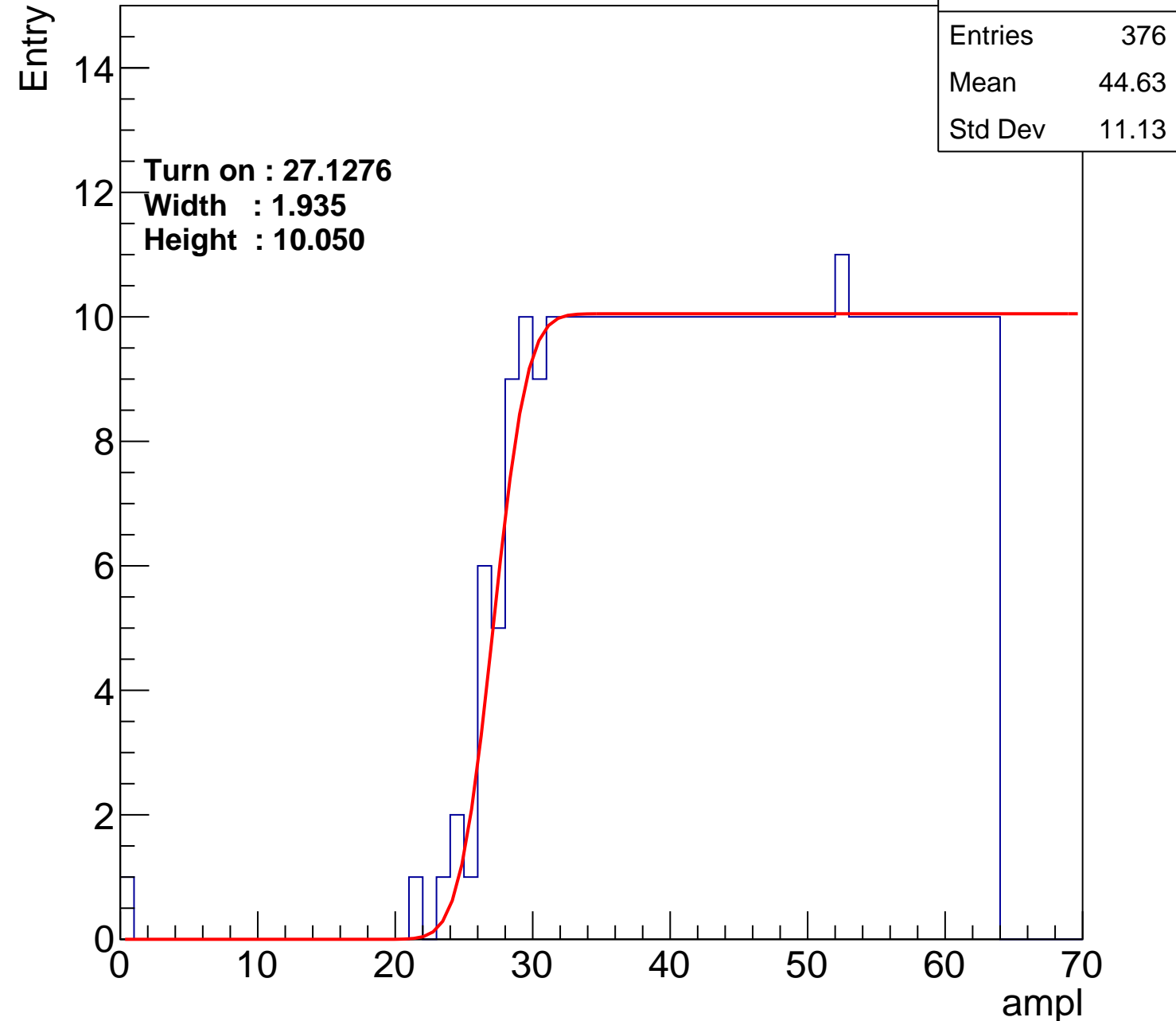
Width : 1.935

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	395
Mean	43.41
Std Dev	12.2

Turn on : 24.4702

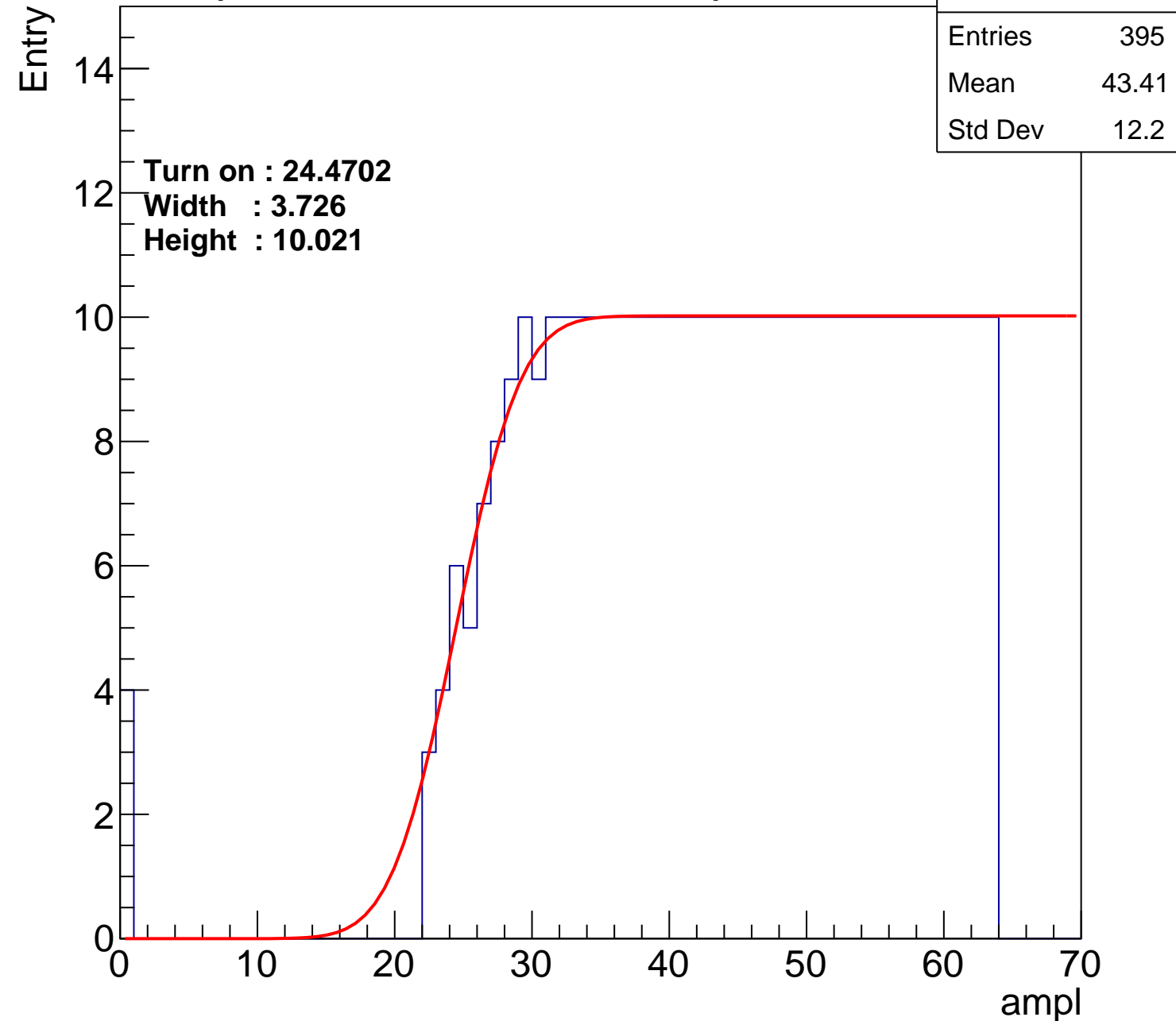
Width : 3.726

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.9057

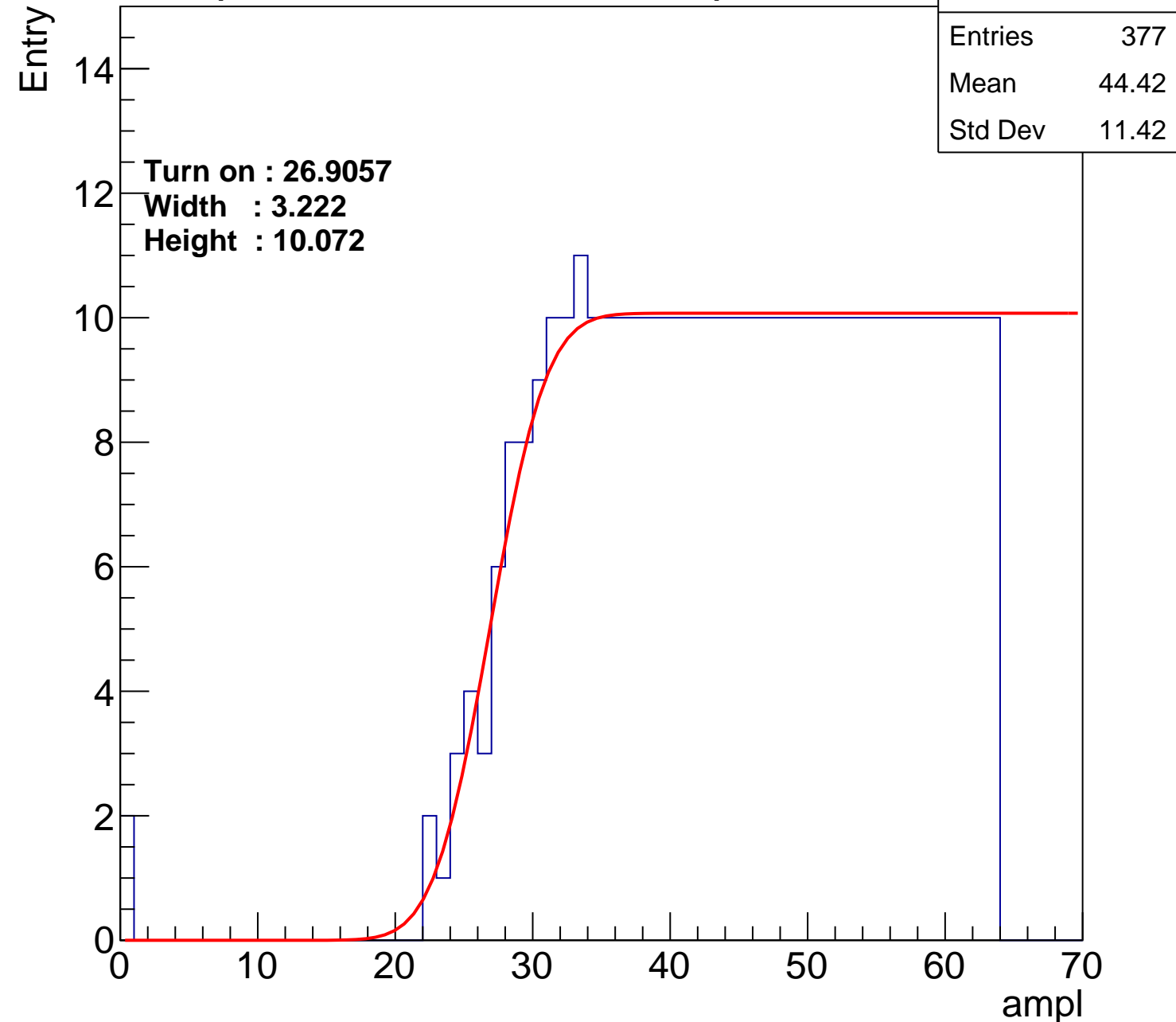
Width : 3.222

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.8792

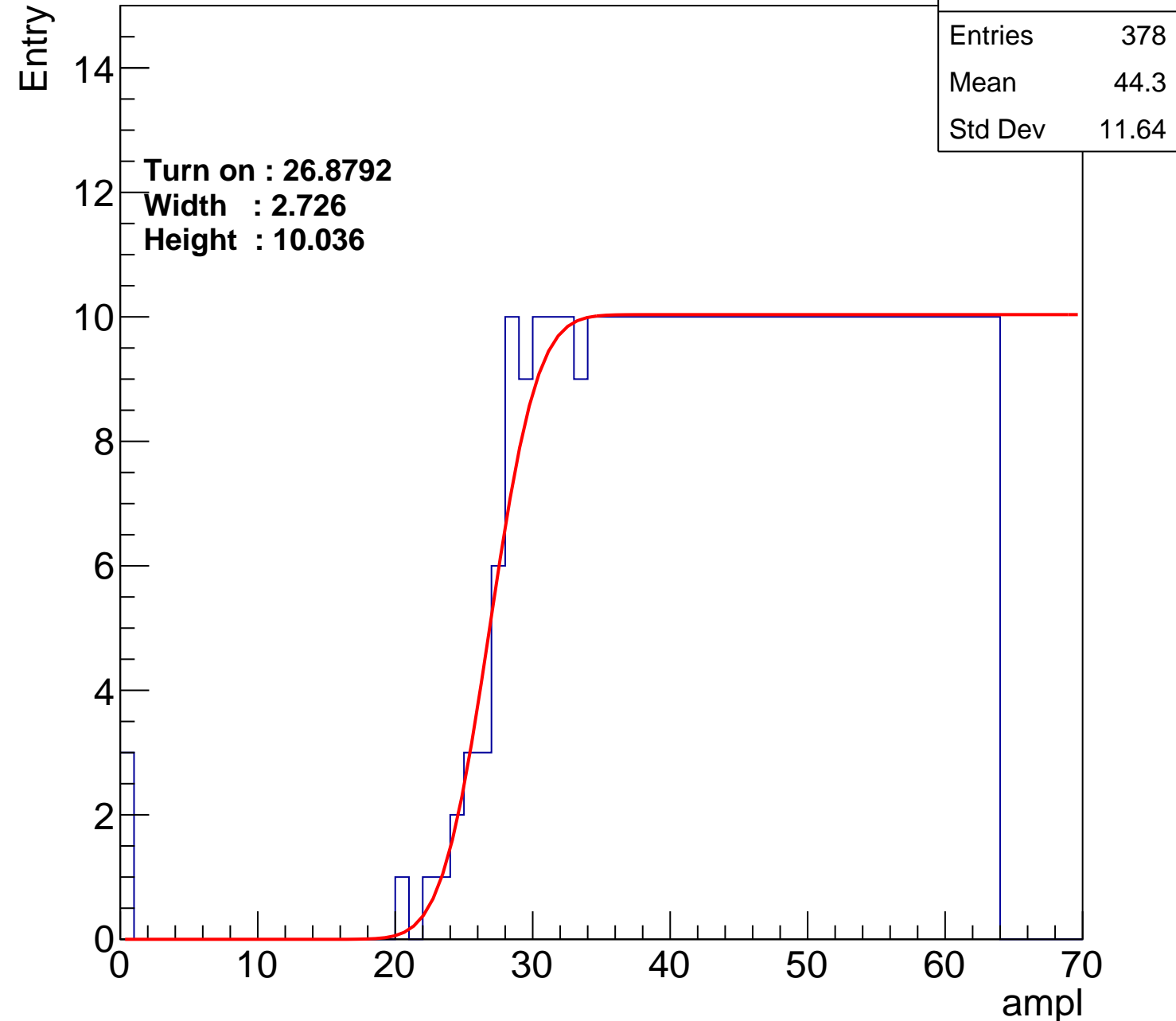
Width : 2.726

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch10

calib_packv5_042523_0143.root, FC#13, port D2

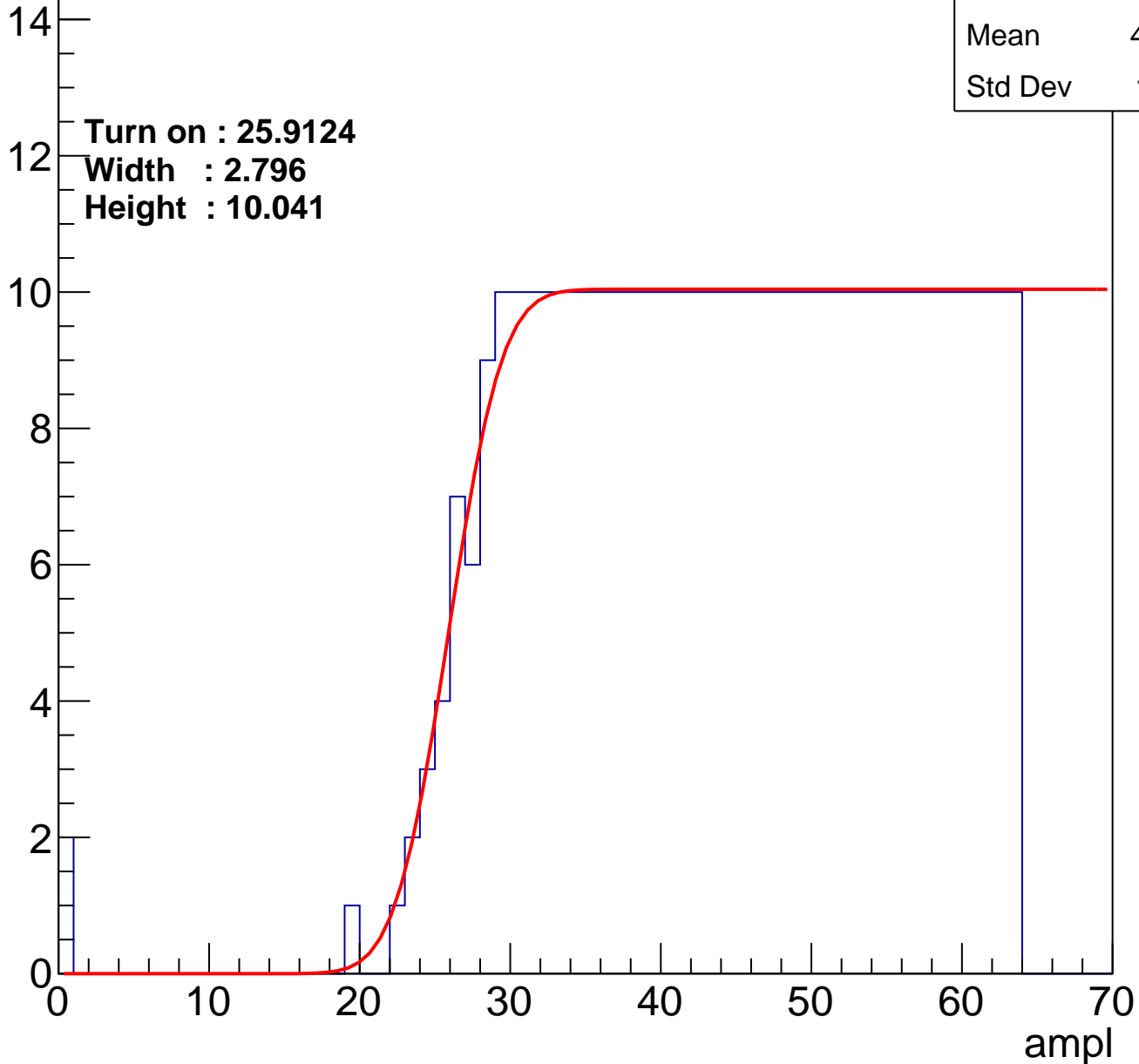
Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.9124

Width : 2.796

Height : 10.041

Entry



B1L003S, U23-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 27.7265

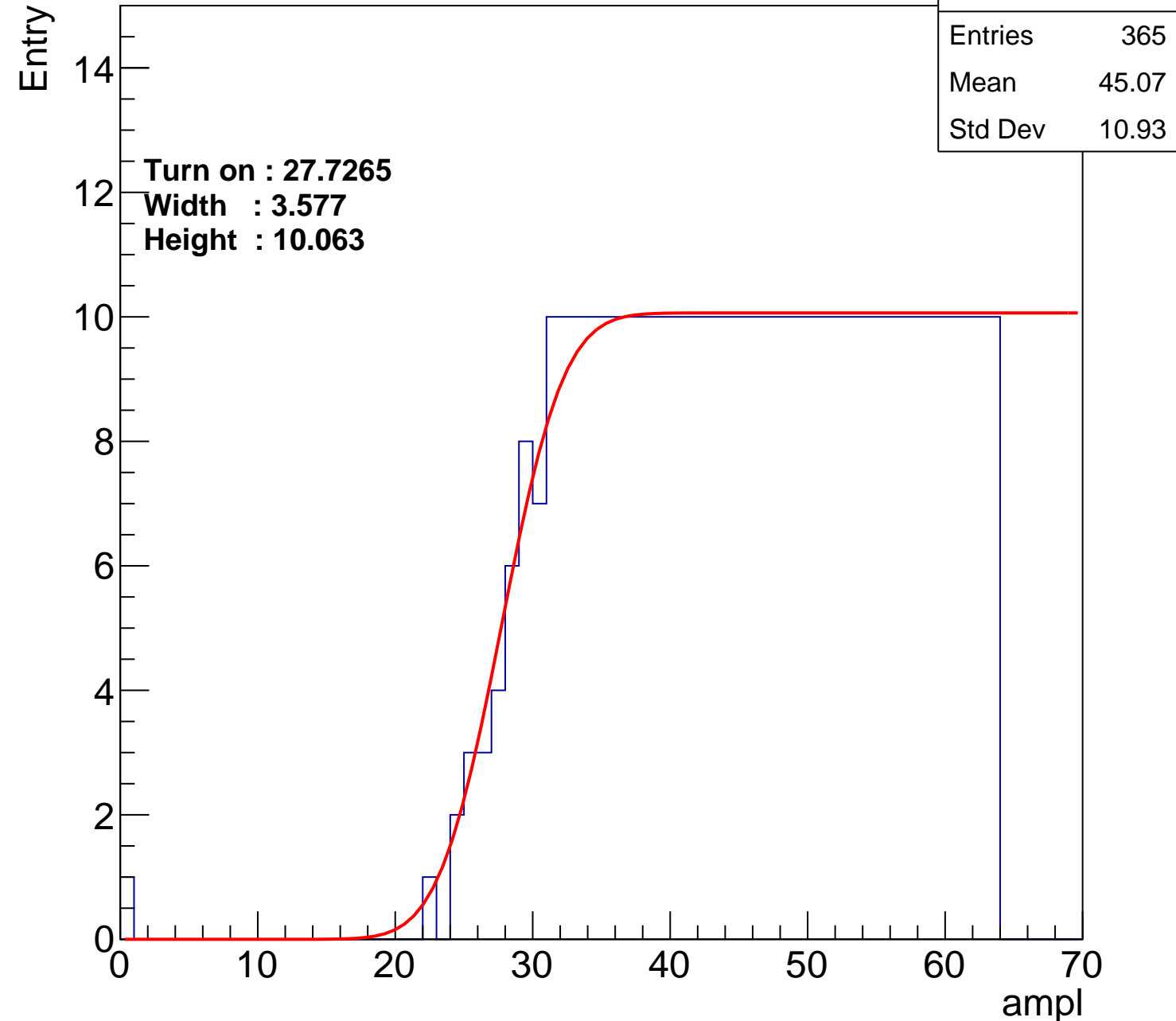
Width : 3.577

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	45.04
Std Dev	10.87

Turn on : 27.2960

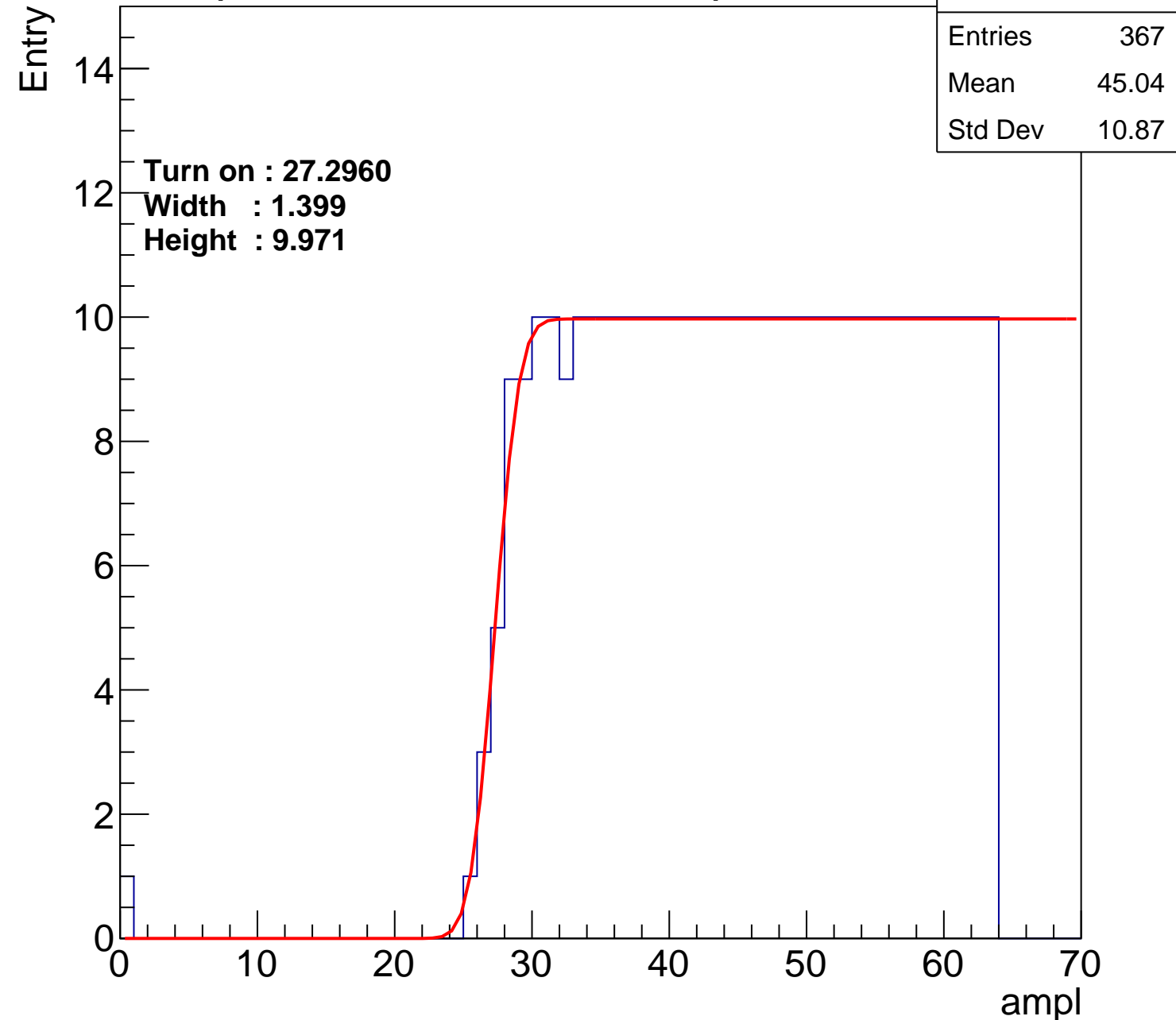
Width : 1.399

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch13

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.79
Std Dev	10.56

Turn on : 29.2001

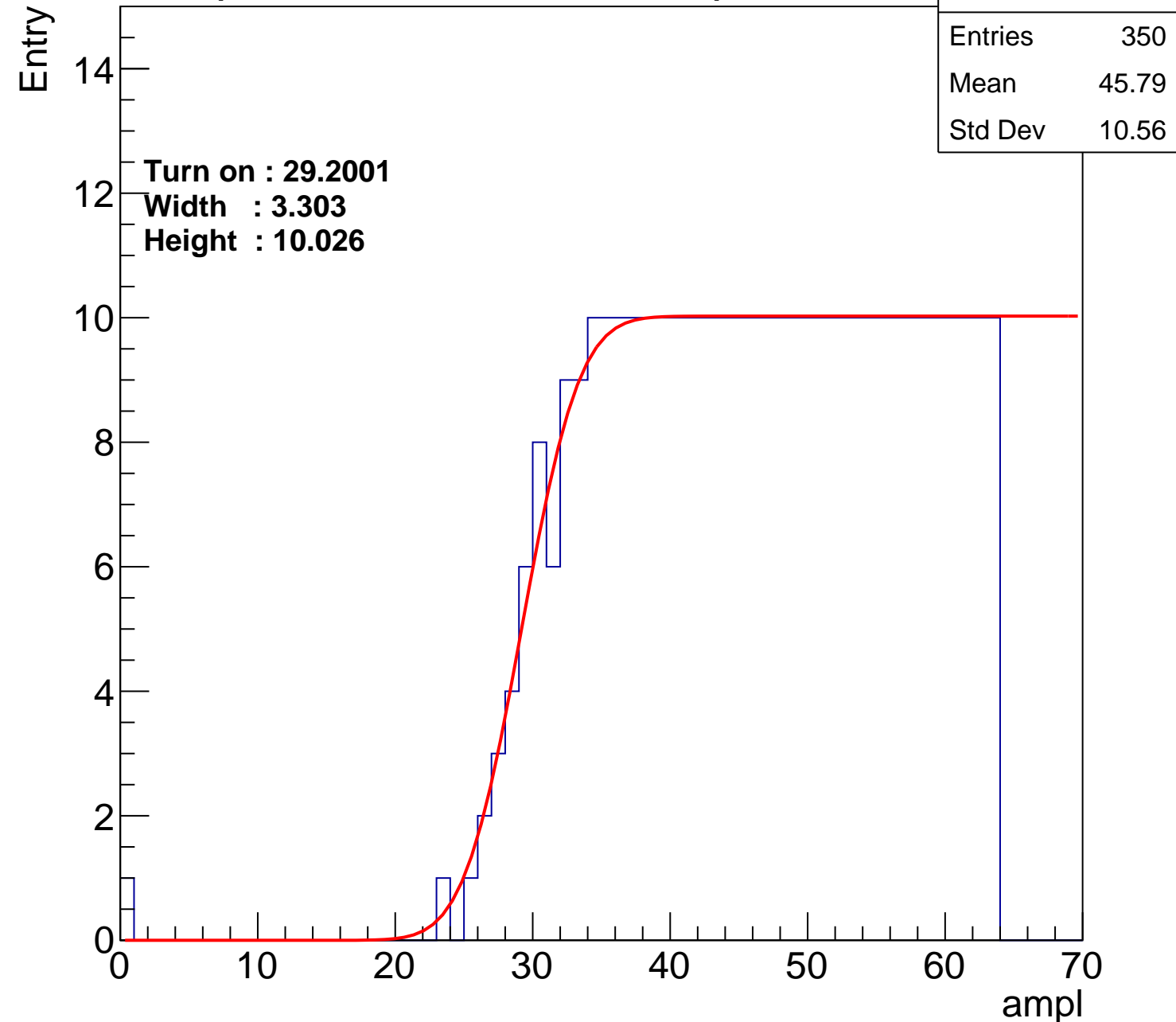
Width : 3.303

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.88
Std Dev	11.71

Turn on : 25.8325

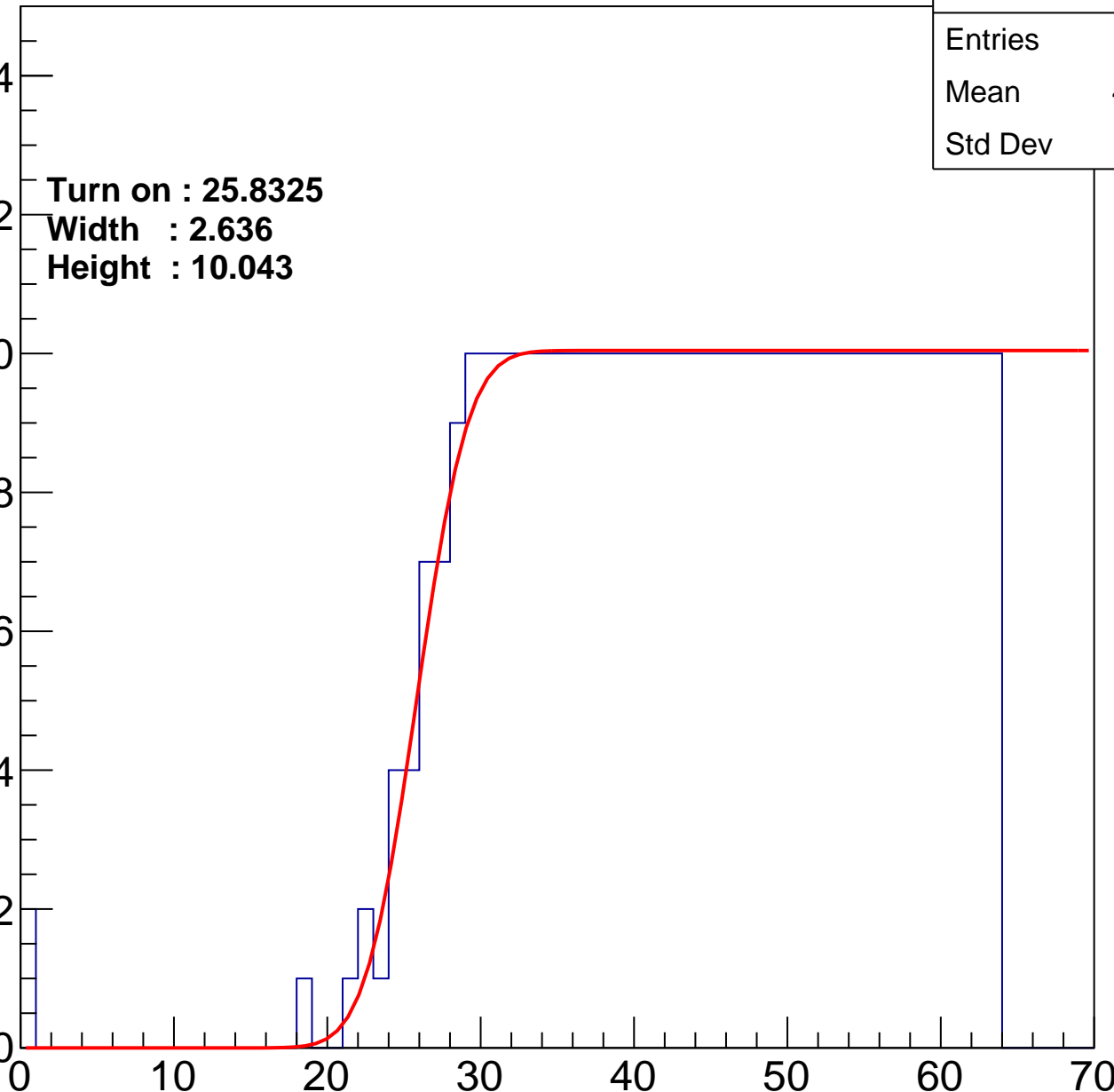
Width : 2.636

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.16
Std Dev	10.83

Turn on : 28.1360

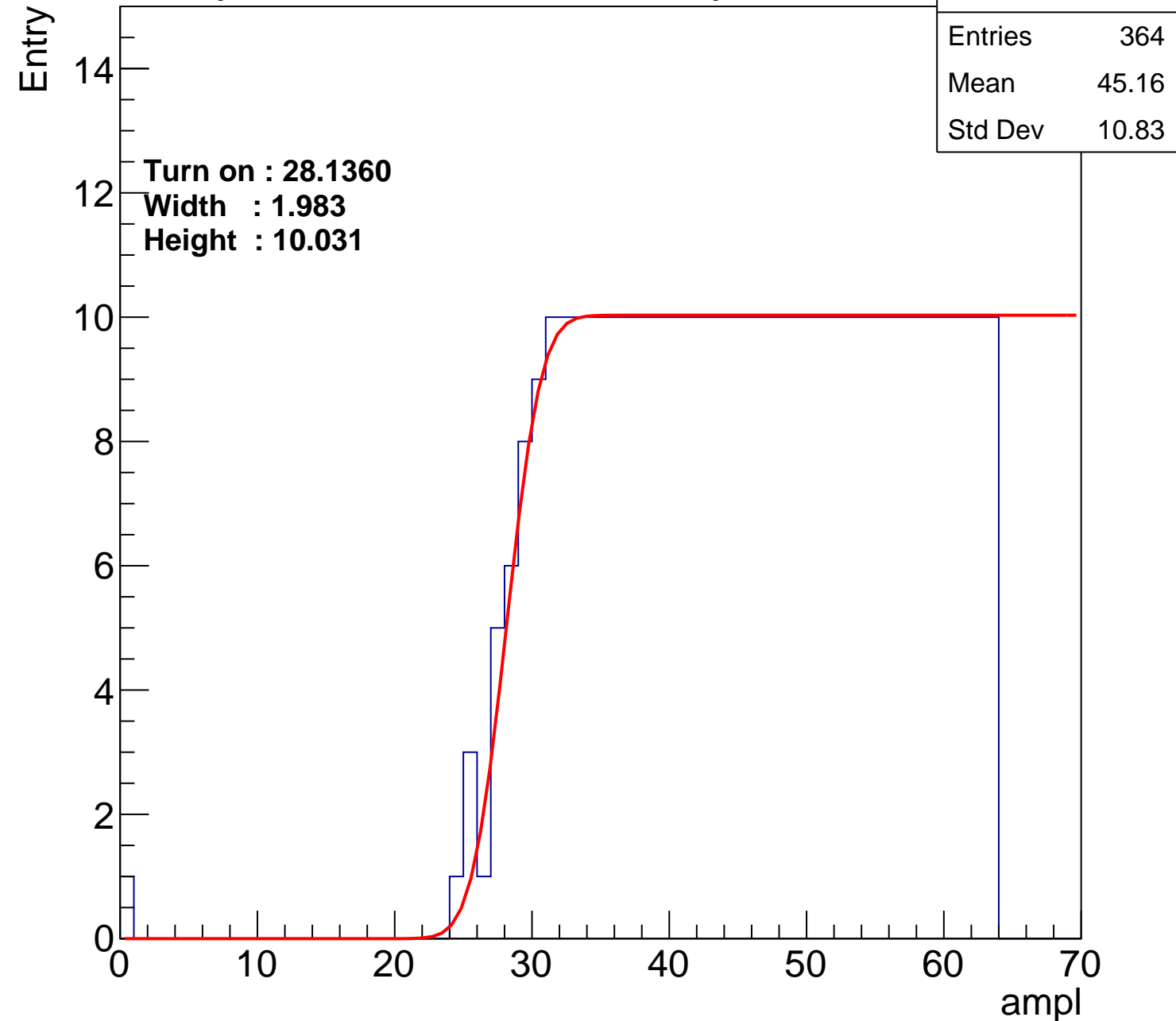
Width : 1.983

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.61
Std Dev	11.13

Turn on : 26.6145

Width : 1.976

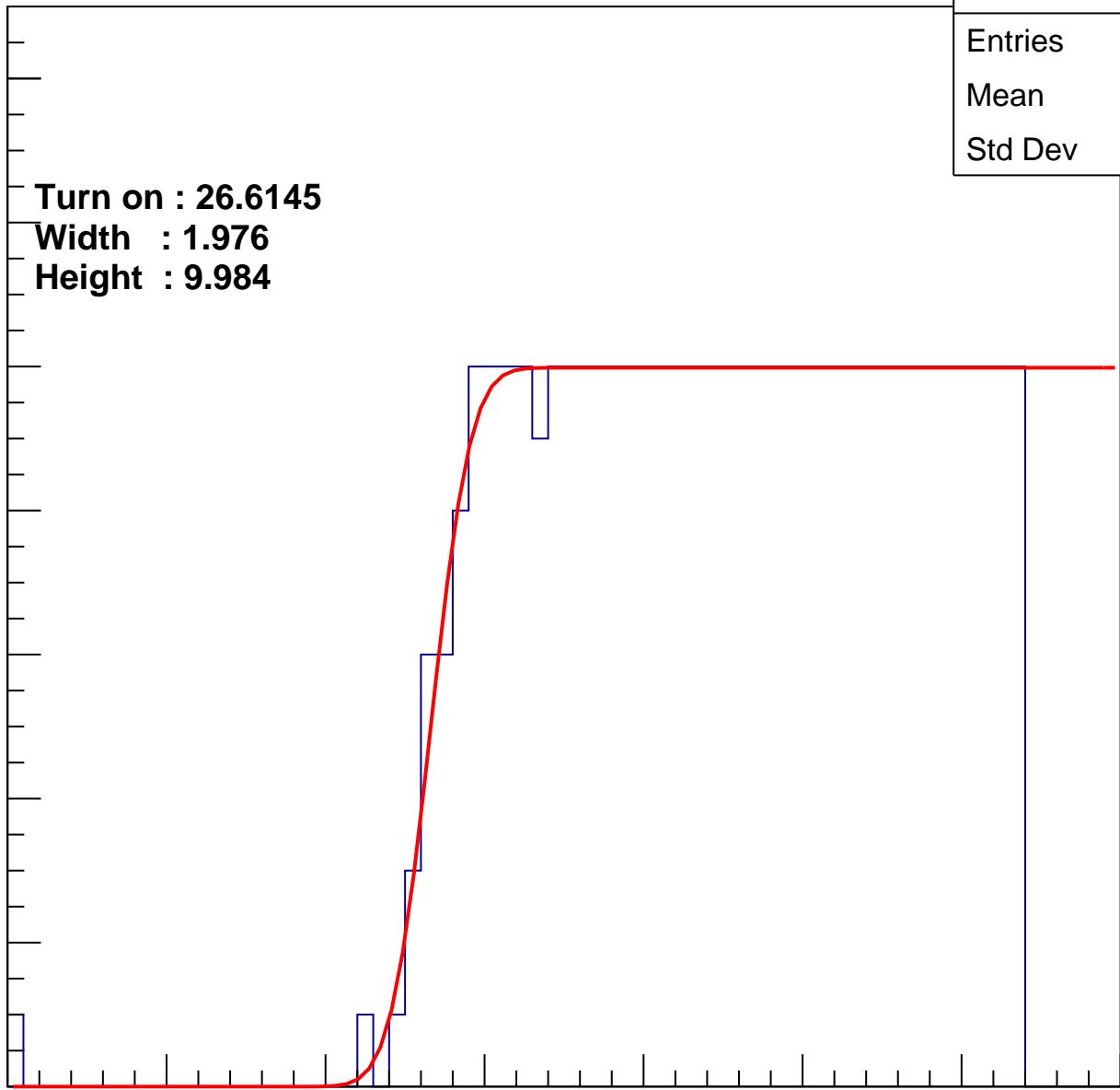
Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U23-ch17

calib_packv5_042523_0143.root, FC#13, port D2

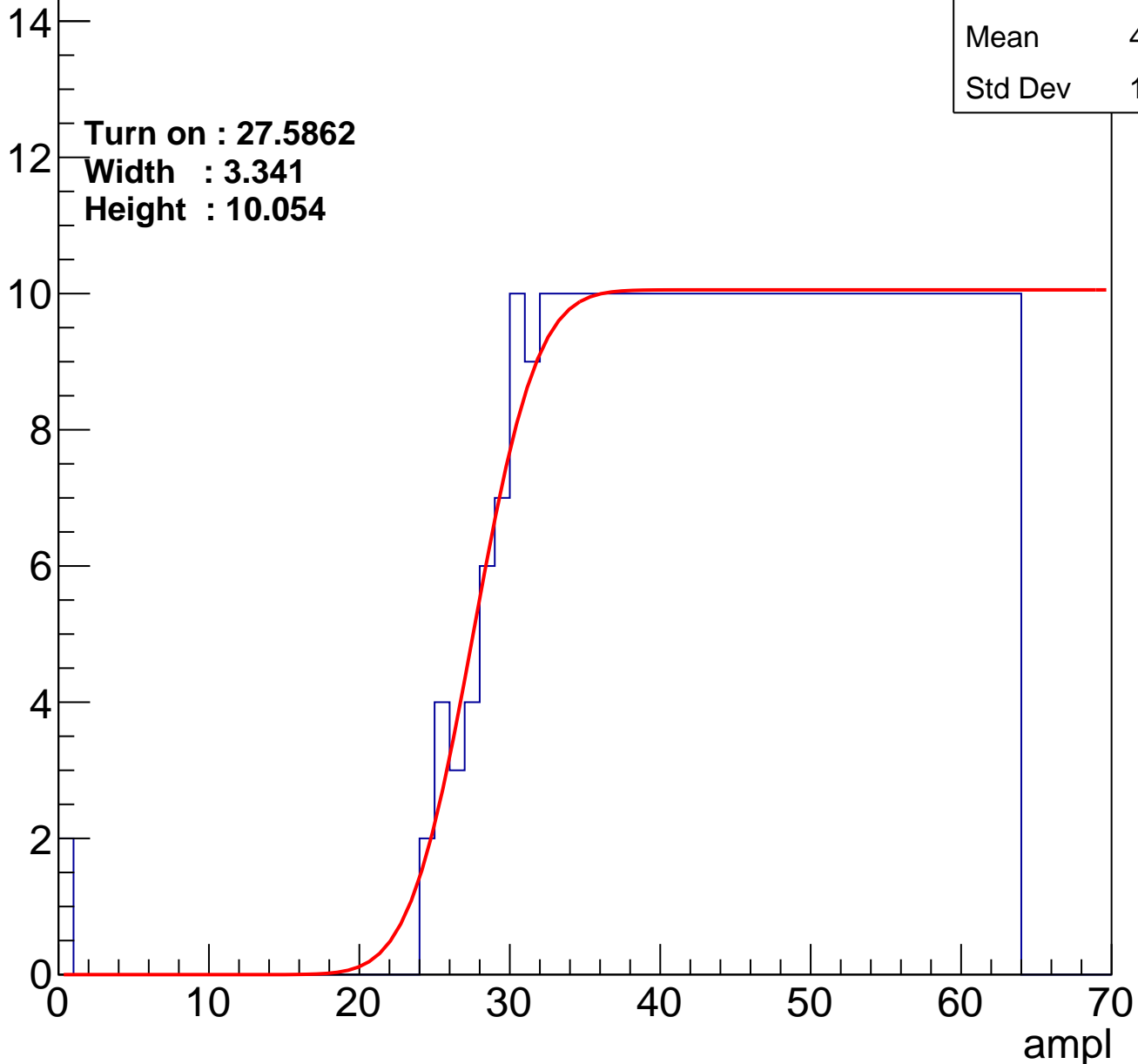
Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.5862

Width : 3.341

Height : 10.054

Entry



B1L003S, U23-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9060

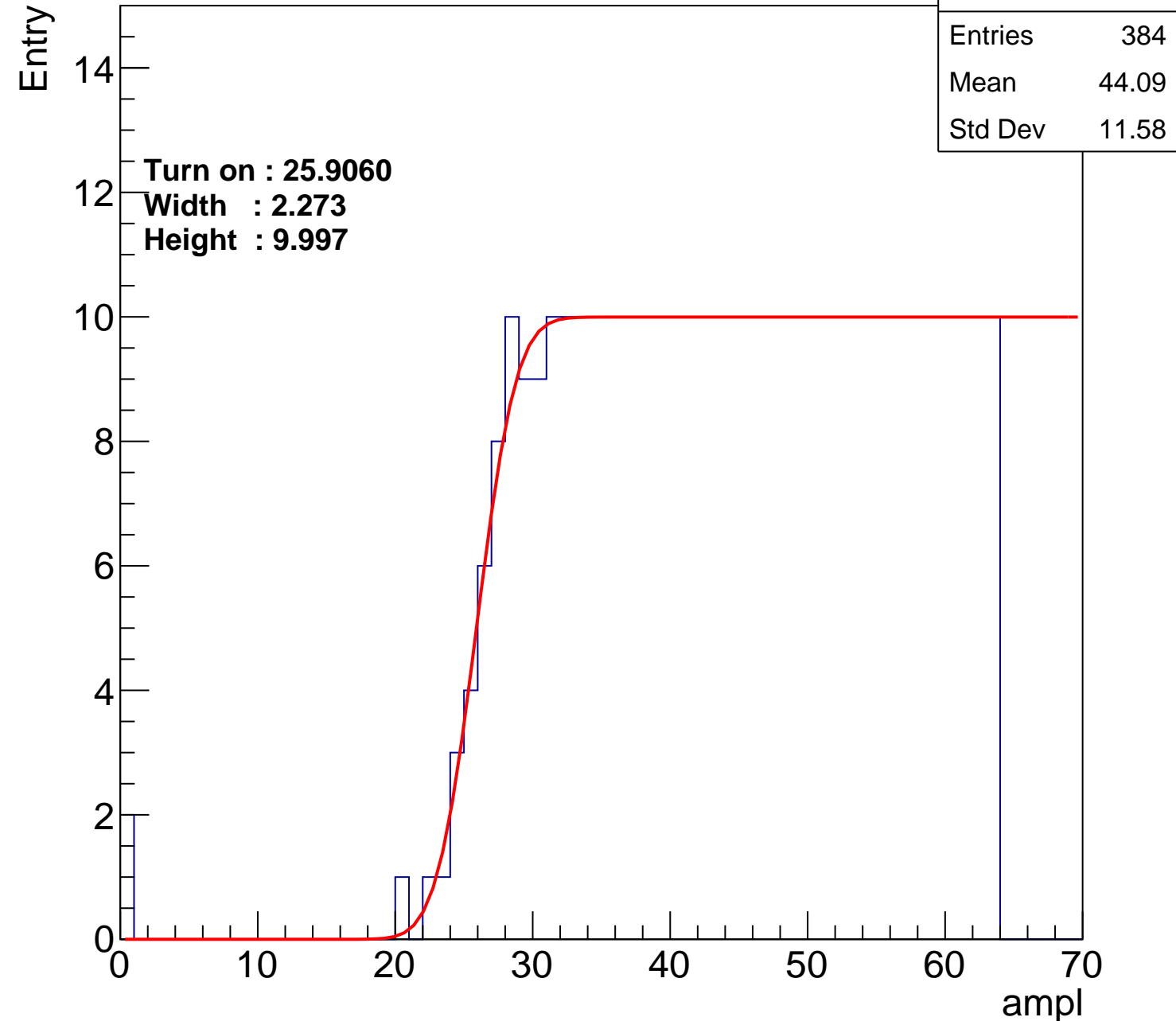
Width : 2.273

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch19

calib_packv5_042523_0143.root, FC#13, port D2

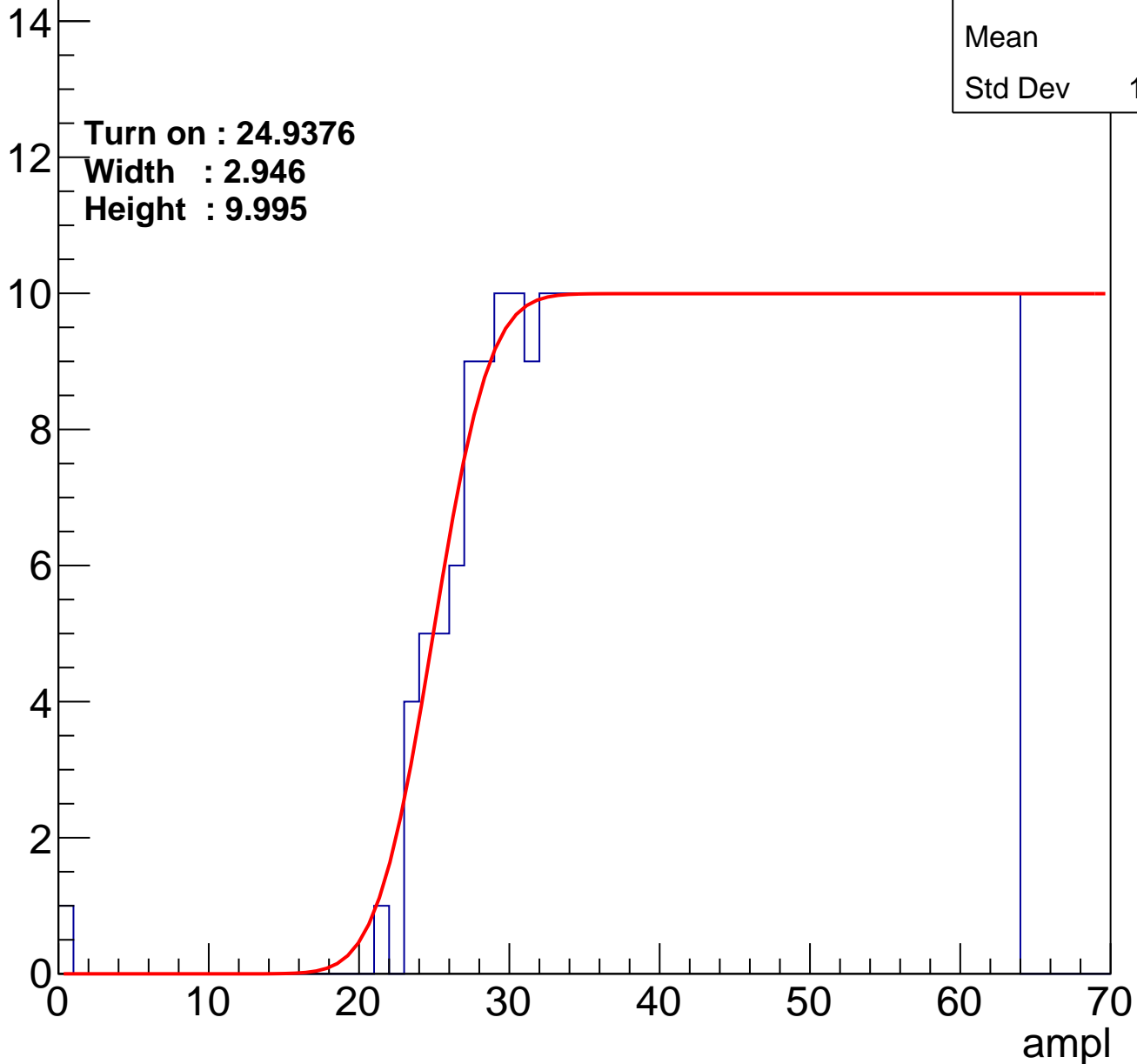
Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 24.9376

Width : 2.946

Height : 9.995

Entry



B1L003S, U23-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.27
Std Dev	11.55

Turn on : 26.5383

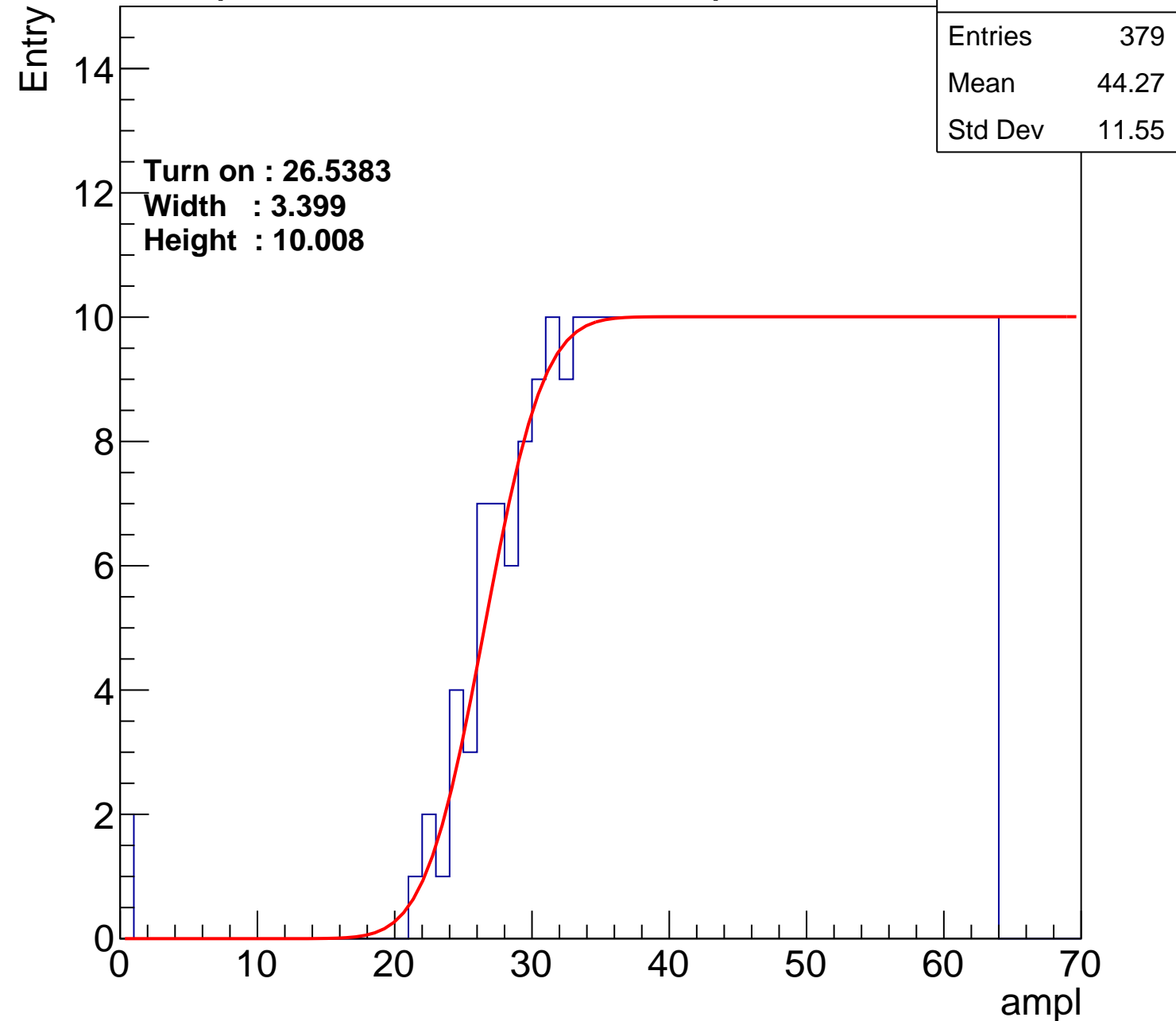
Width : 3.399

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.17
Std Dev	11.81

Turn on : 26.9745

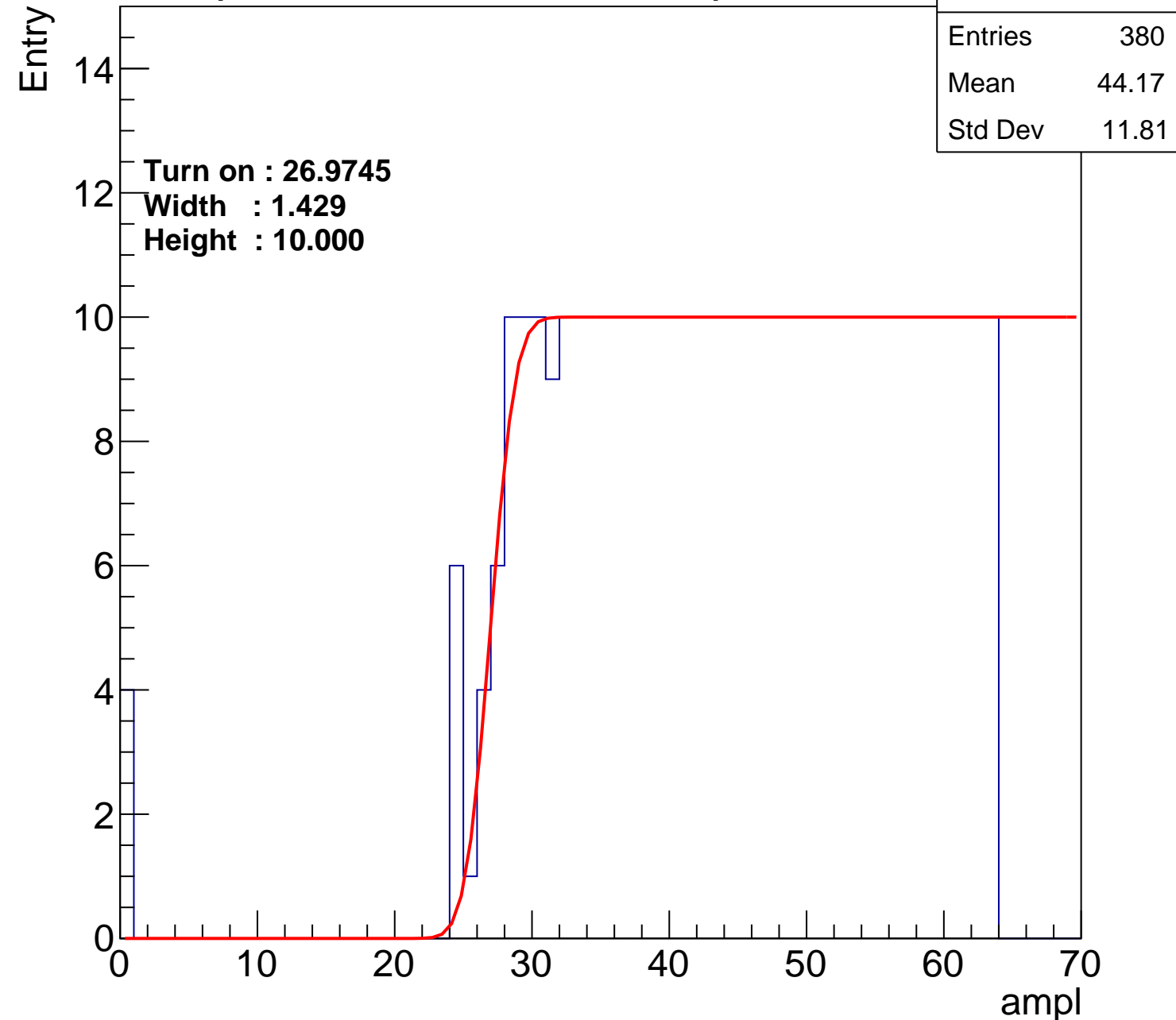
Width : 1.429

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch22

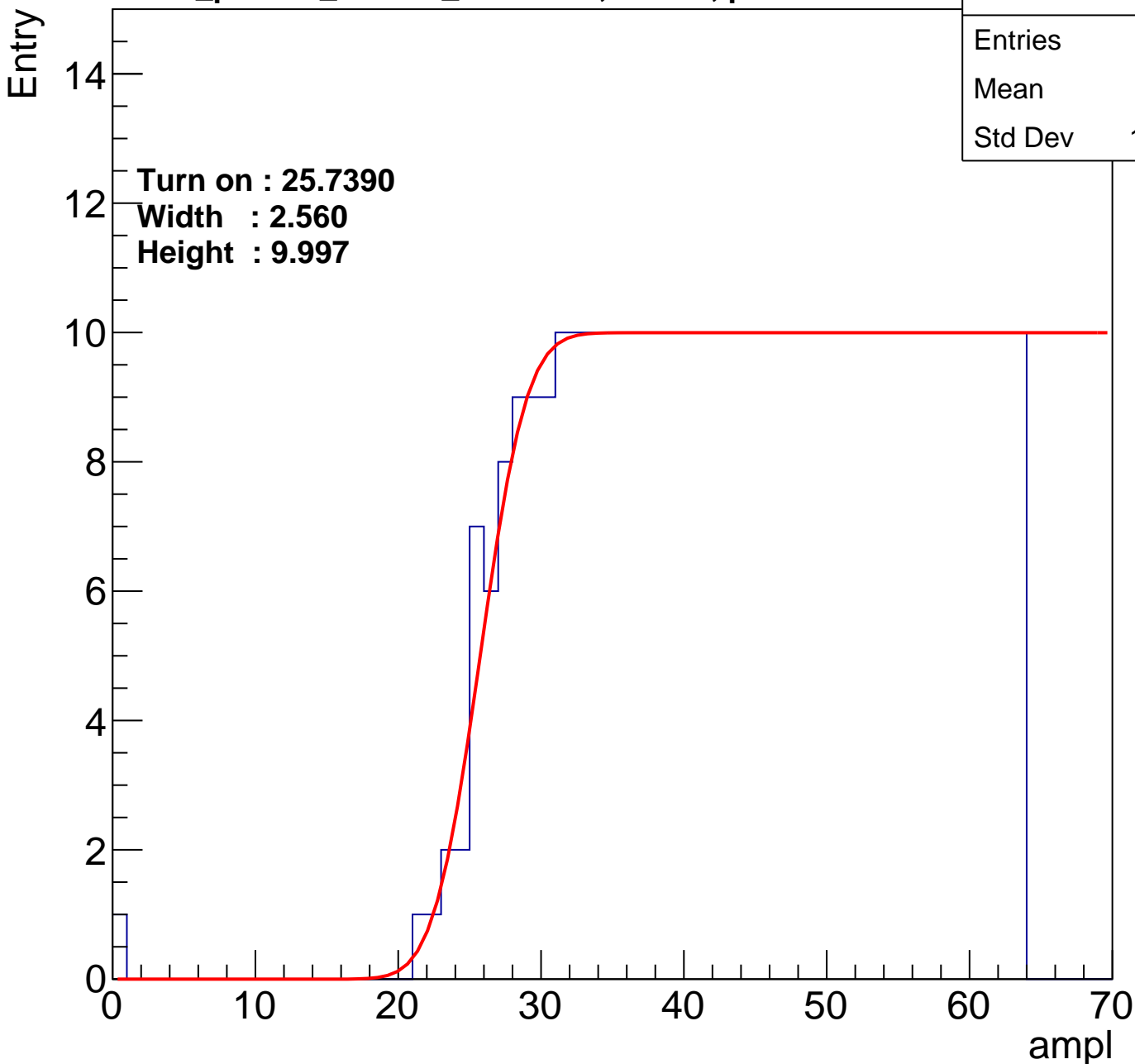
calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.7390

Width : 2.560

Height : 9.997



B1L003S, U23-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 27.0921

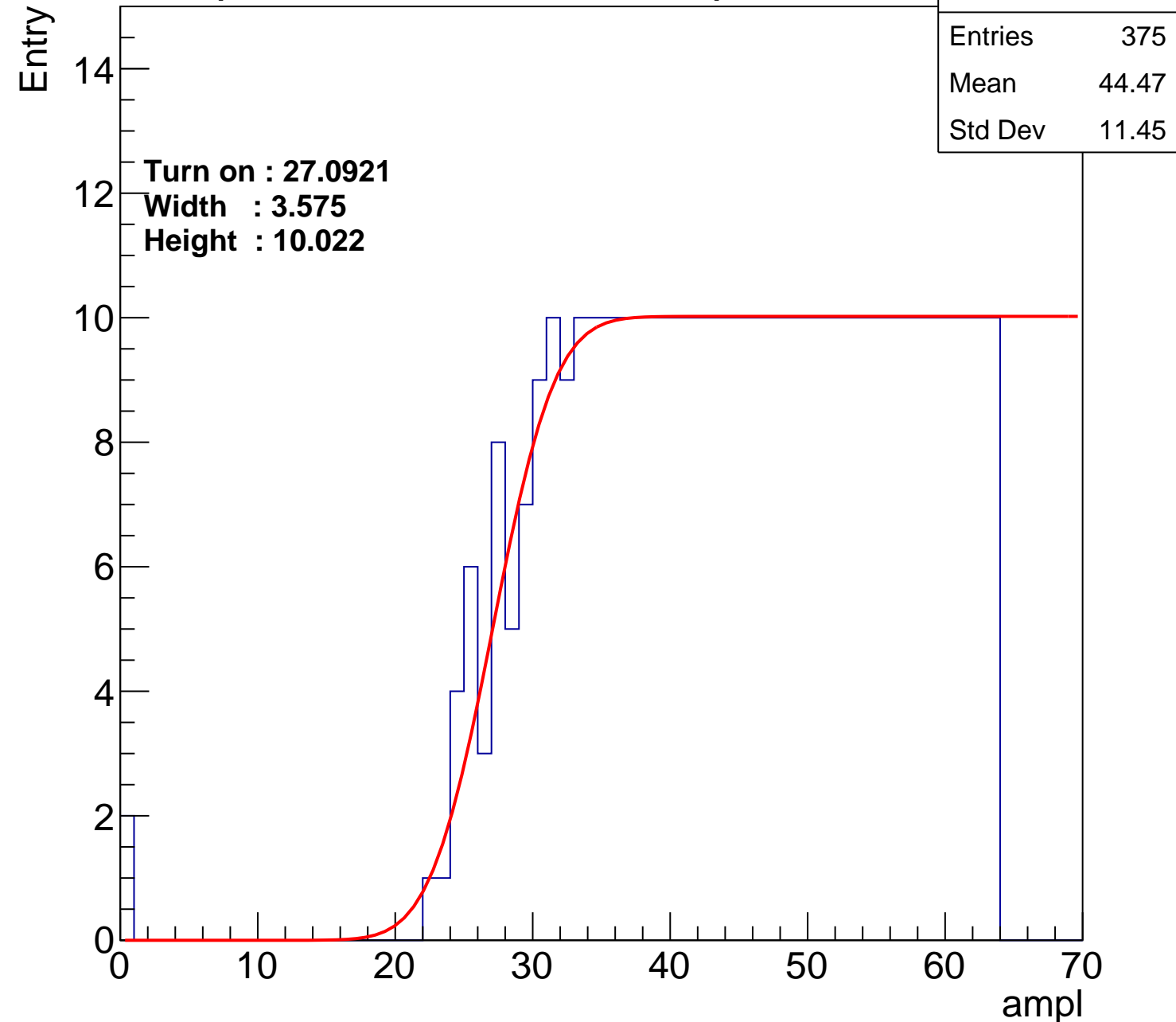
Width : 3.575

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.67
Std Dev	11.48

Turn on : 27.2453

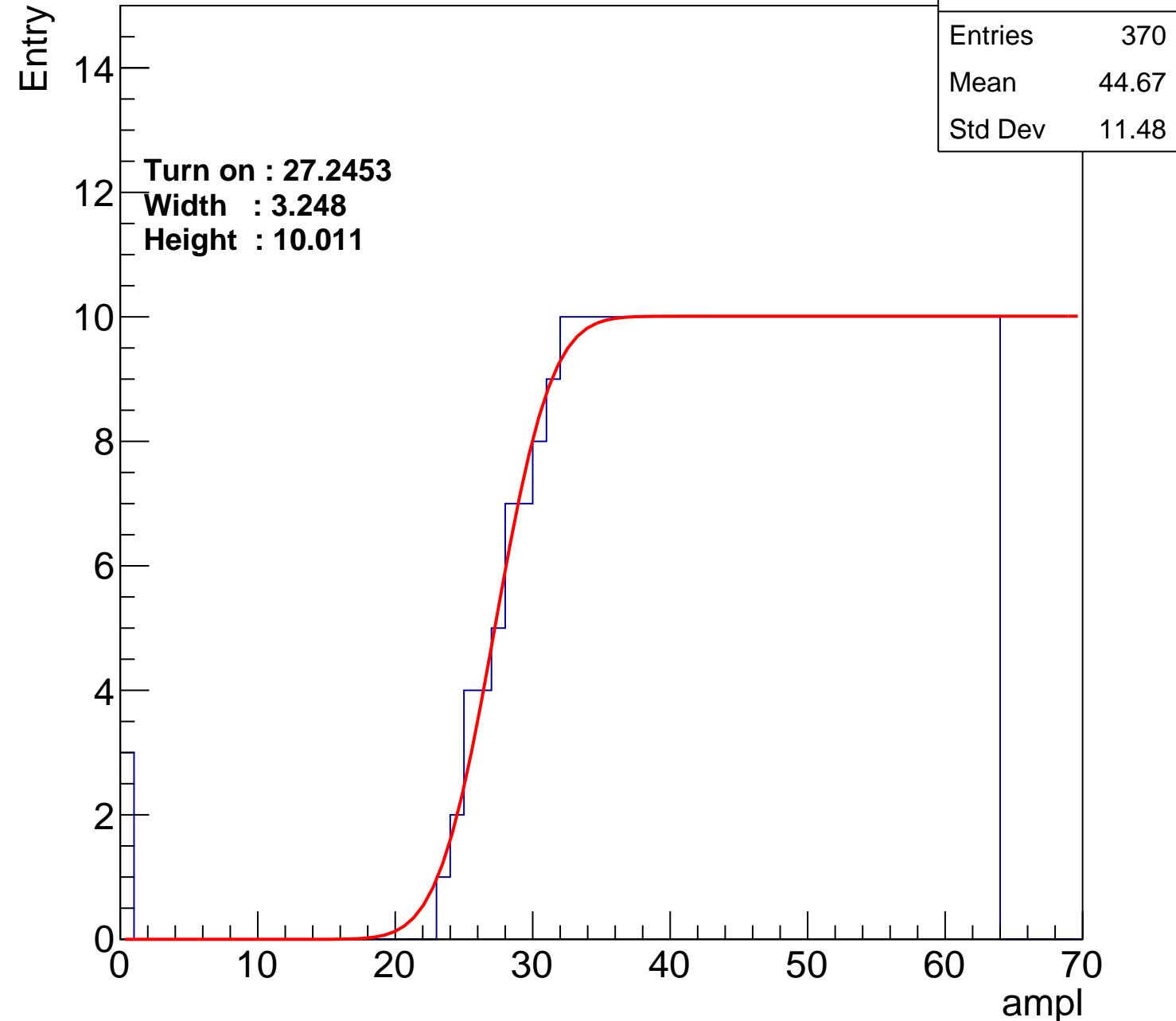
Width : 3.248

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch25

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 28.5073

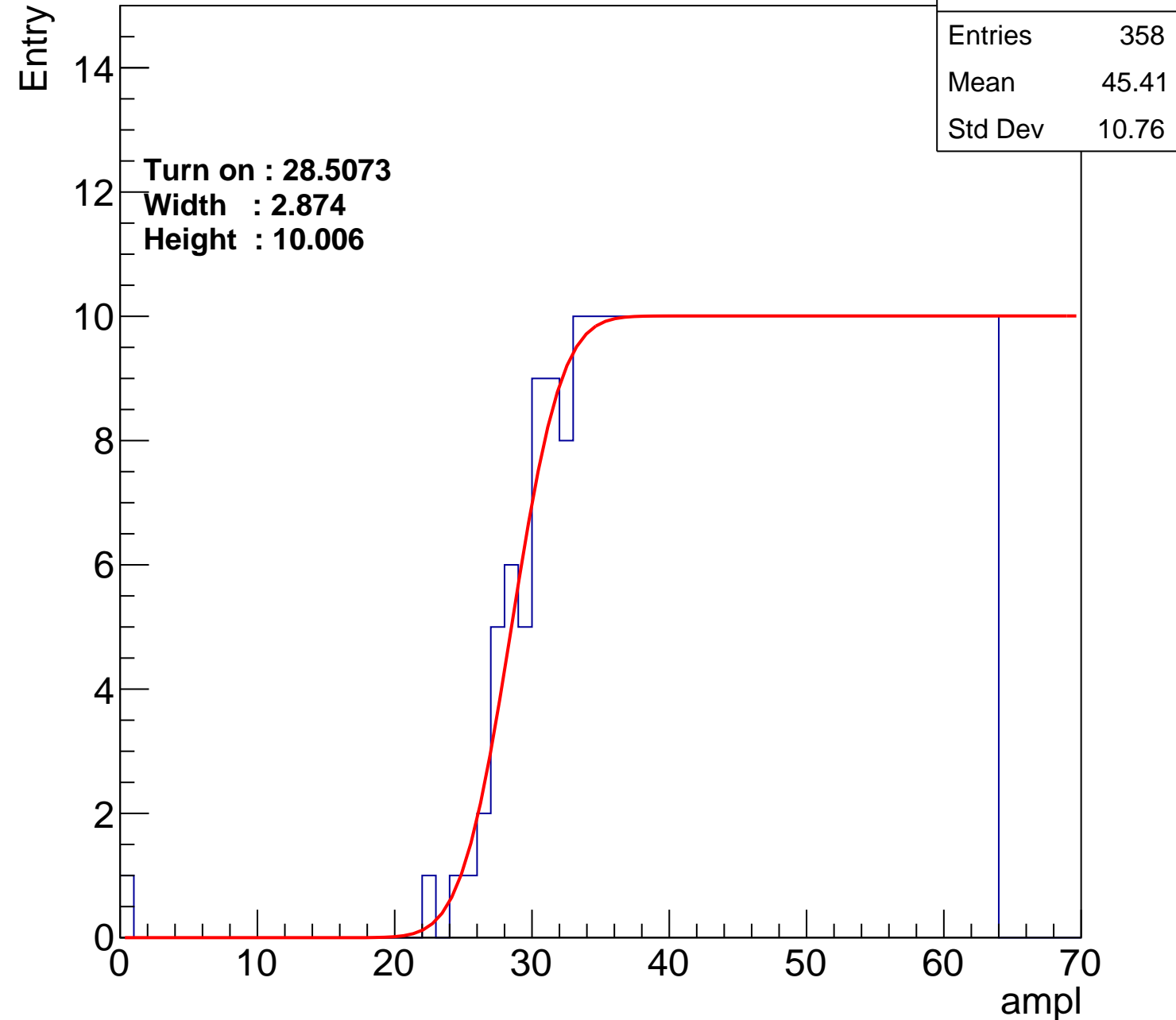
Width : 2.874

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch26

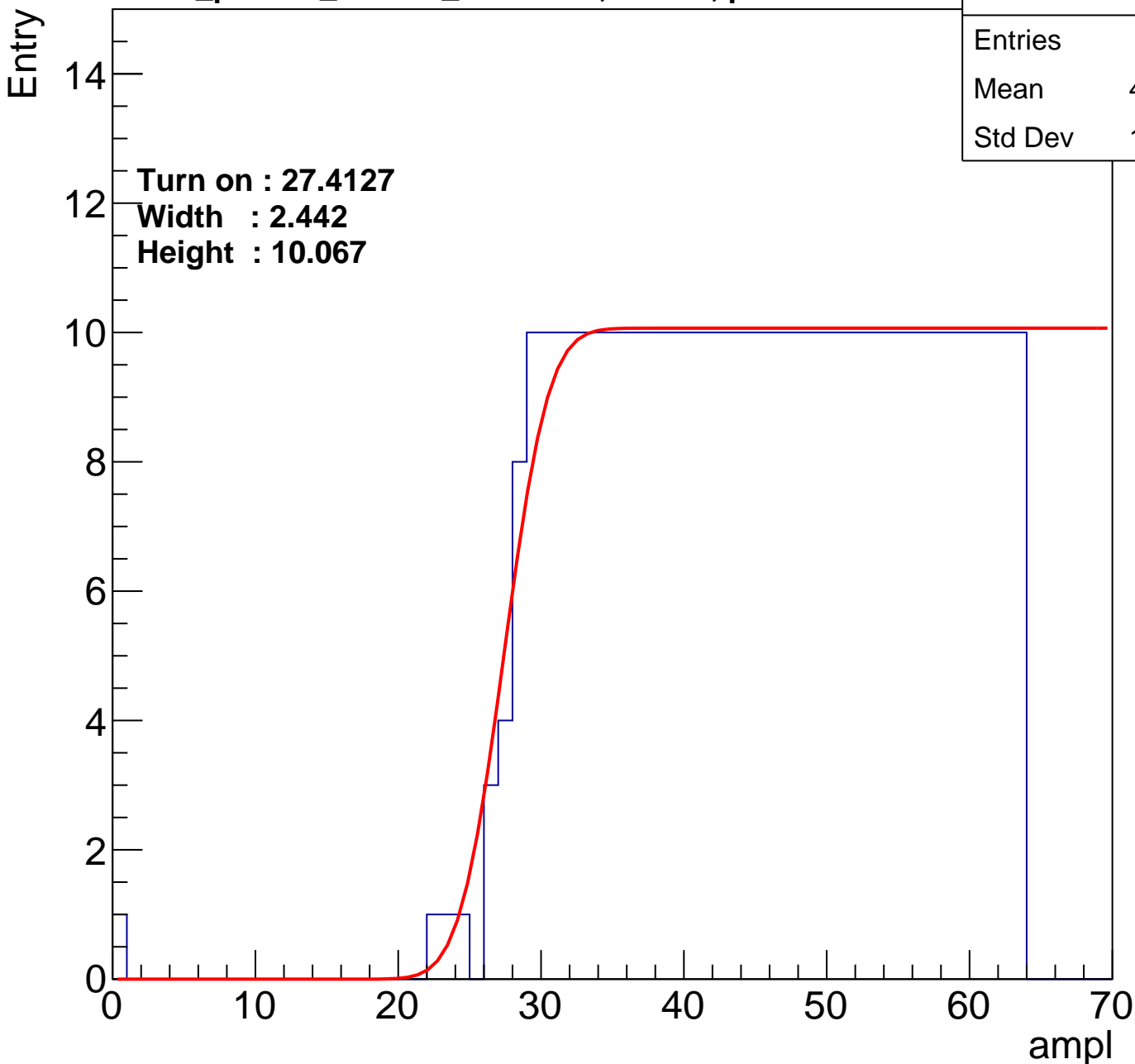
calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.93
Std Dev	10.95

Turn on : 27.4127

Width : 2.442

Height : 10.067



B1L003S, U23-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.01
Std Dev	11.3

Turn on : 27.9020

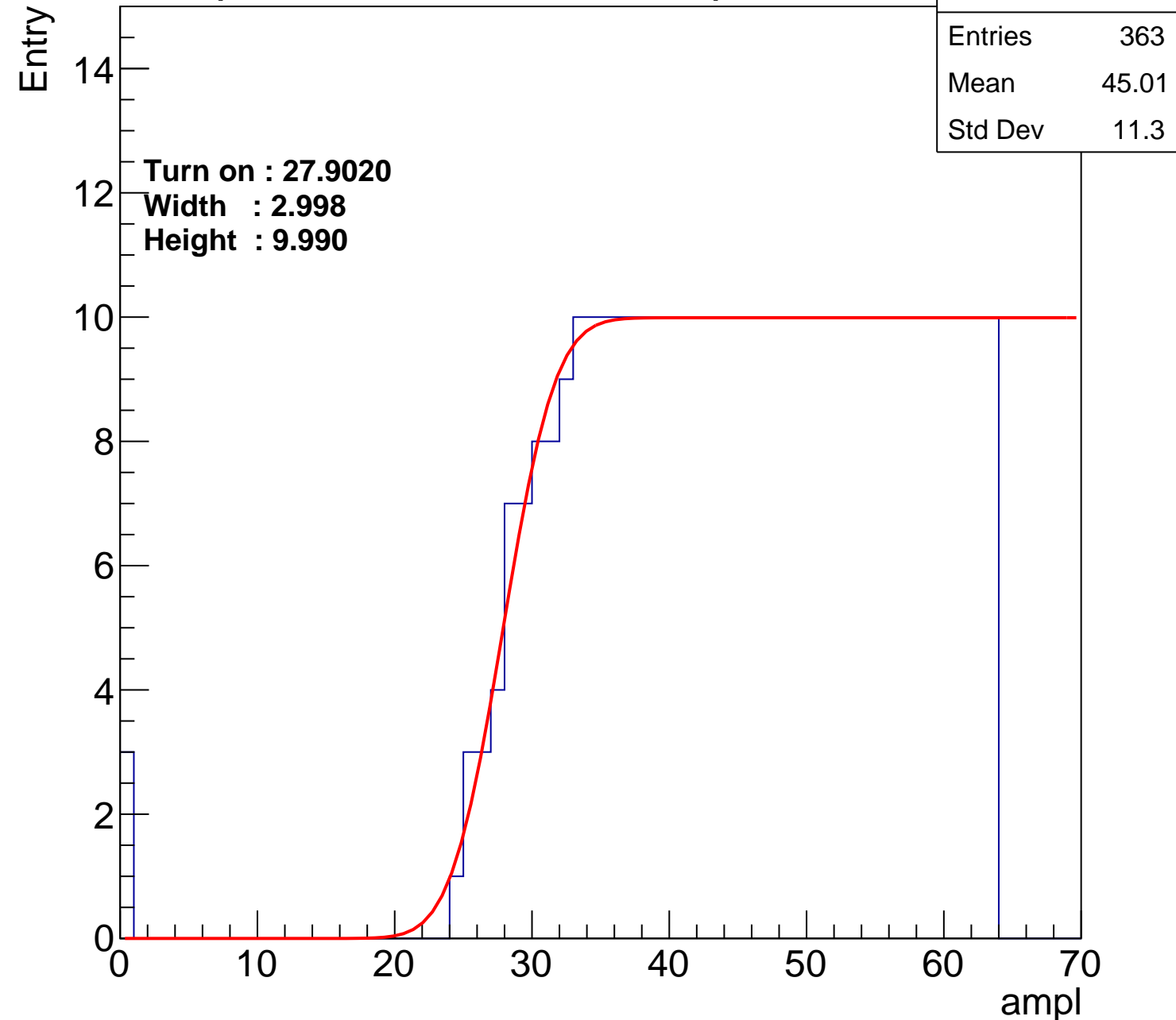
Width : 2.998

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch28

calib_packv5_042523_0143.root, FC#13, port D2

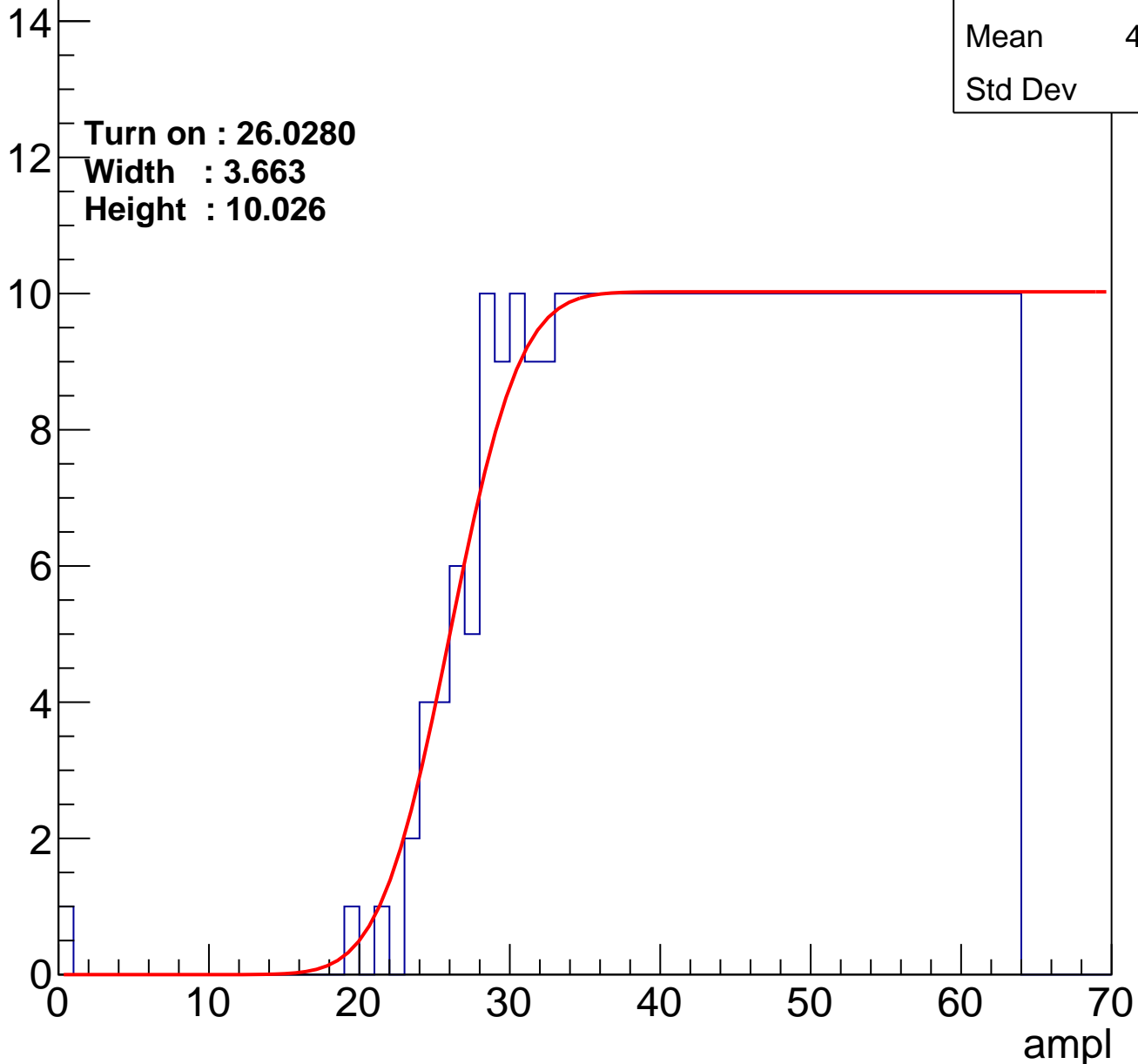
Entries	381
Mean	44.25
Std Dev	11.4

Turn on : 26.0280

Width : 3.663

Height : 10.026

Entry



B1L003S, U23-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.8
Std Dev	11.6

Turn on : 25.0592

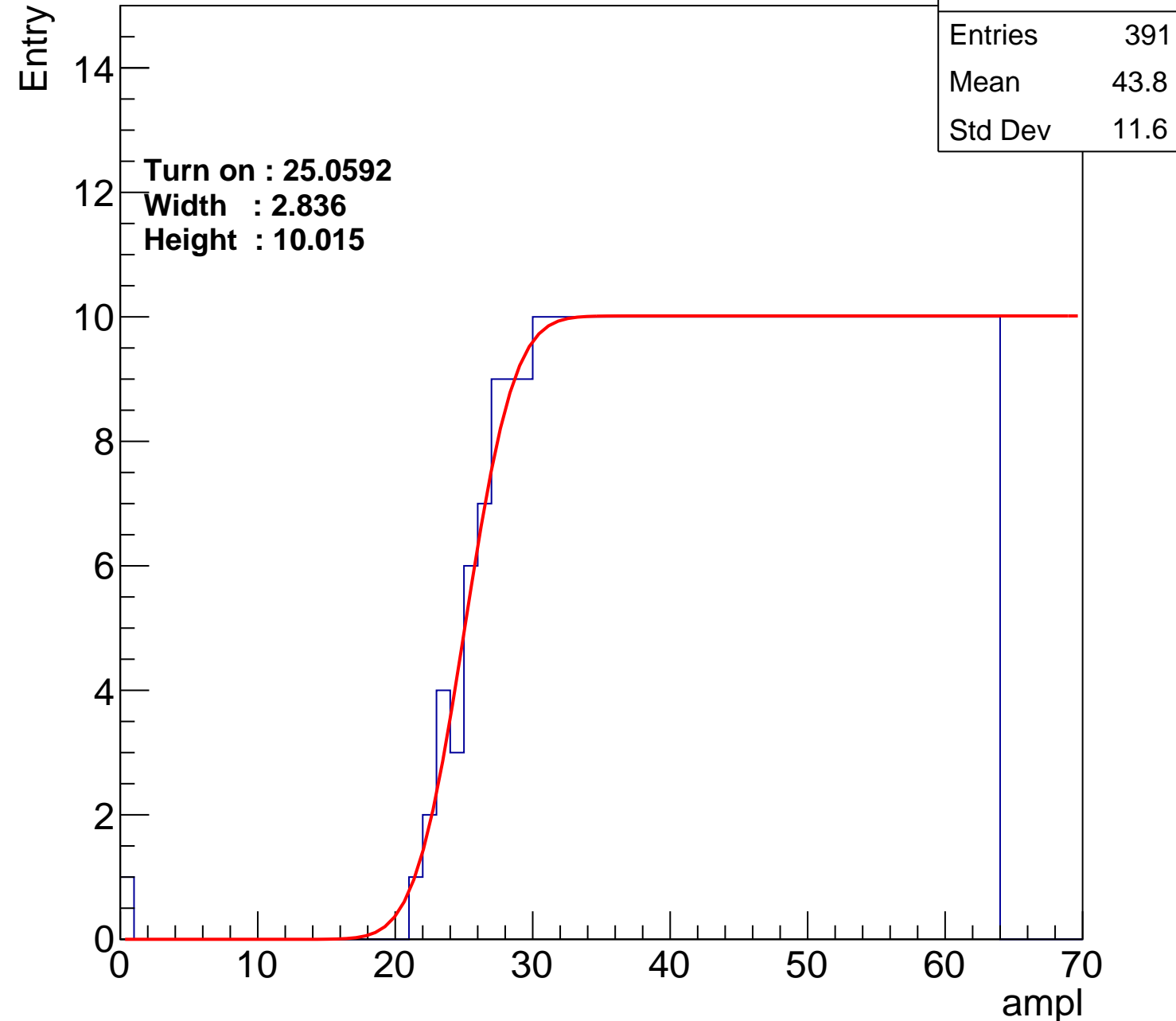
Width : 2.836

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entries	397
Mean	43.34
Std Dev	12.15

Turn on : 24.9445

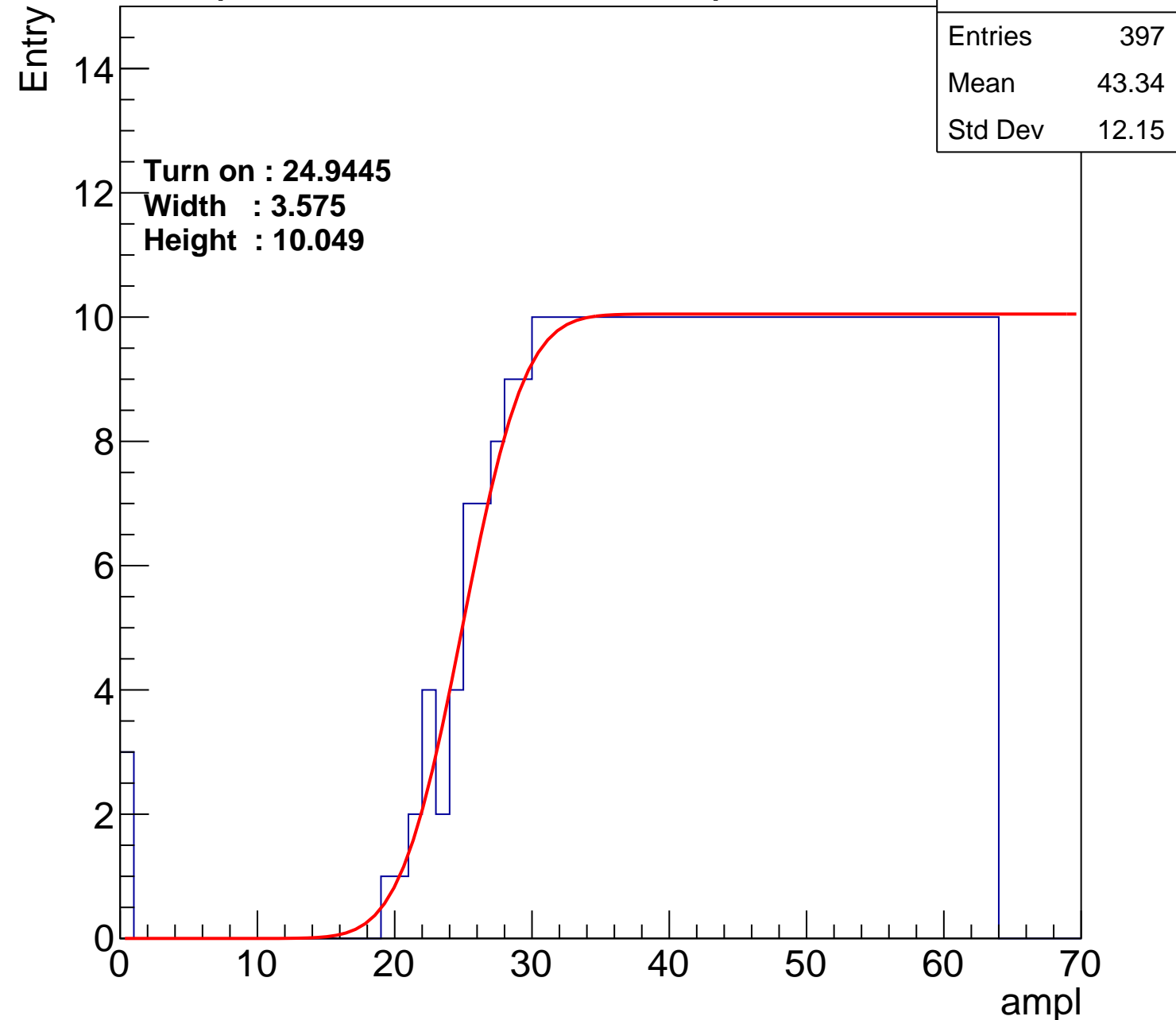
Width : 3.575

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch31

calib_packv5_042523_0143.root, FC#13, port D2

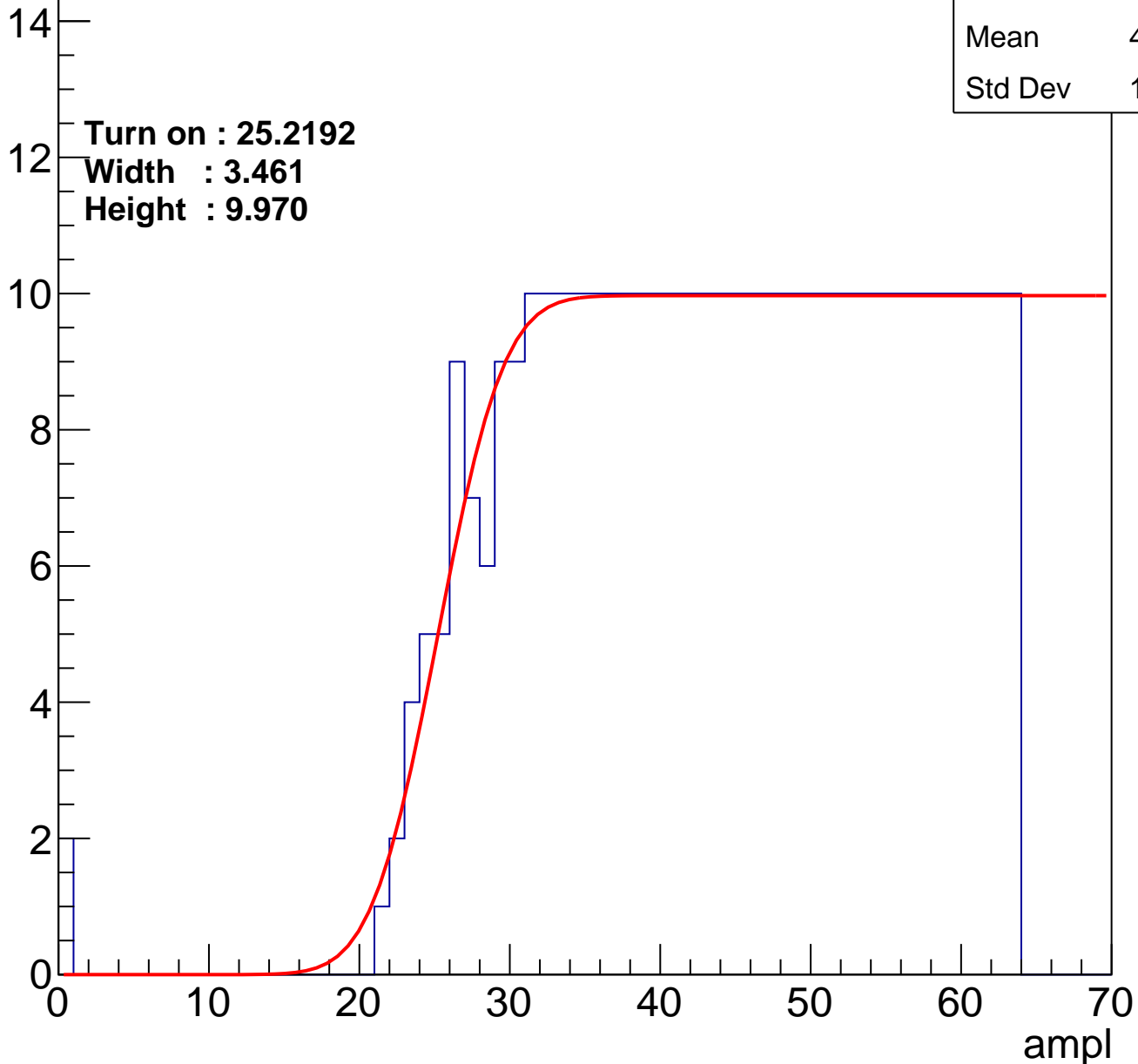
Entries	389
Mean	43.79
Std Dev	11.79

Turn on : 25.2192

Width : 3.461

Height : 9.970

Entry



B1L003S, U23-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.74
Std Dev	11.22

Turn on : 27.1537

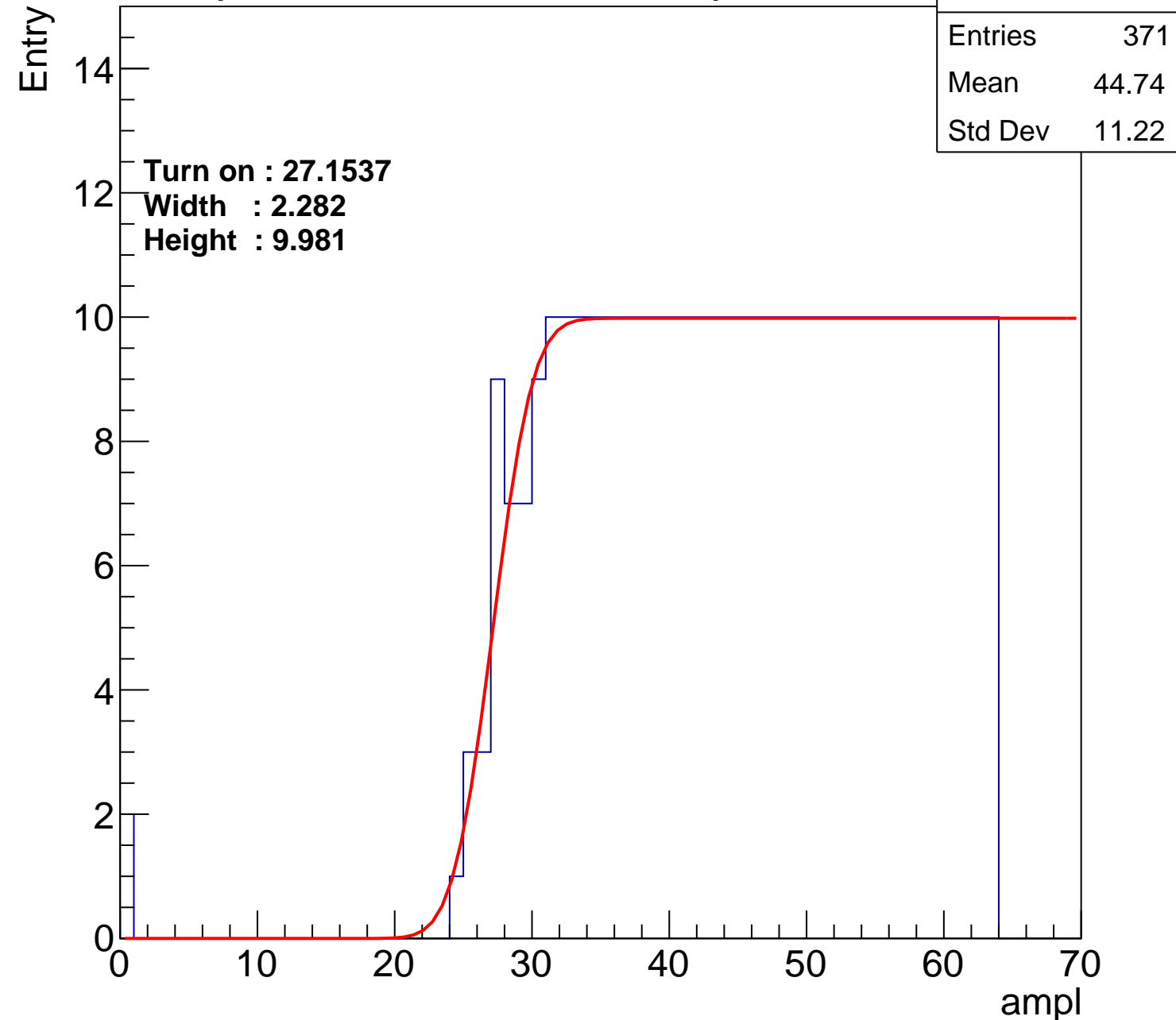
Width : 2.282

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.05
Std Dev	11.11

Turn on : 27.9339

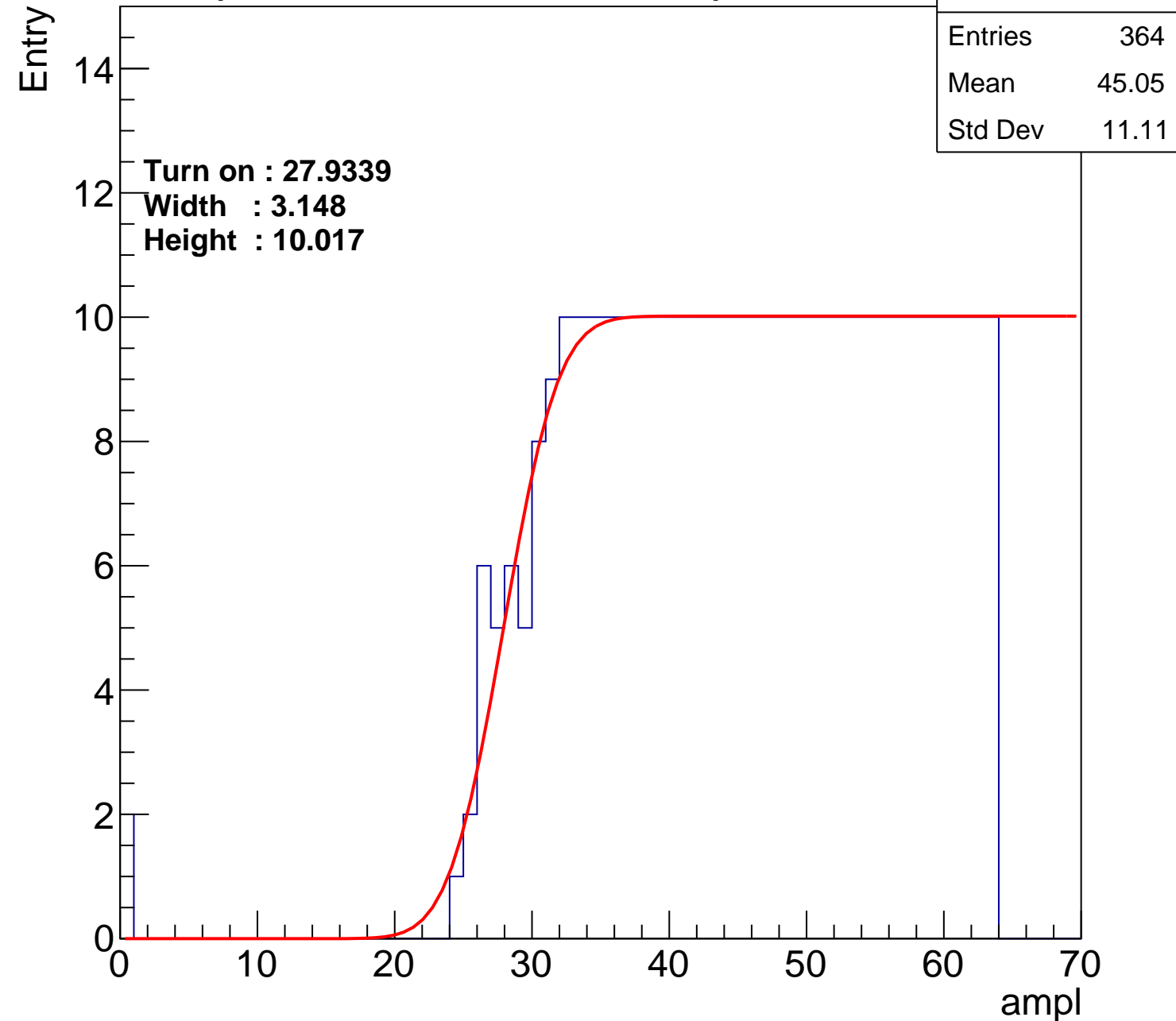
Width : 3.148

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch34

calib_packv5_042523_0143.root, FC#13, port D2

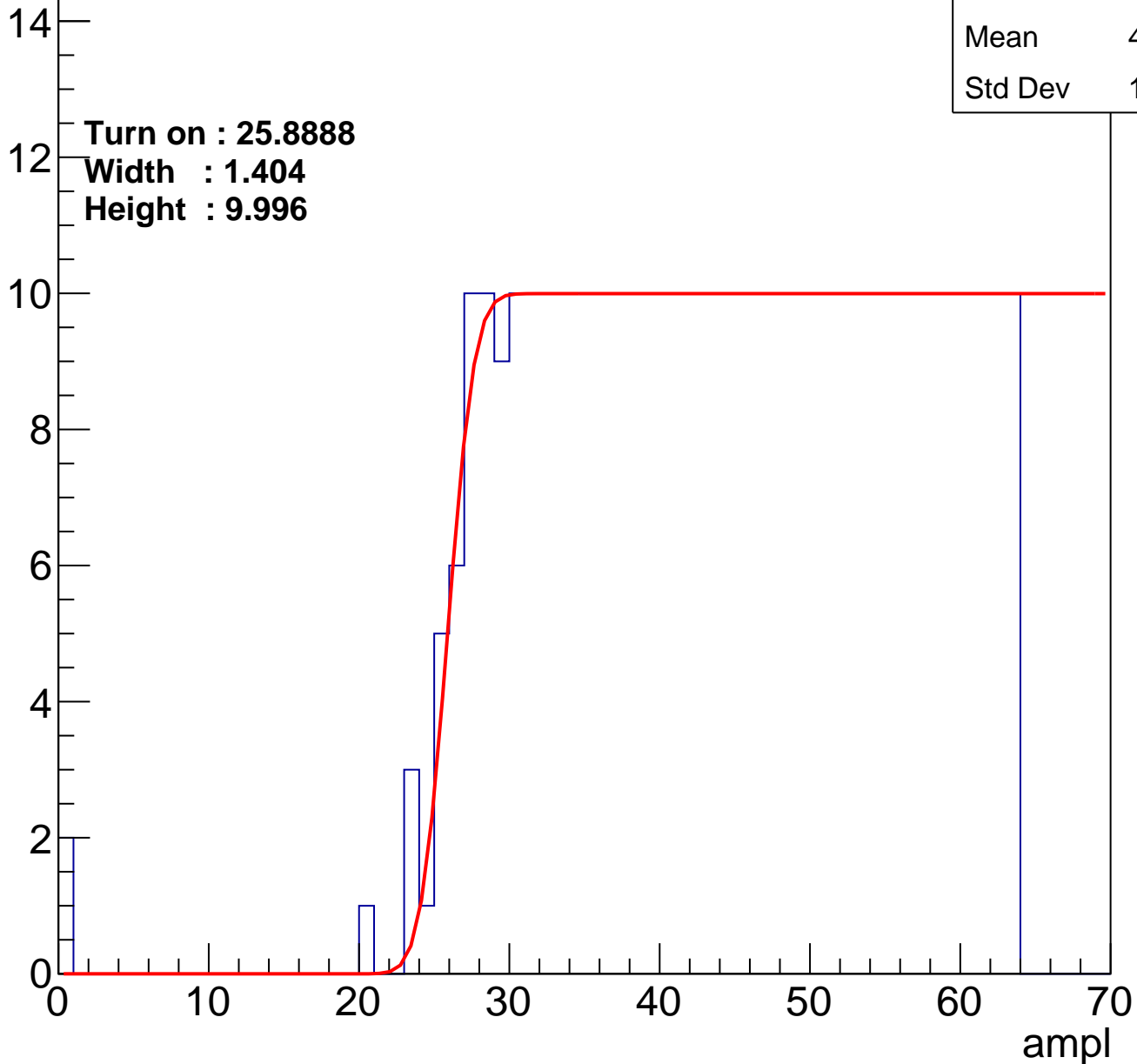
Entries	387
Mean	43.97
Std Dev	11.62

Turn on : 25.8888

Width : 1.404

Height : 9.996

Entry



B1L003S, U23-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.16
Std Dev	11.03

Turn on : 27.8455

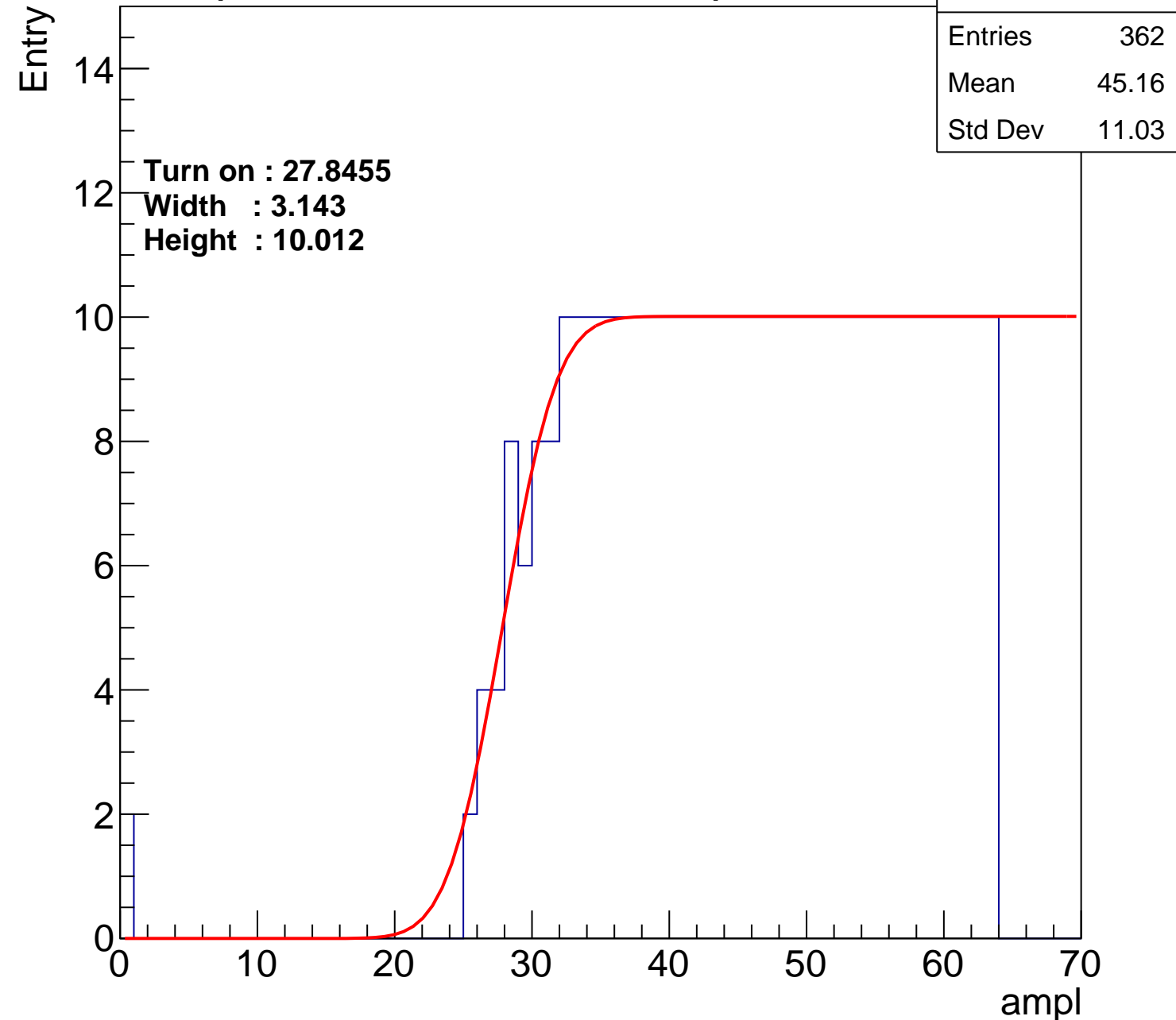
Width : 3.143

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.05
Std Dev	11.89

Turn on : 26.3175

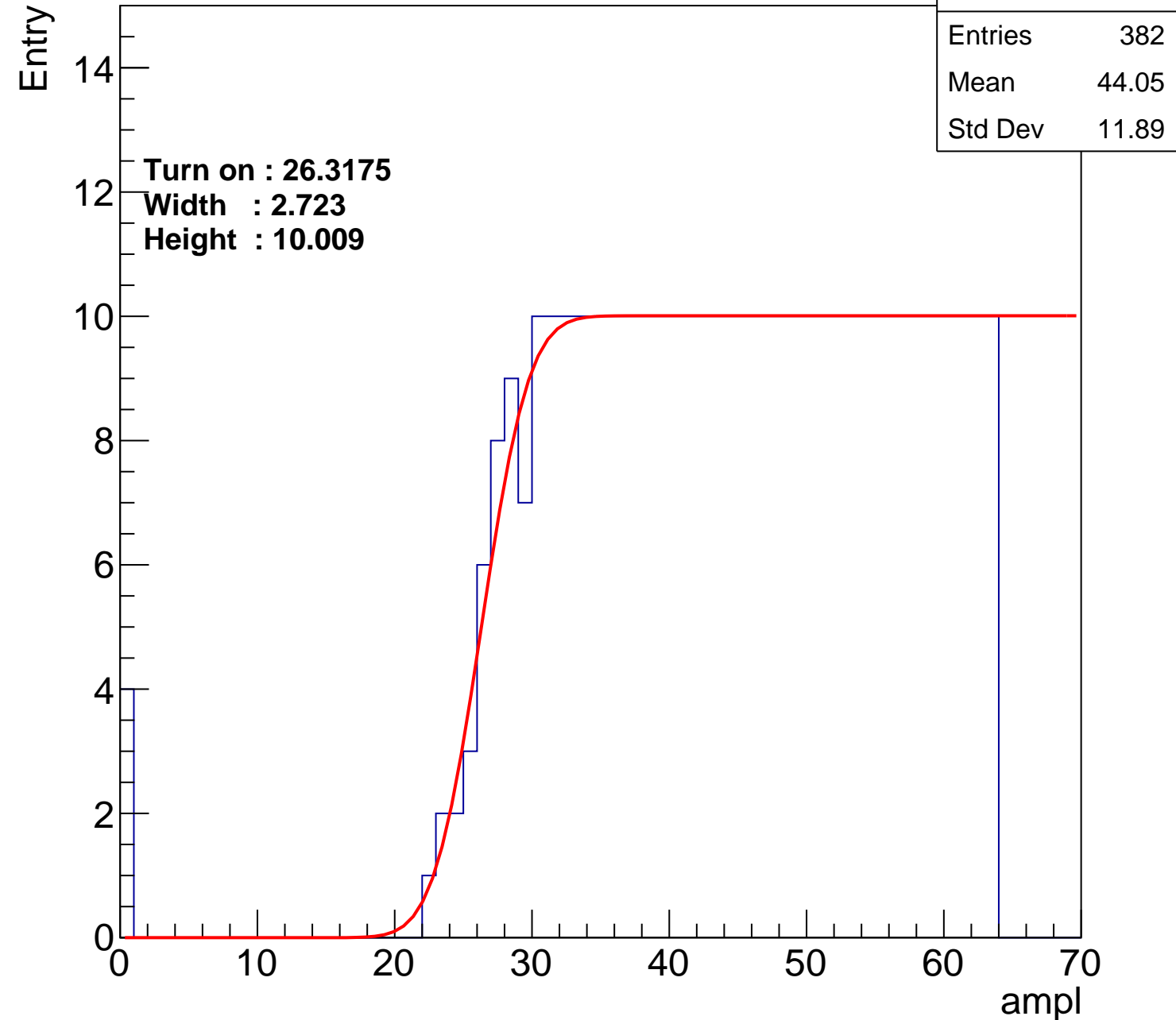
Width : 2.723

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch37

calib_packv5_042523_0143.root, FC#13, port D2

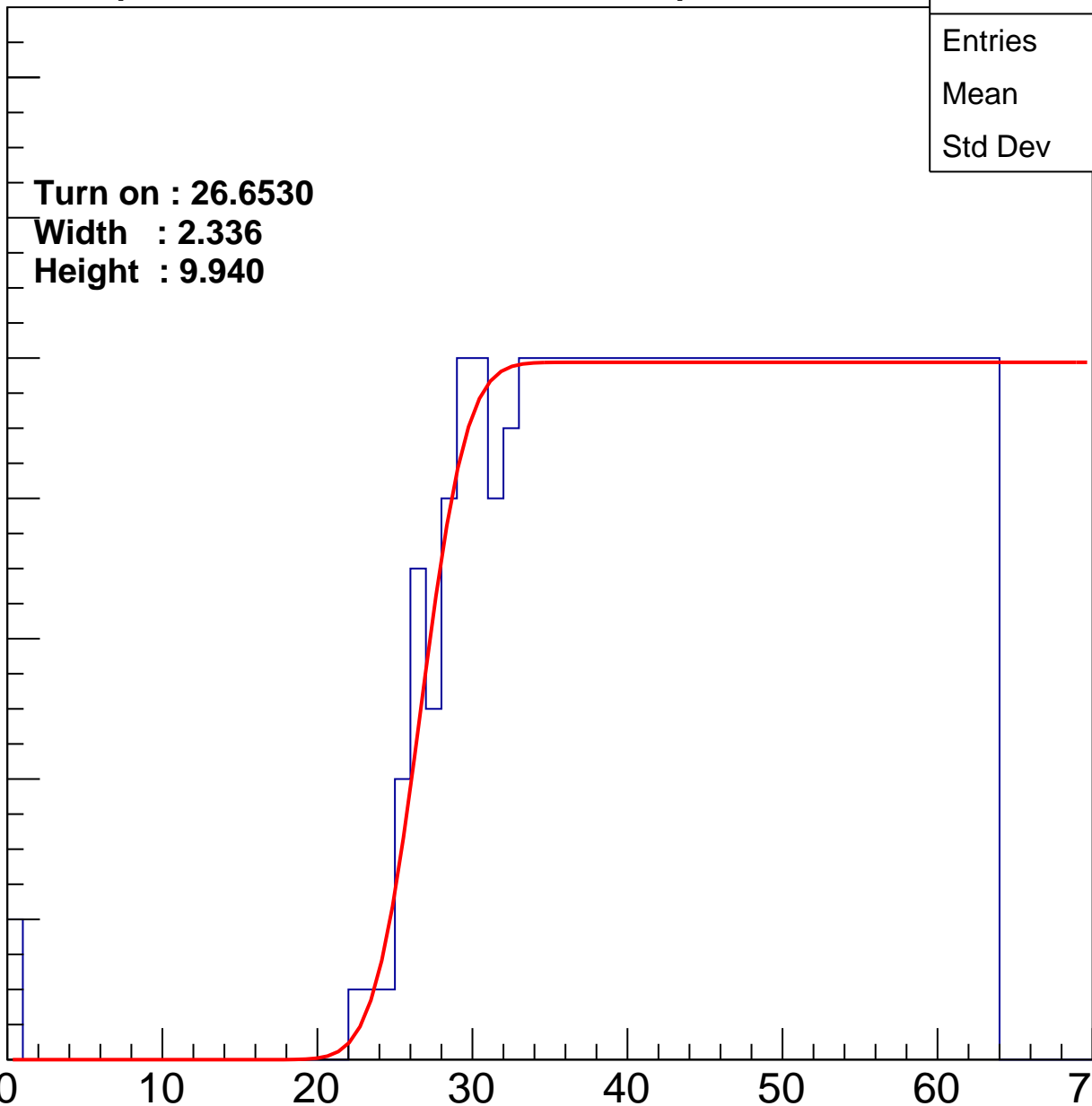
Entry

14
12
10
8
6
4
2
0

Turn on : 26.6530
Width : 2.336
Height : 9.940

Entries	376
Mean	44.46
Std Dev	11.41

ampl



B1L003S, U23-ch38

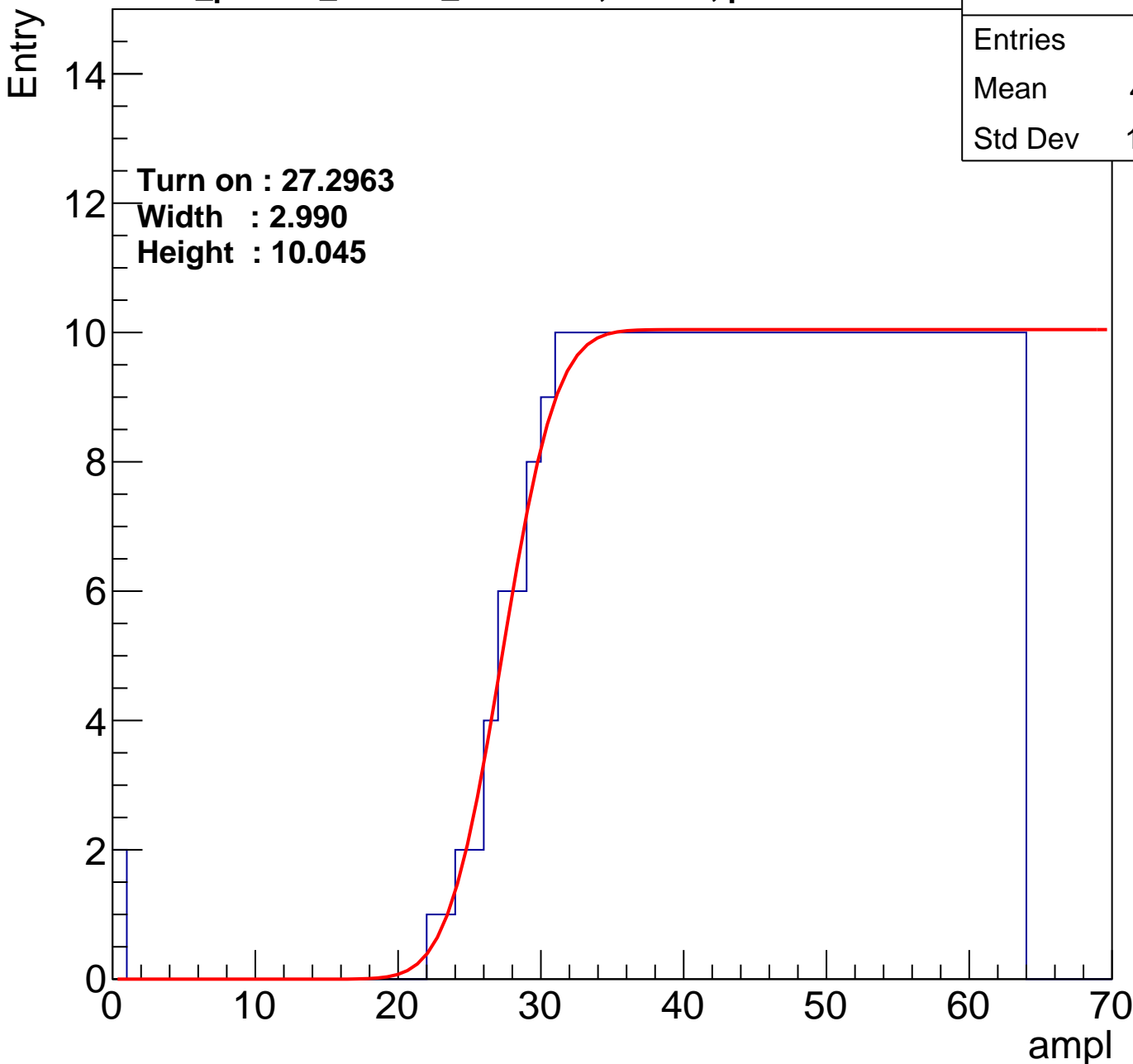
calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 27.2963

Width : 2.990

Height : 10.045



B1L003S, U23-ch39

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.7
Std Dev	11.14

Turn on : 26.9271

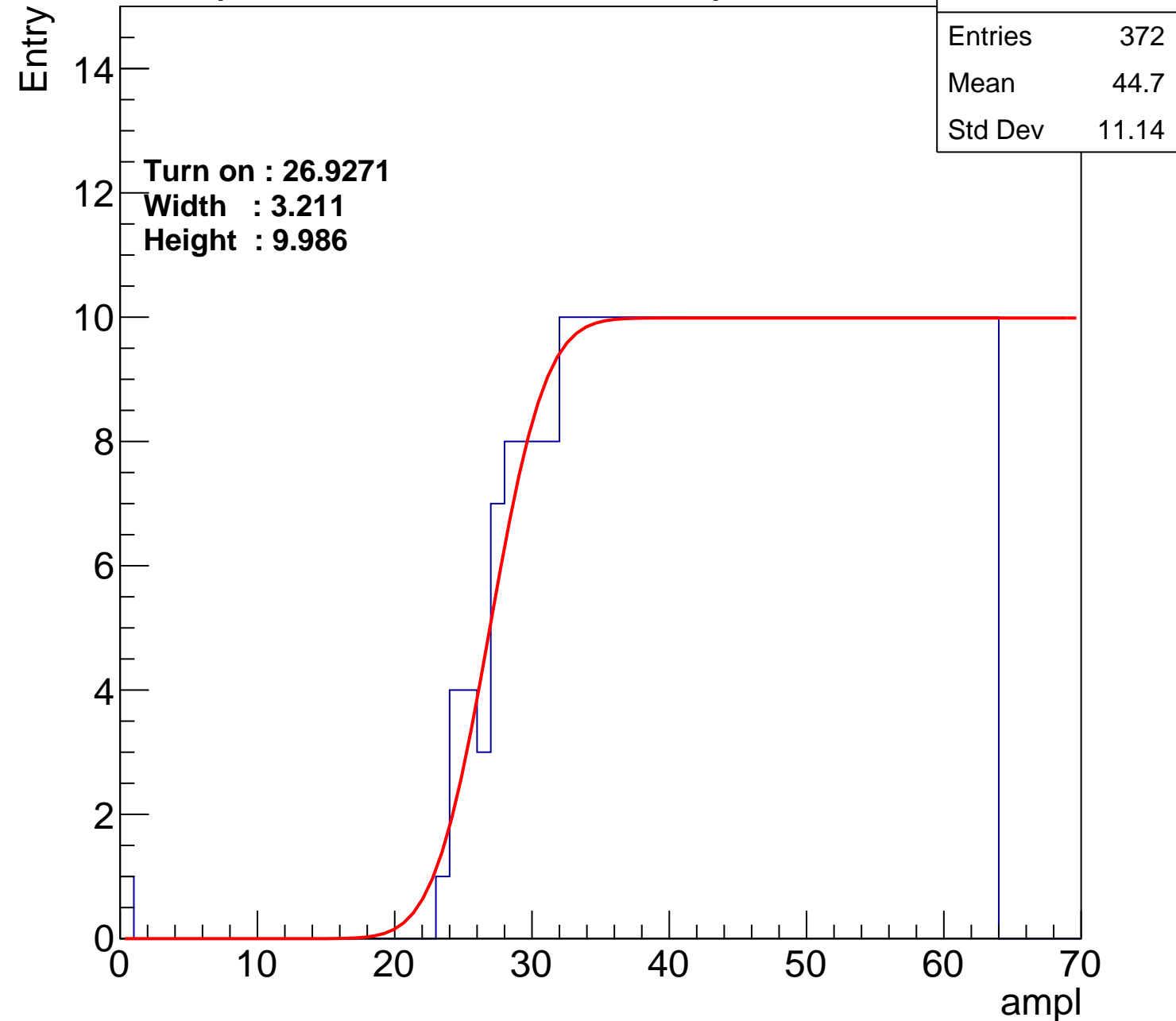
Width : 3.211

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch40

calib_packv5_042523_0143.root, FC#13, port D2

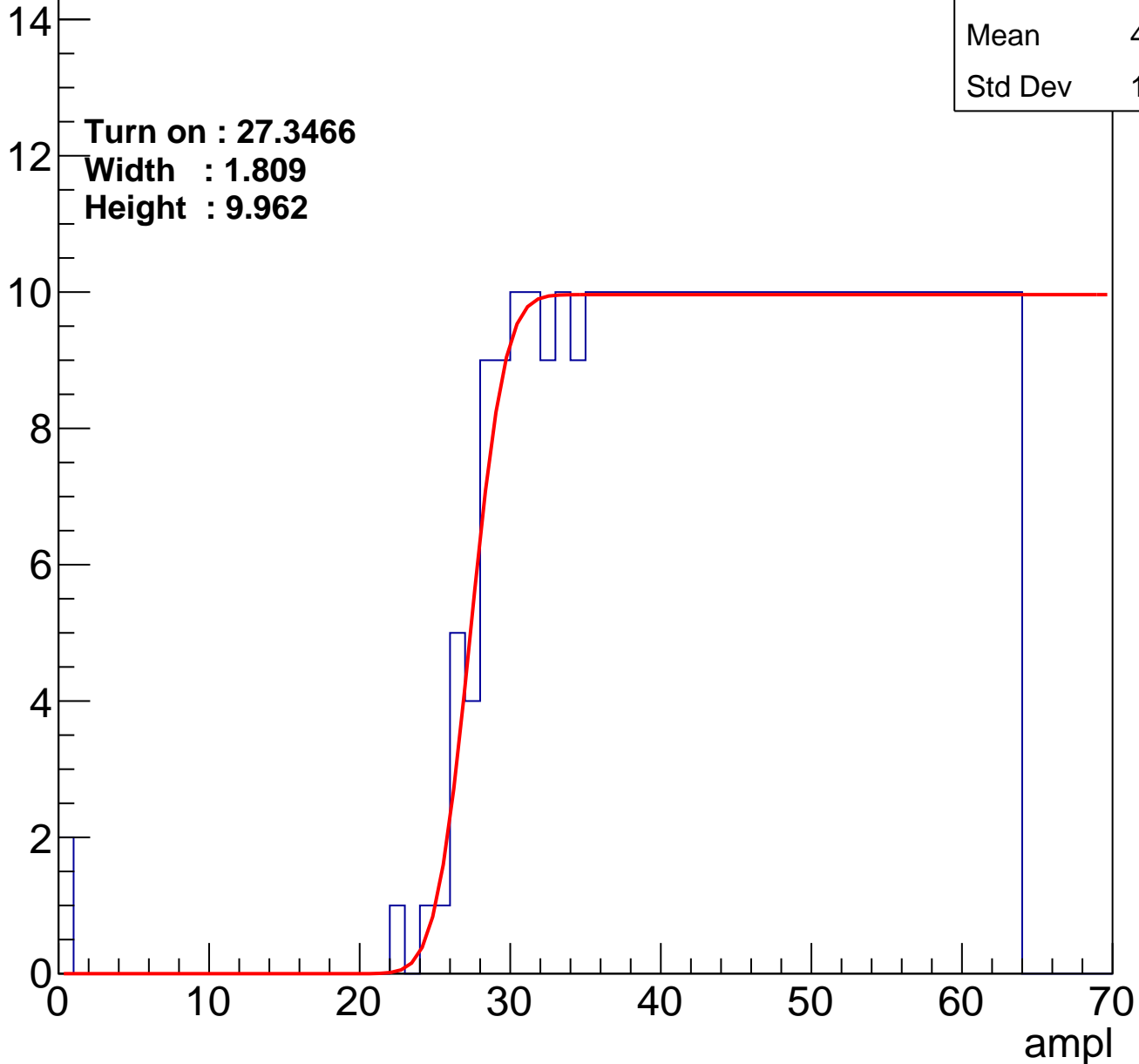
Entries	370
Mean	44.77
Std Dev	11.22

Turn on : 27.3466

Width : 1.809

Height : 9.962

Entry



B1L003S, U23-ch41

calib_packv5_042523_0143.root, FC#13, port D2

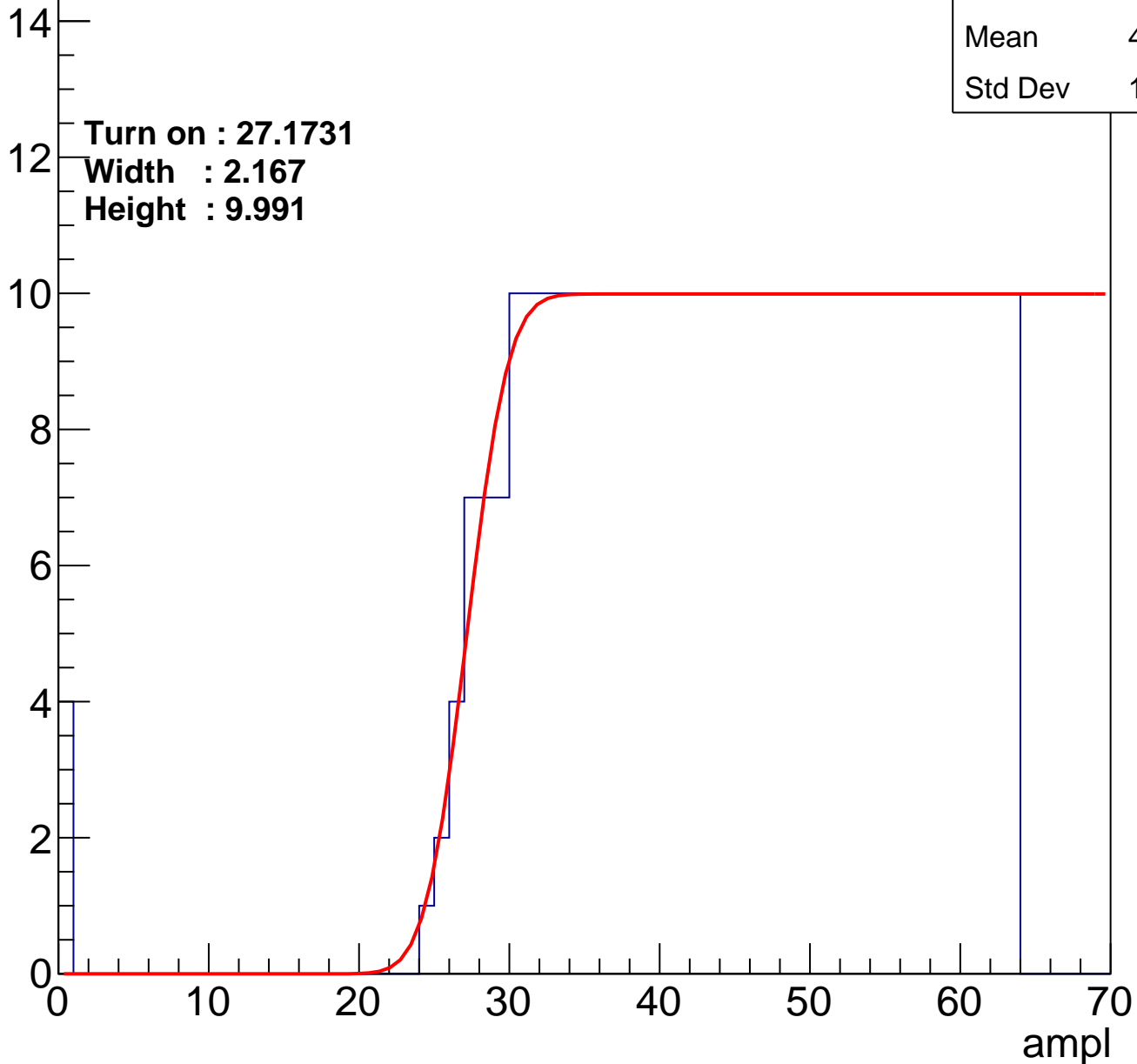
Entries	372
Mean	44.56
Std Dev	11.63

Turn on : 27.1731

Width : 2.167

Height : 9.991

Entry



B1L003S, U23-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.0635

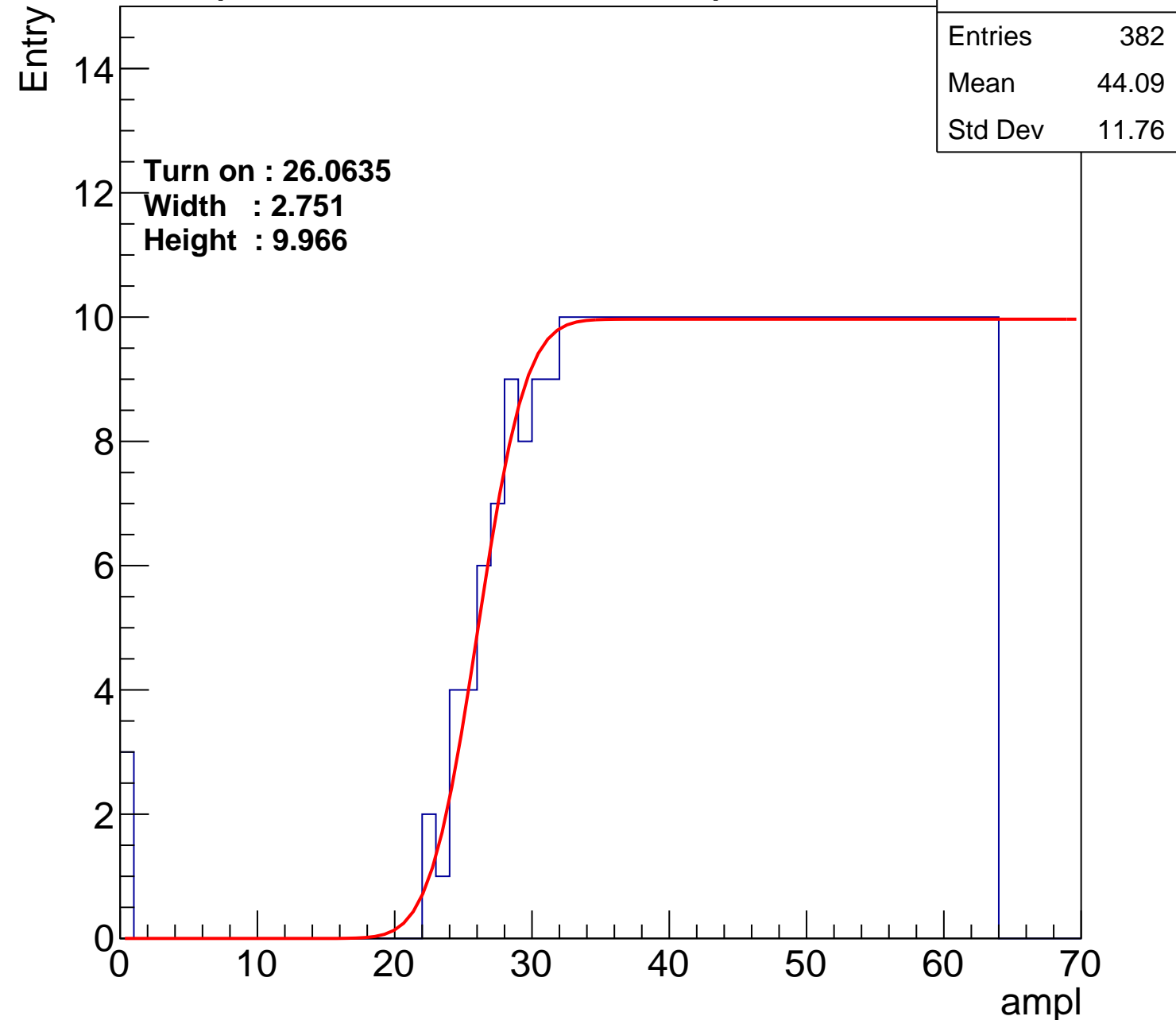
Width : 2.751

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.69
Std Dev	11.45

Turn on : 27.5950

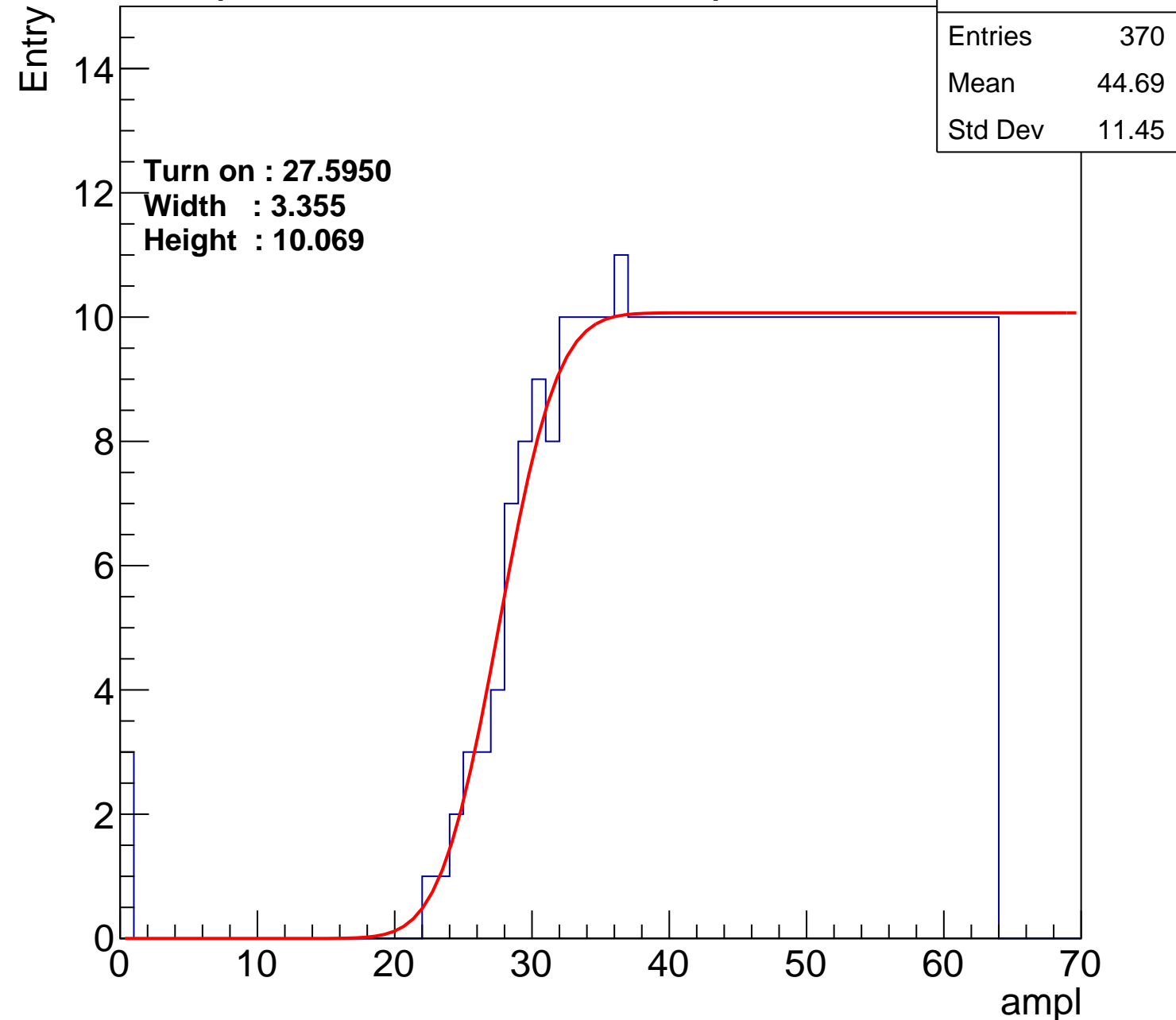
Width : 3.355

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch44

calib_packv5_042523_0143.root, FC#13, port D2

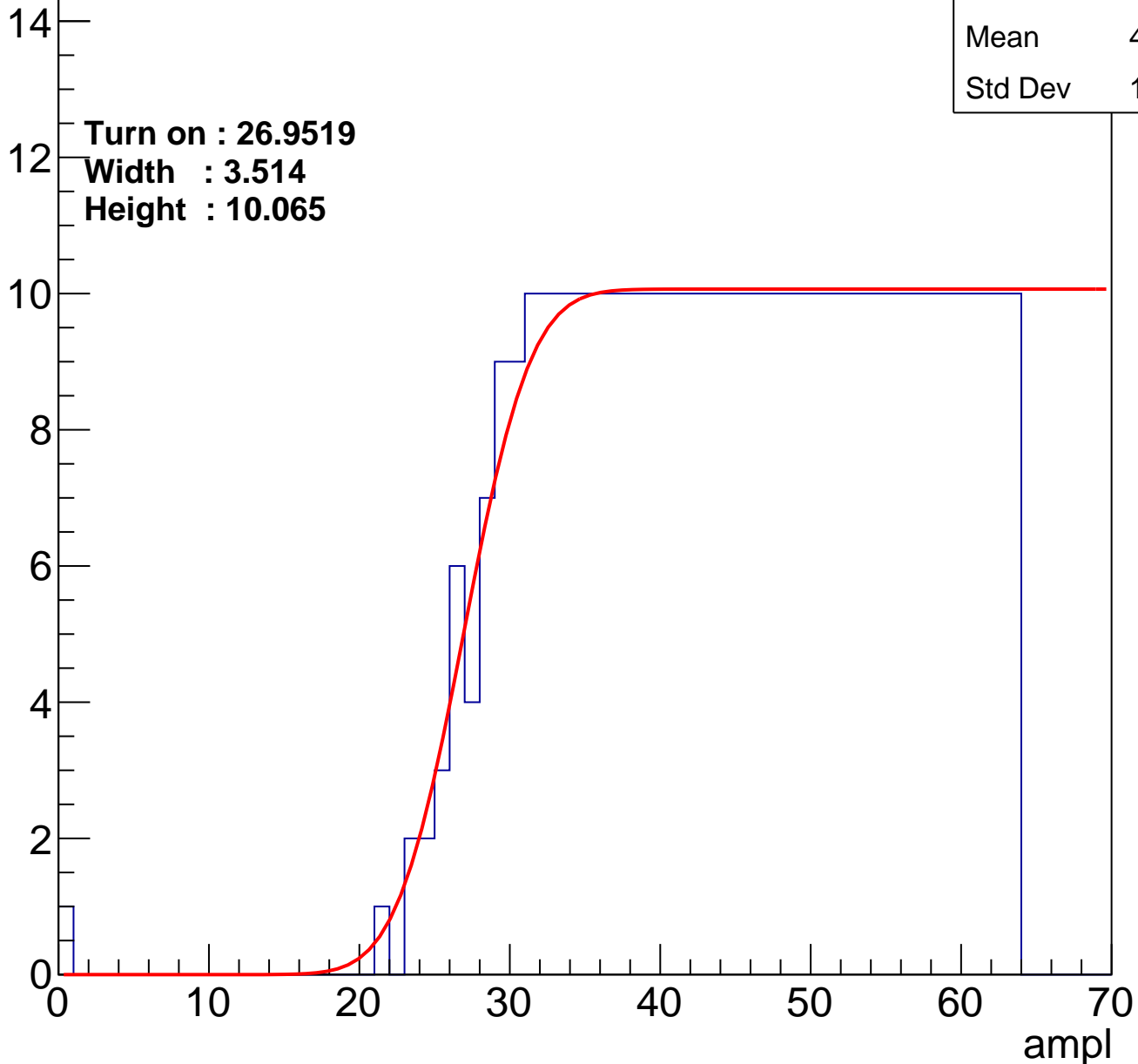
Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 26.9519

Width : 3.514

Height : 10.065

Entry



B1L003S, U23-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.83
Std Dev	12.16

Turn on : 26.2738

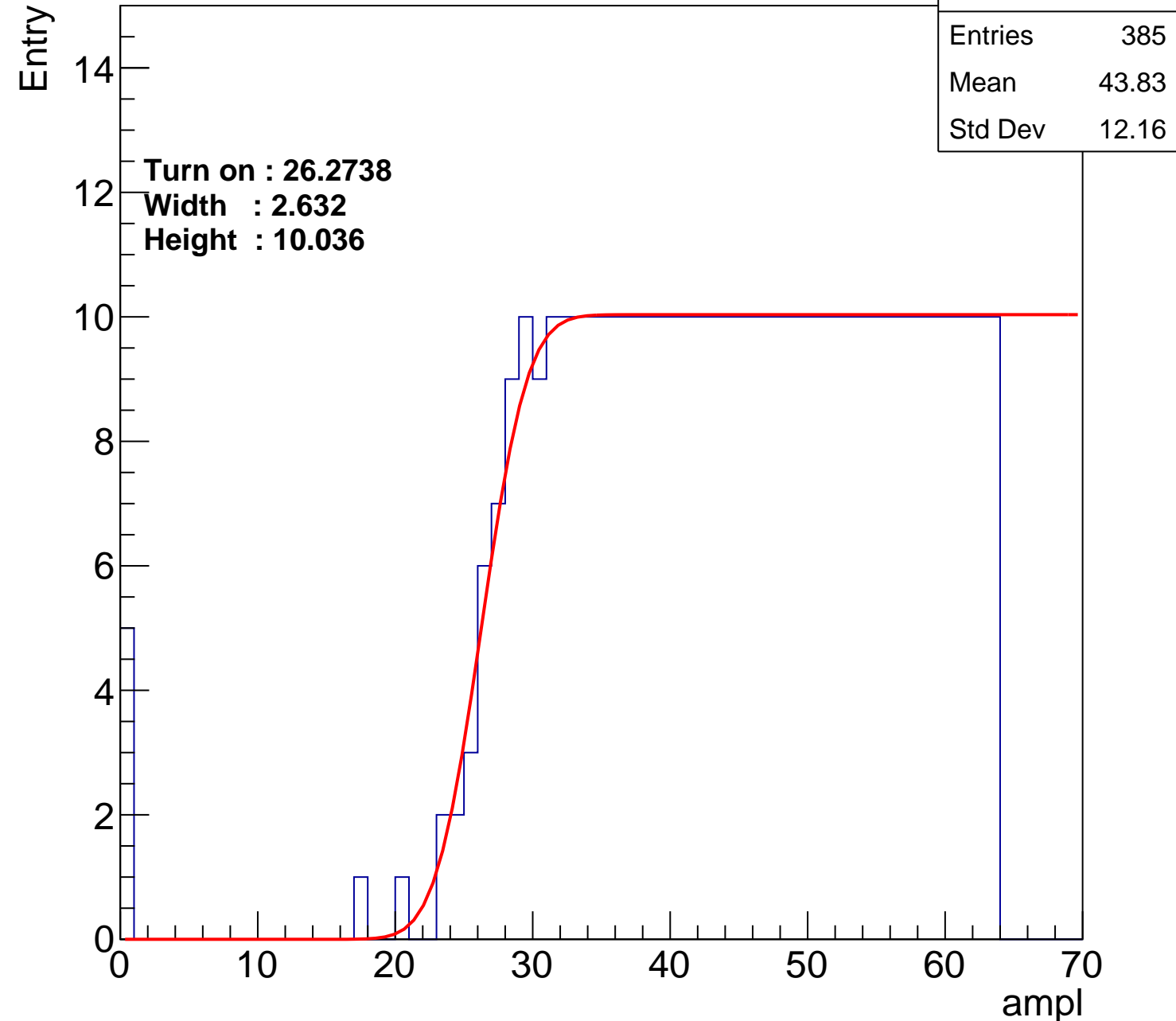
Width : 2.632

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.22
Std Dev	11.47

Turn on : 25.9687

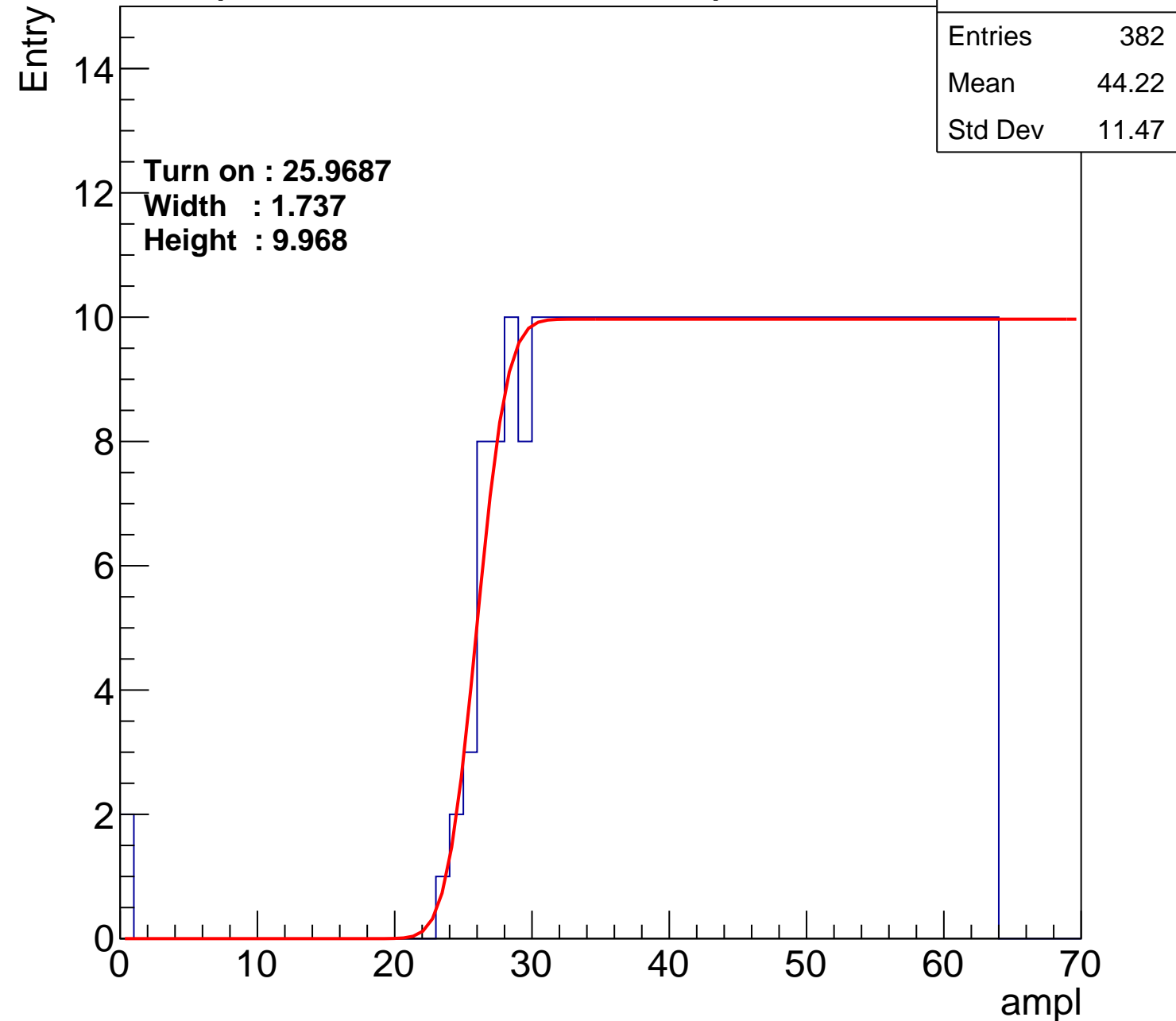
Width : 1.737

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.72
Std Dev	11.74

Turn on : 25.3973

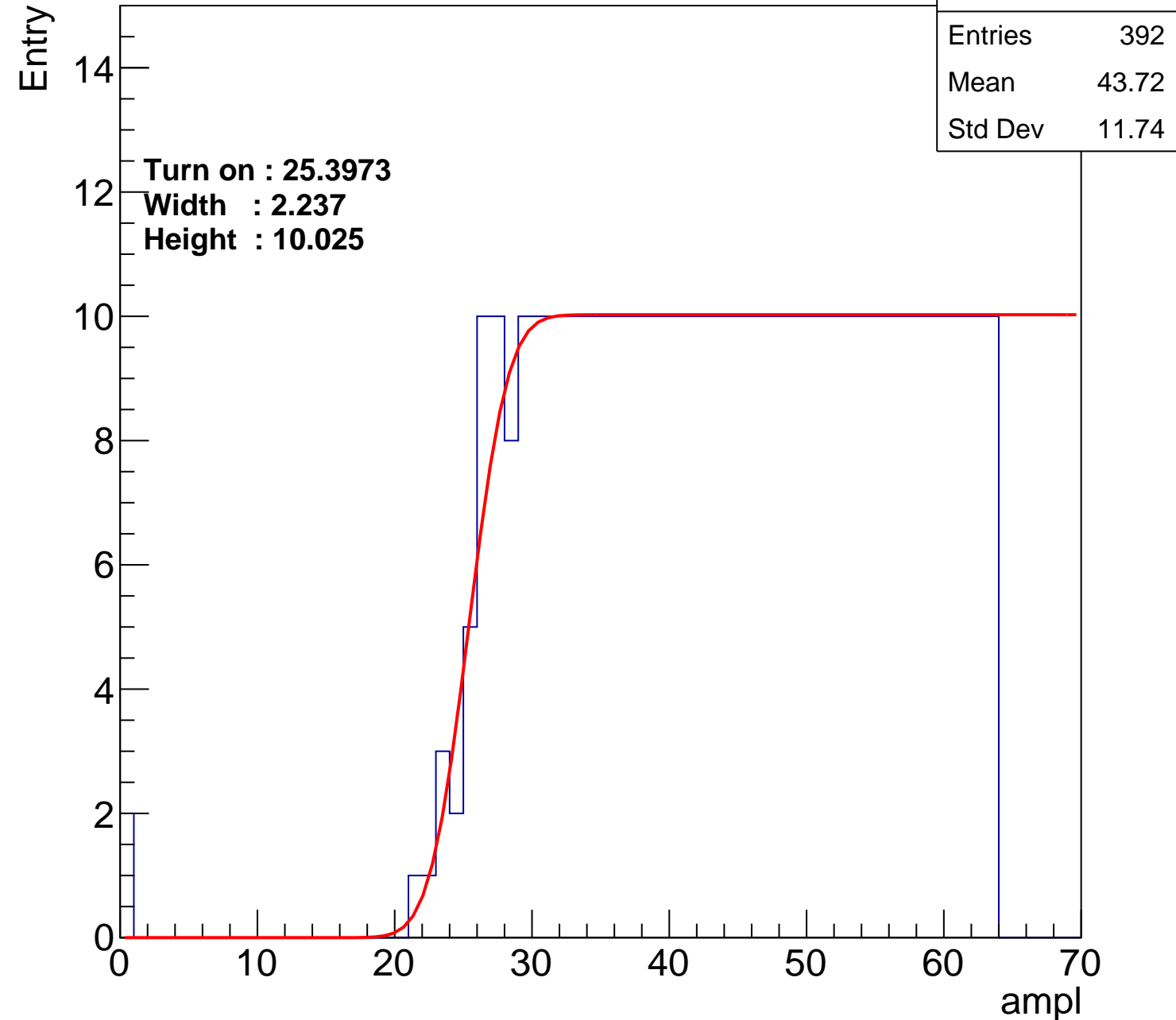
Width : 2.237

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch48

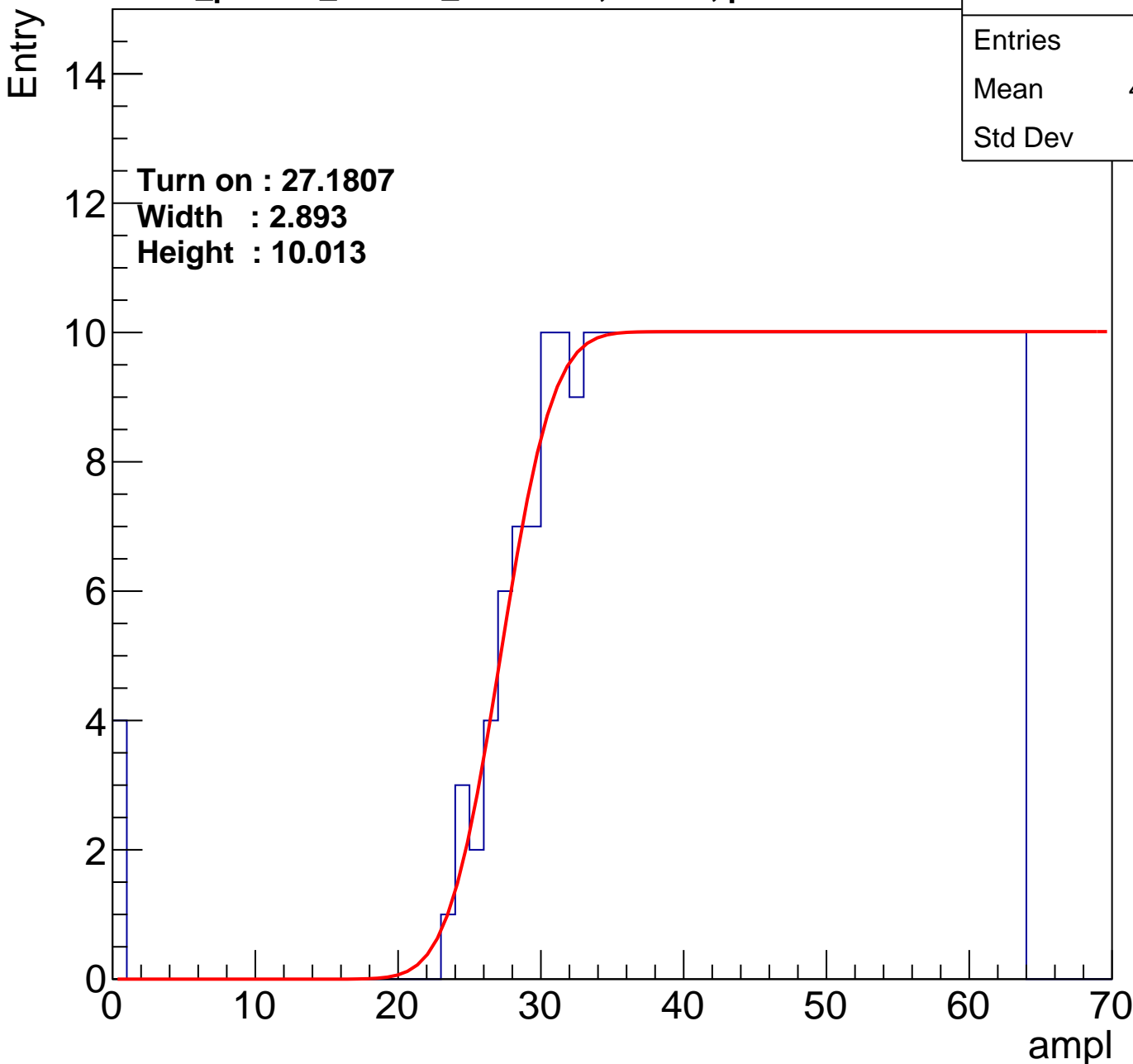
calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.47
Std Dev	11.71

Turn on : 27.1807

Width : 2.893

Height : 10.013



B1L003S, U23-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.18
Std Dev	11.53

Turn on : 26.0494

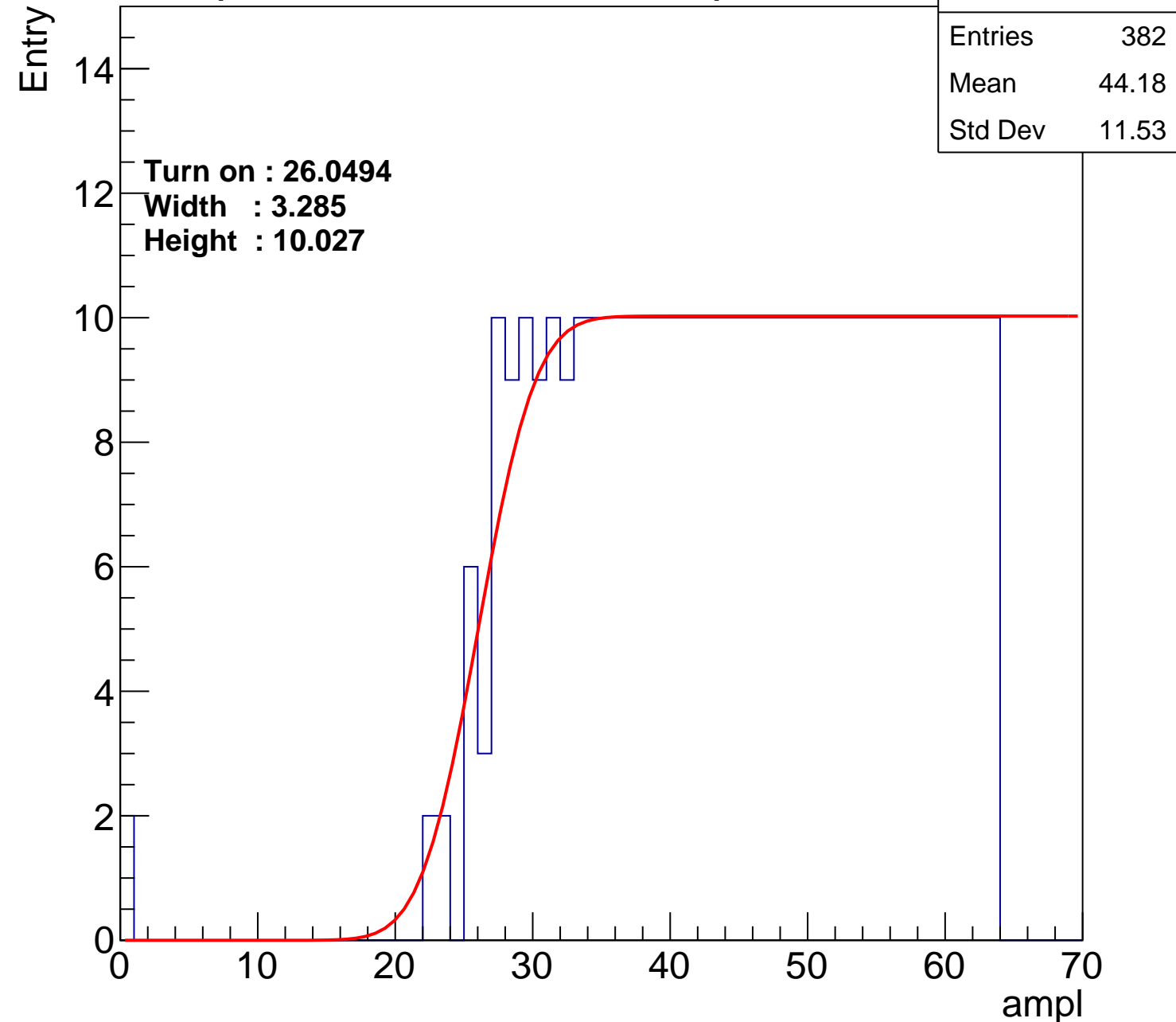
Width : 3.285

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.82
Std Dev	11.7

Turn on : 25.1580

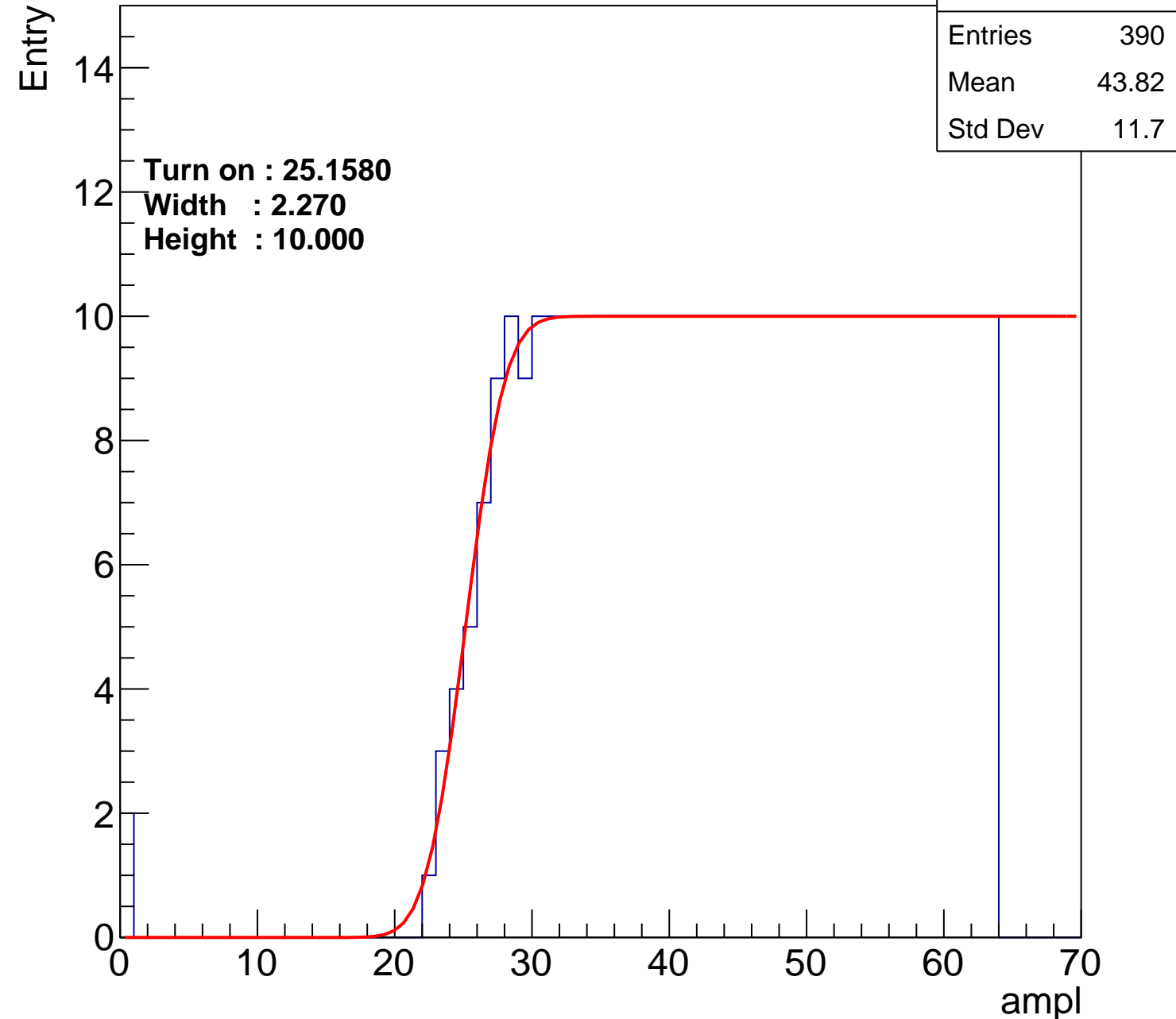
Width : 2.270

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.09
Std Dev	11.94

Turn on : 26.5398

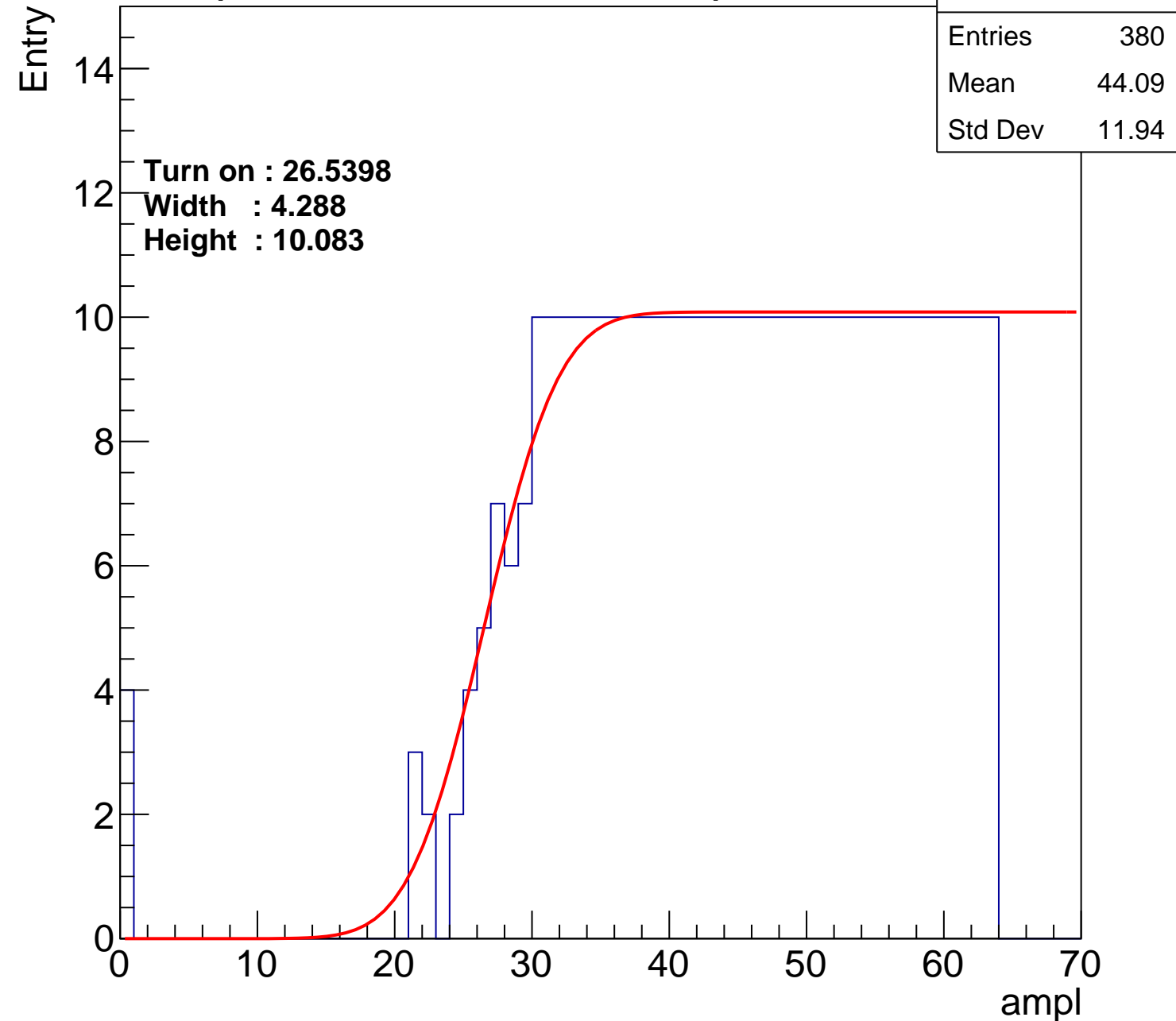
Width : 4.288

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.84
Std Dev	11.08

Turn on : 27.3423

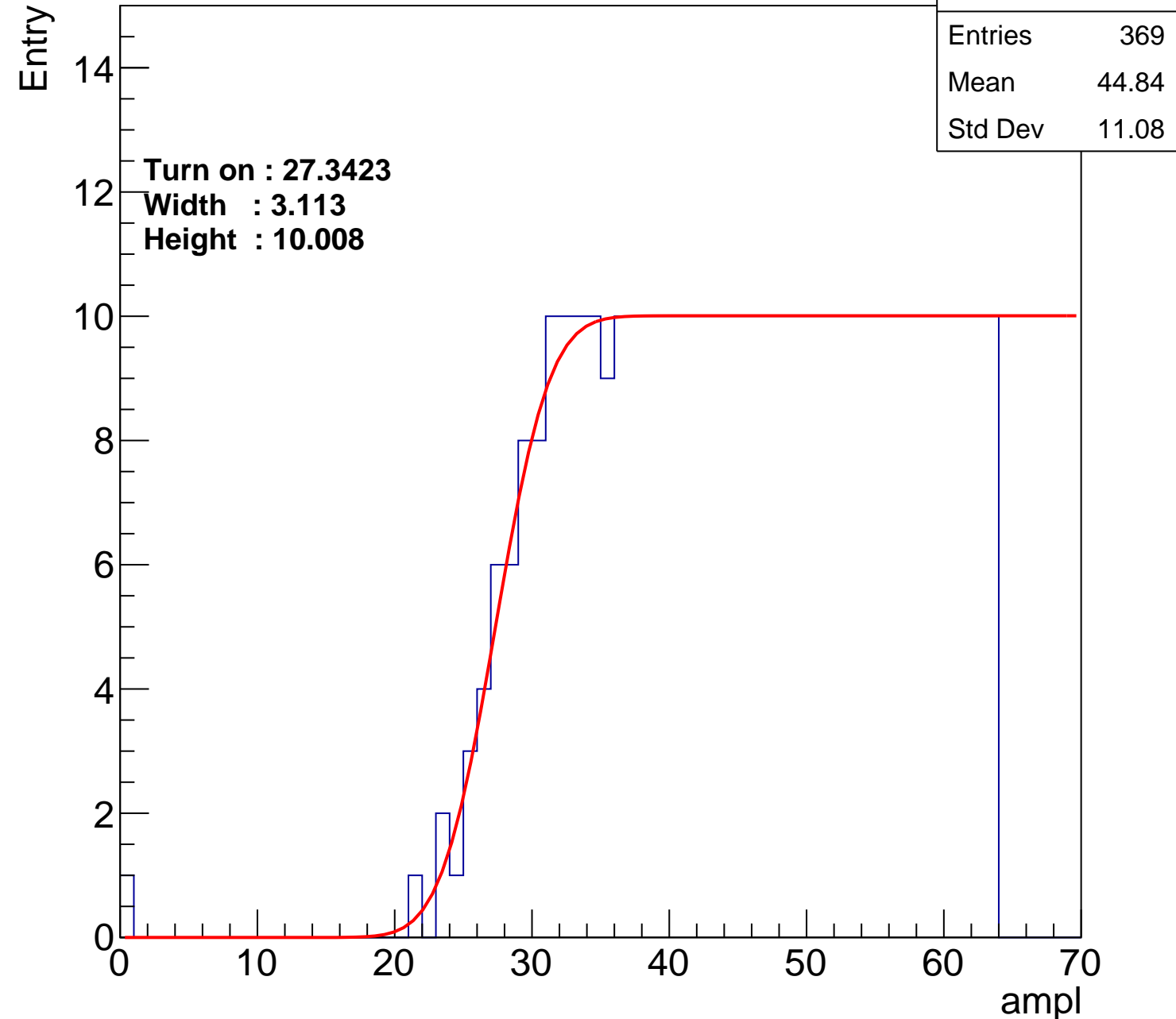
Width : 3.113

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	342
Mean	46.23
Std Dev	10.27

Turn on : 29.9249

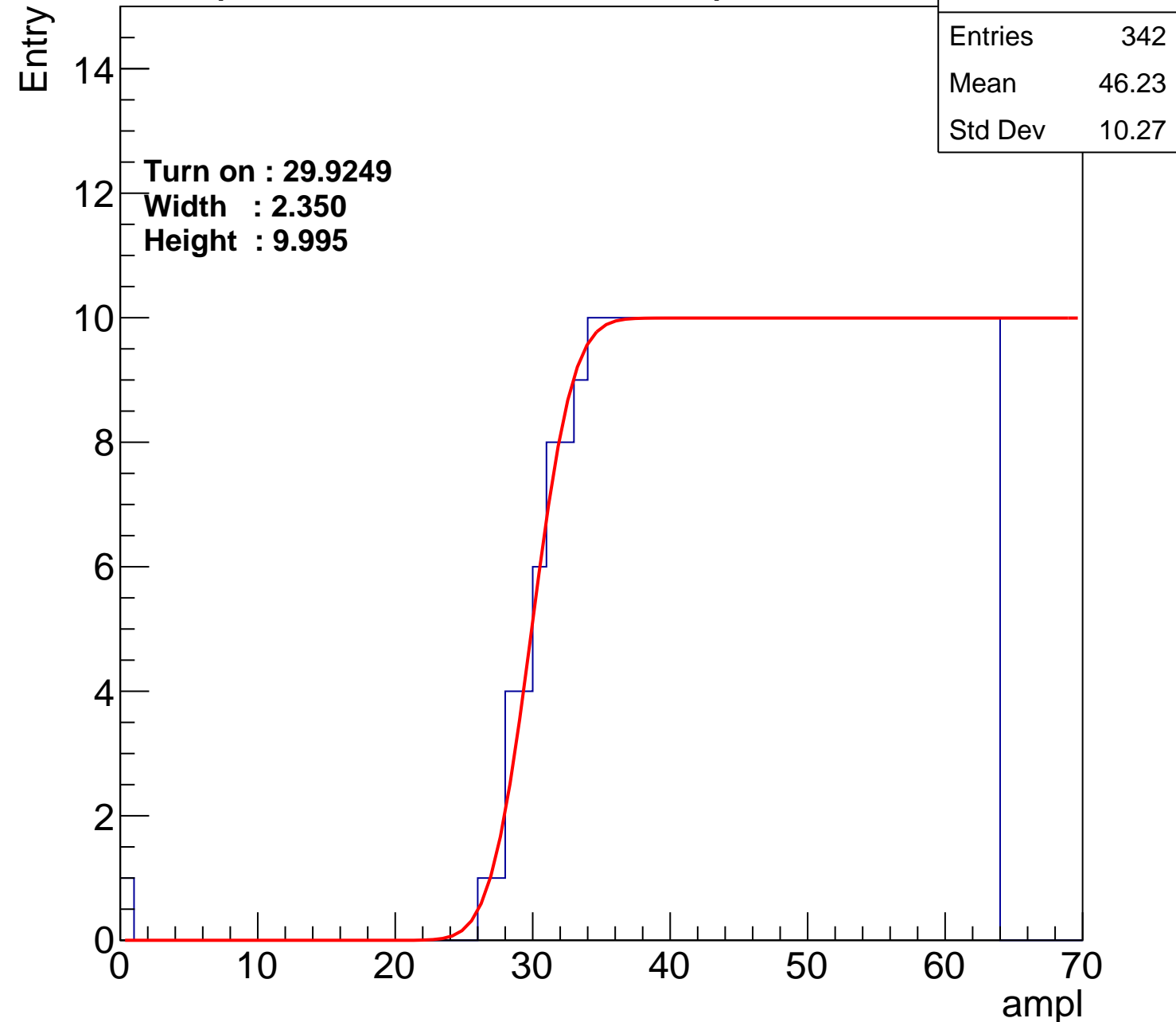
Width : 2.350

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.9791

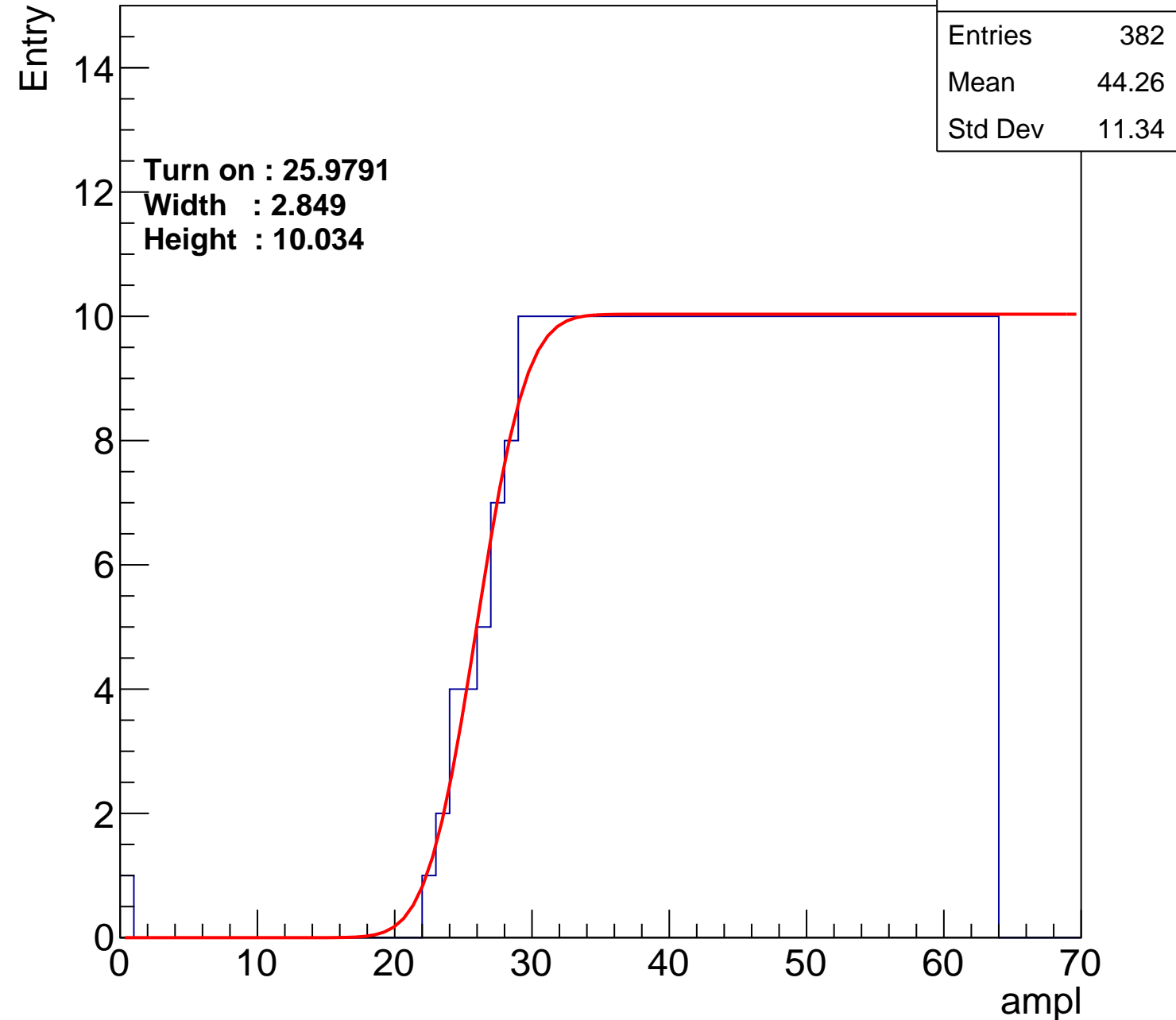
Width : 2.849

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.0419

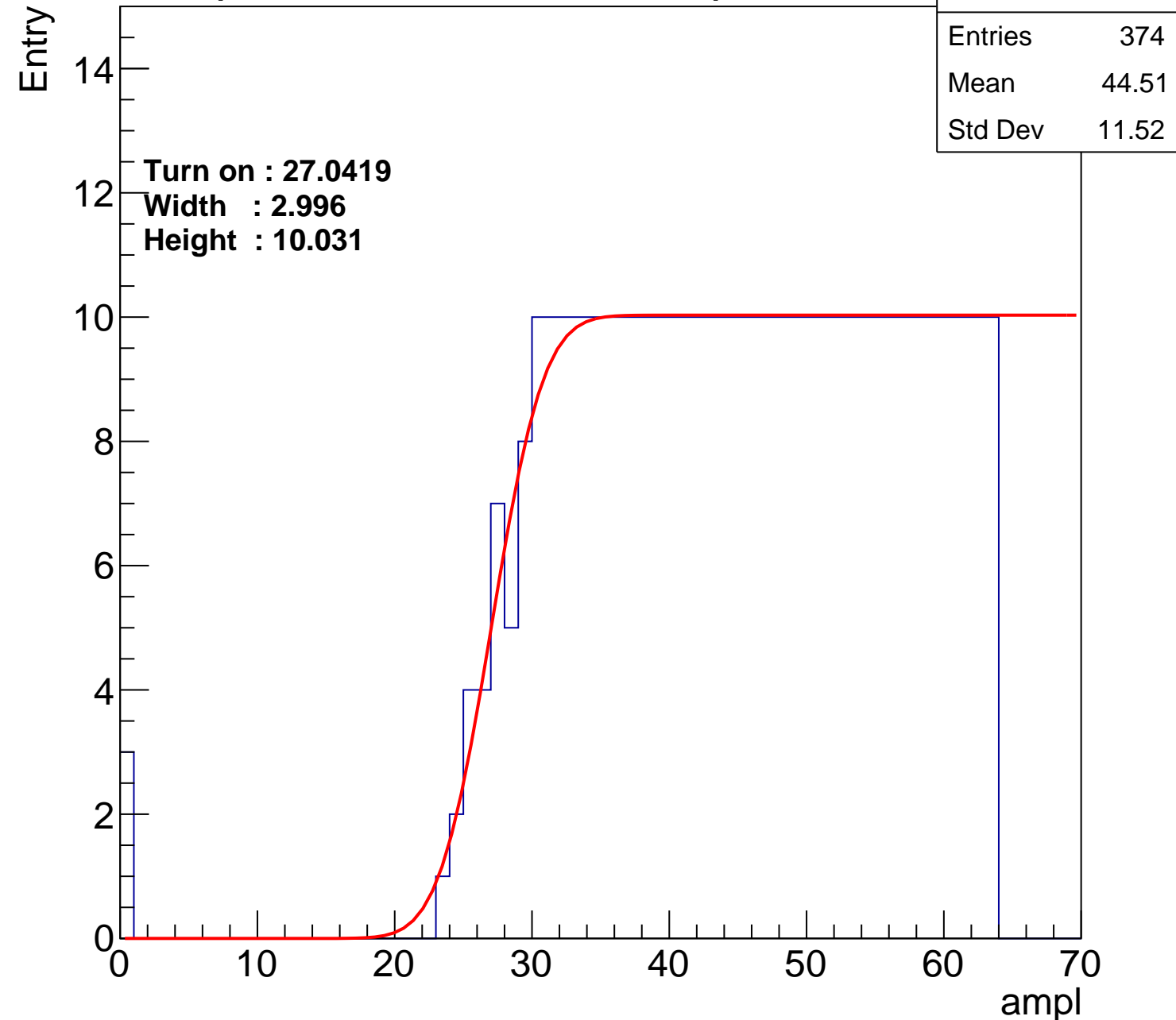
Width : 2.996

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.01
Std Dev	11.13

Turn on : 28.3590

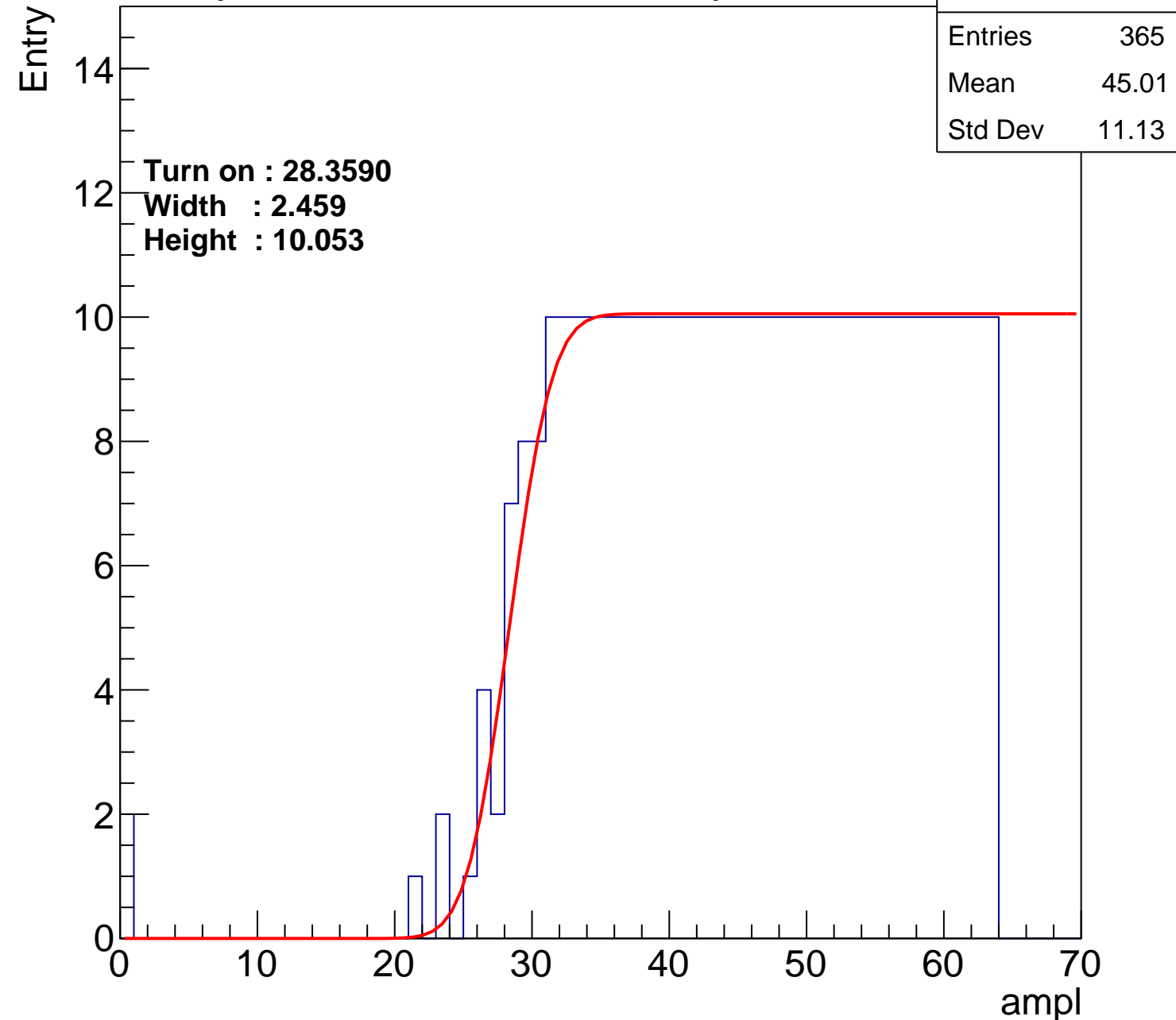
Width : 2.459

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.3275

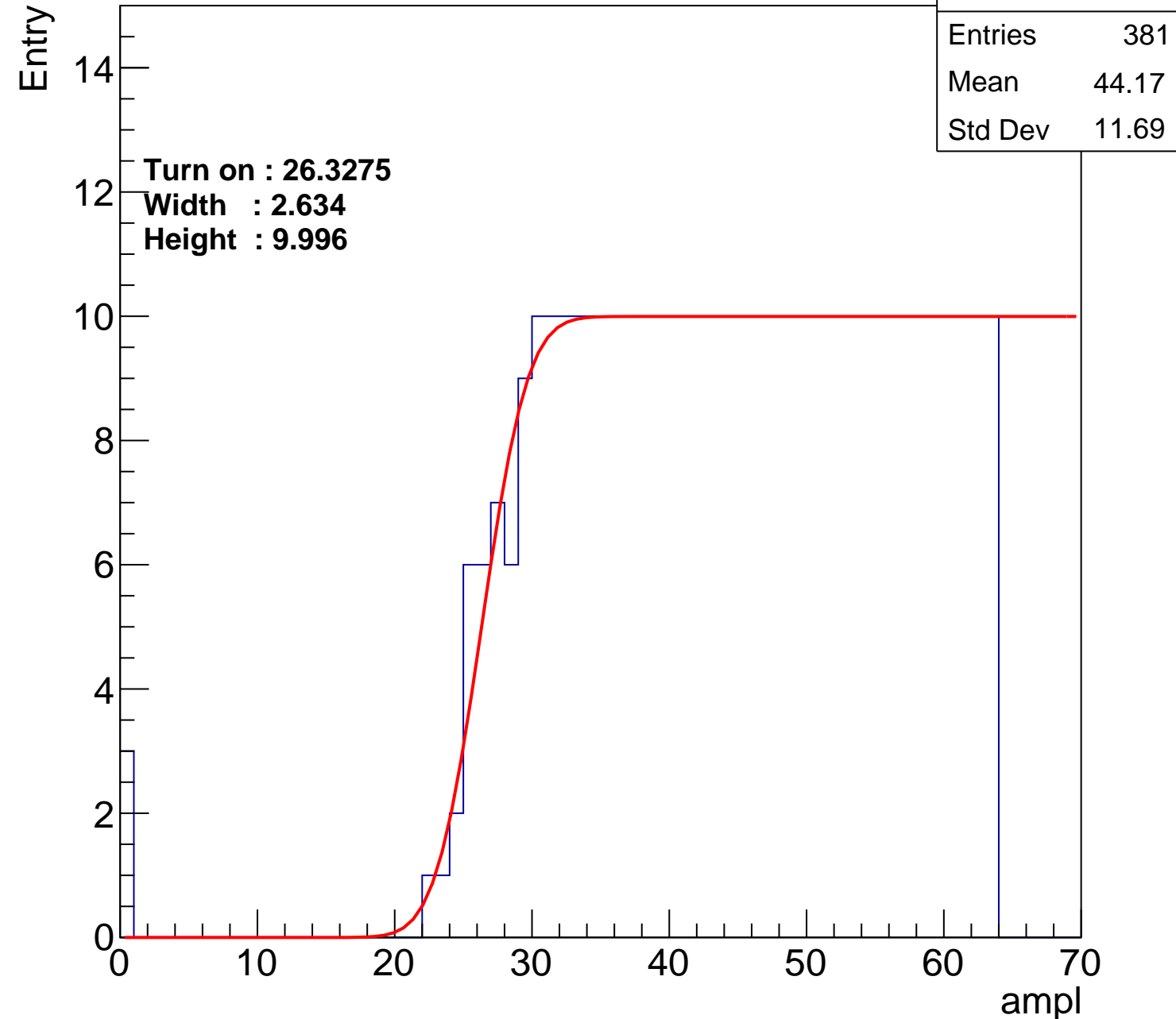
Width : 2.634

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch58

calib_packv5_042523_0143.root, FC#13, port D2

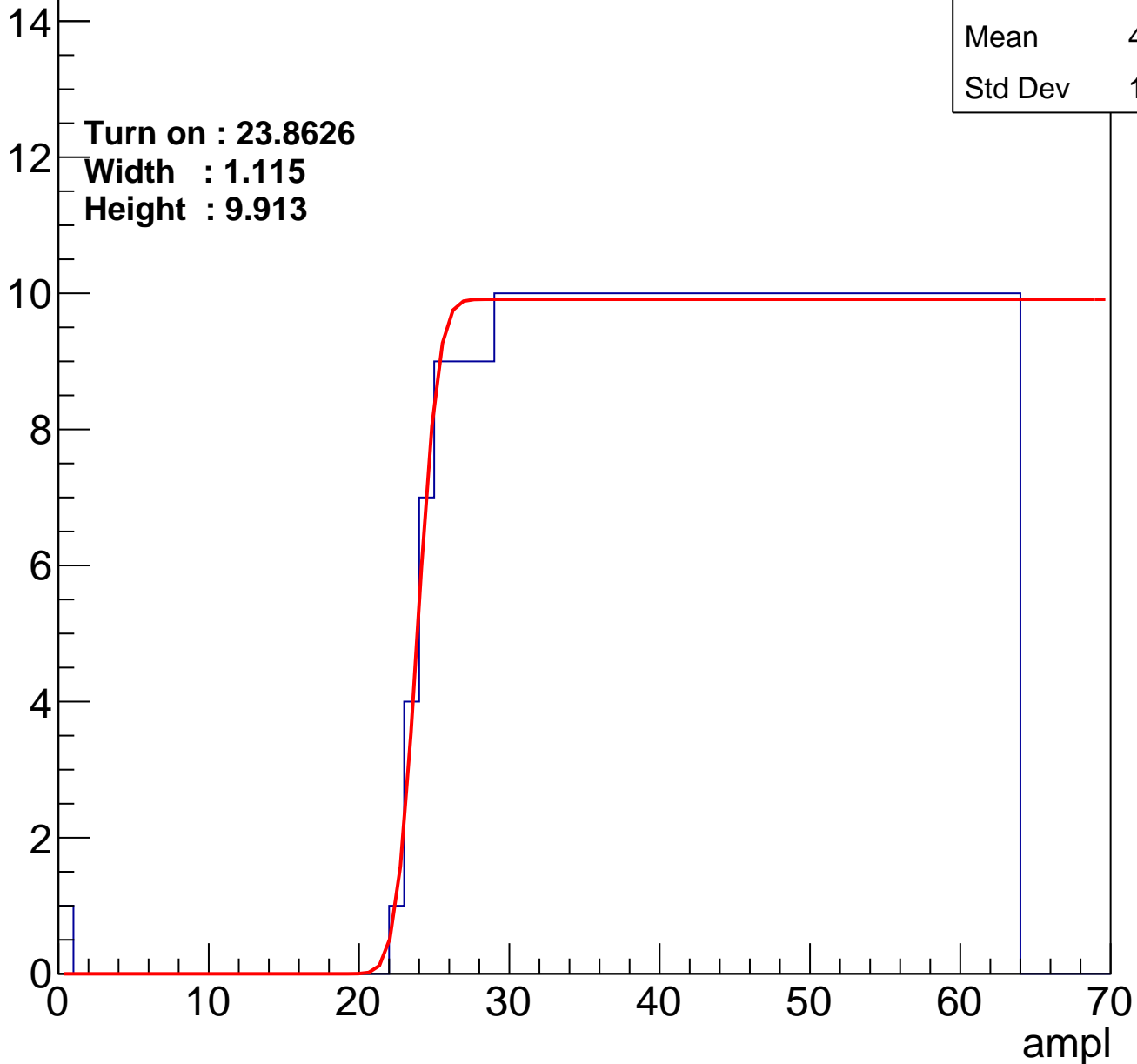
Entries	399
Mean	43.45
Std Dev	11.74

Turn on : 23.8626

Width : 1.115

Height : 9.913

Entry



B1L003S, U23-ch59

calib_packv5_042523_0143.root, FC#13, port D2

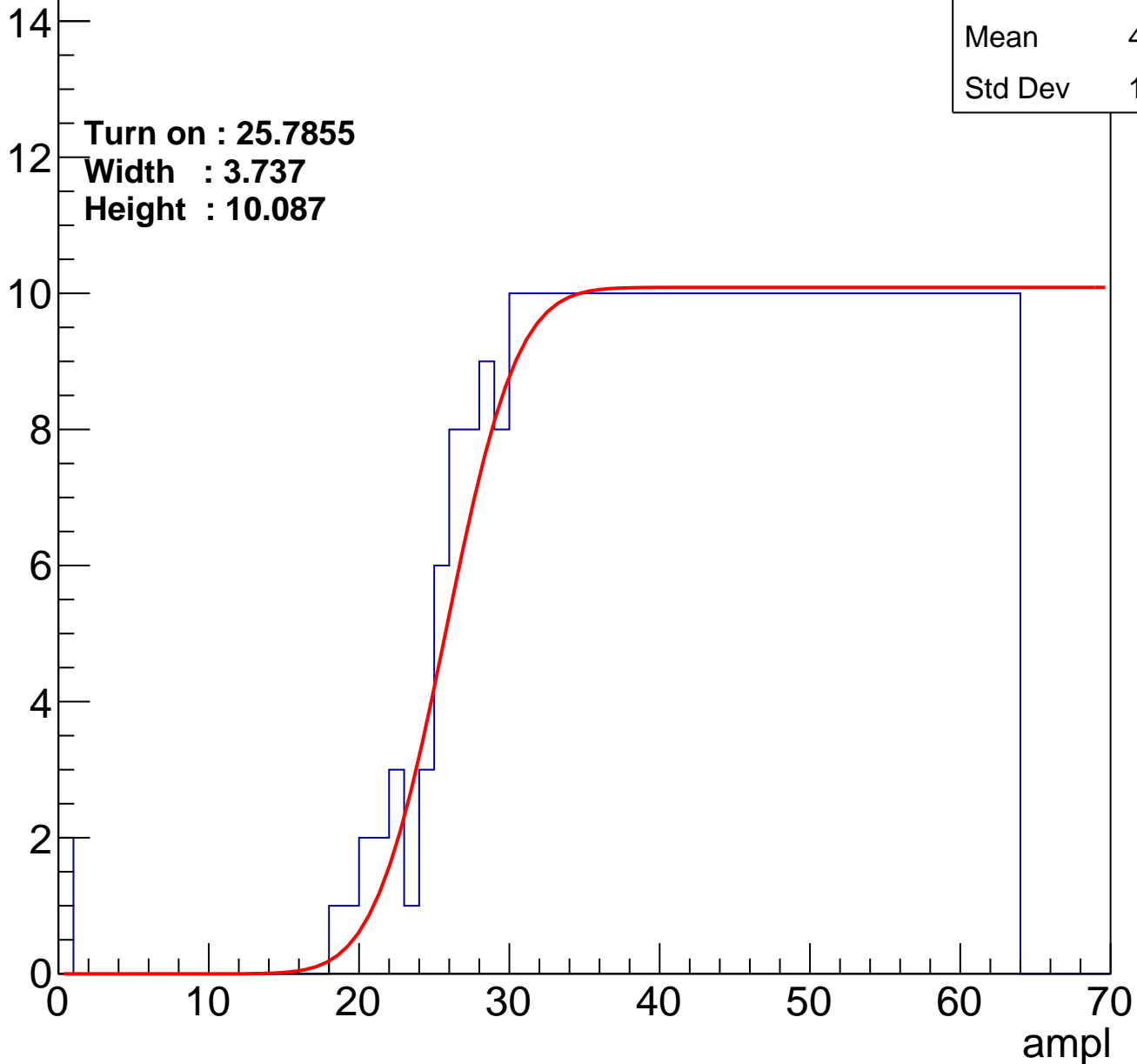
Entries	394
Mean	43.52
Std Dev	11.97

Turn on : 25.7855

Width : 3.737

Height : 10.087

Entry



B1L003S, U23-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.3347

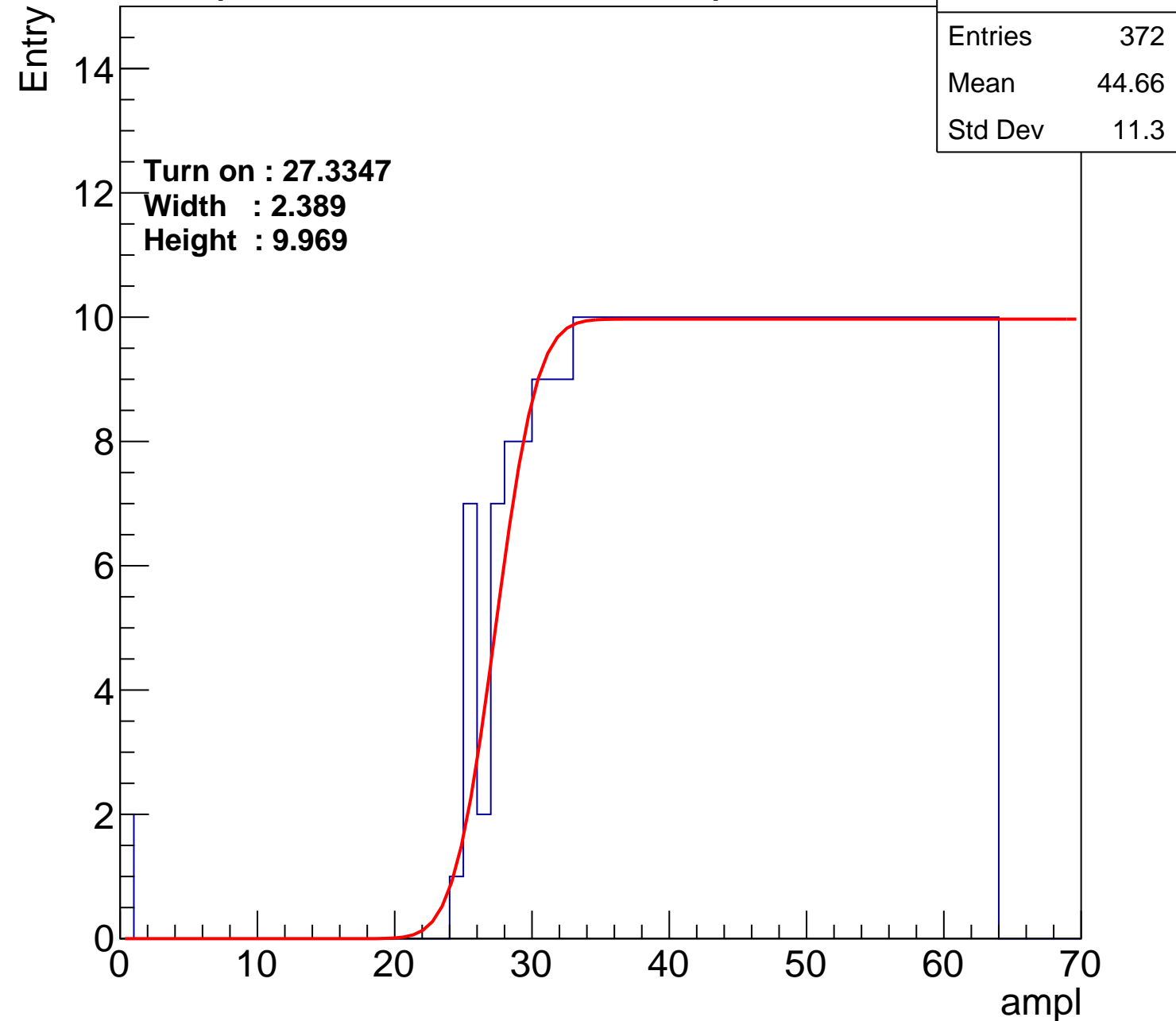
Width : 2.389

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.29
Std Dev	11.66

Turn on : 26.9540

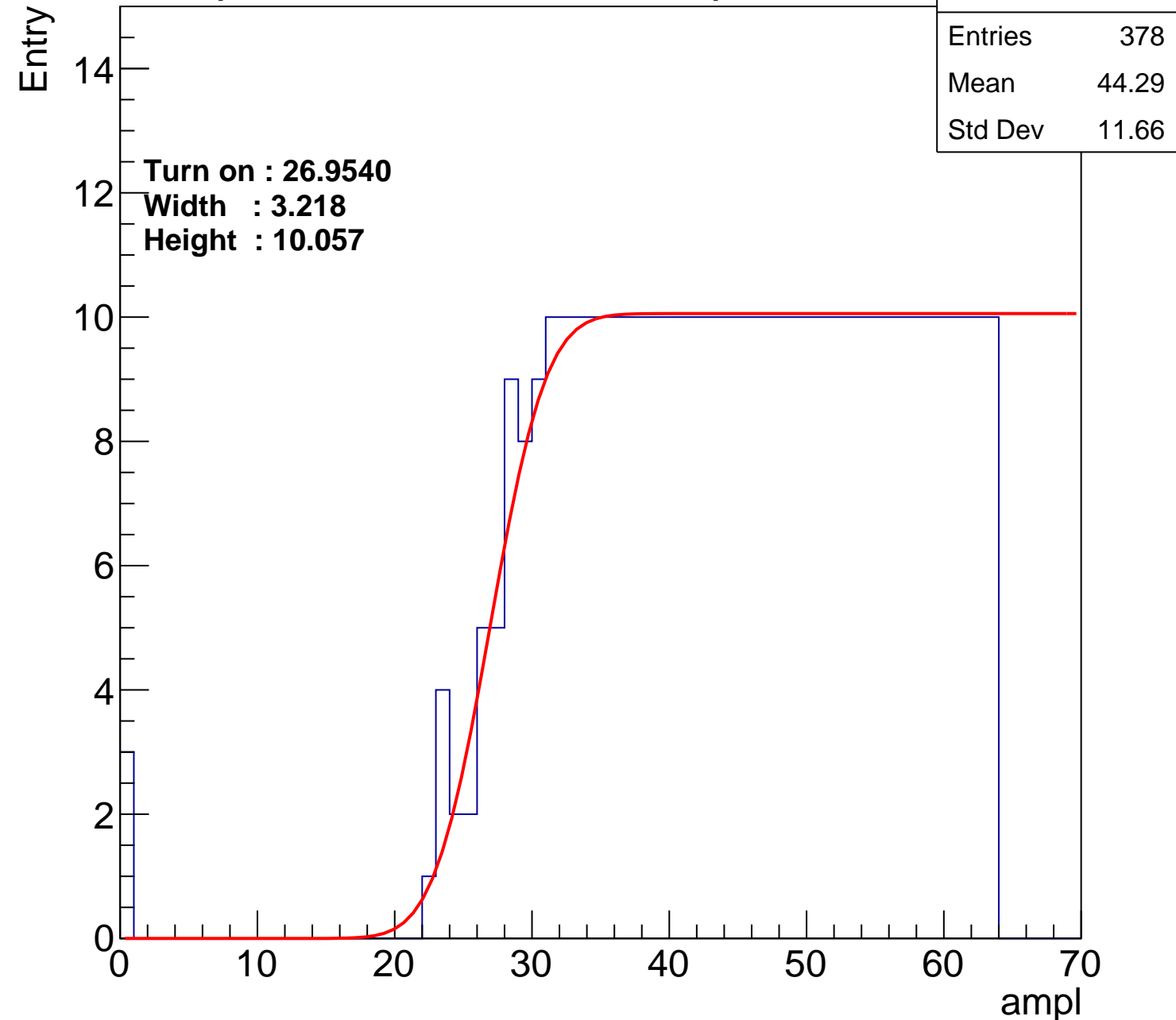
Width : 3.218

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.51
Std Dev	11.51

Std Dev	11.51
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Height : 10.008



B1L003S, U23-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.2284

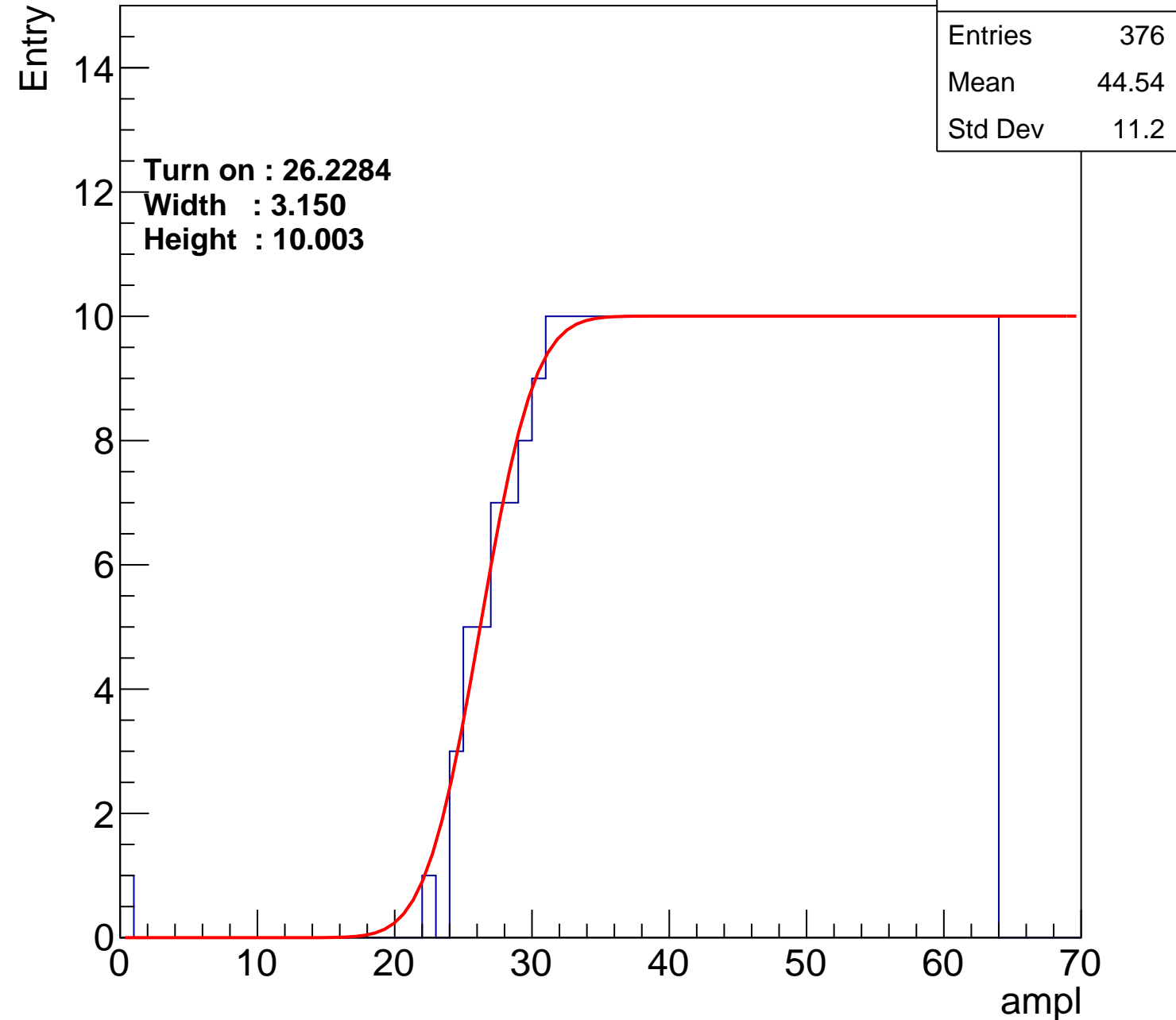
Width : 3.150

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch64

calib_packv5_042523_0143.root, FC#13, port D2

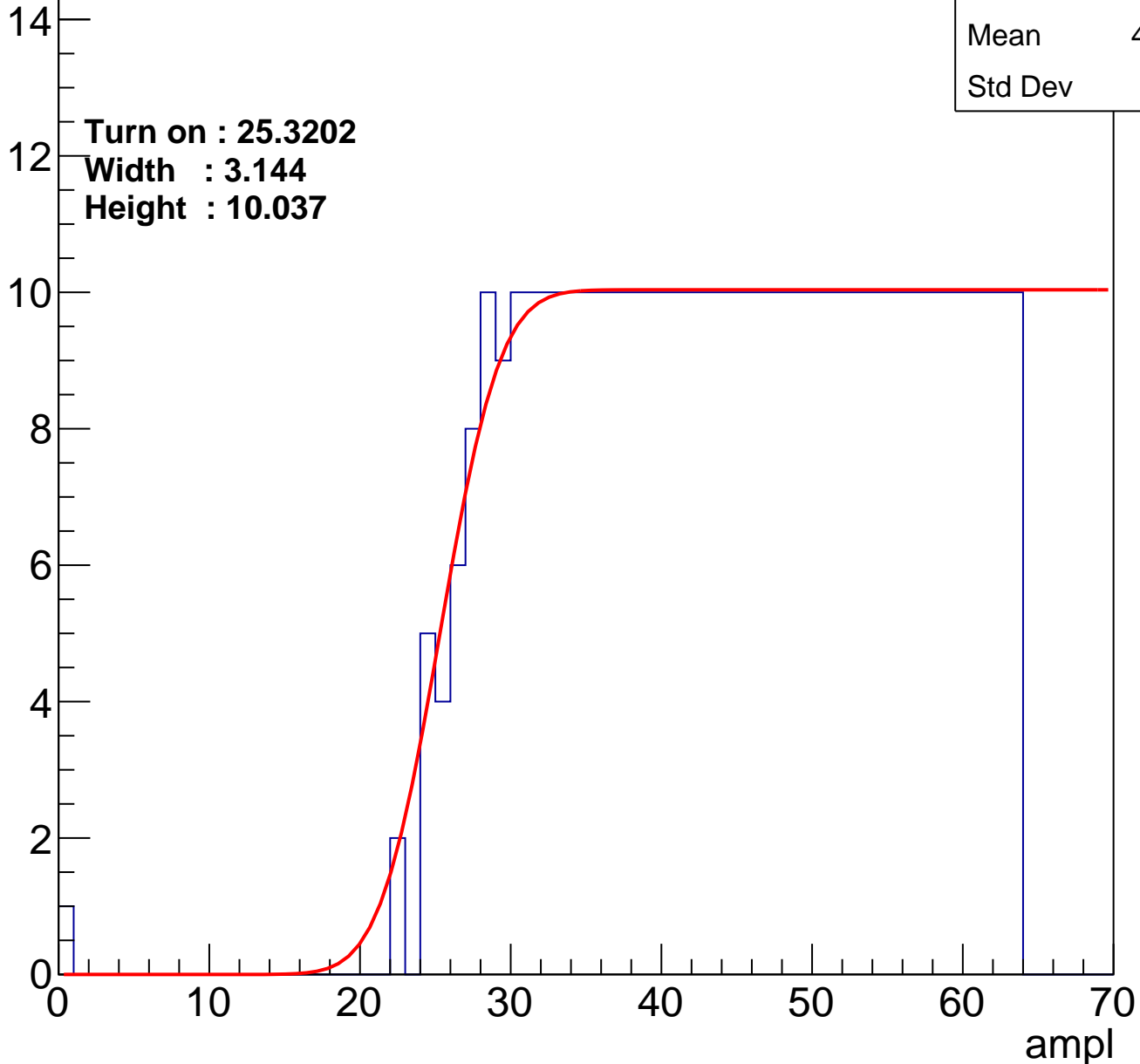
Entries	385
Mean	44.12
Std Dev	11.4

Turn on : 25.3202

Width : 3.144

Height : 10.037

Entry



B1L003S, U23-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.97
Std Dev	12.09

Turn on : 26.3434

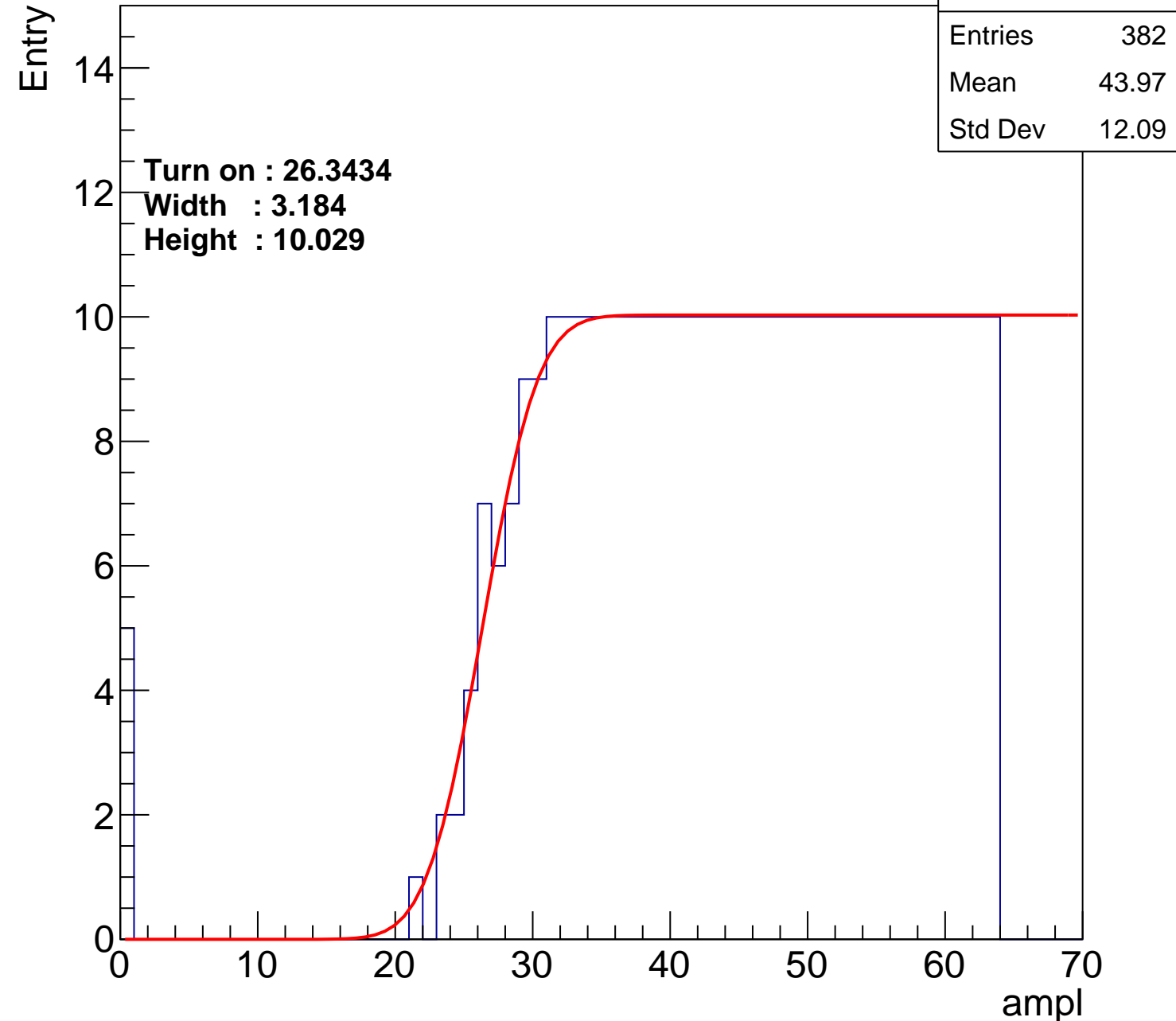
Width : 3.184

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	43.99
Std Dev	12.52

Turn on : 27.0788

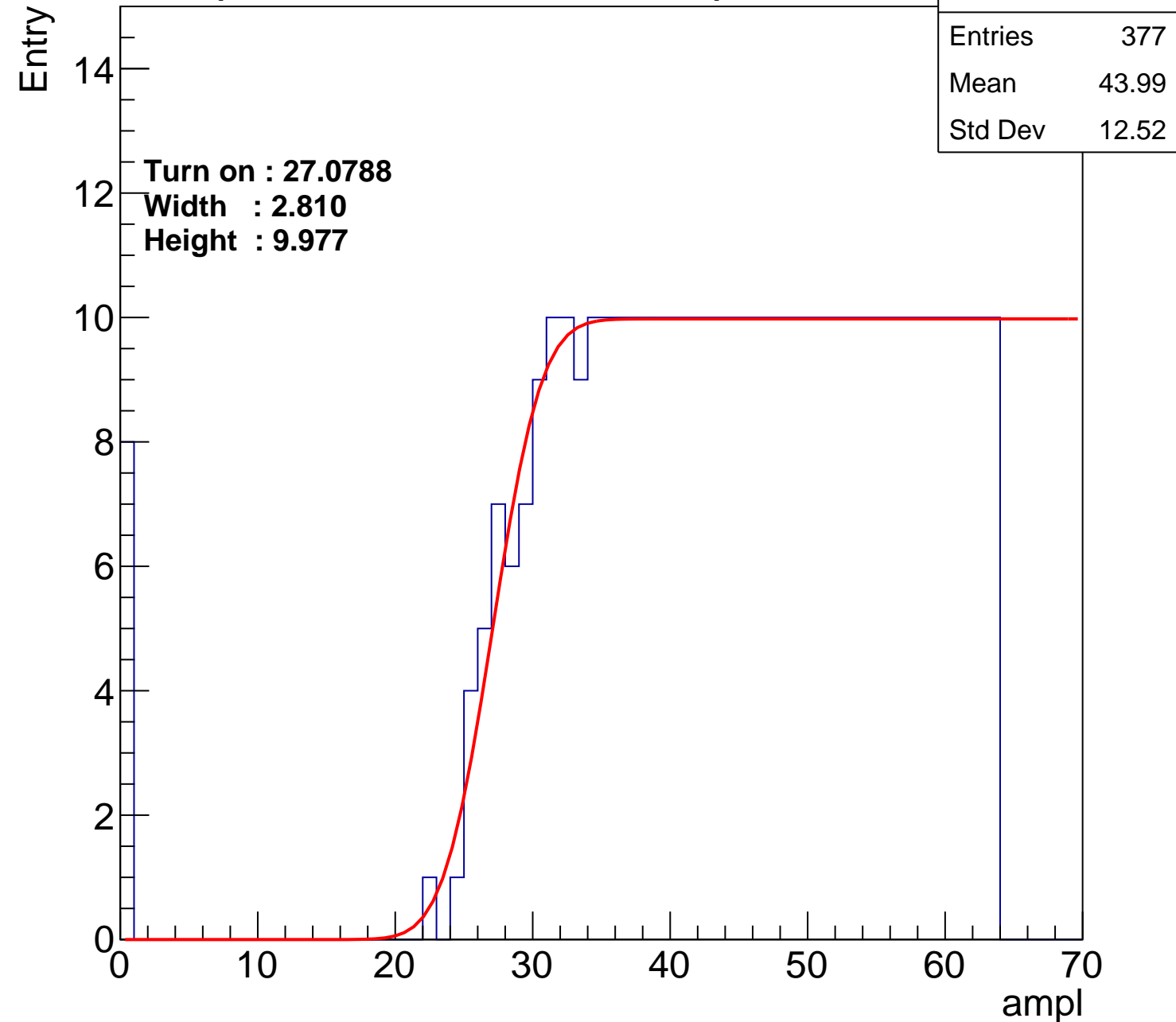
Width : 2.810

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.9484
Width : 2.906
Height : 10.050



B1L003S, U23-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.36
Std Dev	11.78

Turn on : 27.6648

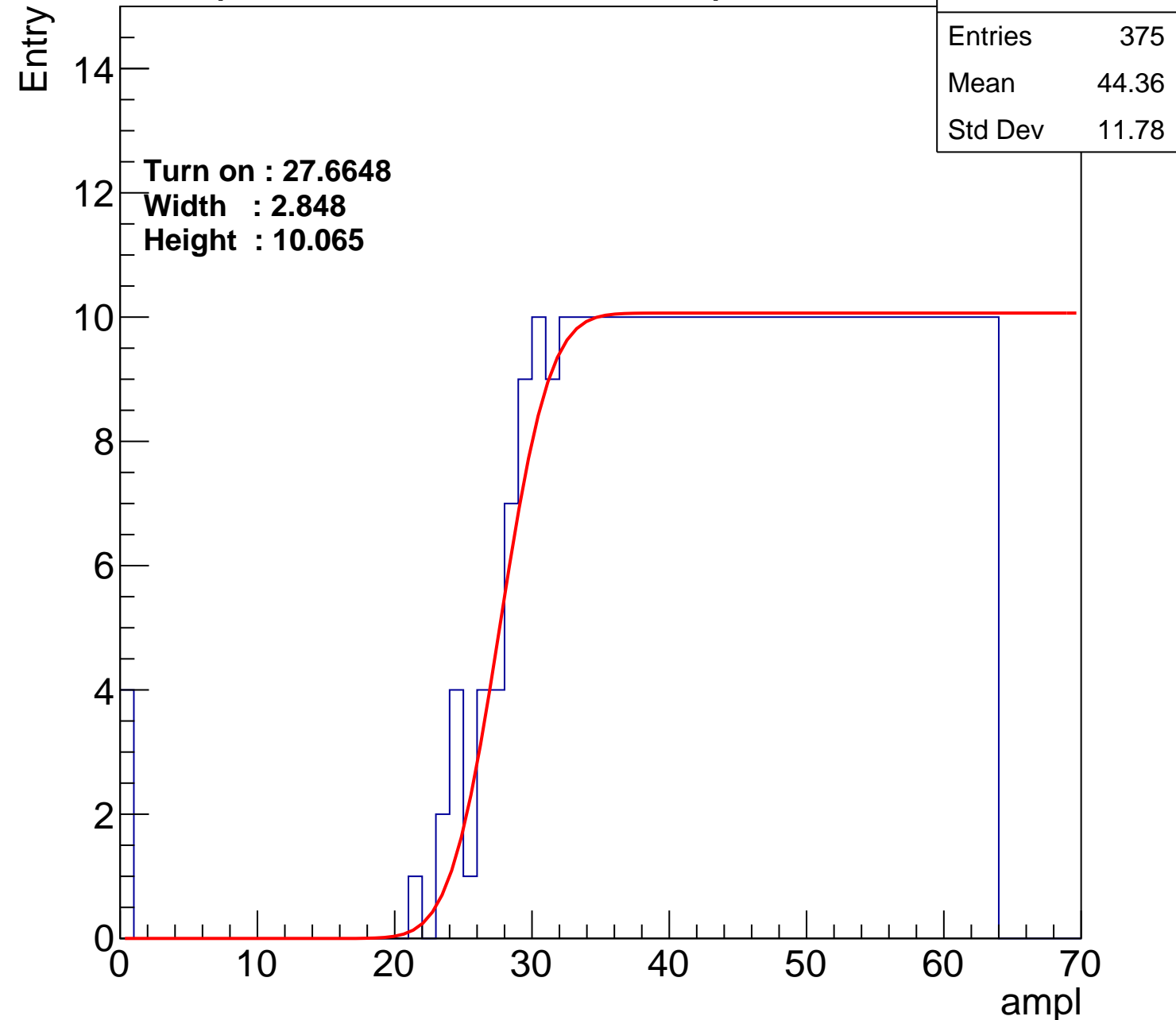
Width : 2.848

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.22
Std Dev	10.84

Turn on : 28.0630

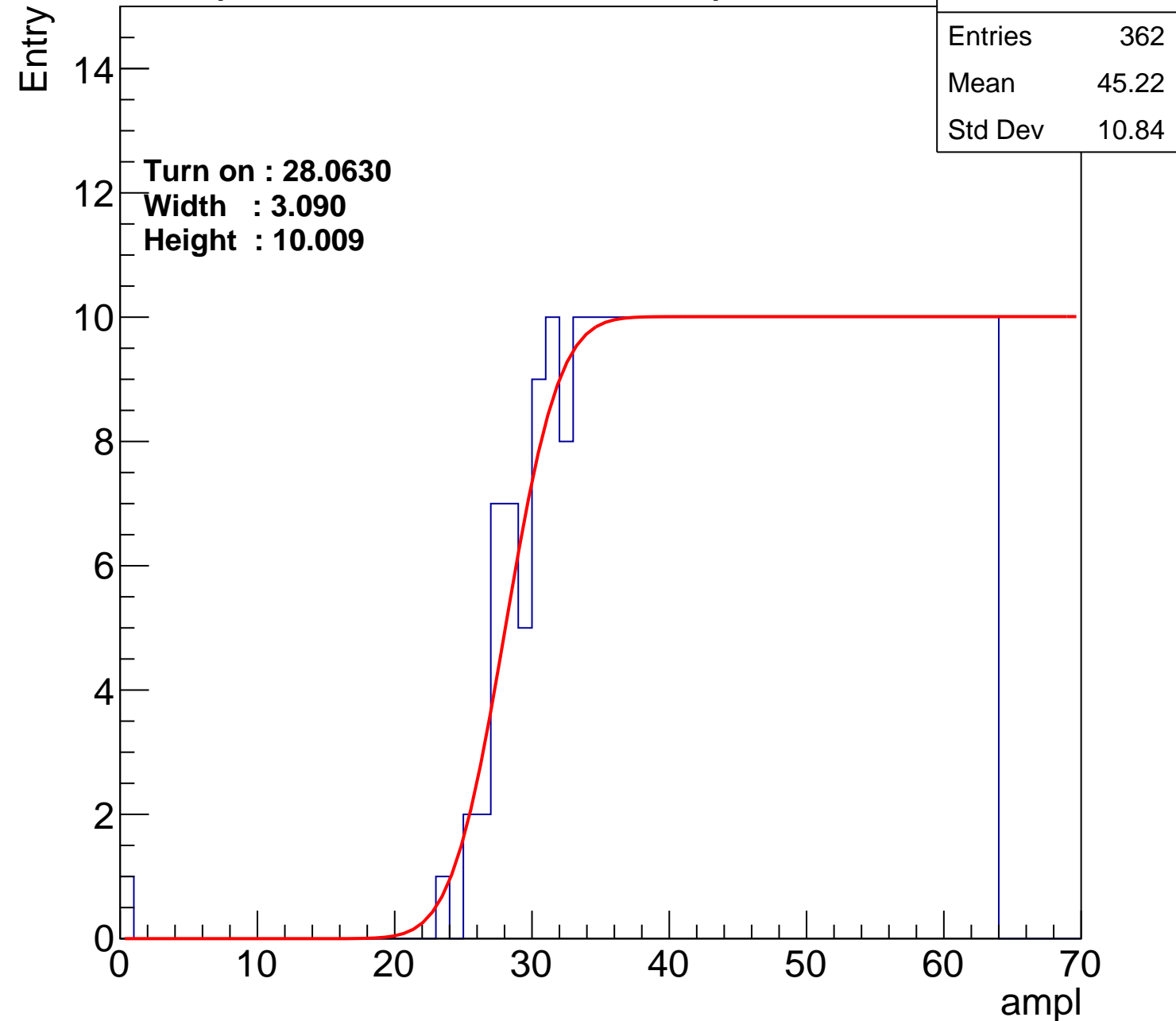
Width : 3.090

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch70

calib_packv5_042523_0143.root, FC#13, port D2

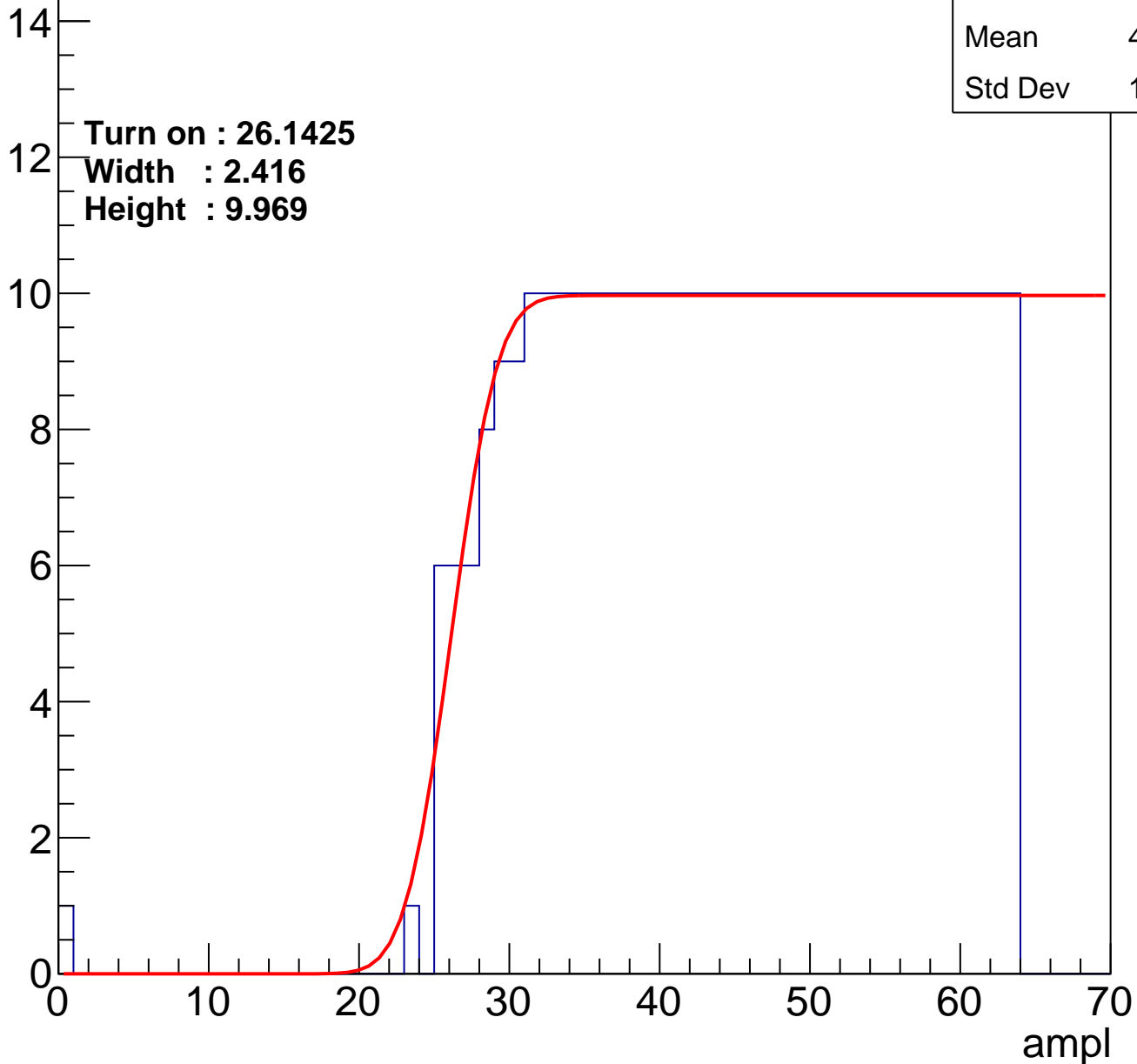
Entries	376
Mean	44.56
Std Dev	11.16

Turn on : 26.1425

Width : 2.416

Height : 9.969

Entry



B1L003S, U23-ch71

calib_packv5_042523_0143.root, FC#13, port D2

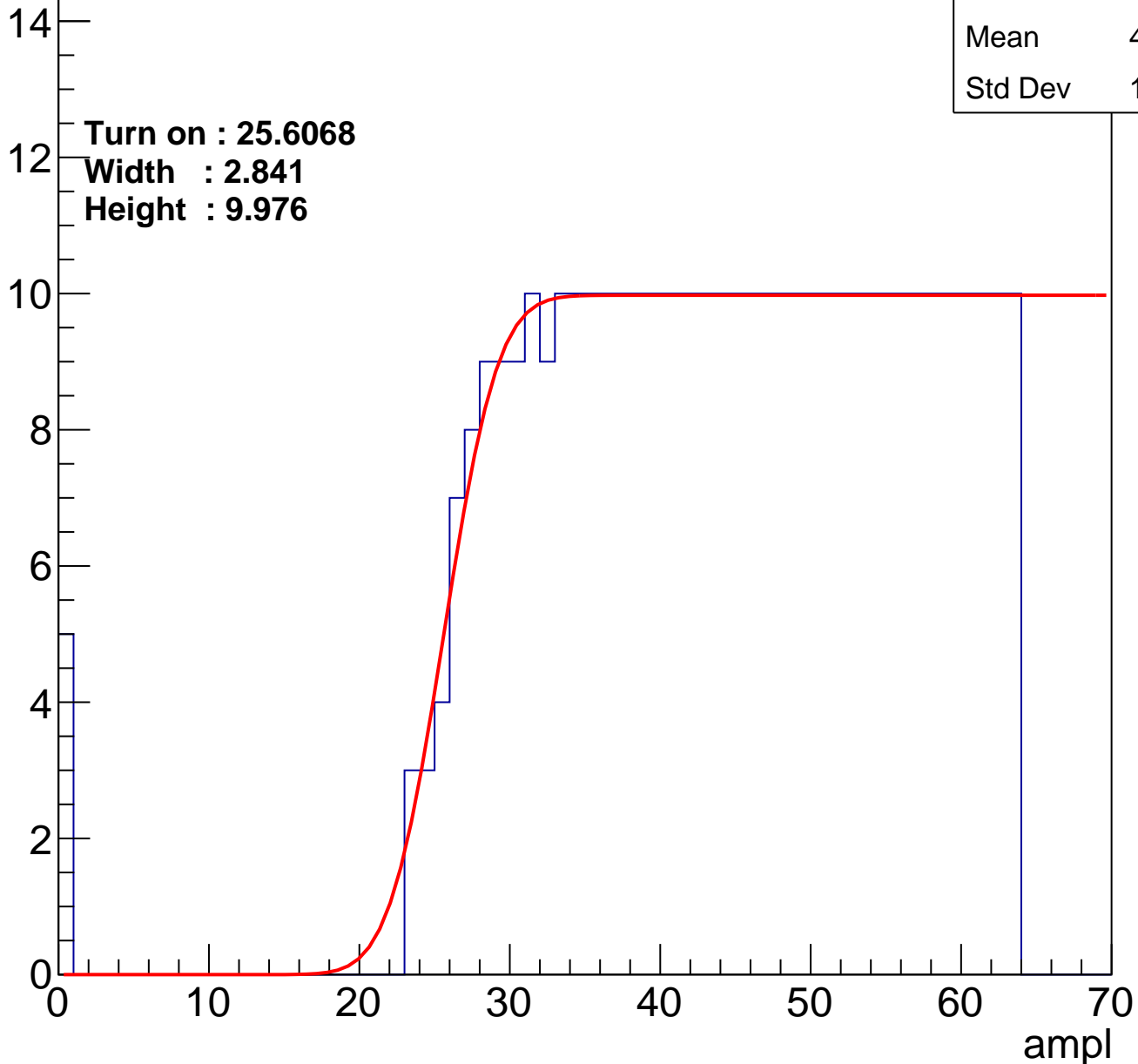
Entries	386
Mean	43.78
Std Dev	12.16

Turn on : 25.6068

Width : 2.841

Height : 9.976

Entry



B1L003S, U23-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
---------	-----

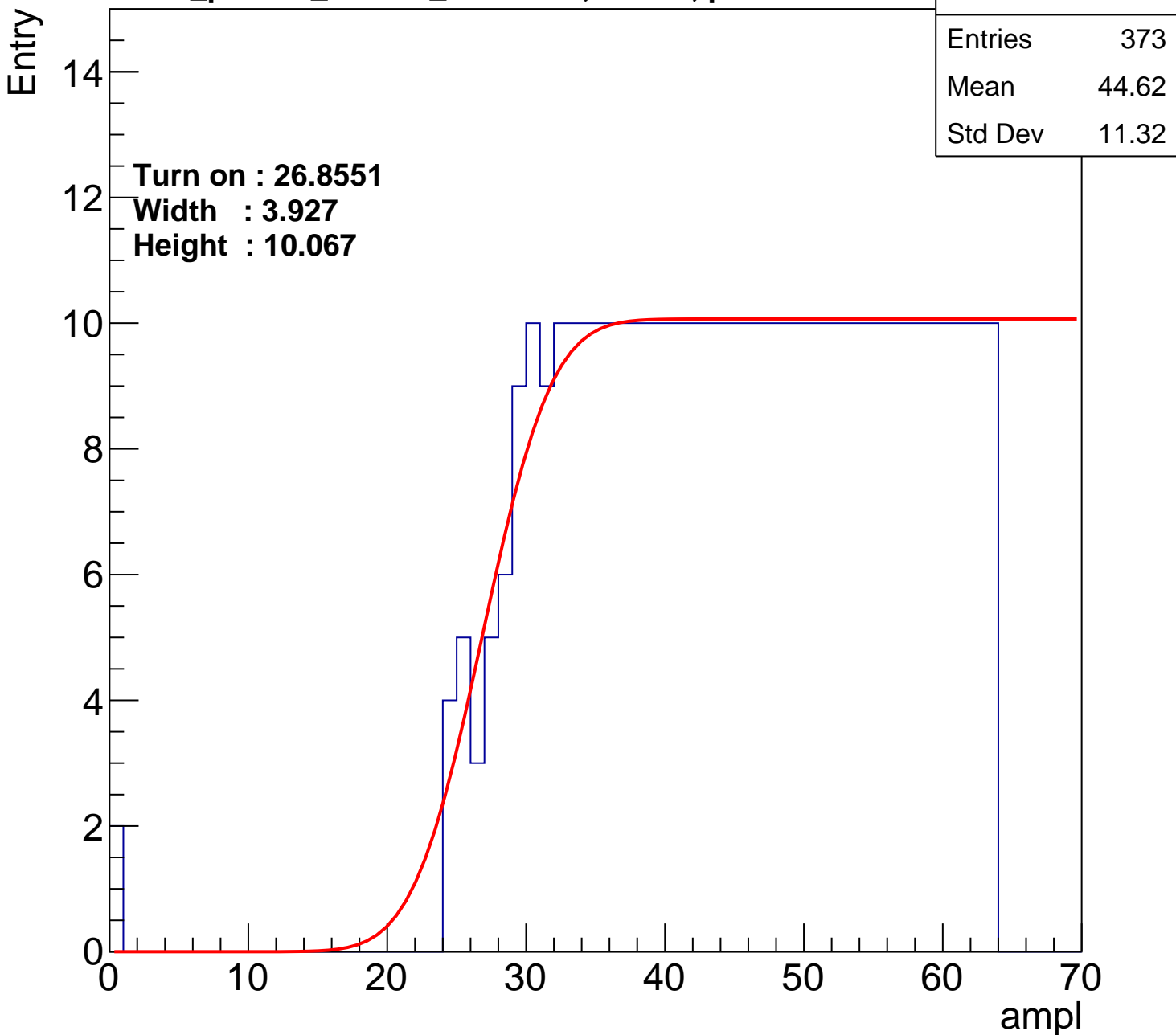
Mean	44.62
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Std Dev	11.32
---------	-------

Turn on : 26.8551

Width : 3.927

Height : 10.067



B1L003S, U23-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 28.1055

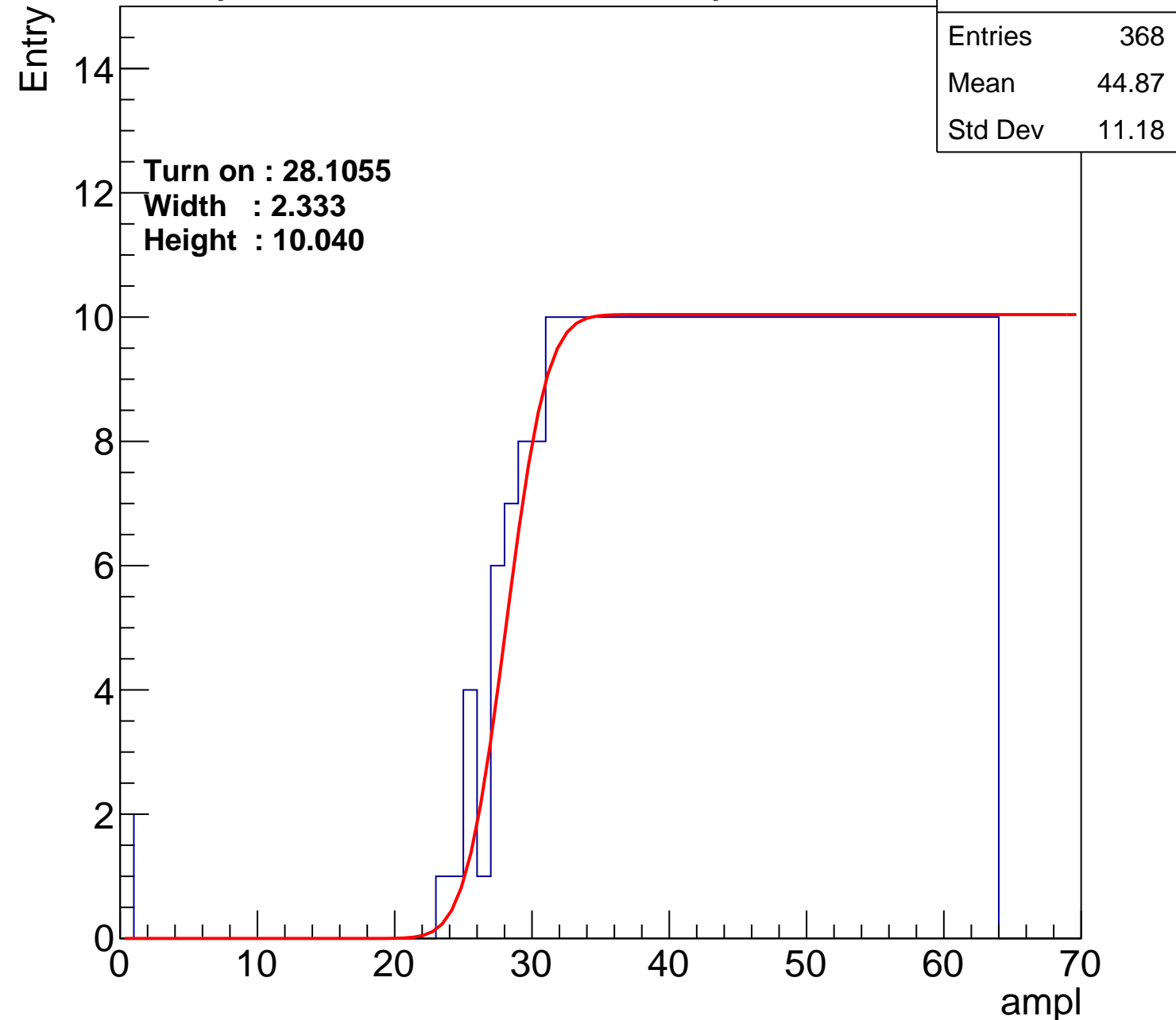
Width : 2.333

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.7
Std Dev	11.7

Turn on : 27.7571

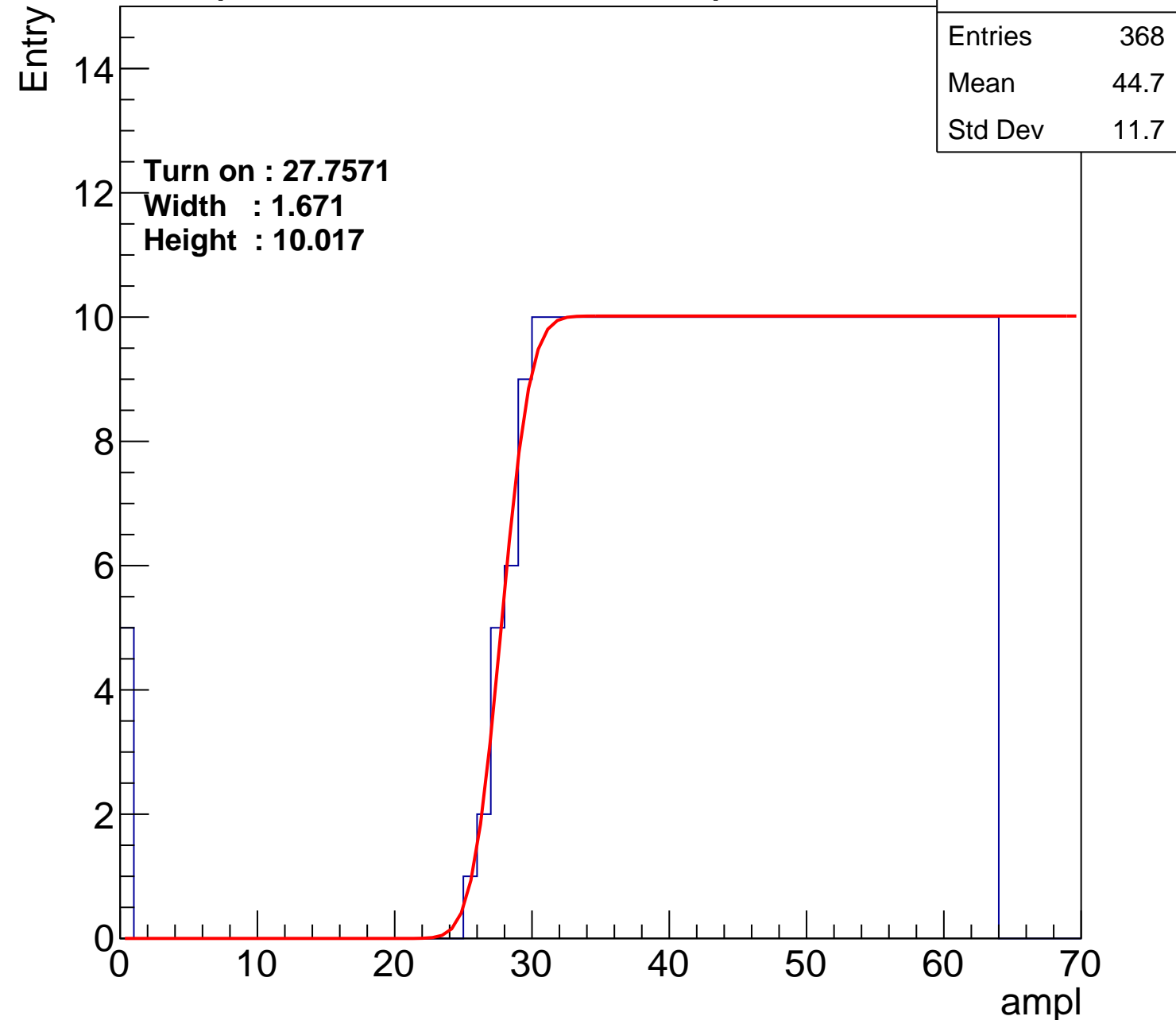
Width : 1.671

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch75

calib_packv5_042523_0143.root, FC#13, port D2

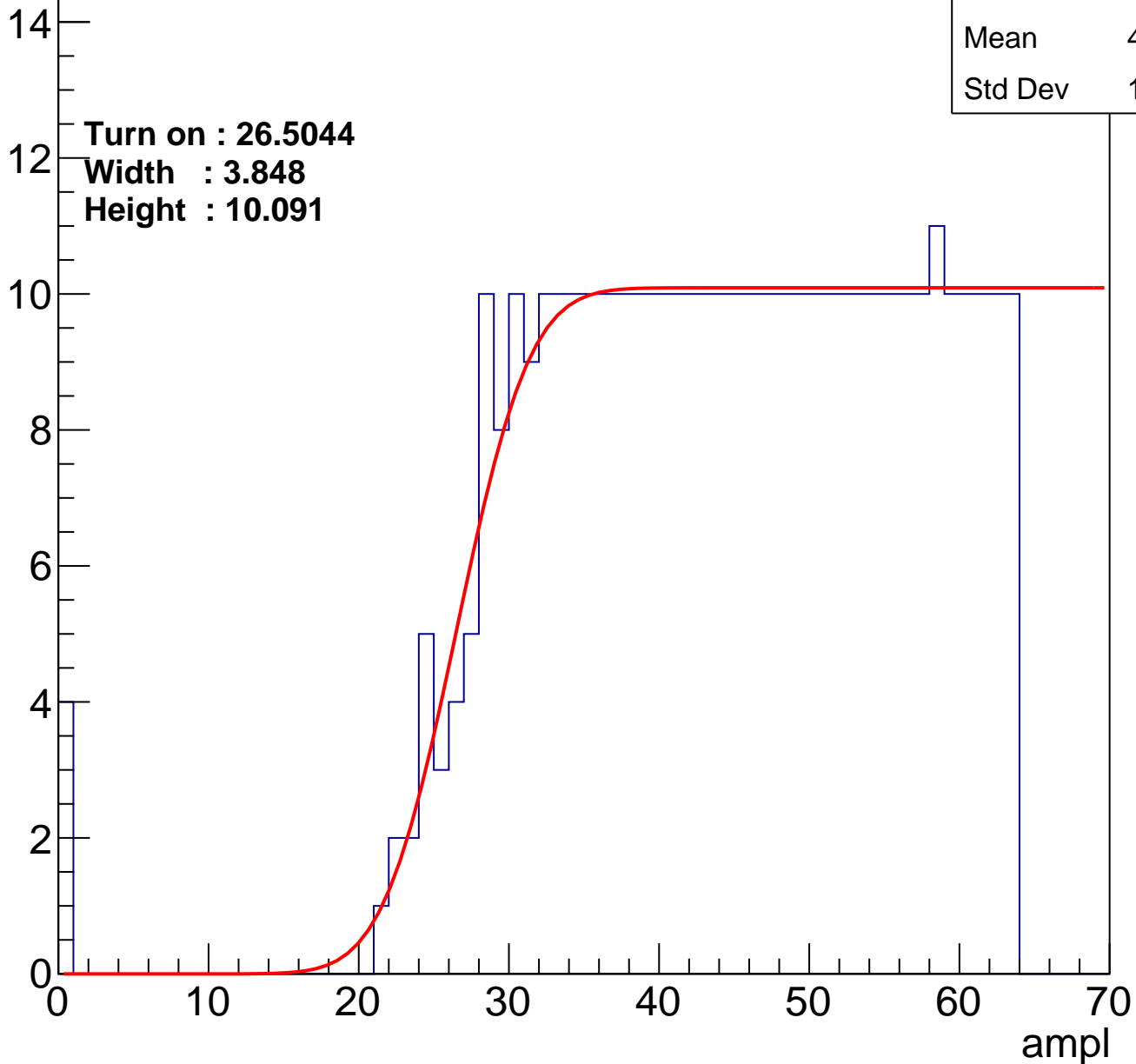
Entry

Entries	384
Mean	43.99
Std Dev	11.99

Turn on : 26.5044

Width : 3.848

Height : 10.091



B1L003S, U23-ch76

calib_packv5_042523_0143.root, FC#13, port D2

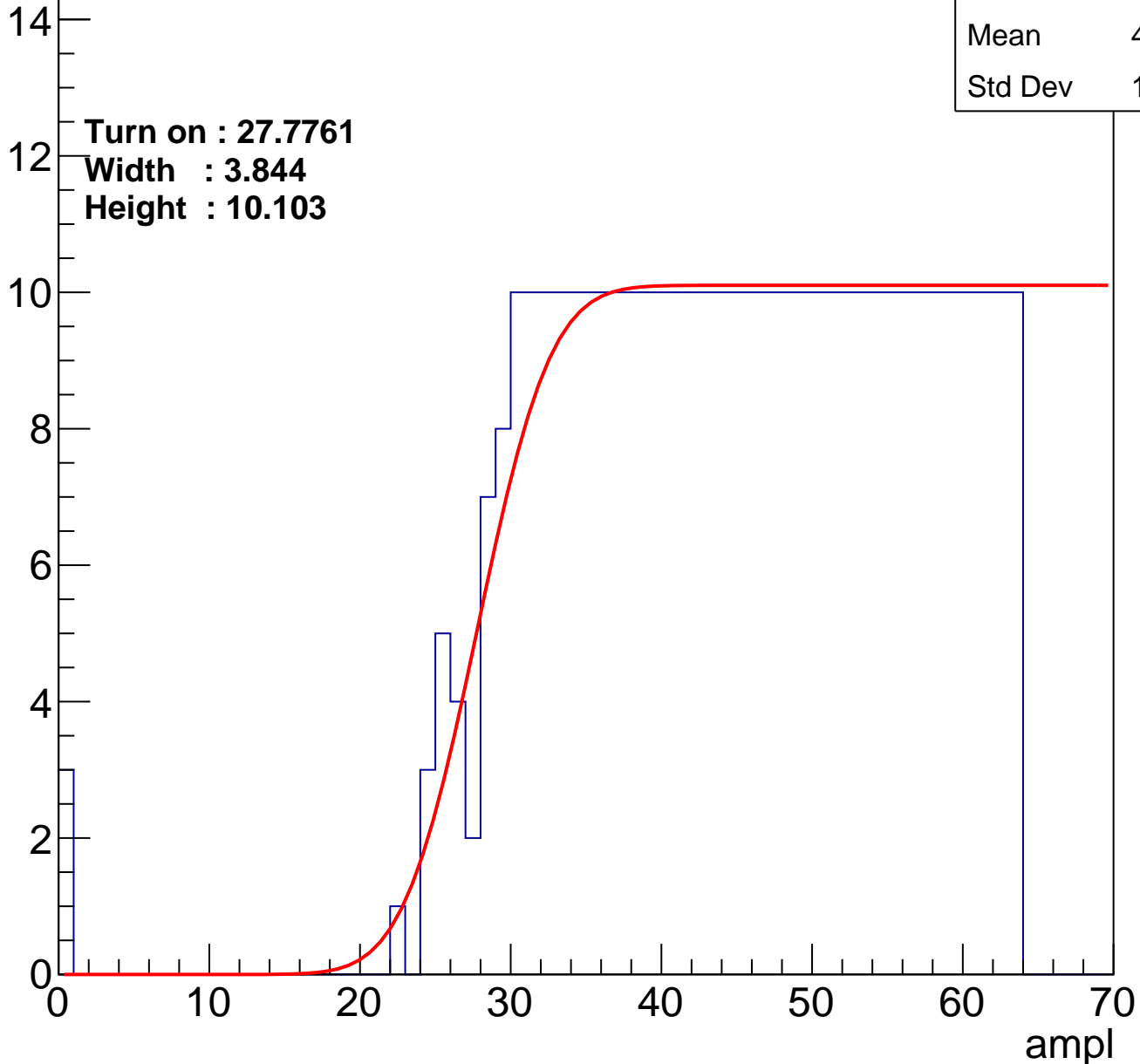
Entries	373
Mean	44.54
Std Dev	11.52

Turn on : 27.7761

Width : 3.844

Height : 10.103

Entry



B1L003S, U23-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	44.02
Std Dev	11.63

Turn on : 25.8029

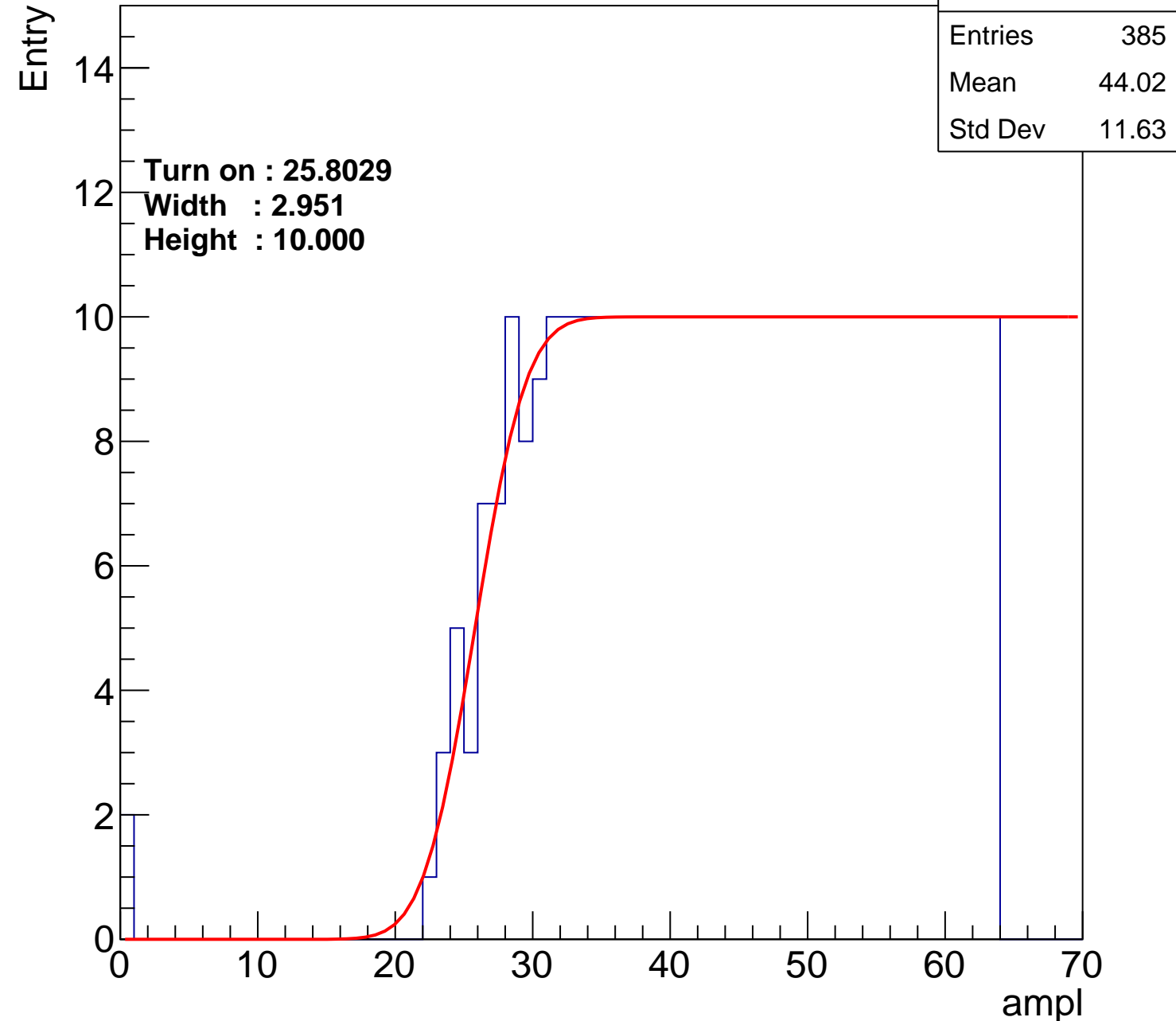
Width : 2.951

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch78

calib_packv5_042523_0143.root, FC#13, port D2

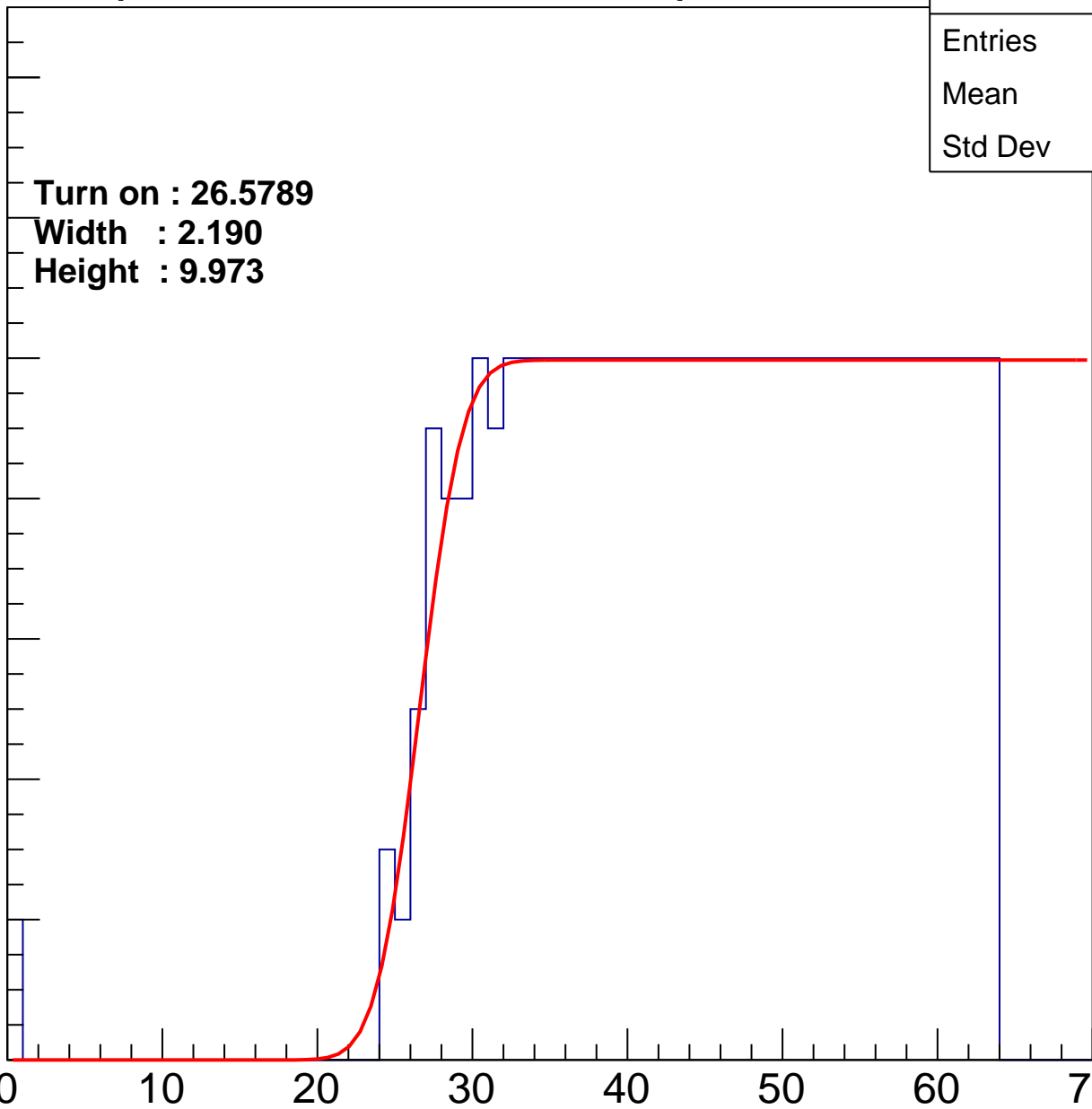
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5789
Width : 2.190
Height : 9.973

Entries	376
Mean	44.49
Std Dev	11.35

ampl



B1L003S, U23-ch79

calib_packv5_042523_0143.root, FC#13, port D2

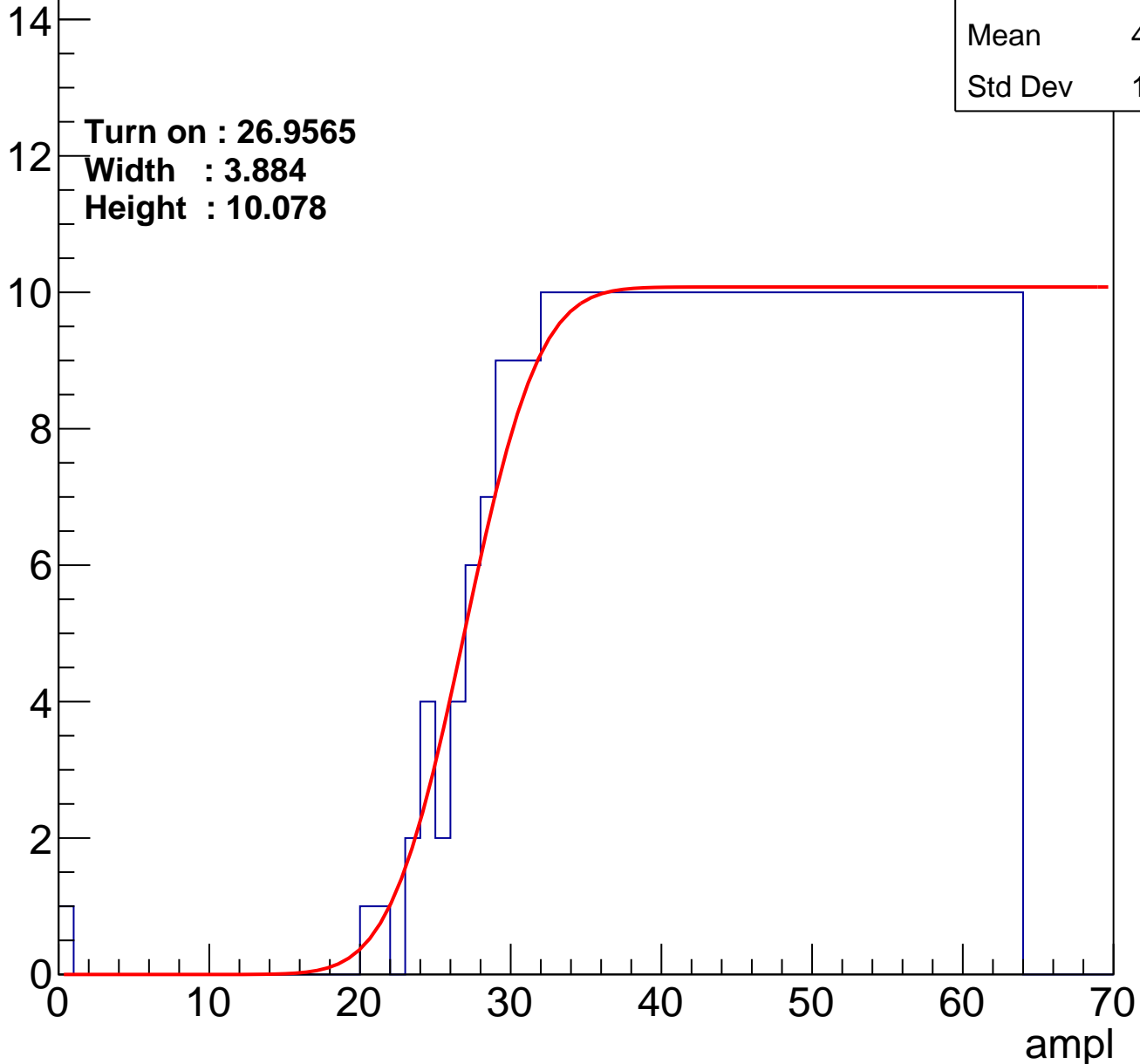
Entries	375
Mean	44.55
Std Dev	11.25

Turn on : 26.9565

Width : 3.884

Height : 10.078

Entry



B1L003S, U23-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.1697

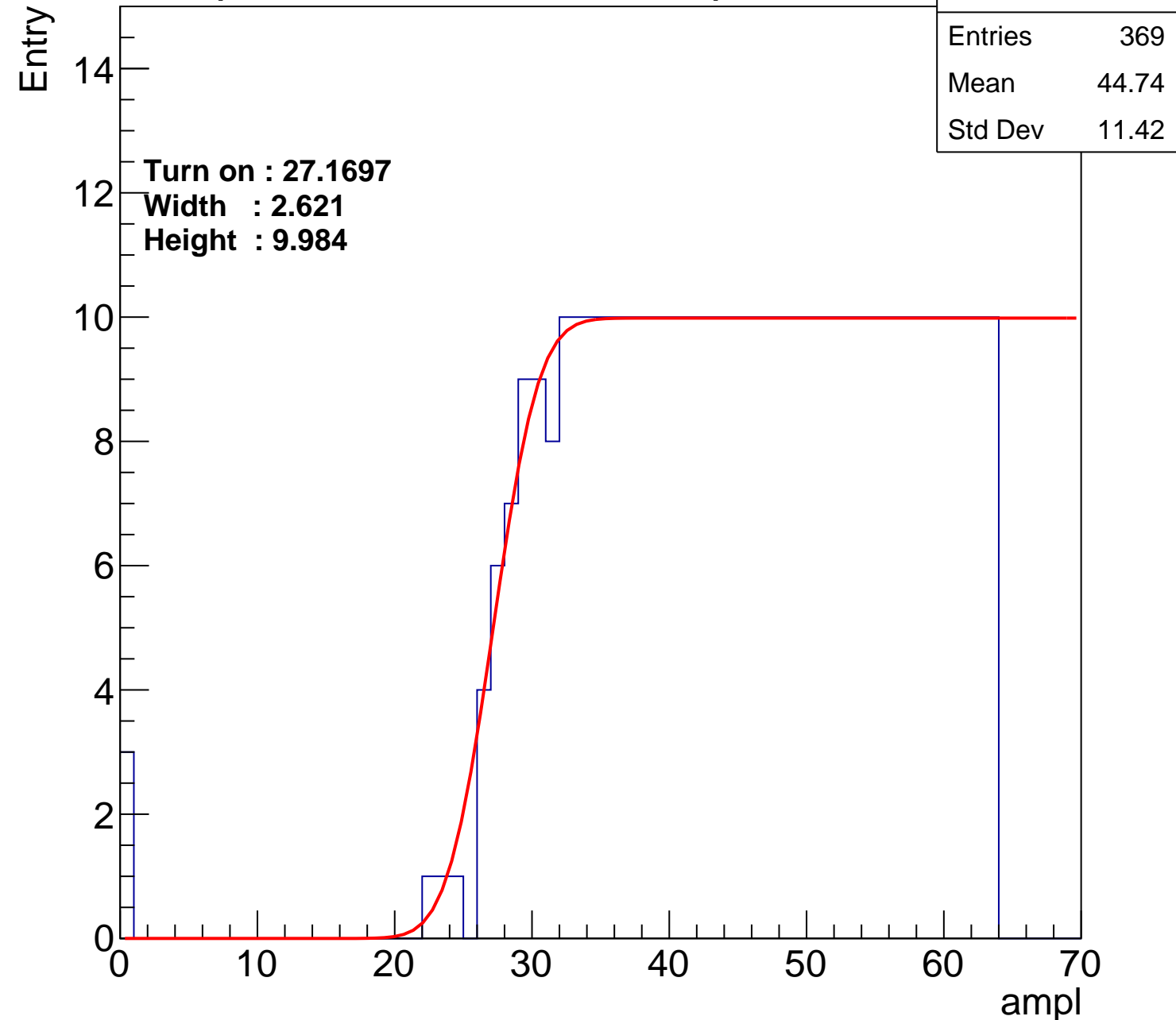
Width : 2.621

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 26.9617

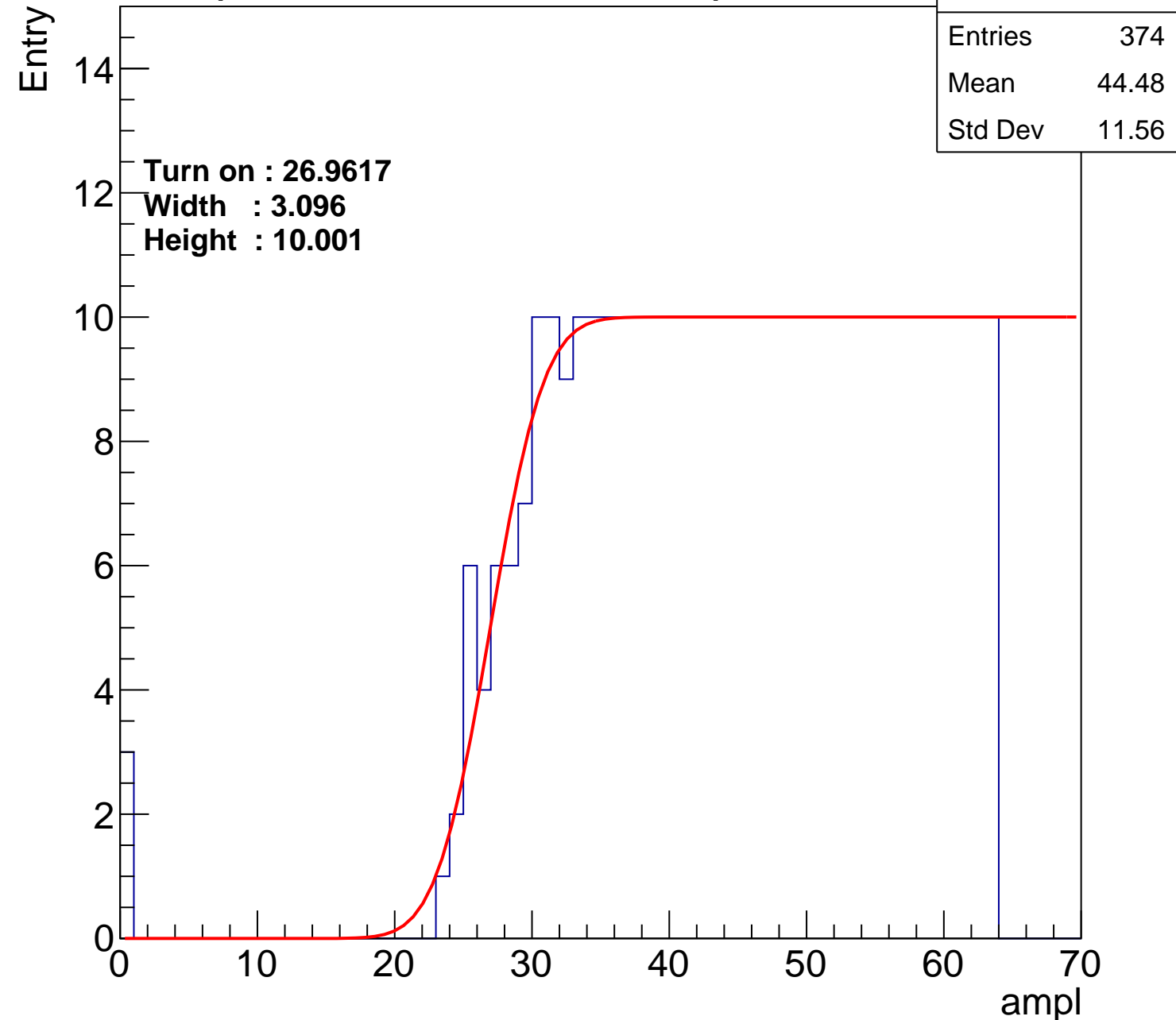
Width : 3.096

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.76
Std Dev	12.31

Turn on : 26.6327

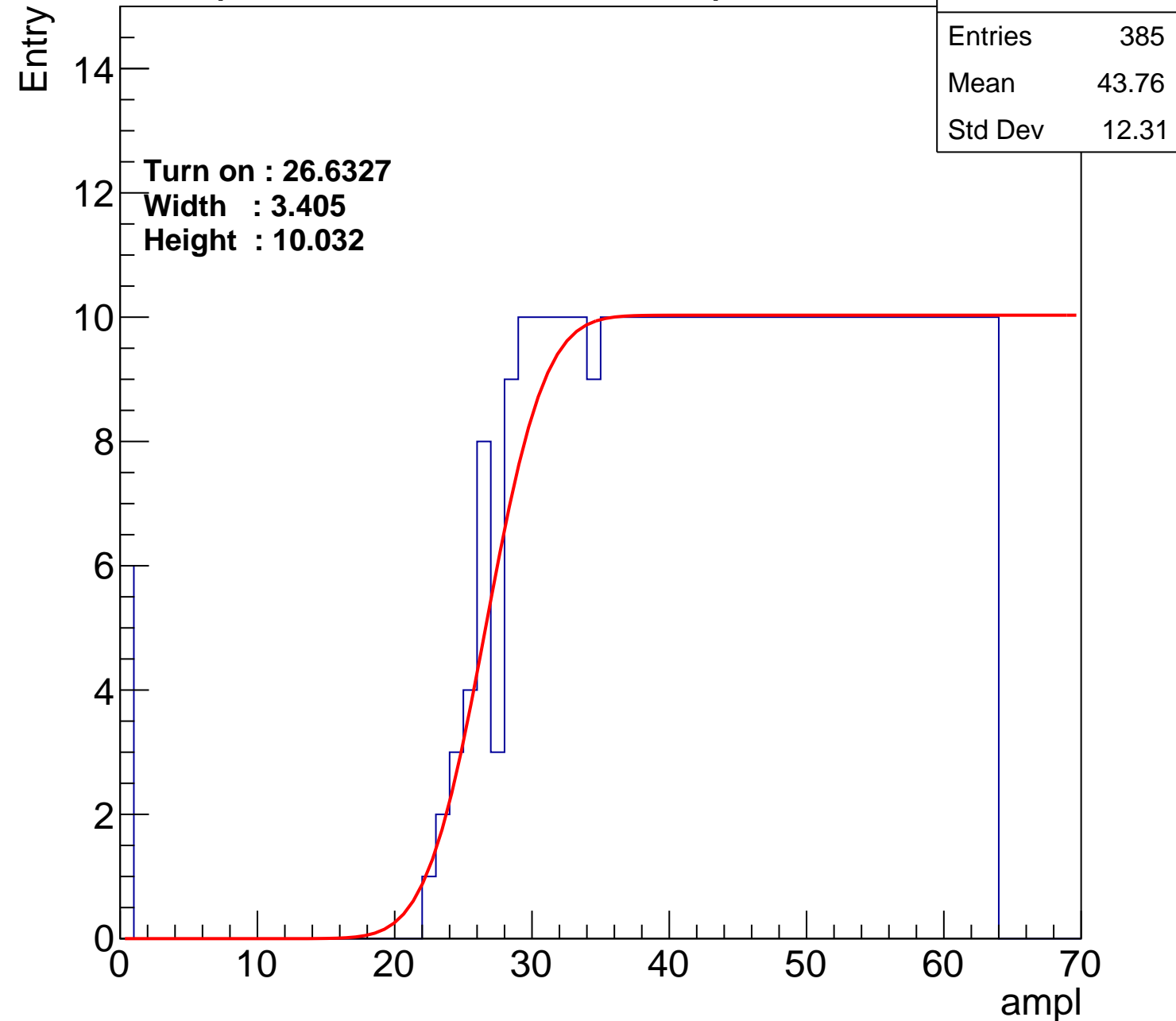
Width : 3.405

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.21
Std Dev	11.2

Turn on : 28.4481

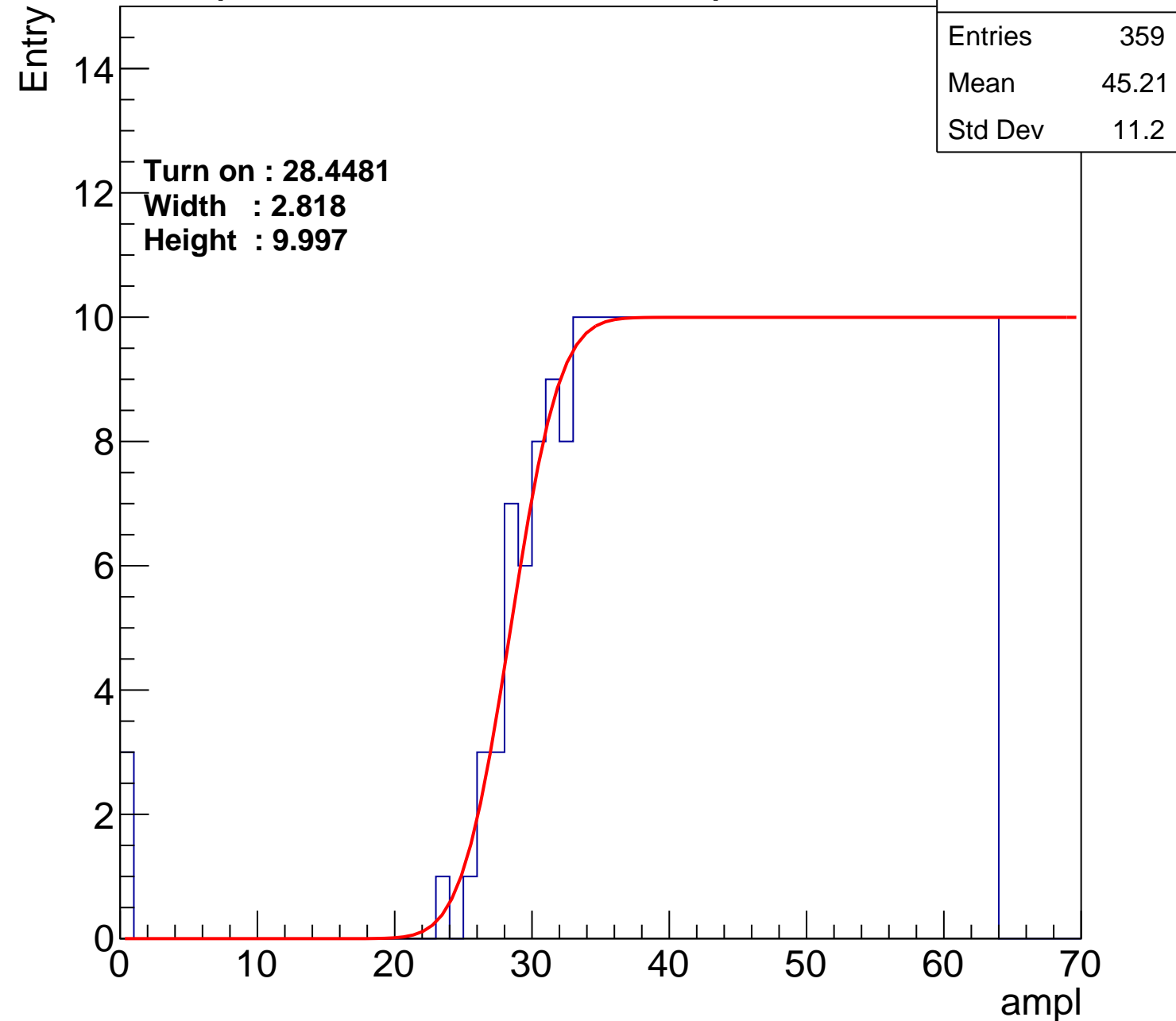
Width : 2.818

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	406
Mean	42.99
Std Dev	12.18

Turn on : 23.2062

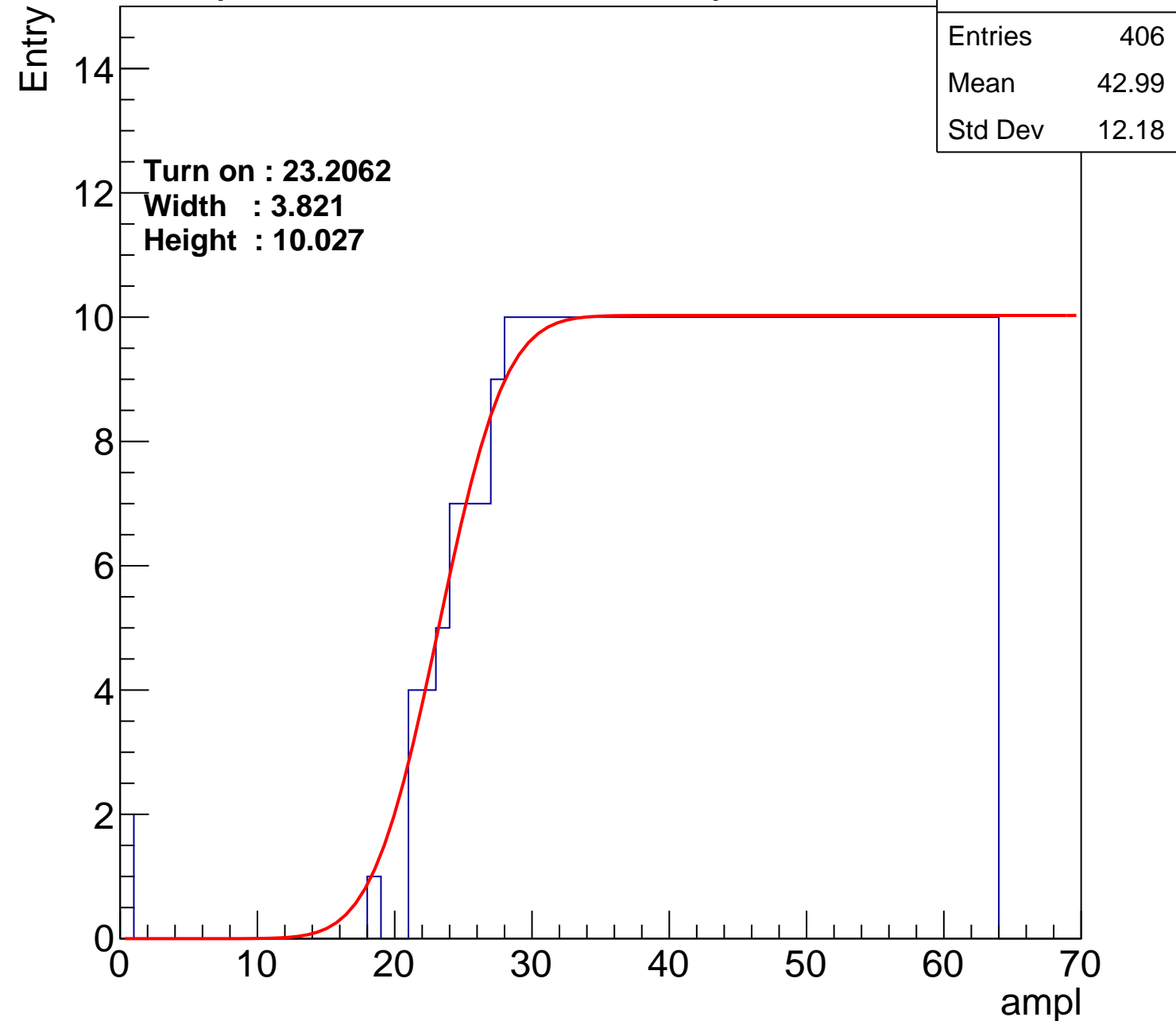
Width : 3.821

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.31
Std Dev	10.75

Turn on : 27.6193

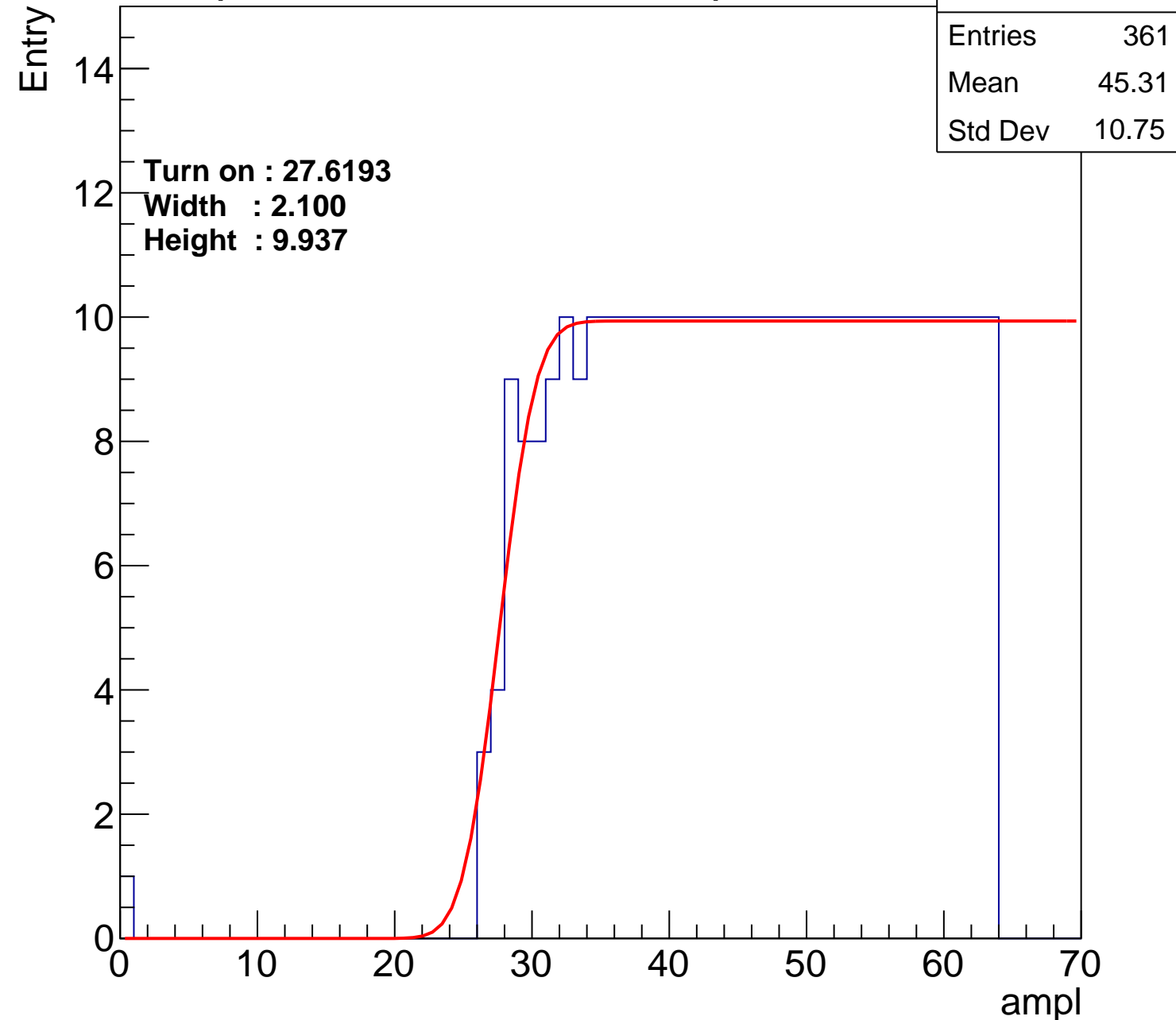
Width : 2.100

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 26.9800

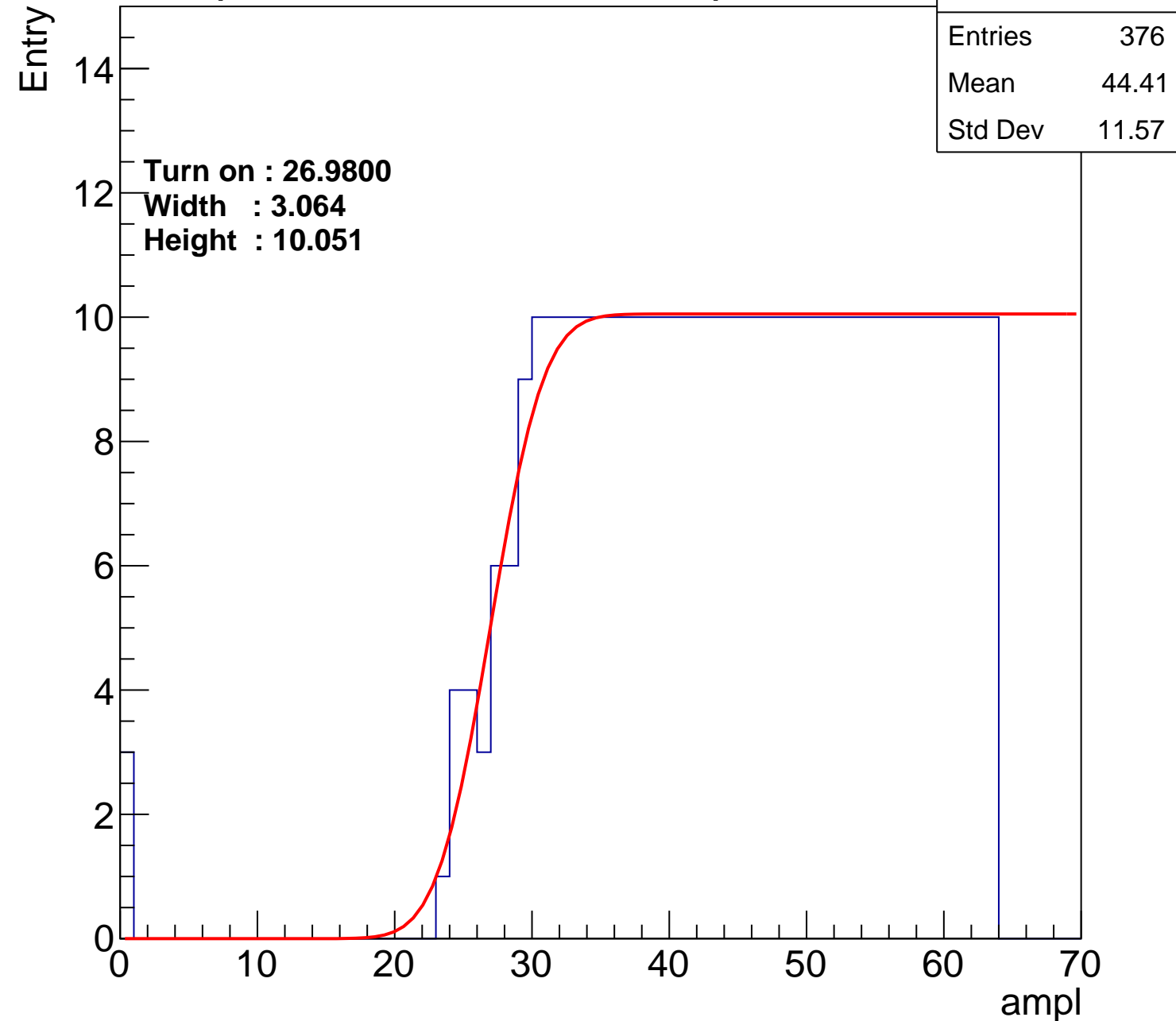
Width : 3.064

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.6015

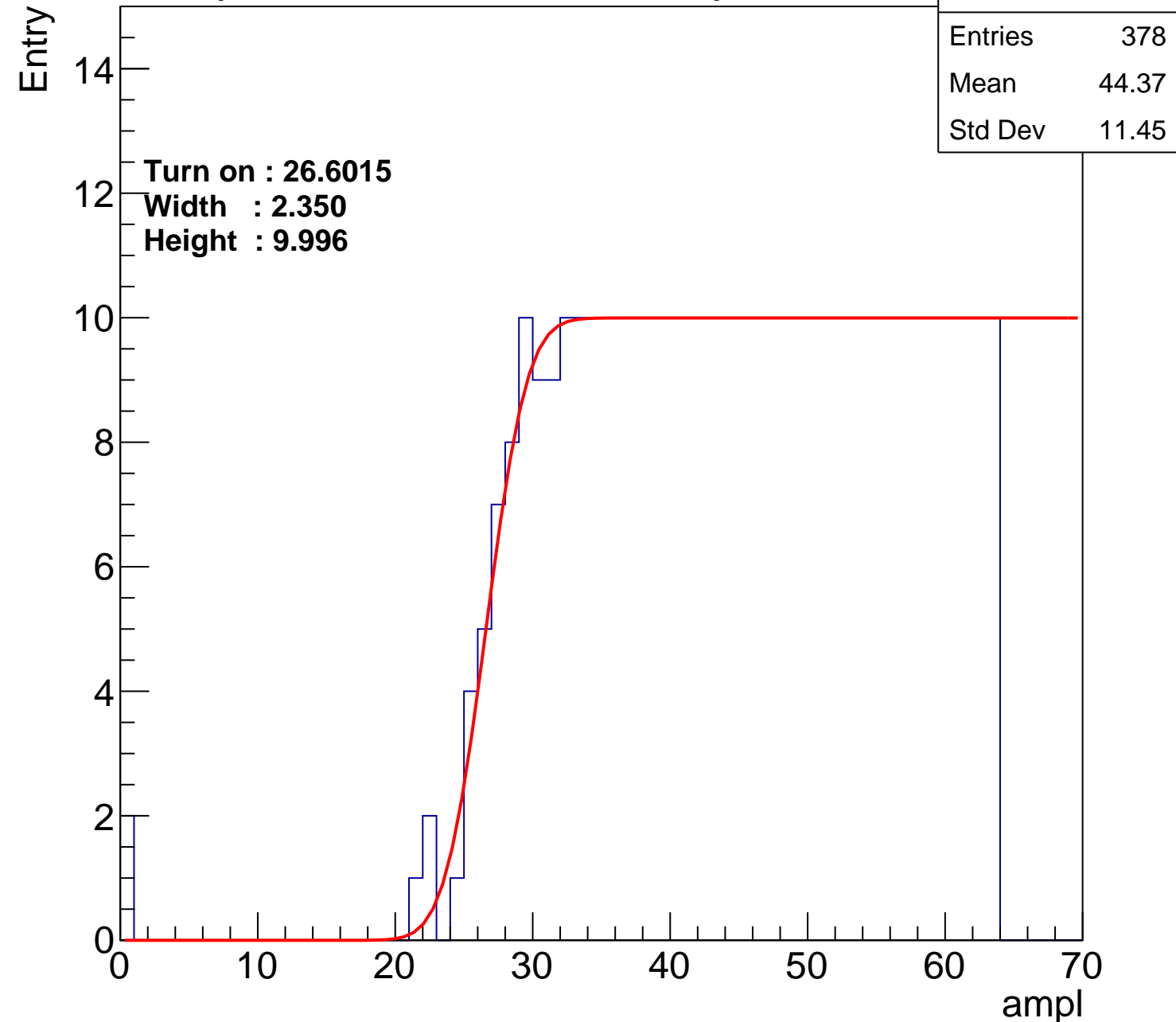
Width : 2.350

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.1
Std Dev	11.43

Turn on : 28.9120

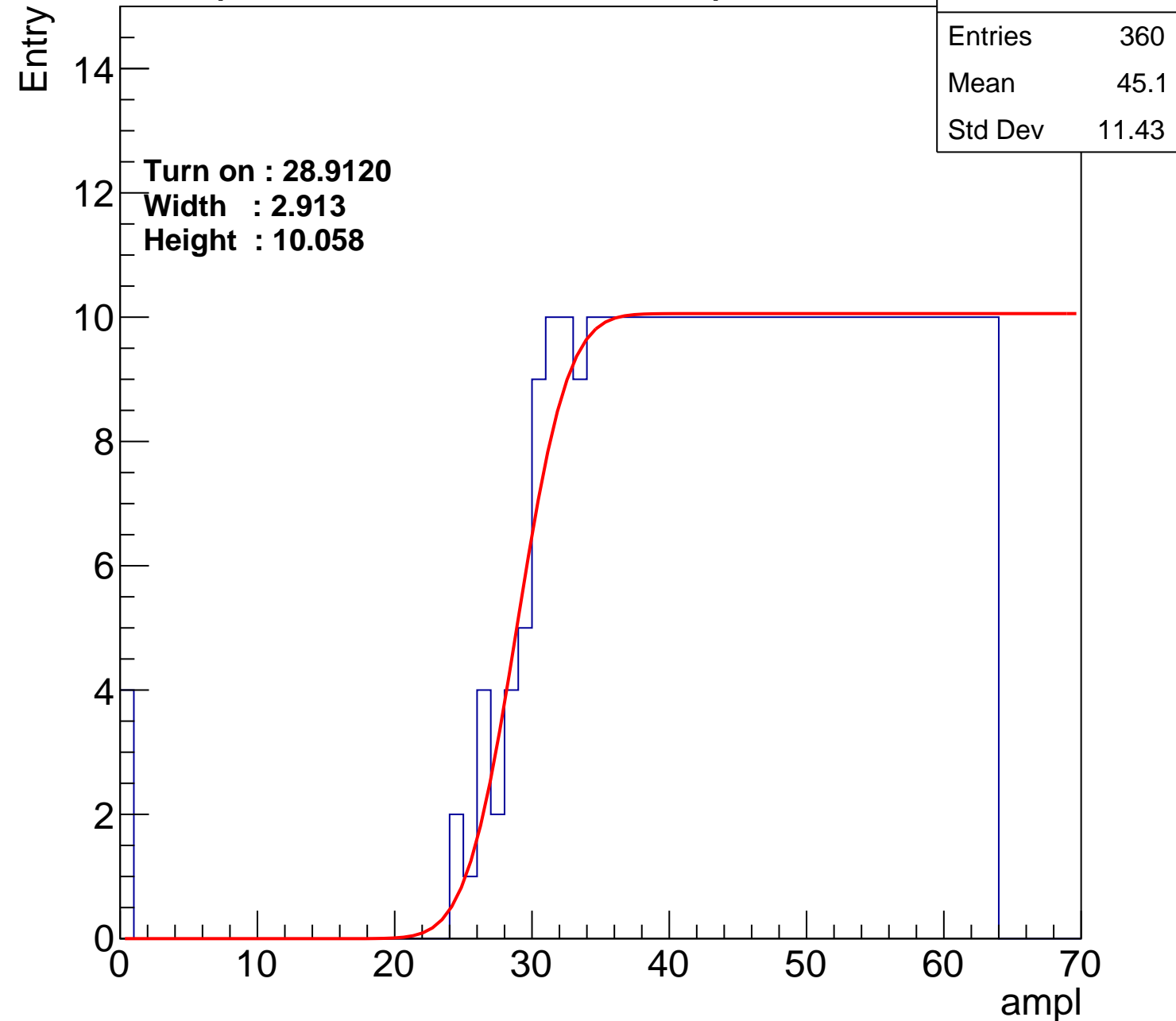
Width : 2.913

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch89

calib_packv5_042523_0143.root, FC#13, port D2

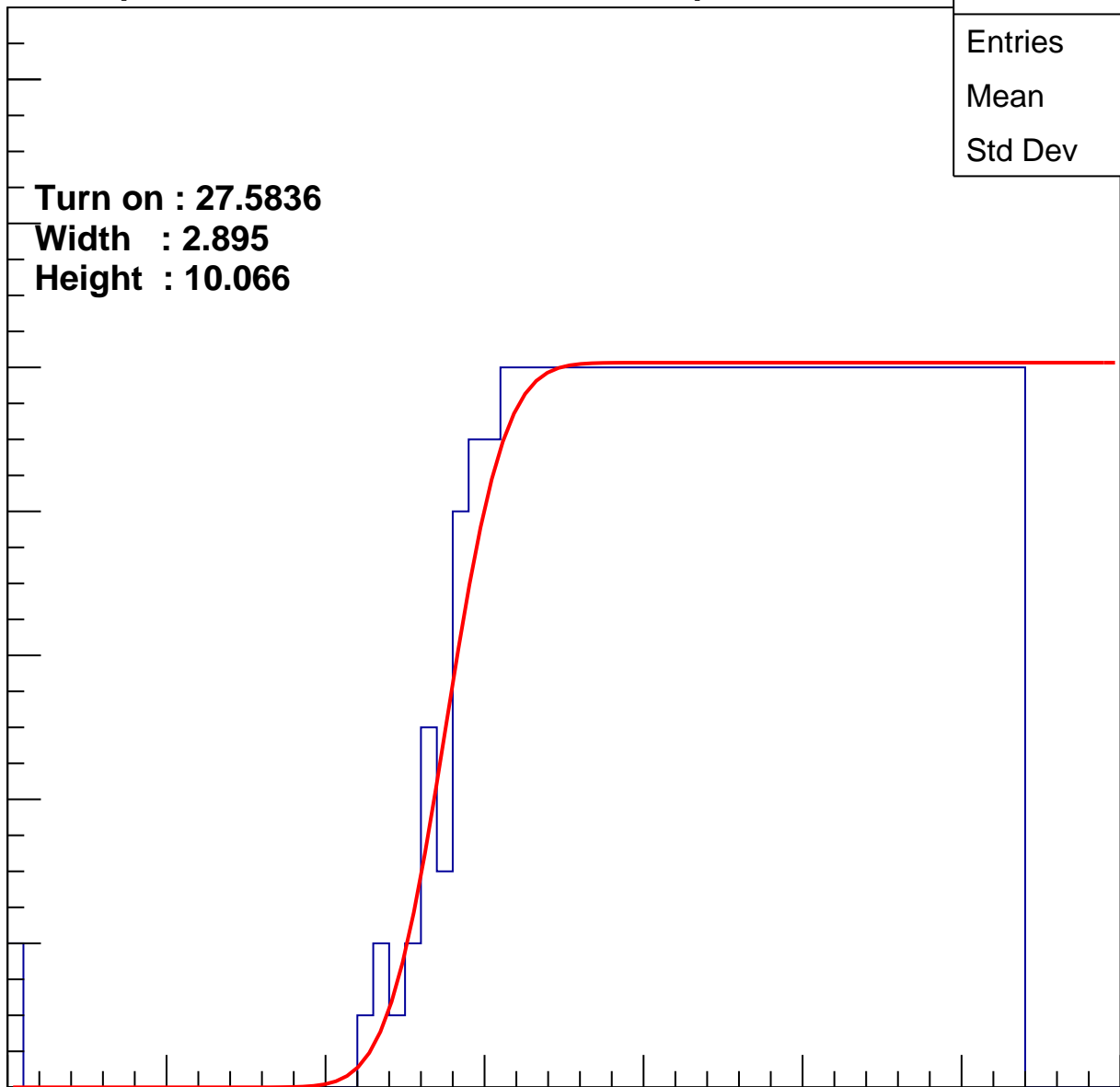
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5836
Width : 2.895
Height : 10.066

Entries	372
Mean	44.67
Std Dev	11.29

ampl



B1L003S, U23-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.46
Std Dev	11.47

Turn on : 27.2484

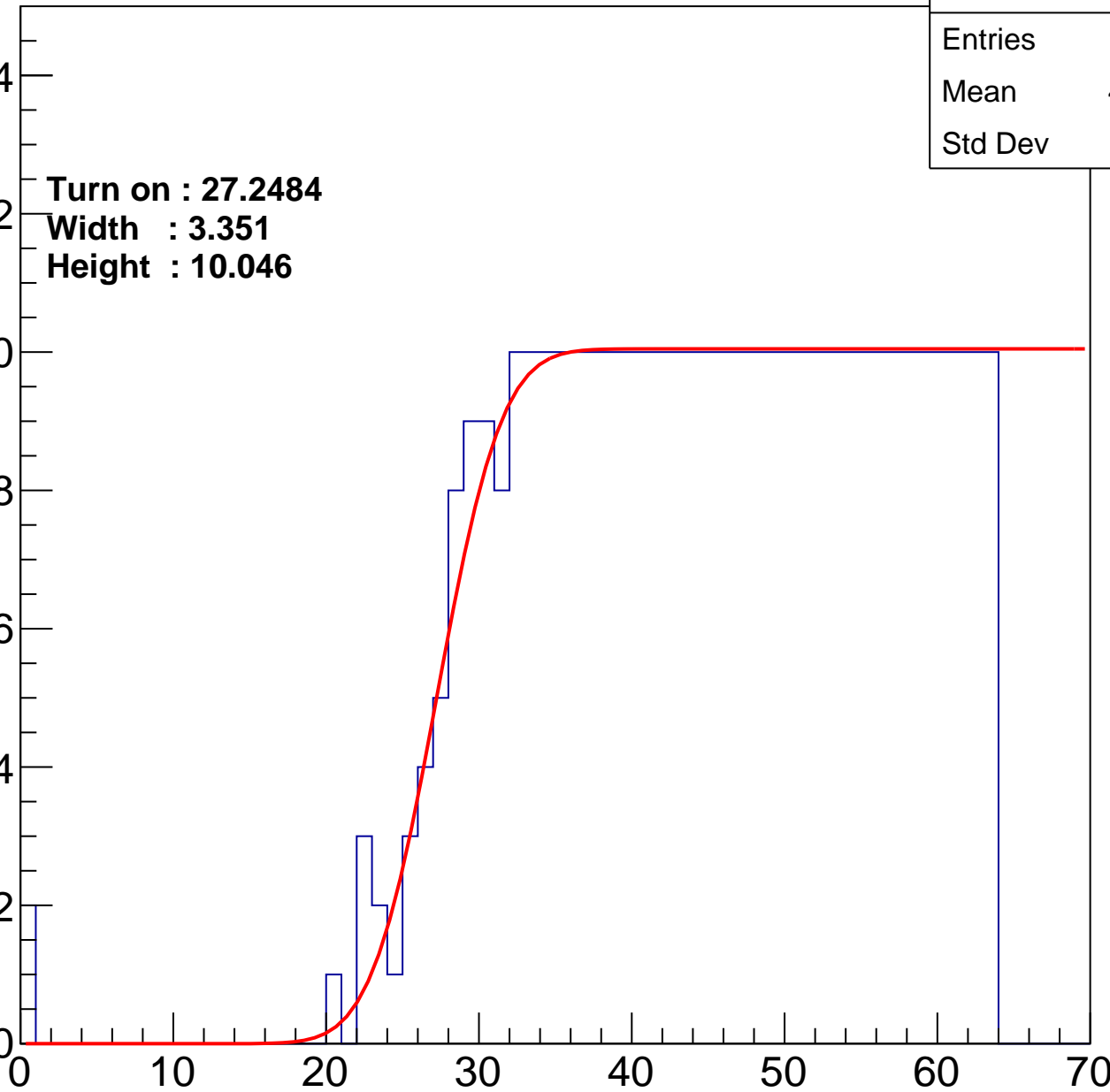
Width : 3.351

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch91

calib_packv5_042523_0143.root, FC#13, port D2

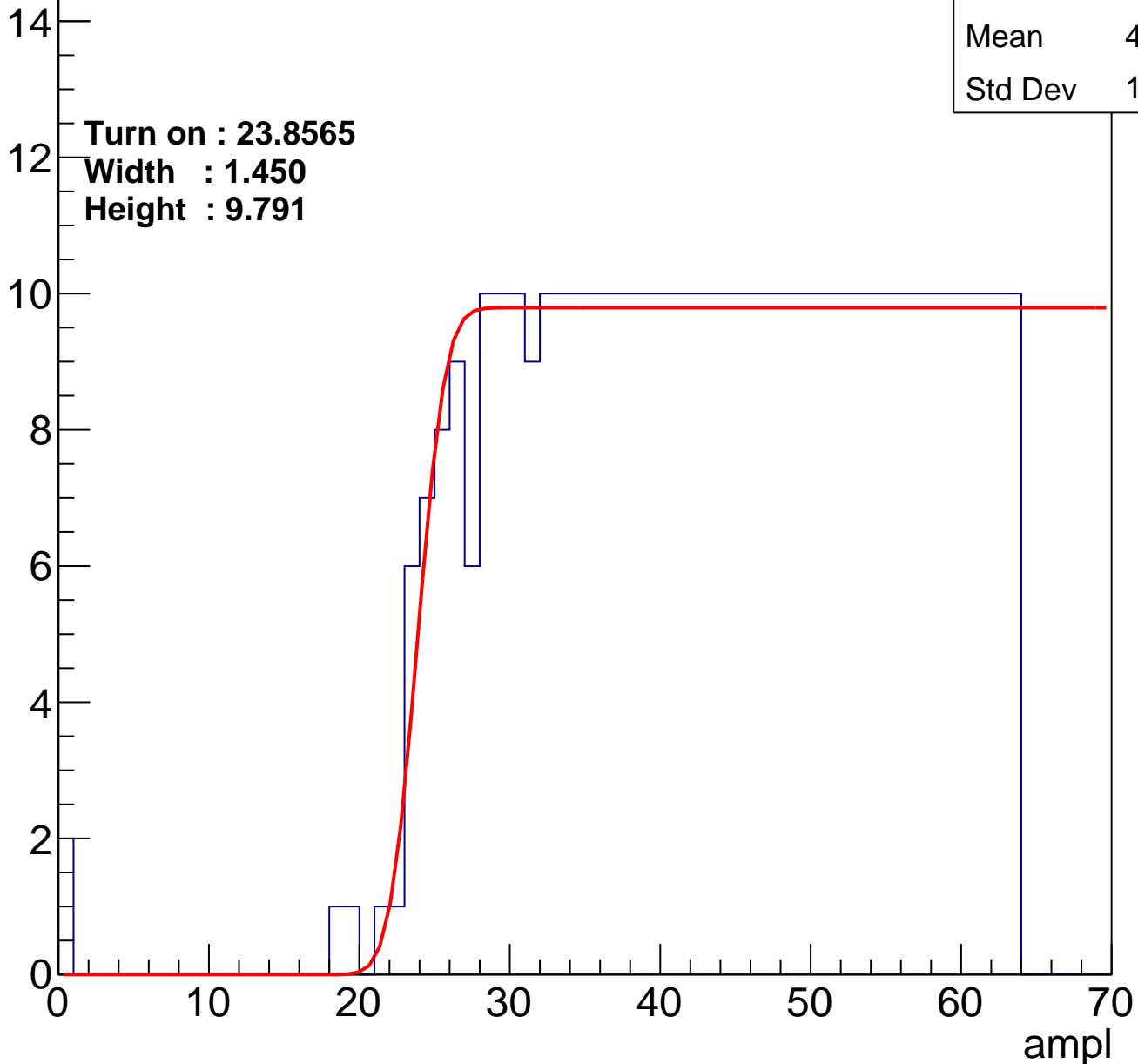
Entries	401
Mean	43.22
Std Dev	12.07

Turn on : 23.8565

Width : 1.450

Height : 9.791

Entry



B1L003S, U23-ch92

calib_packv5_042523_0143.root, FC#13, port D2

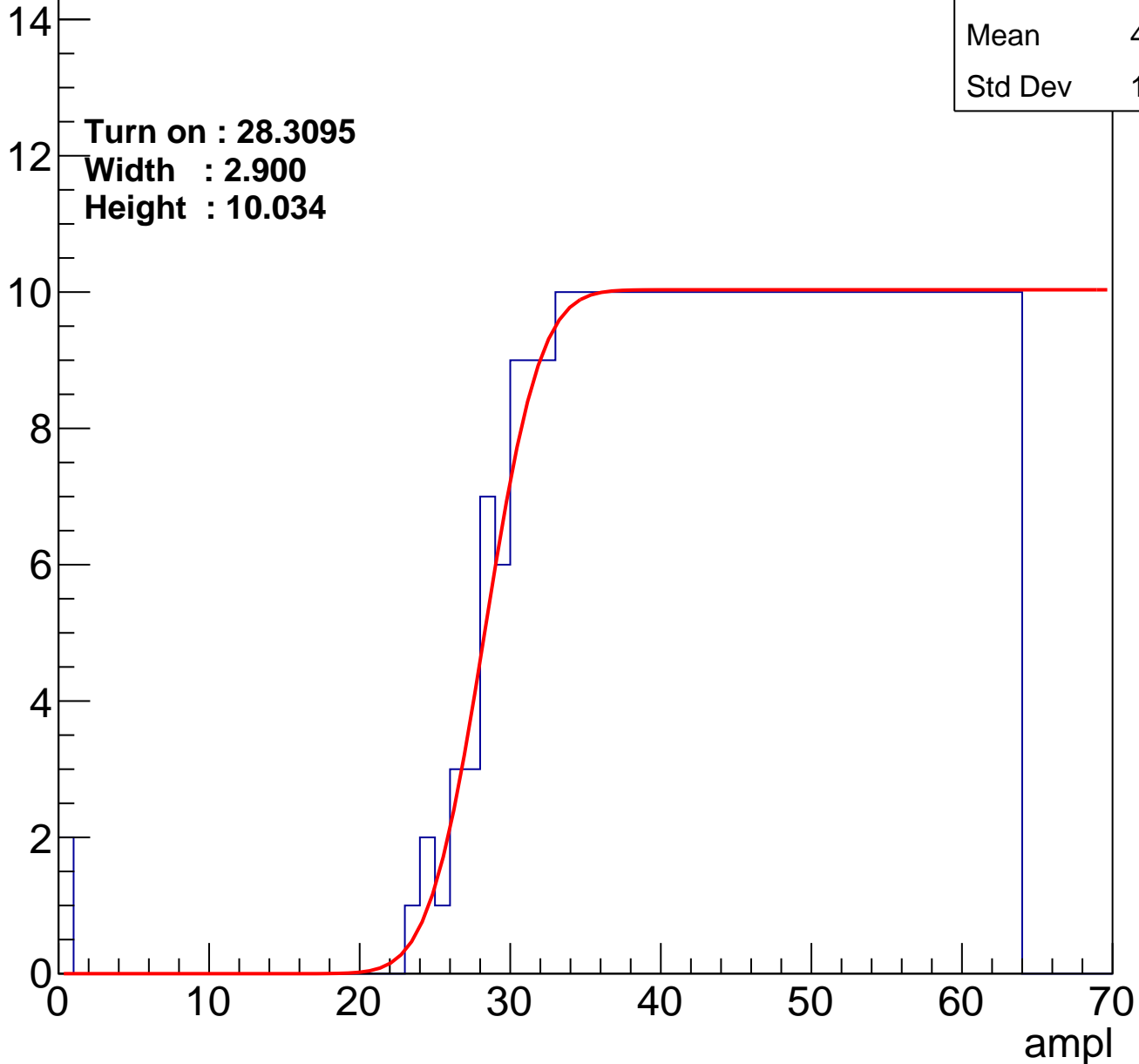
Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 28.3095

Width : 2.900

Height : 10.034

Entry



B1L003S, U23-ch93

calib_packv5_042523_0143.root, FC#13, port D2

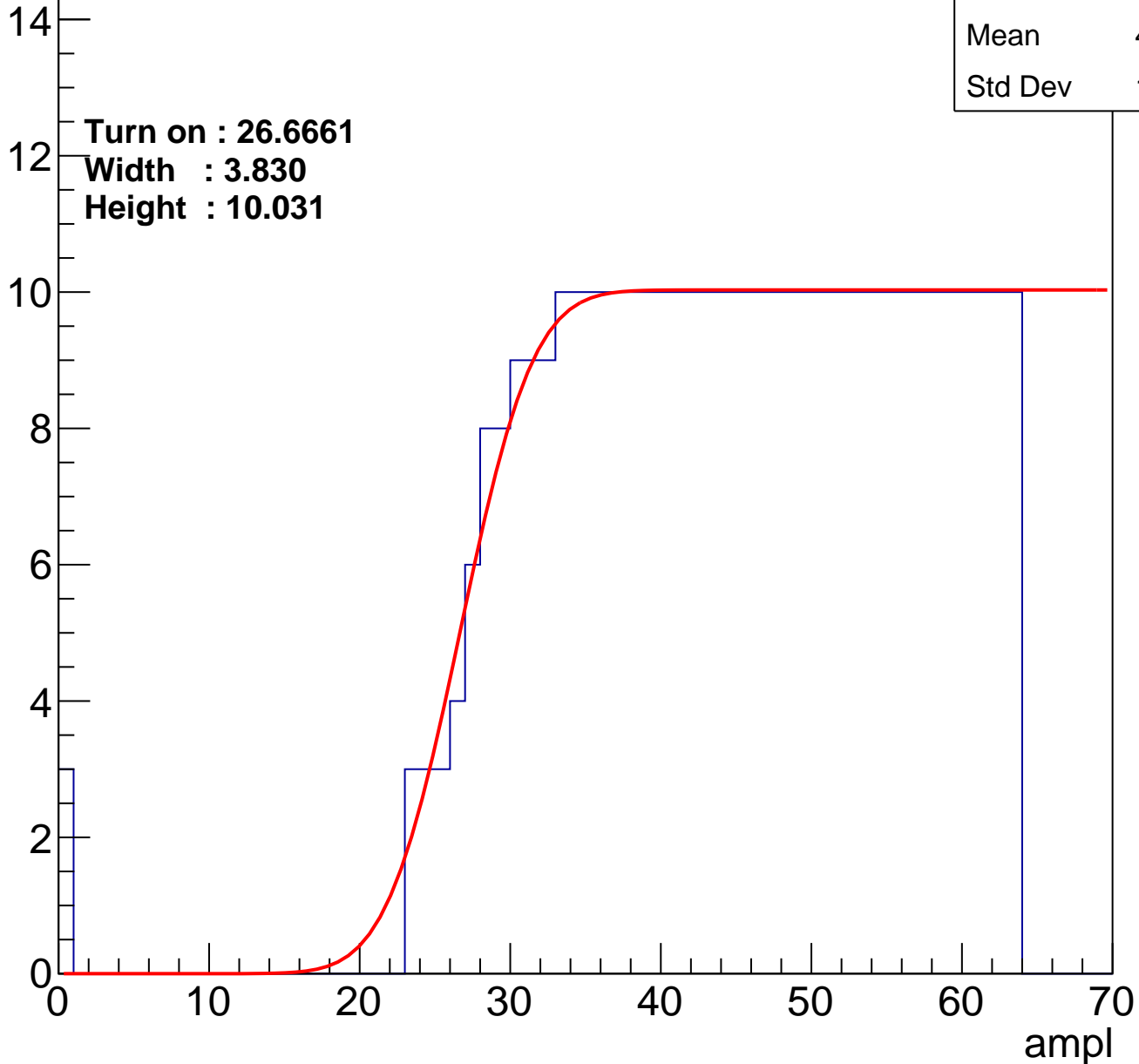
Entries	375
Mean	44.41
Std Dev	11.61

Turn on : 26.6661

Width : 3.830

Height : 10.031

Entry



B1L003S, U23-ch94

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.35
Std Dev	11.62

Turn on : 26.8118

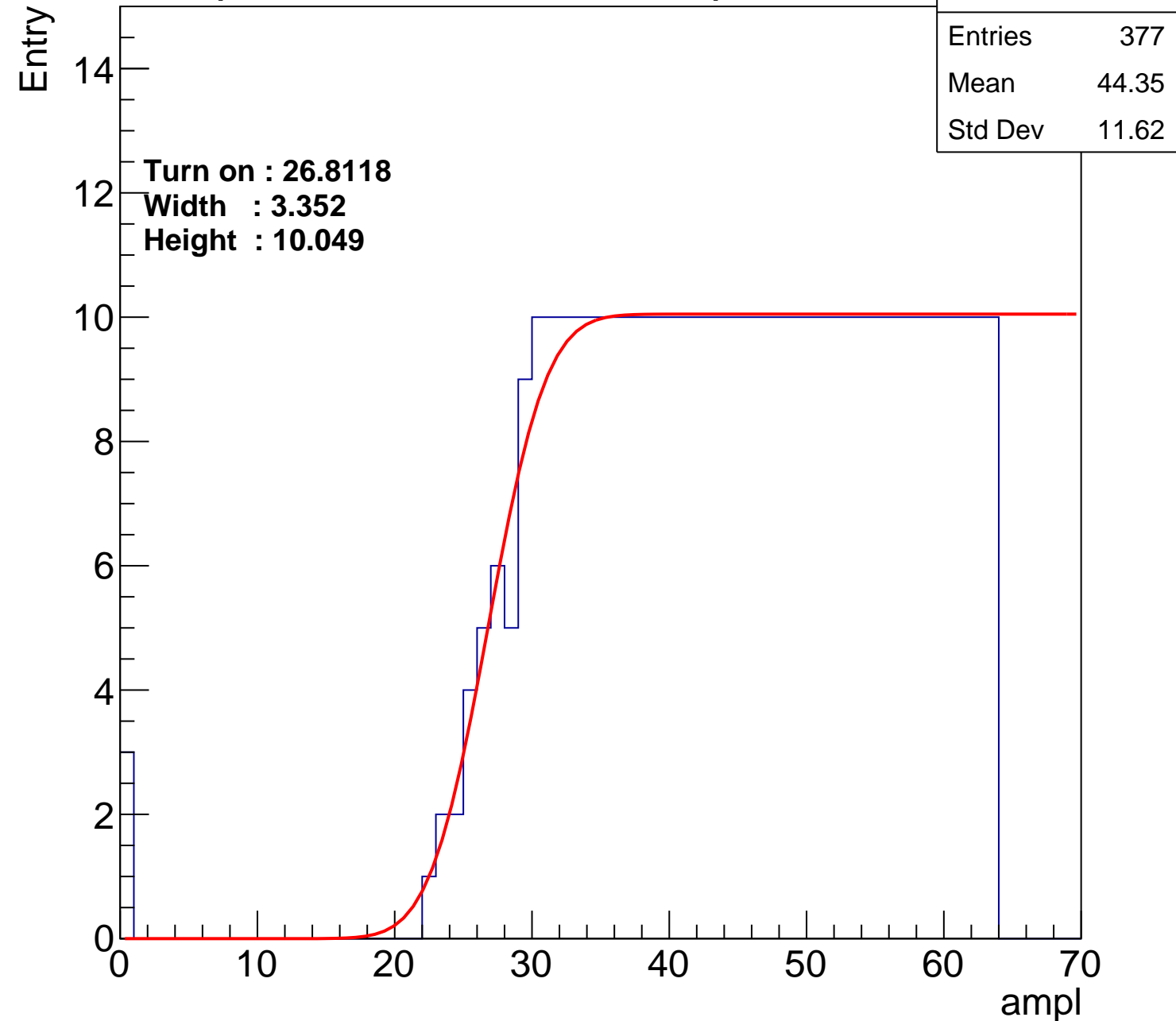
Width : 3.352

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.33
Std Dev	11.65

Turn on : 26.2259

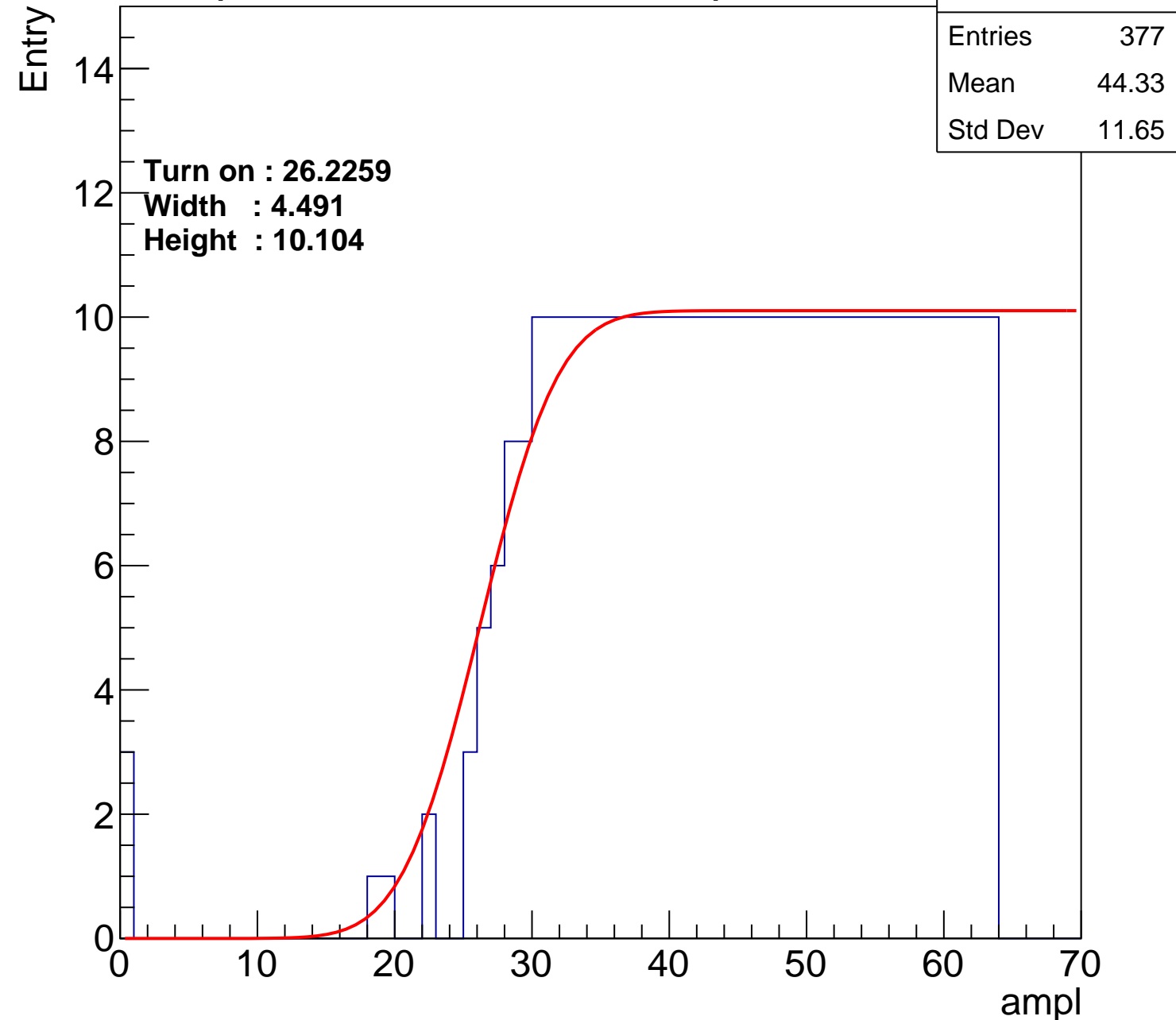
Width : 4.491

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.42
Std Dev	12.35

Turn on : 24.9312

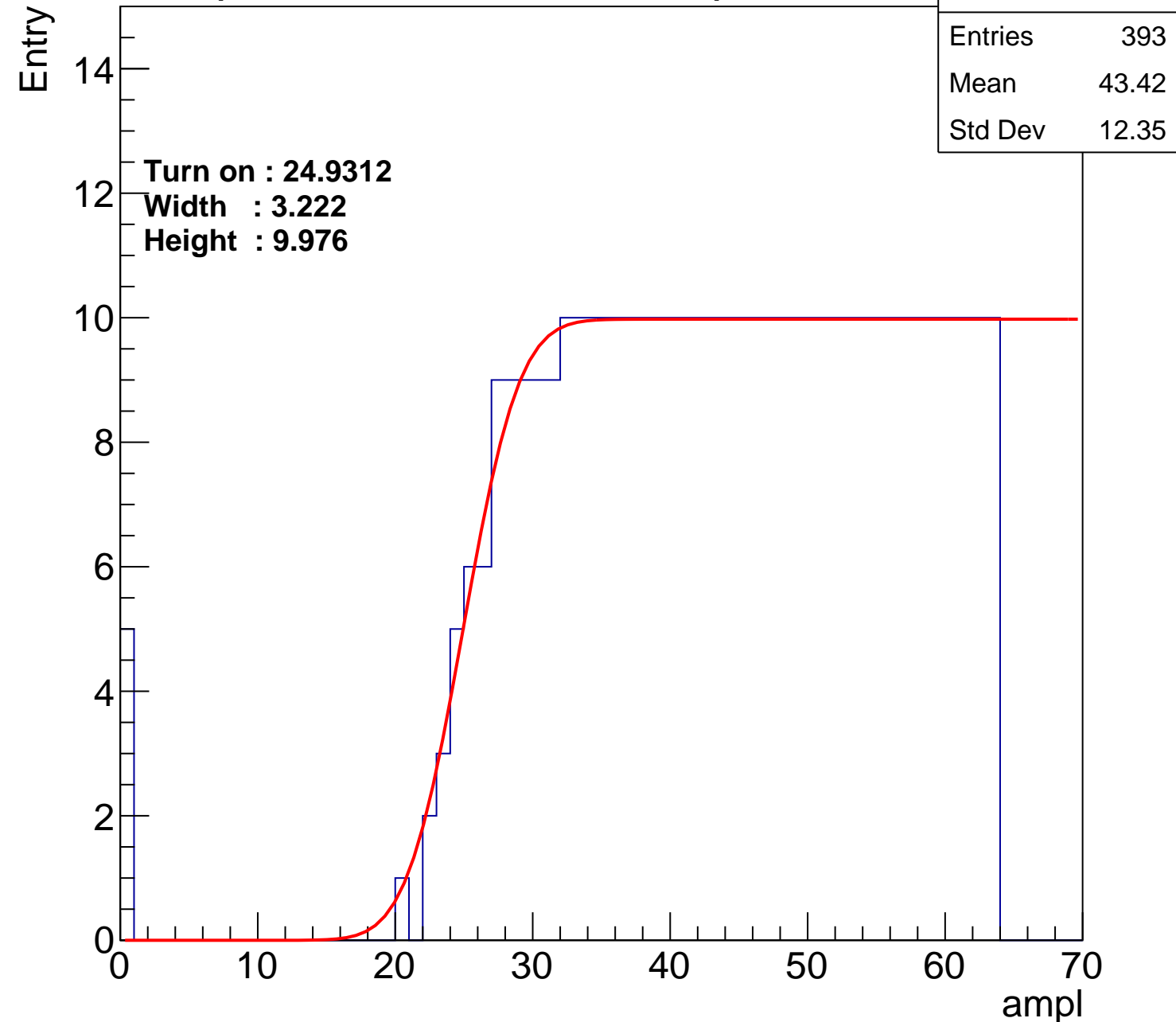
Width : 3.222

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.87
Std Dev	11.24

Turn on : 27.5985

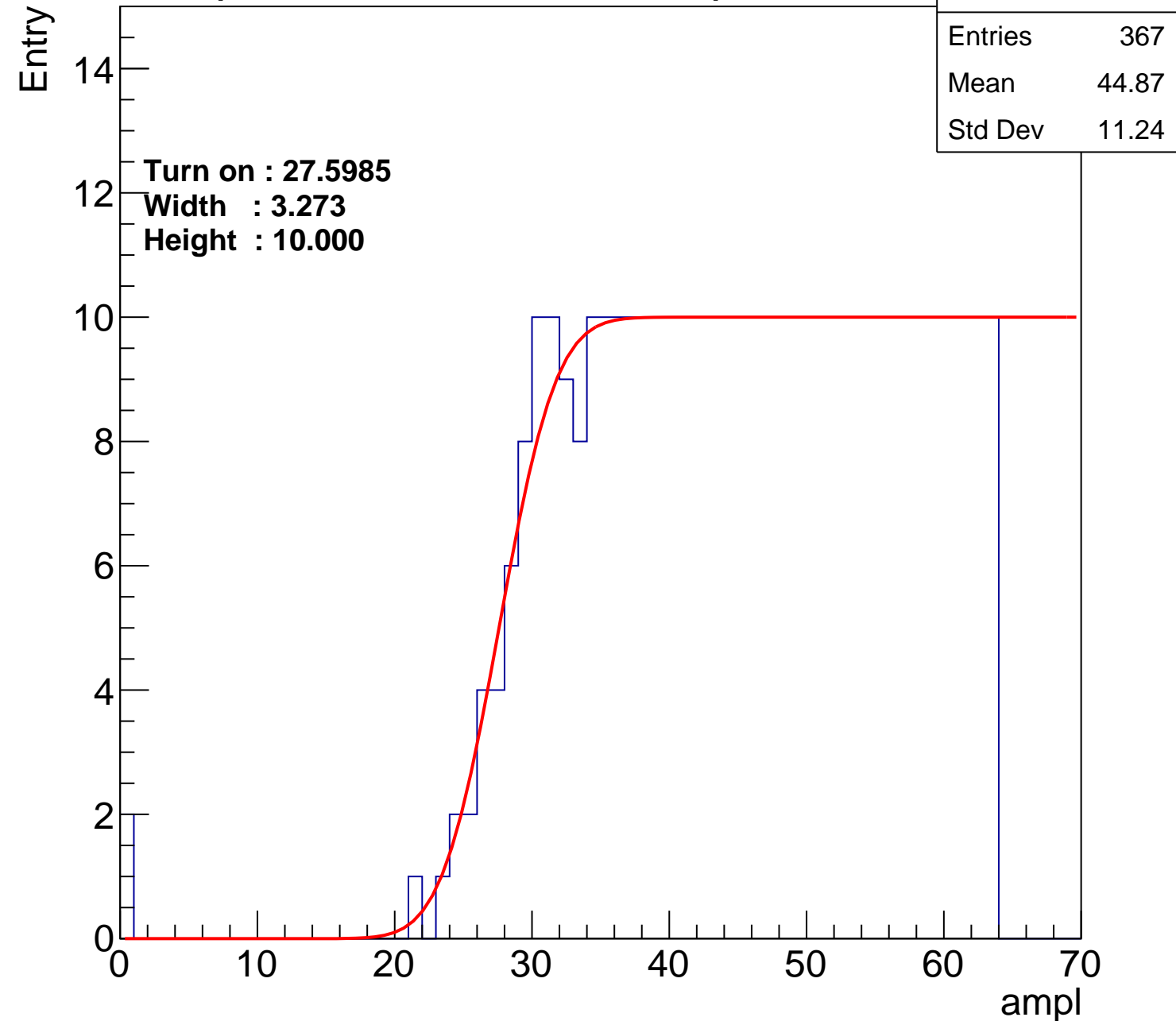
Width : 3.273

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch98

calib_packv5_042523_0143.root, FC#13, port D2

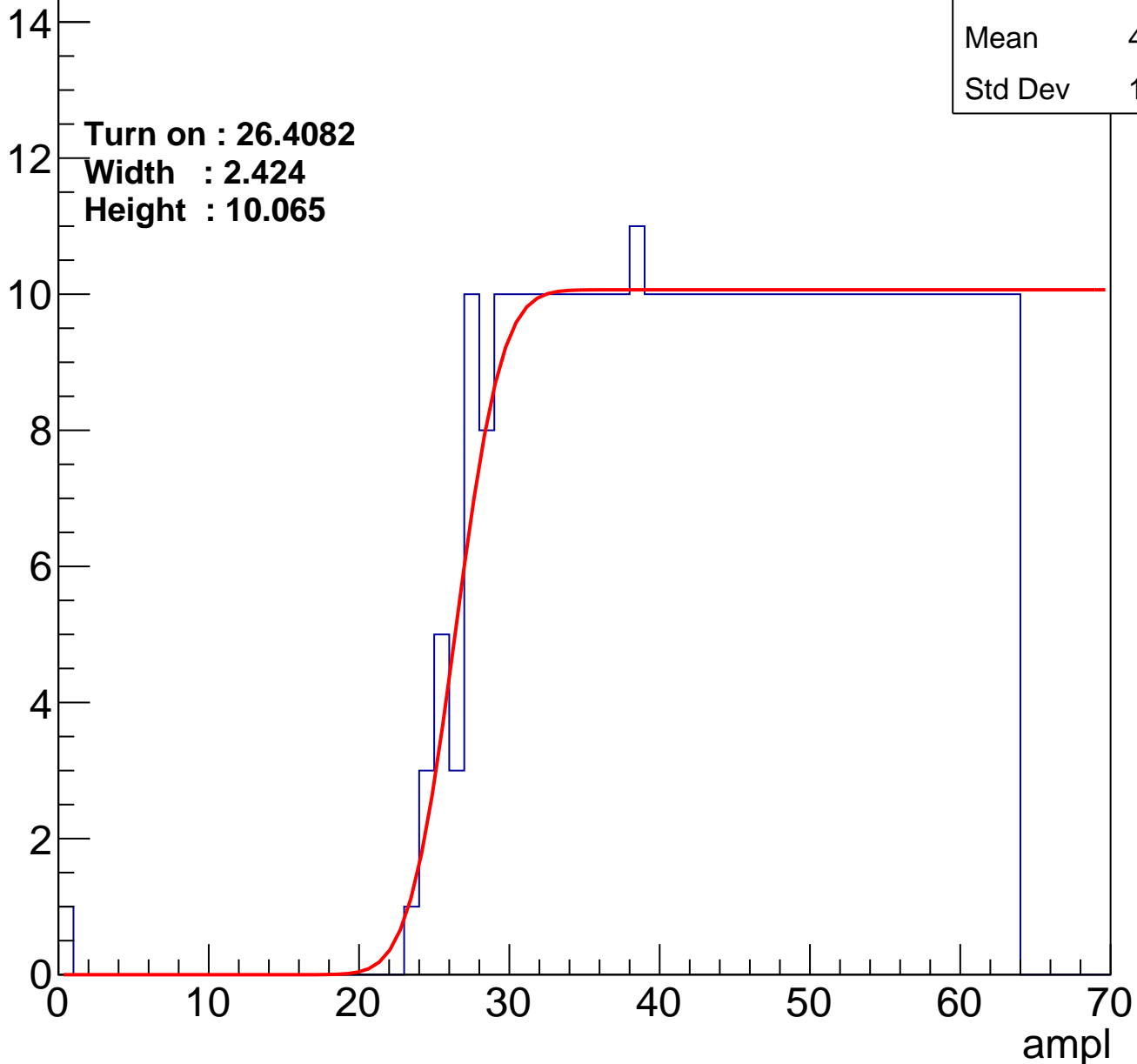
Entries	382
Mean	44.32
Std Dev	11.25

Turn on : 26.4082

Width : 2.424

Height : 10.065

Entry



B1L003S, U23-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.94
Std Dev	10.97

Turn on : 27.2640

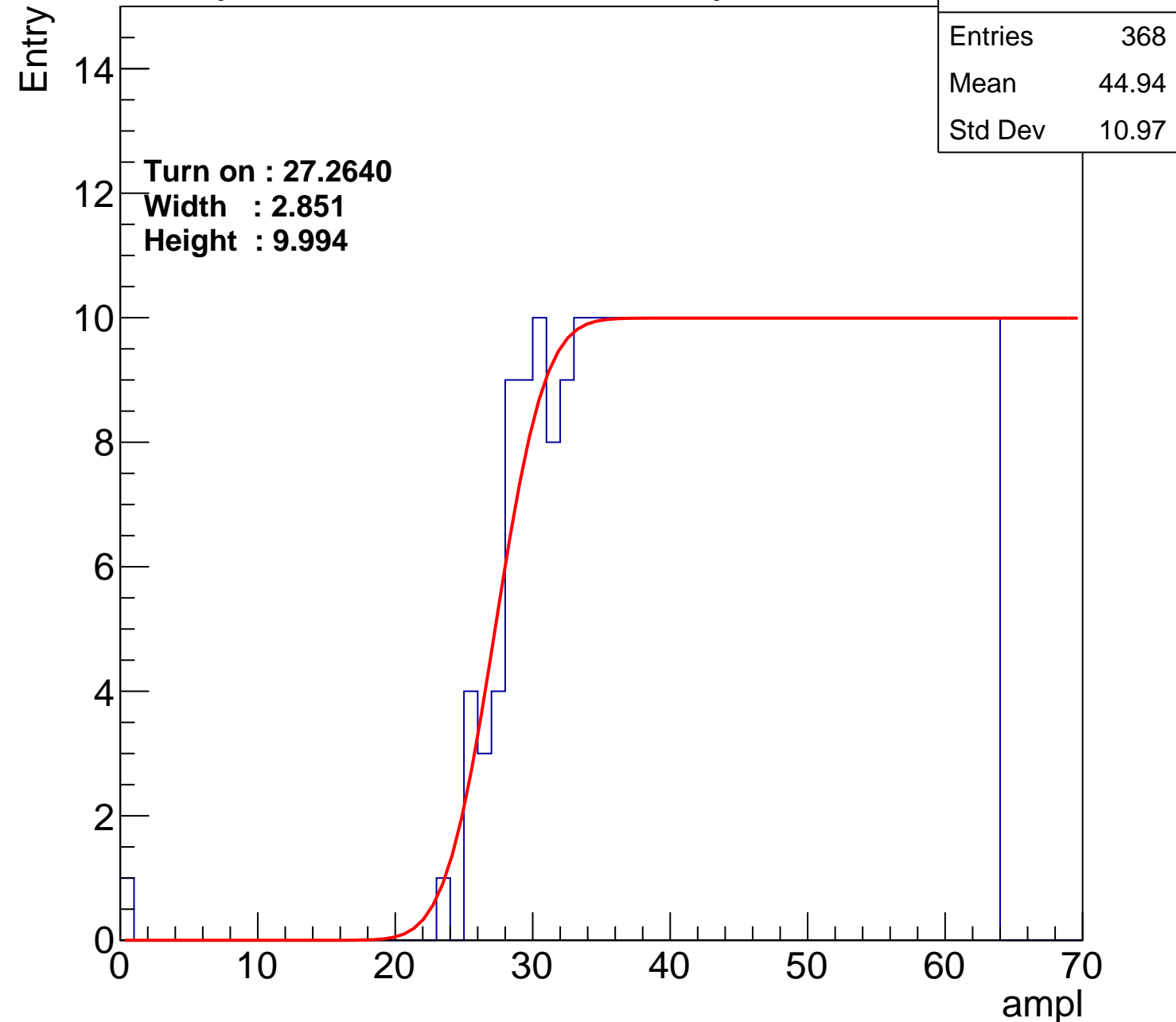
Width : 2.851

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.4840

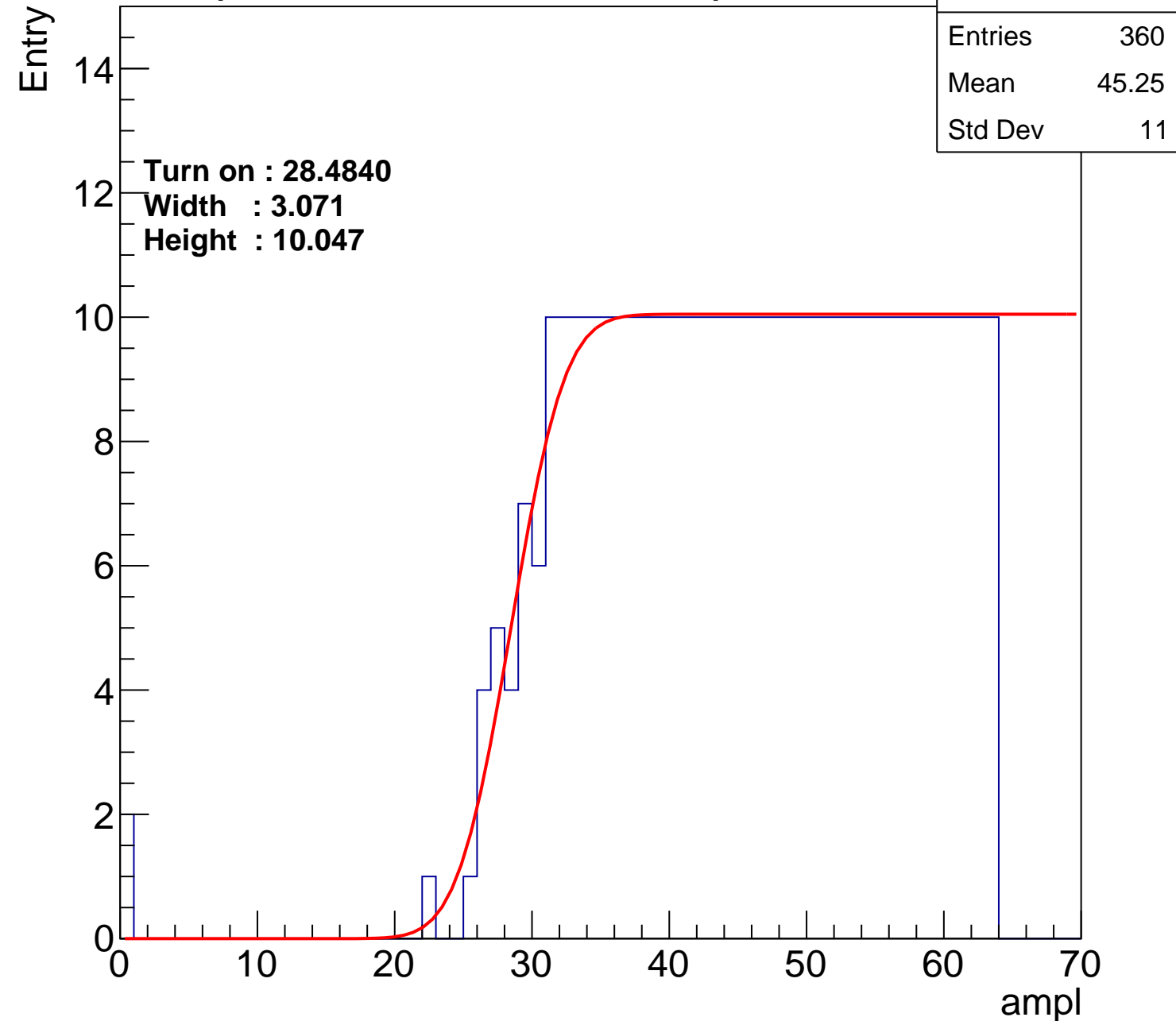
Width : 3.071

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.81
Std Dev	11.55

Turn on : 27.6815

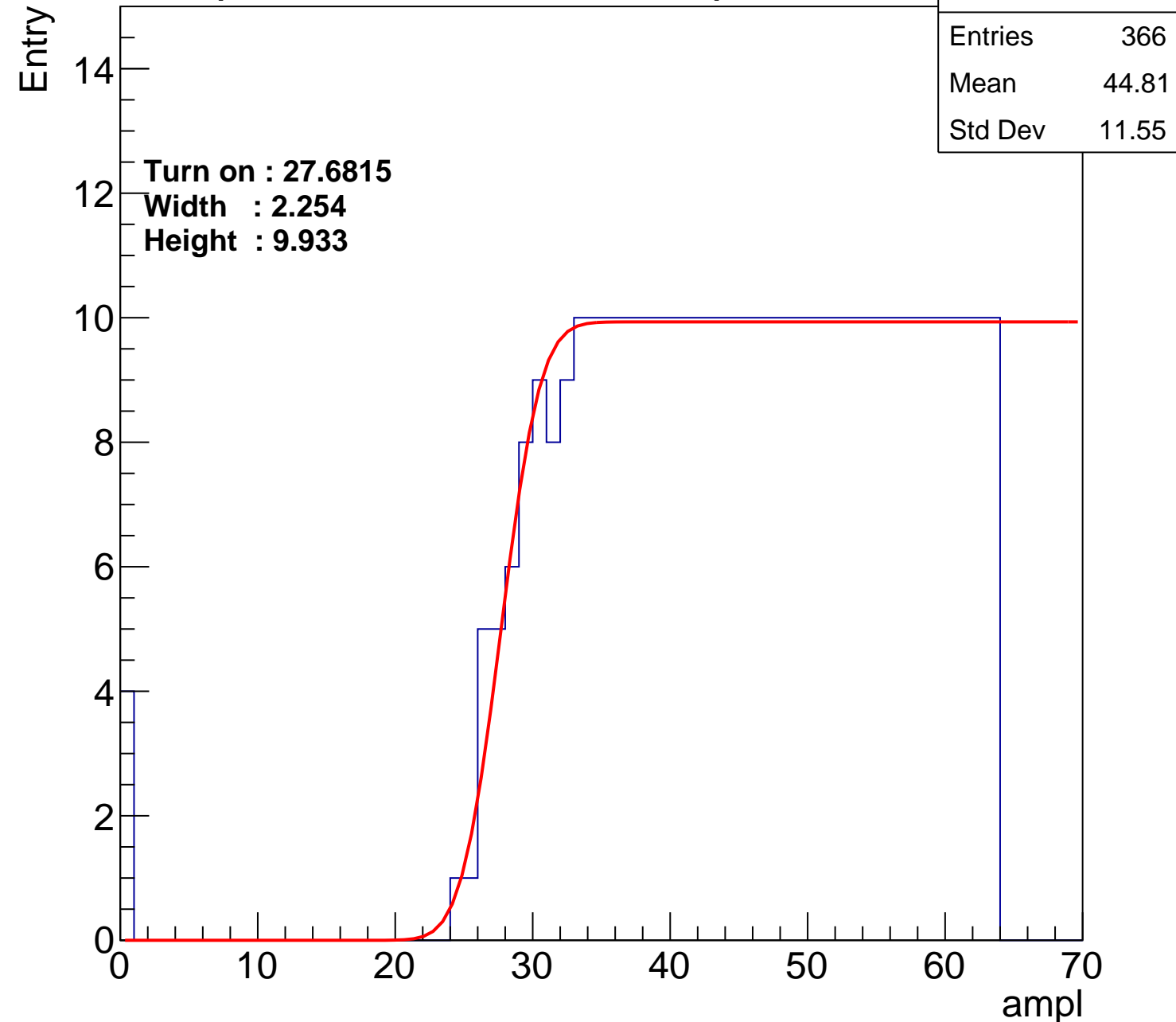
Width : 2.254

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.81
Std Dev	11.89

Turn on : 25.6956

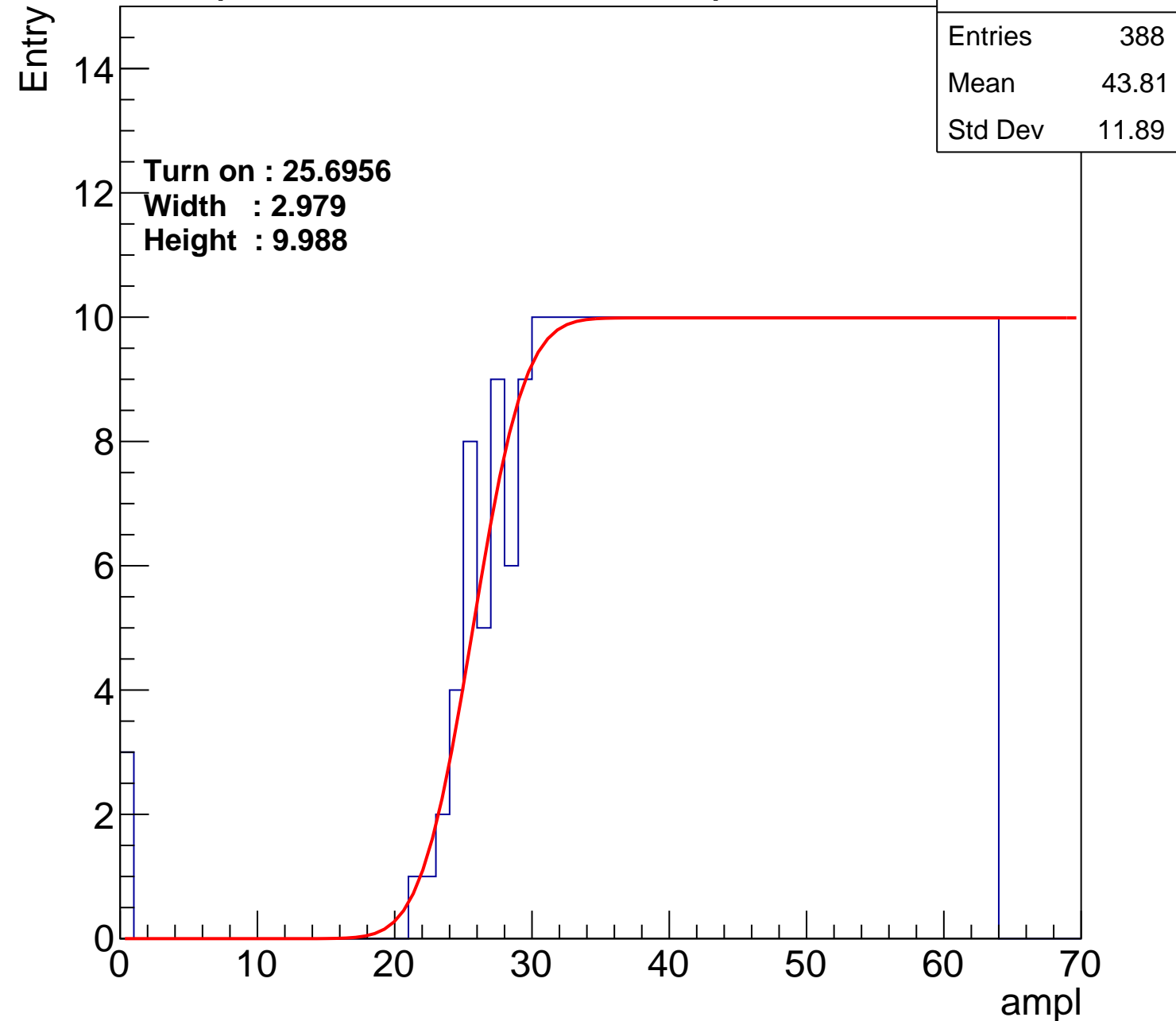
Width : 2.979

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch103

calib_packv5_042523_0143.root, FC#13, port D2

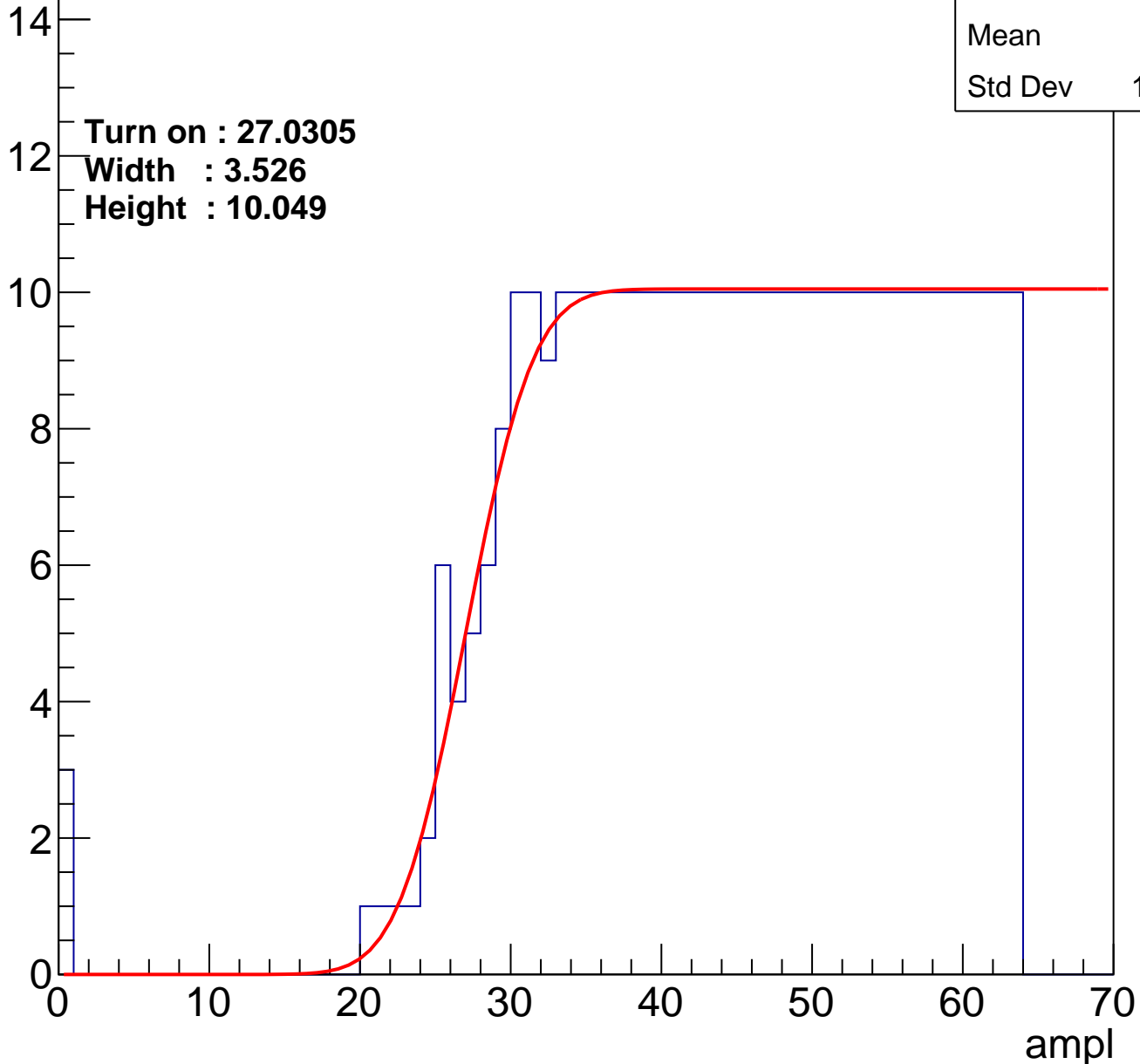
Entries	377
Mean	44.3
Std Dev	11.69

Turn on : 27.0305

Width : 3.526

Height : 10.049

Entry



B1L003S, U23-ch104

calib_packv5_042523_0143.root, FC#13, port D2

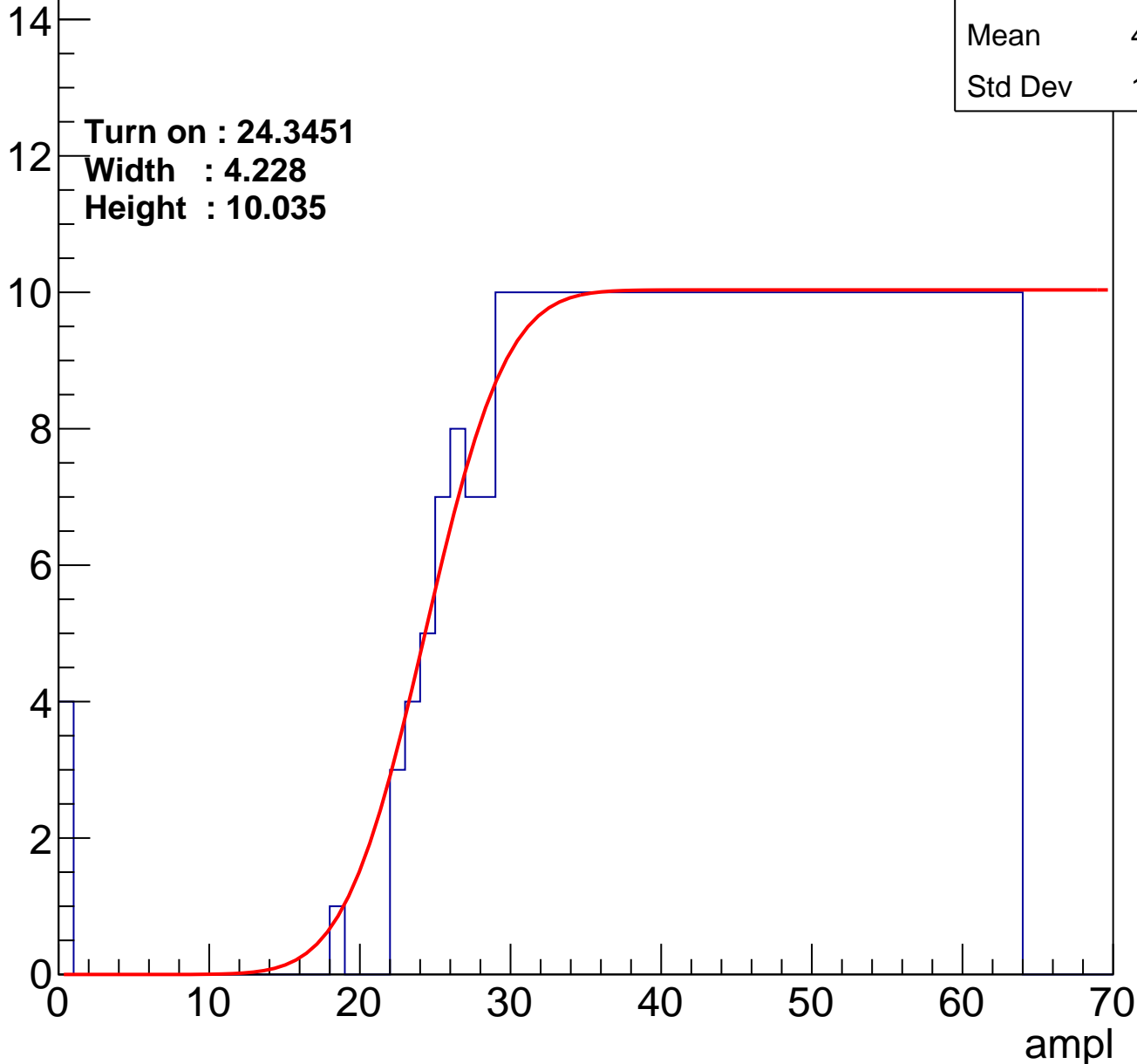
Entries	396
Mean	43.34
Std Dev	12.26

Turn on : 24.3451

Width : 4.228

Height : 10.035

Entry



B1L003S, U23-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.85
Std Dev	11.26

Turn on : 27.9570

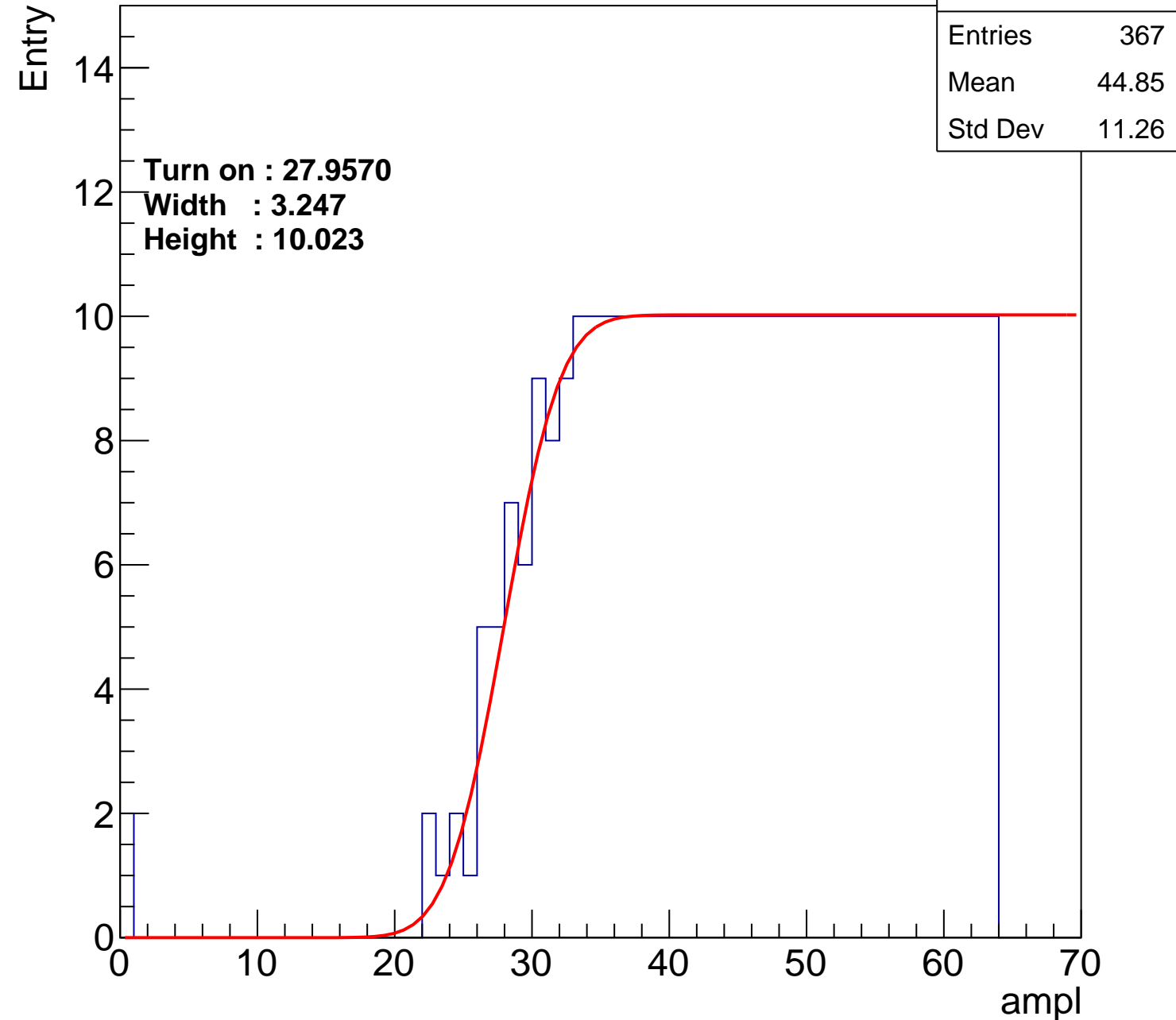
Width : 3.247

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.66
Std Dev	11.48

Turn on : 27.4622

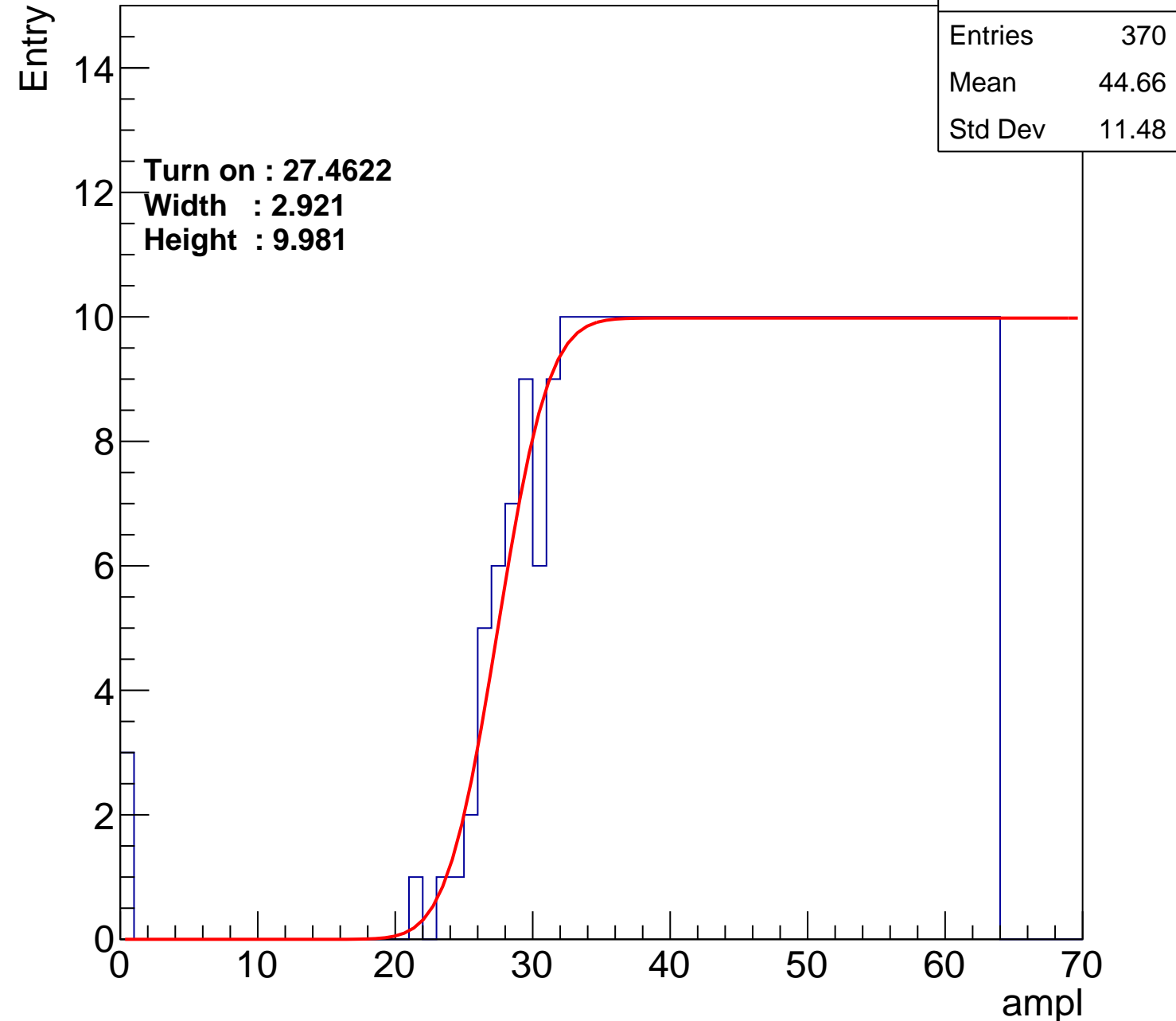
Width : 2.921

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch107

calib_packv5_042523_0143.root, FC#13, port D2

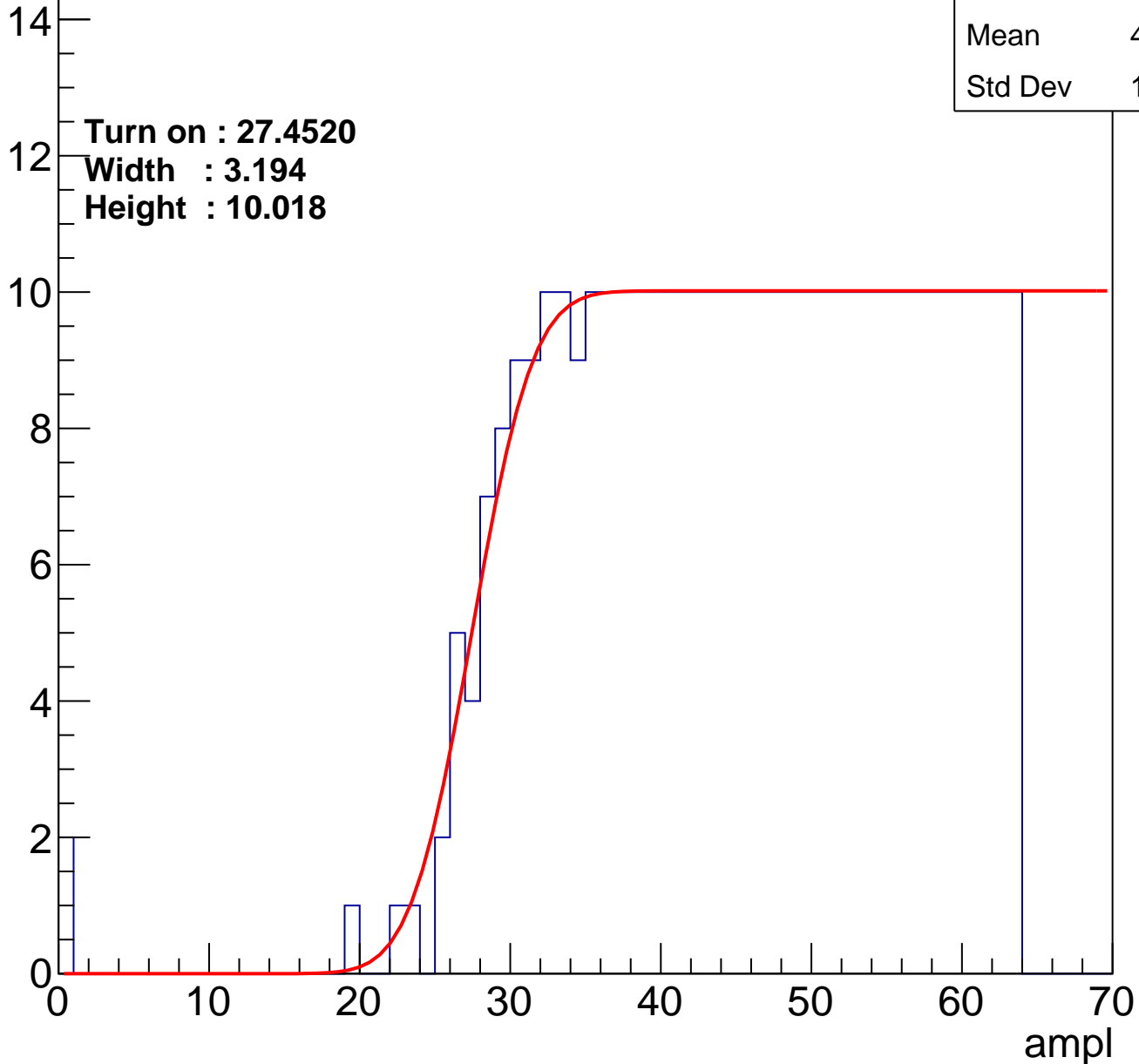
Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.4520

Width : 3.194

Height : 10.018

Entry



B1L003S, U23-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.2333

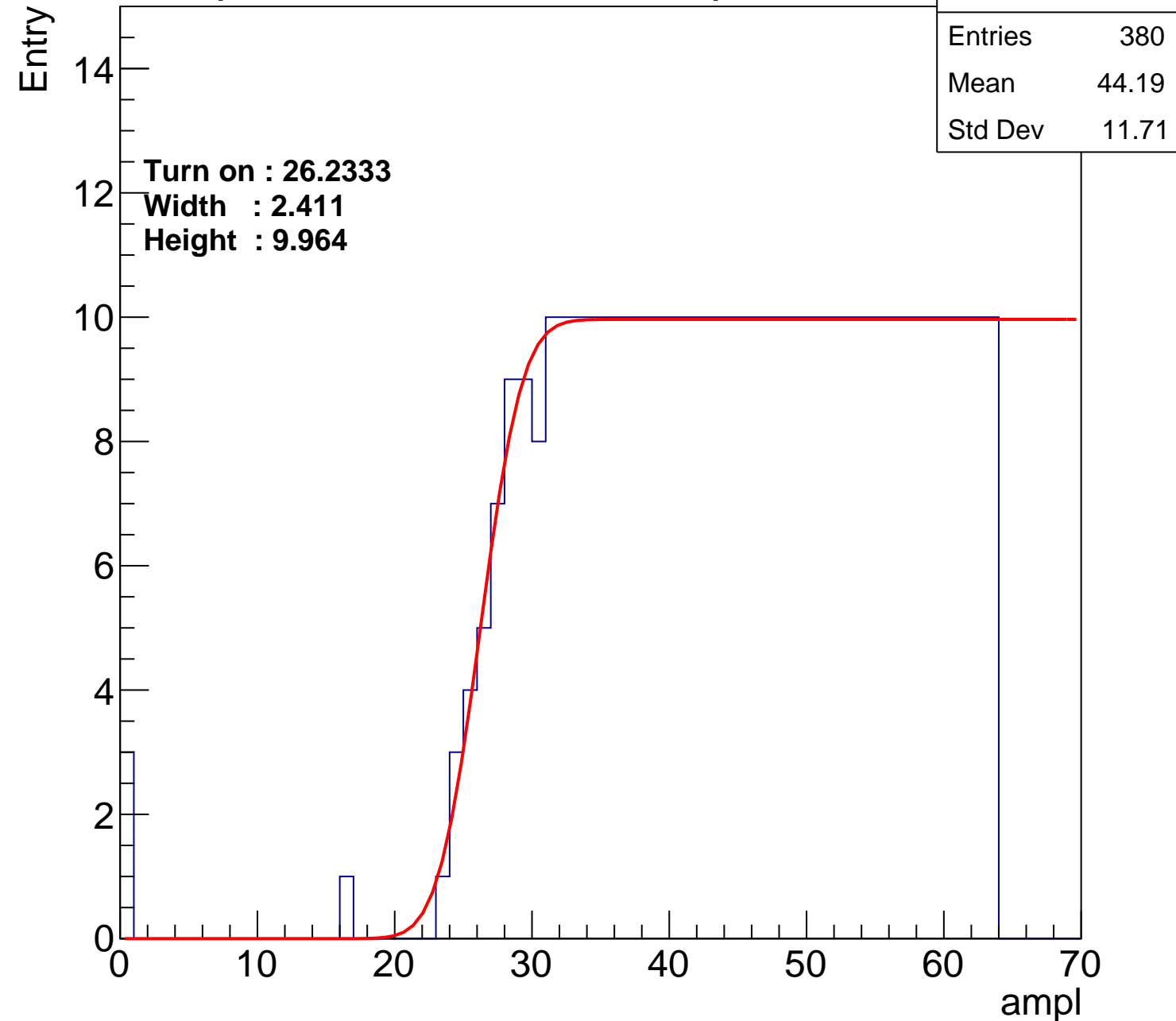
Width : 2.411

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.93
Std Dev	11.35

Turn on : 28.1298

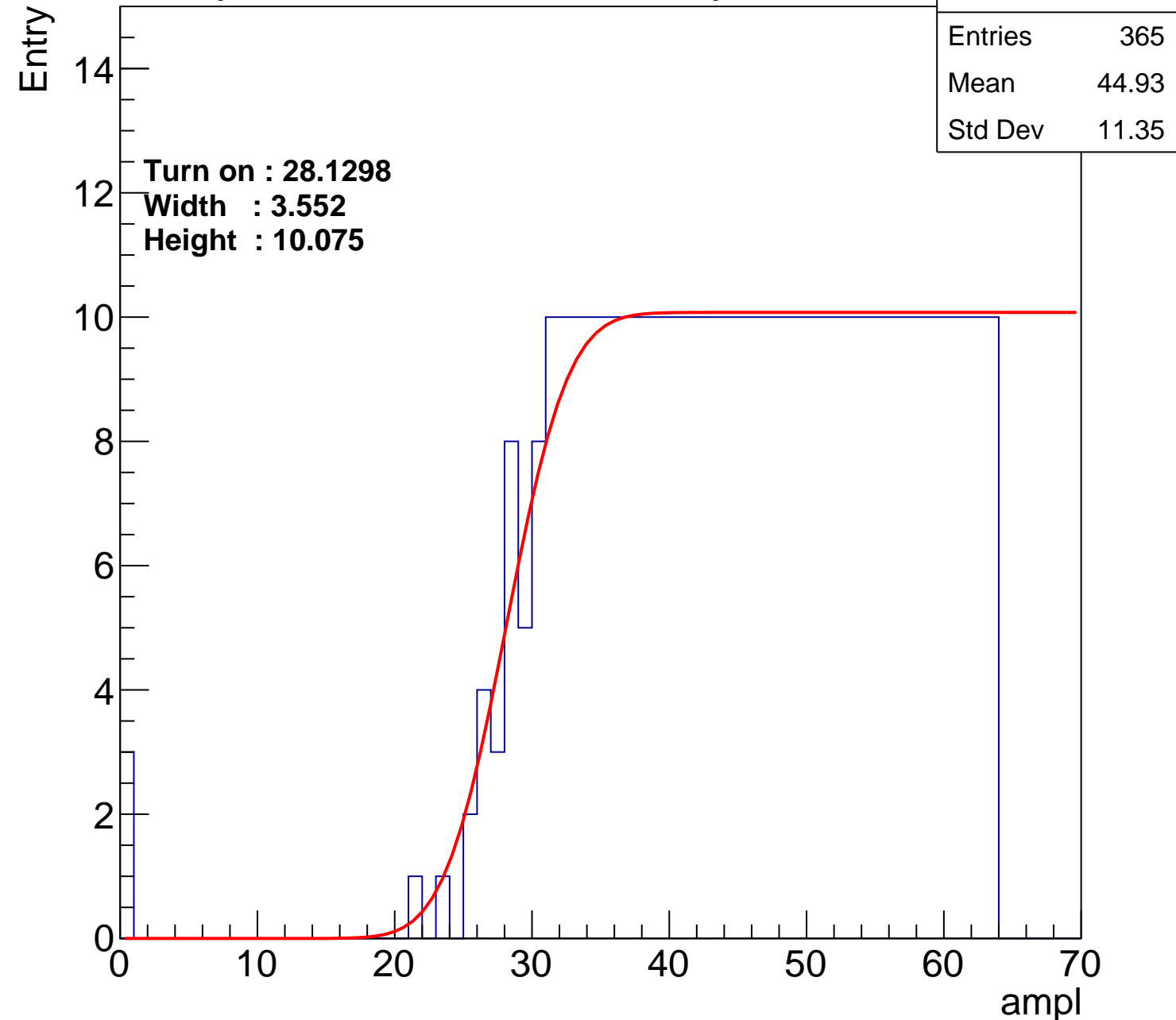
Width : 3.552

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.72
Std Dev	11.77

Turn on : 28.2504

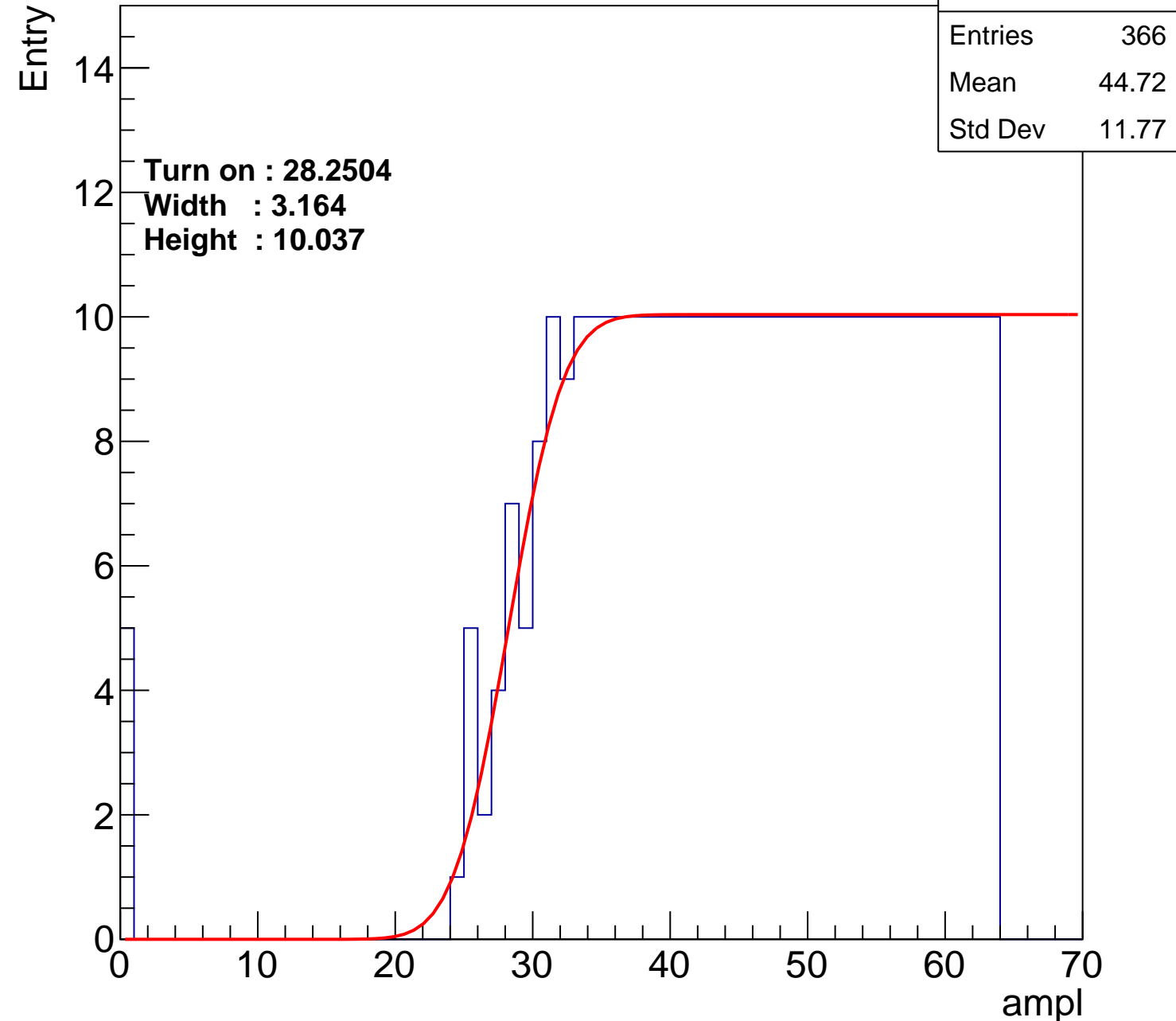
Width : 3.164

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.57
Std Dev	11.65

Turn on : 27.7220

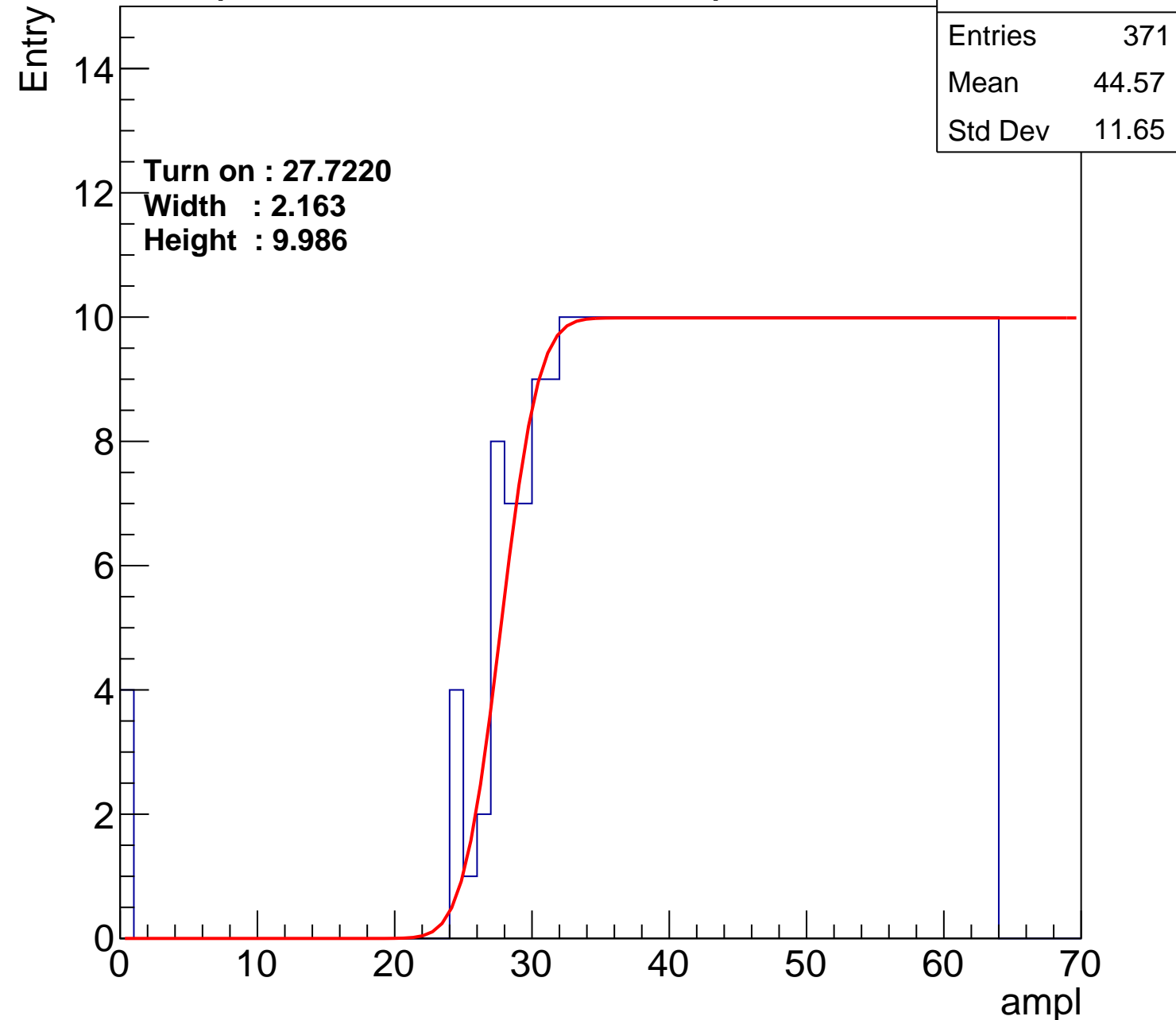
Width : 2.163

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.1115

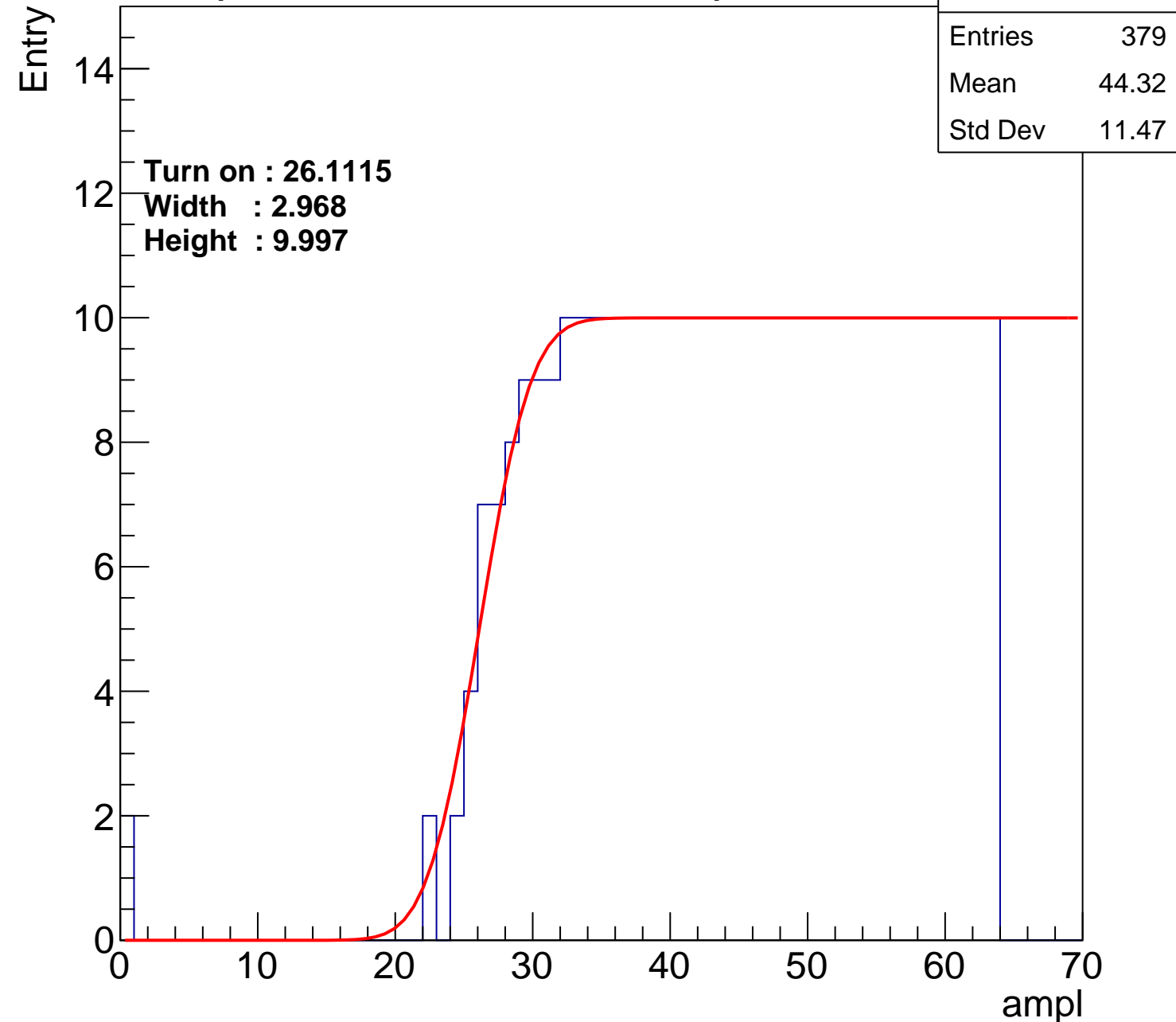
Width : 2.968

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.46
Std Dev	10.93

Turn on : 29.4076

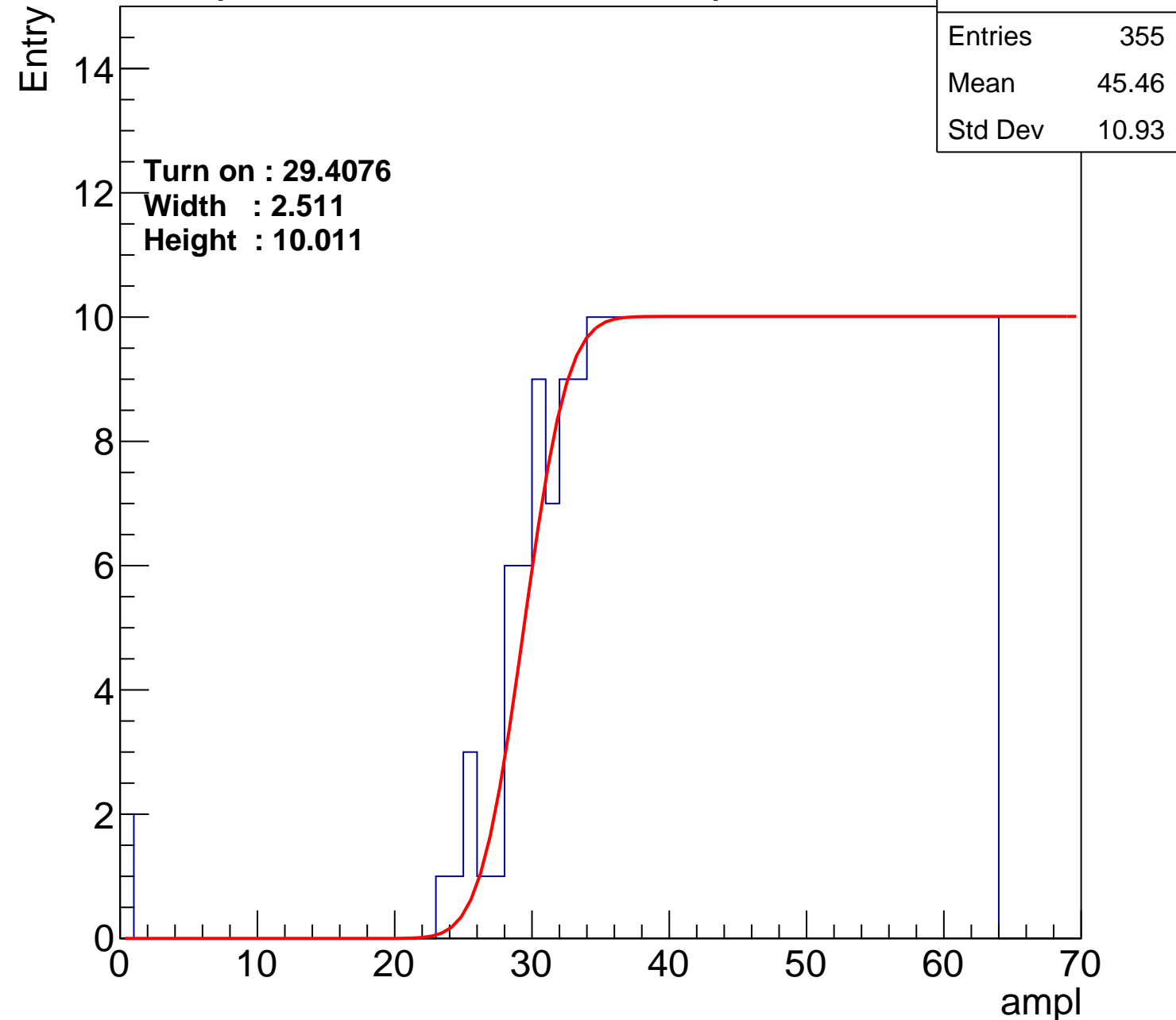
Width : 2.511

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.68
Std Dev	11.45

Turn on : 27.3657

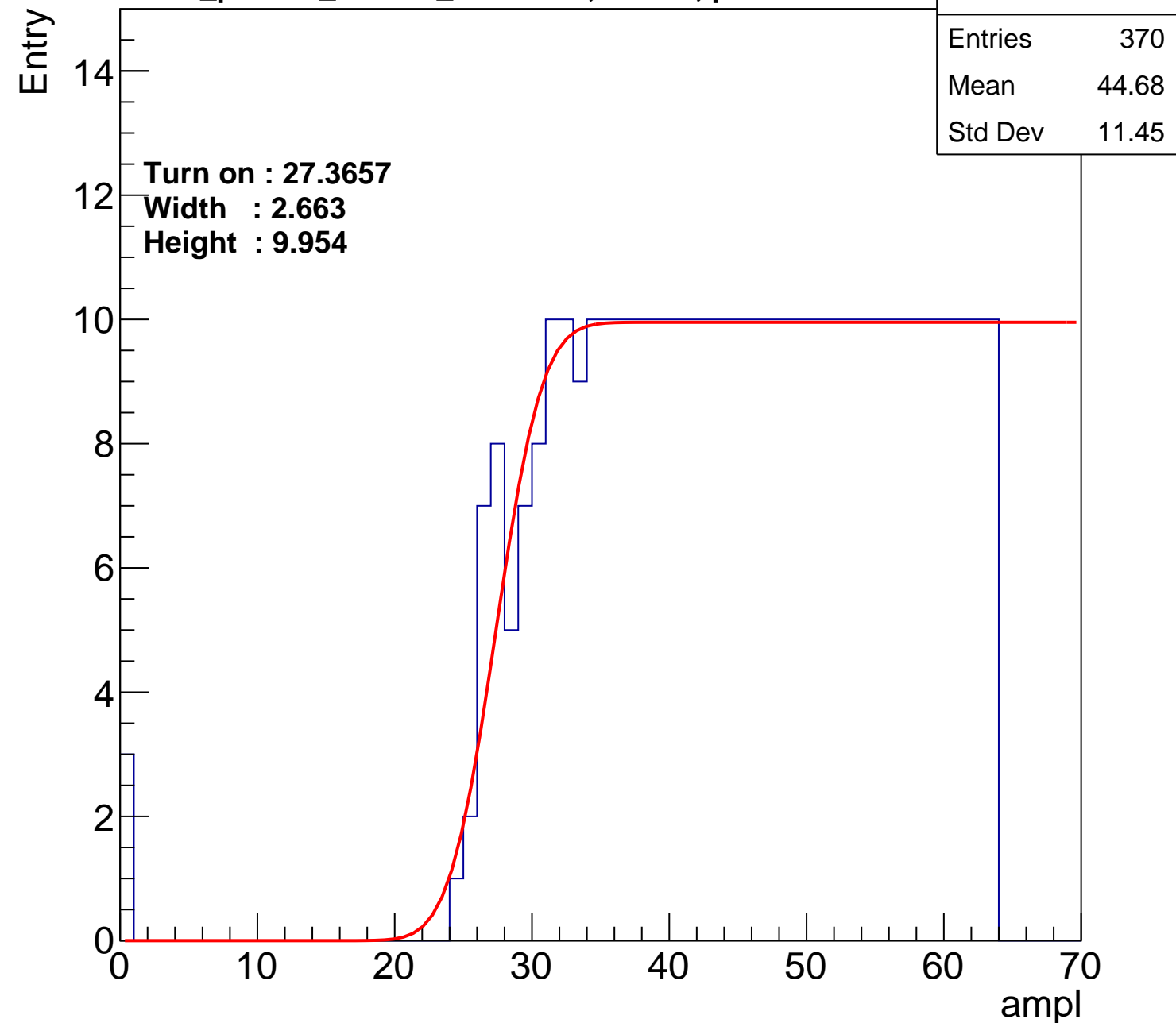
Width : 2.663

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.66
Std Dev	11.57

Turn on : 27.6176

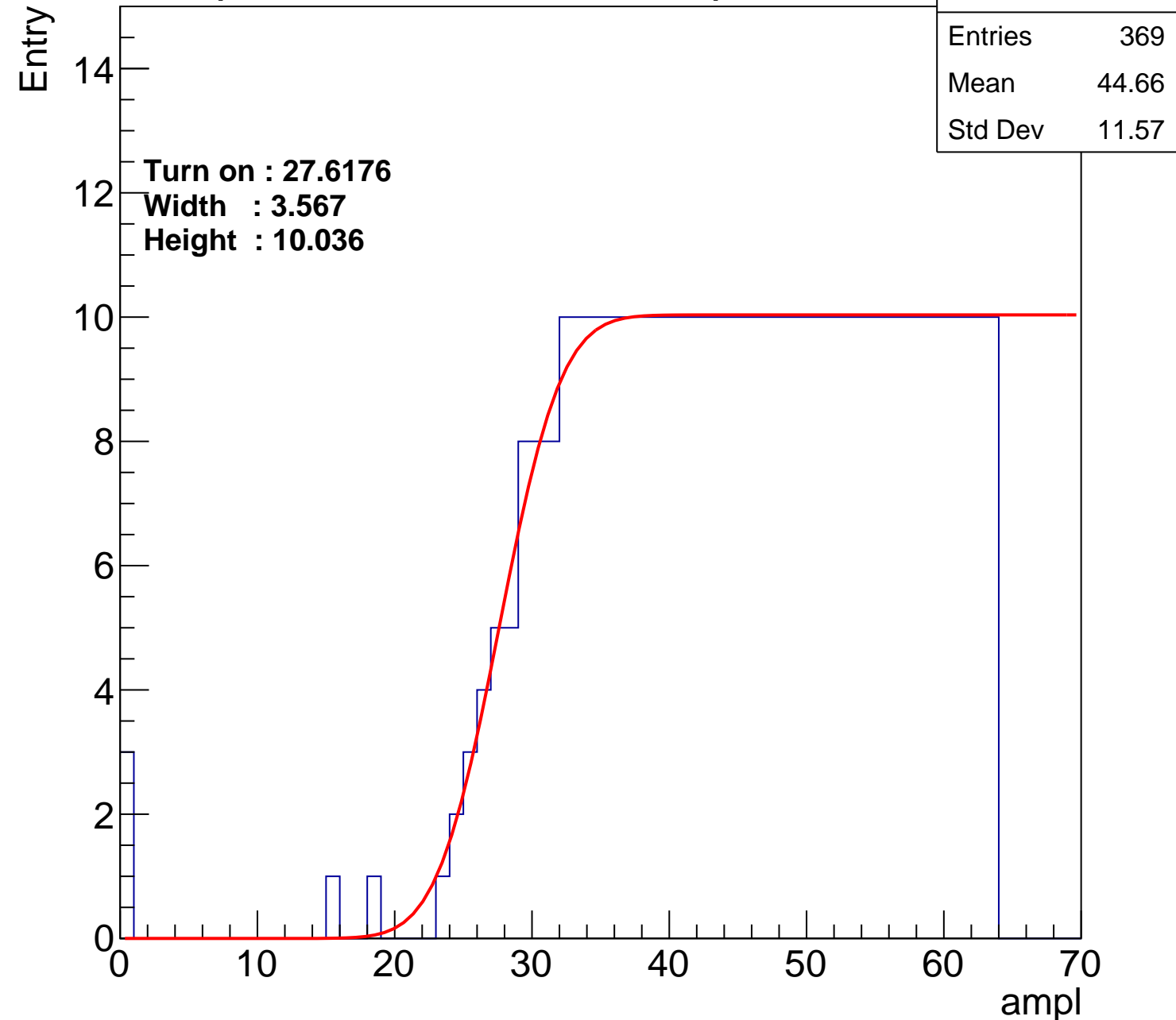
Width : 3.567

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	402
Mean	43.12
Std Dev	12.23

Turn on : 24.2529

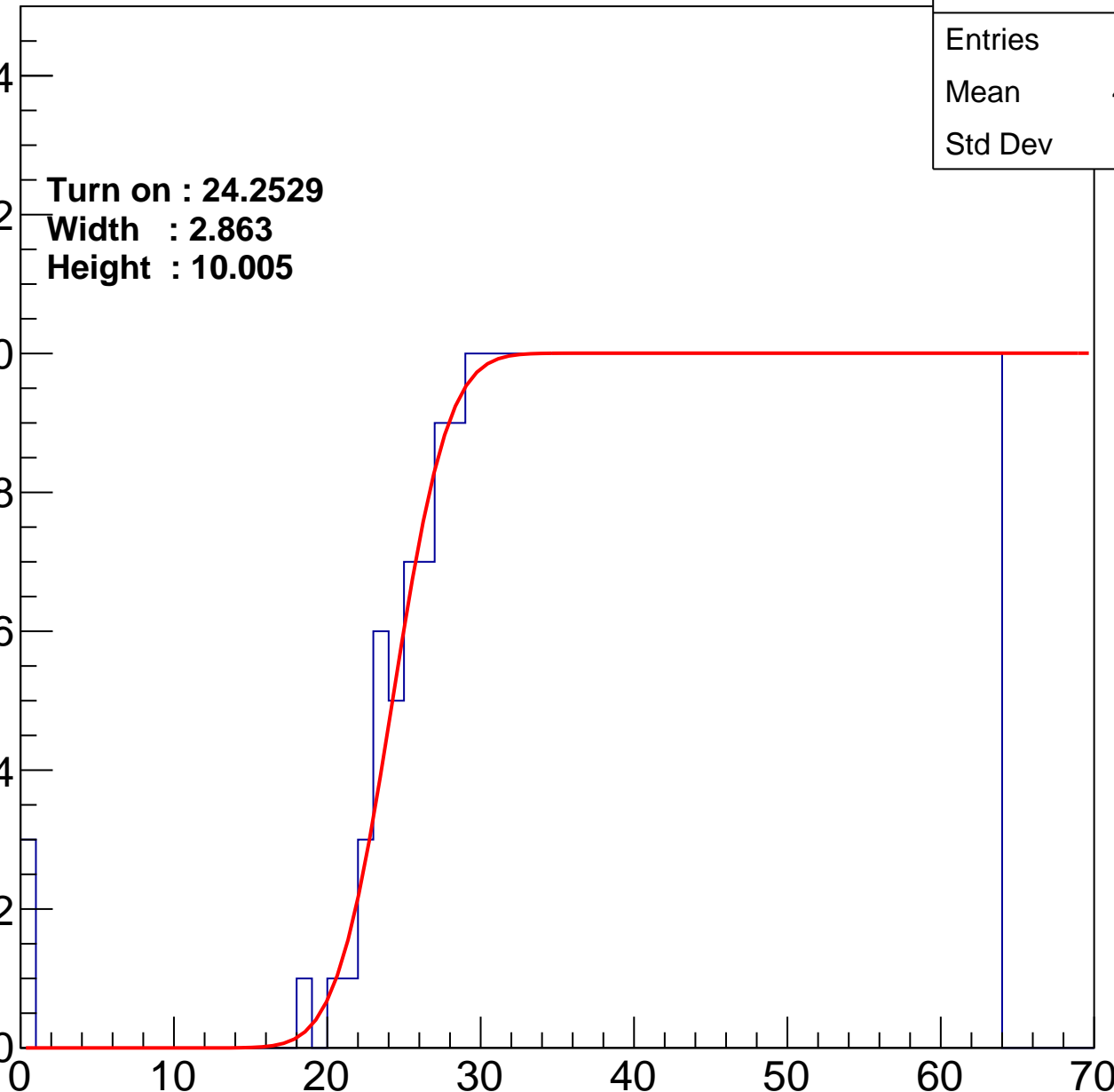
Width : 2.863

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.96
Std Dev	11.04

Turn on : 27.5951

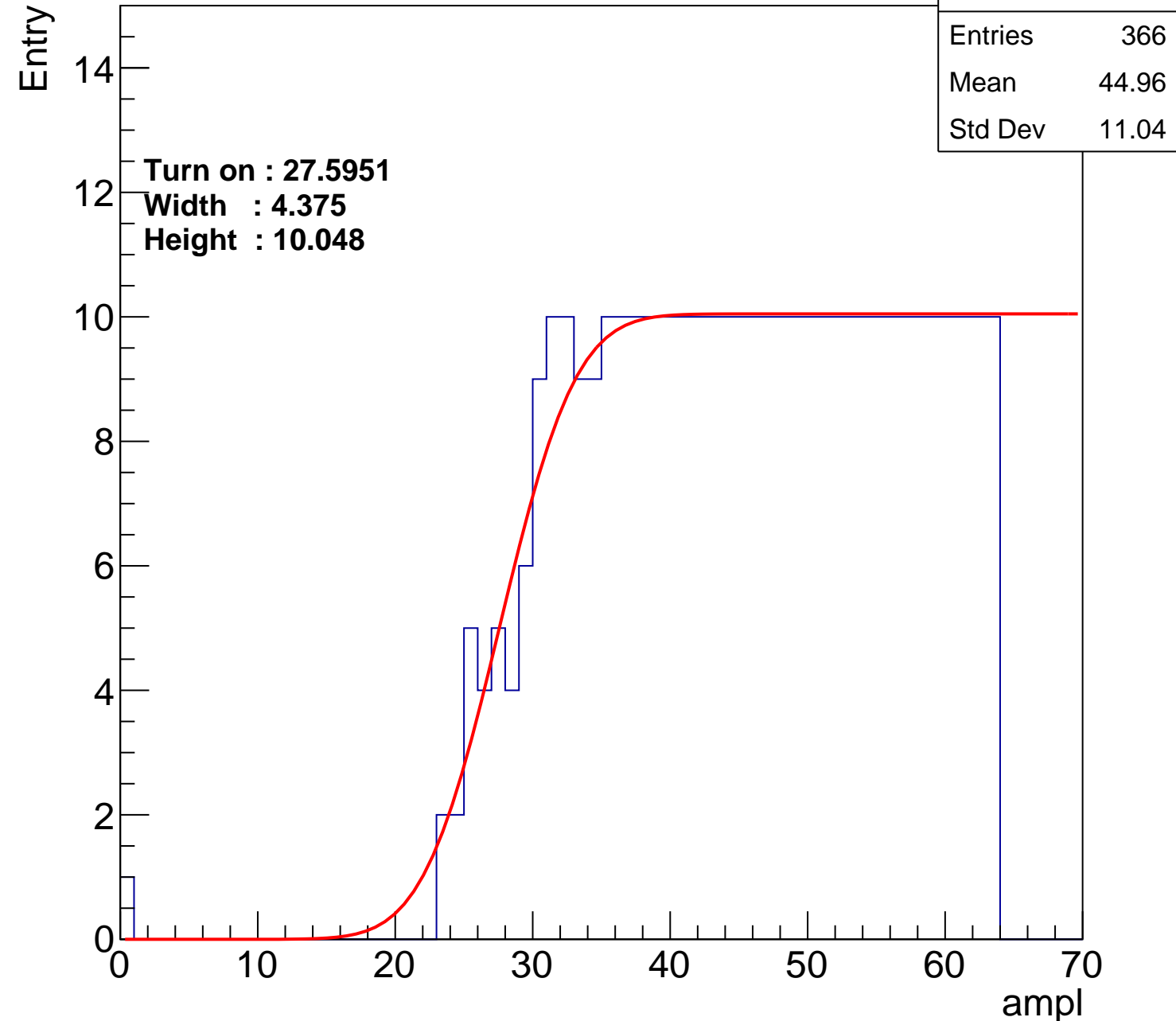
Width : 4.375

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.1618

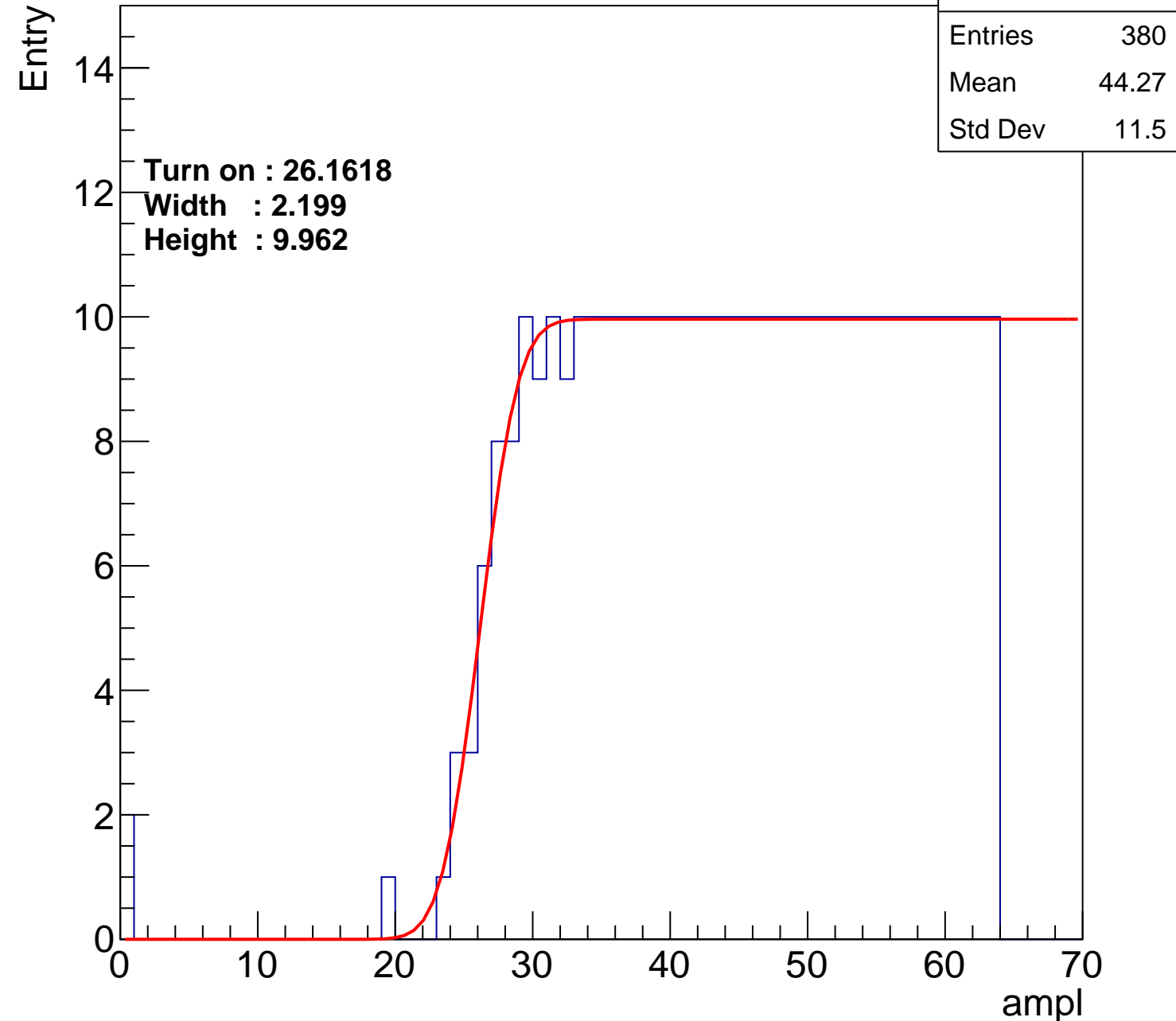
Width : 2.199

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch119

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.63
Std Dev	10.67

Turn on : 29.0180

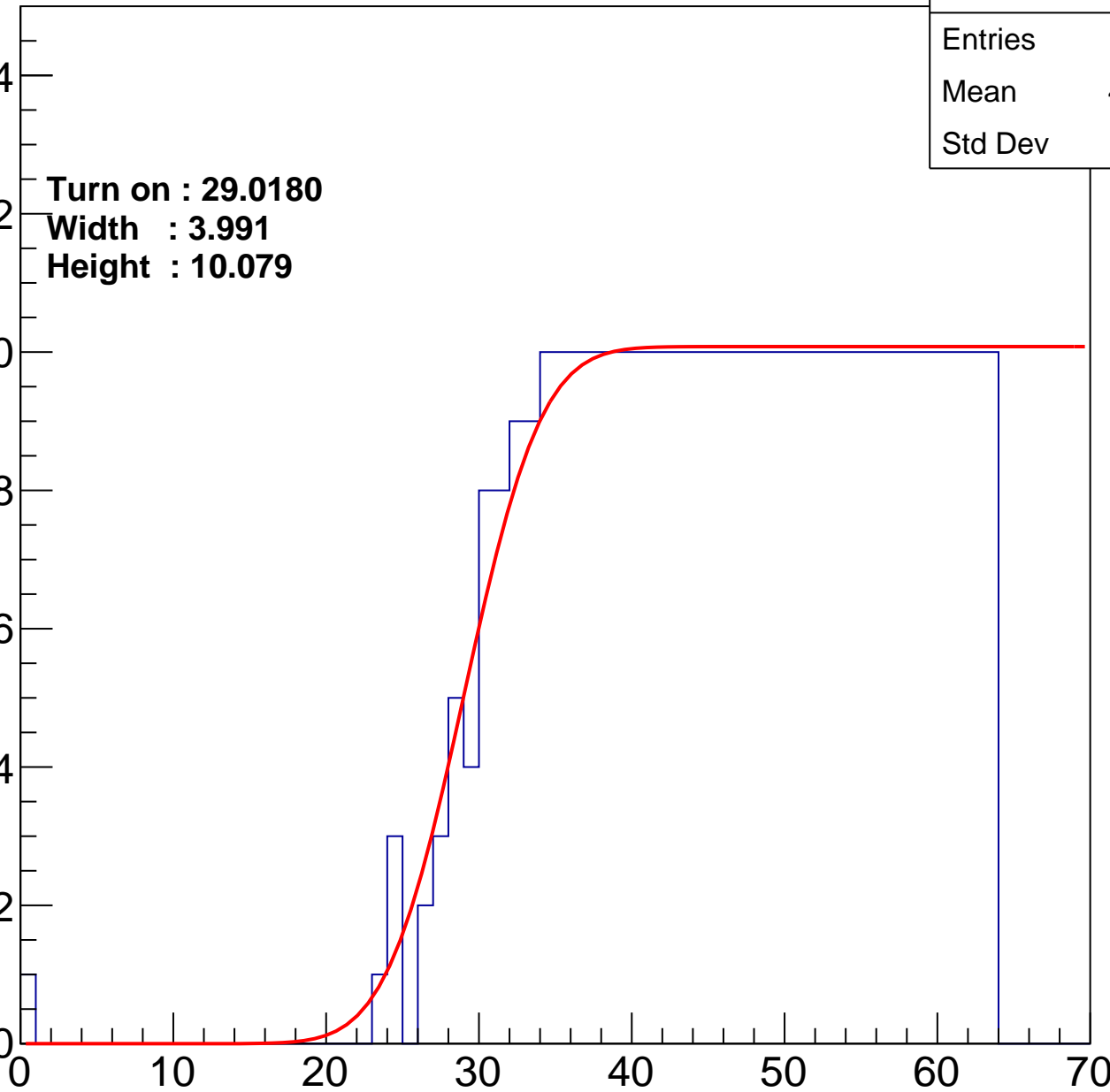
Width : 3.991

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.72
Std Dev	12.33

Turn on : 26.6541

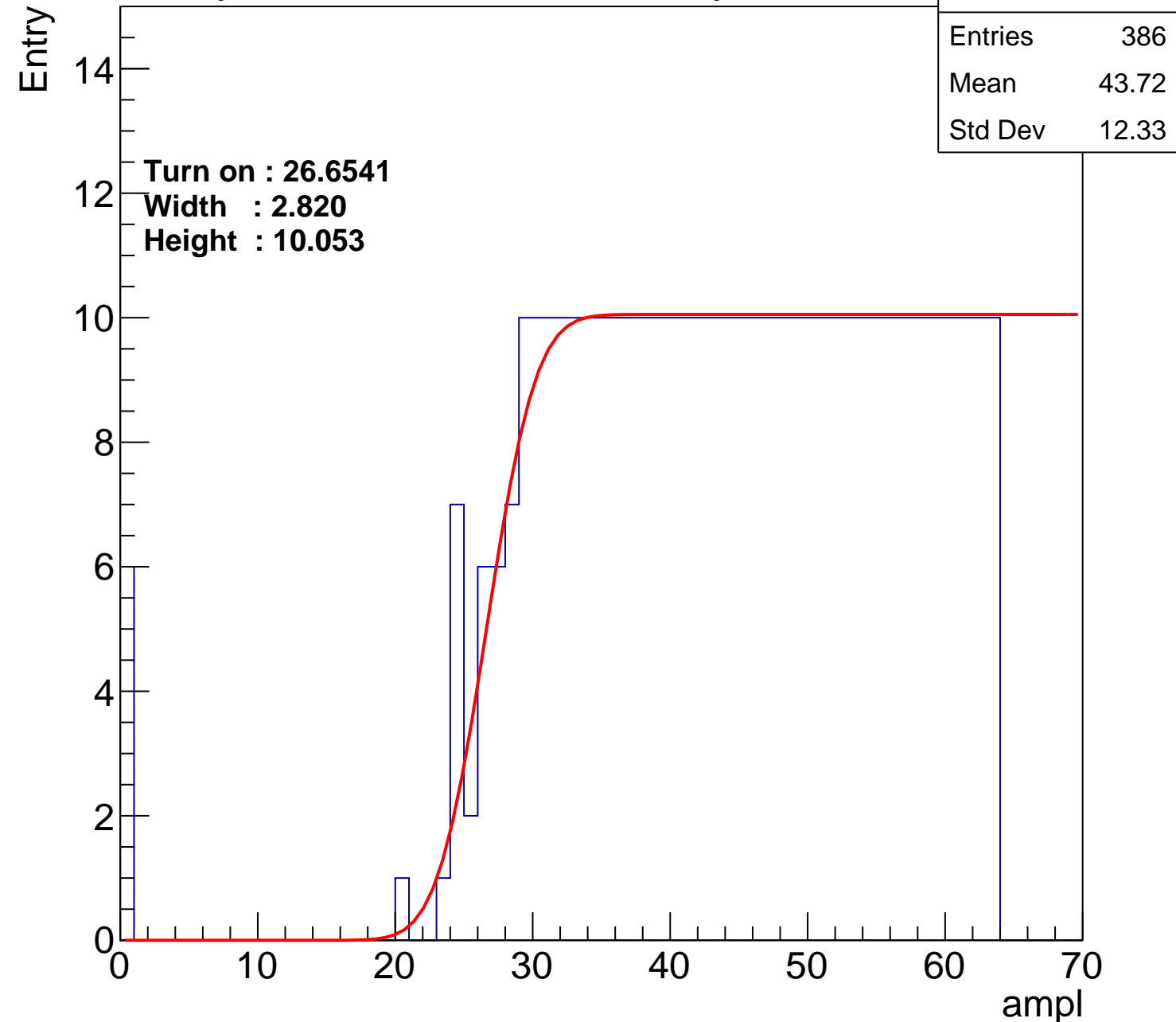
Width : 2.820

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch121

calib_packv5_042523_0143.root, FC#13, port D2

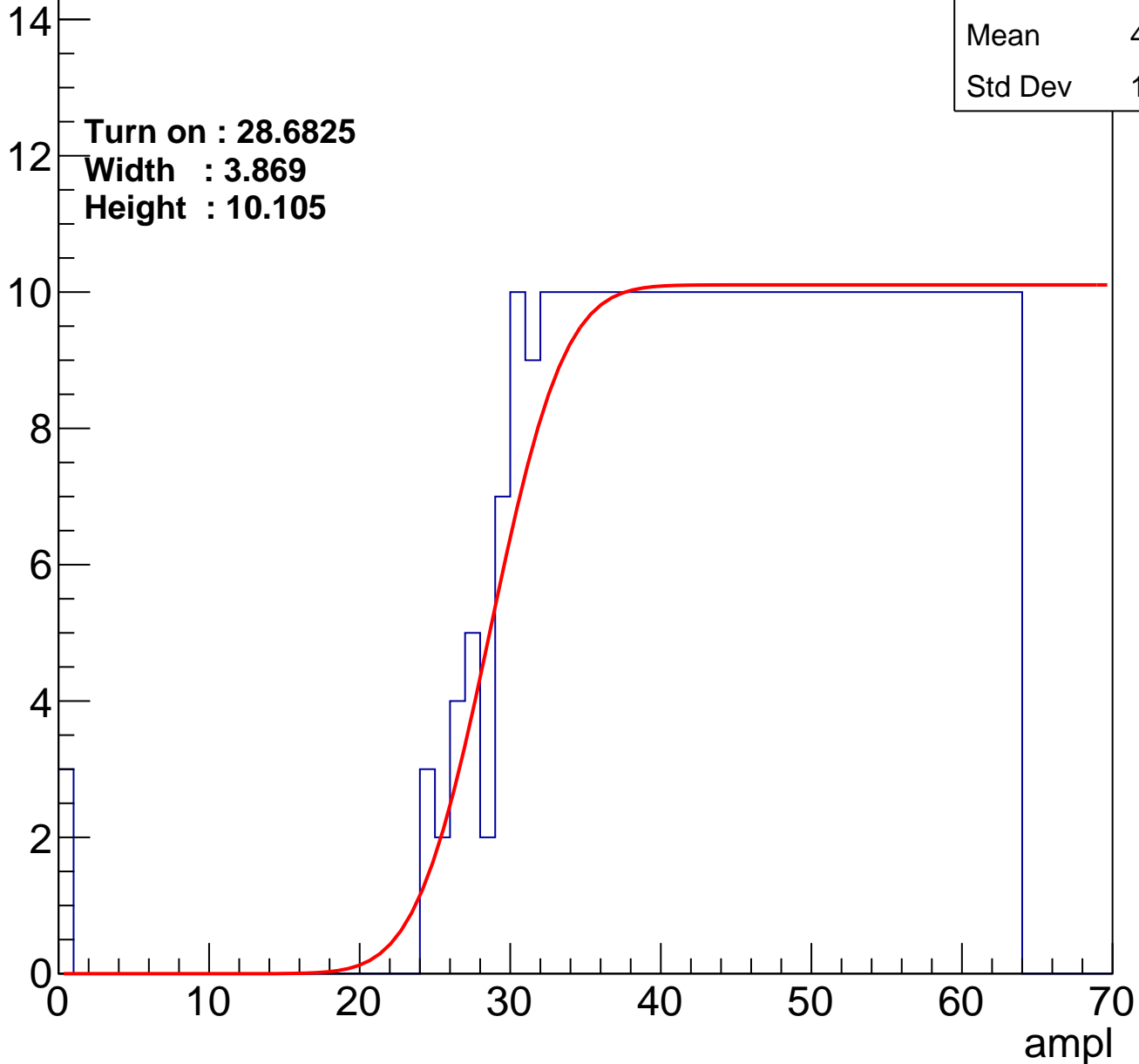
Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.6825

Width : 3.869

Height : 10.105

Entry



B1L003S, U23-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.93
Std Dev	11.05

Turn on : 27.6839

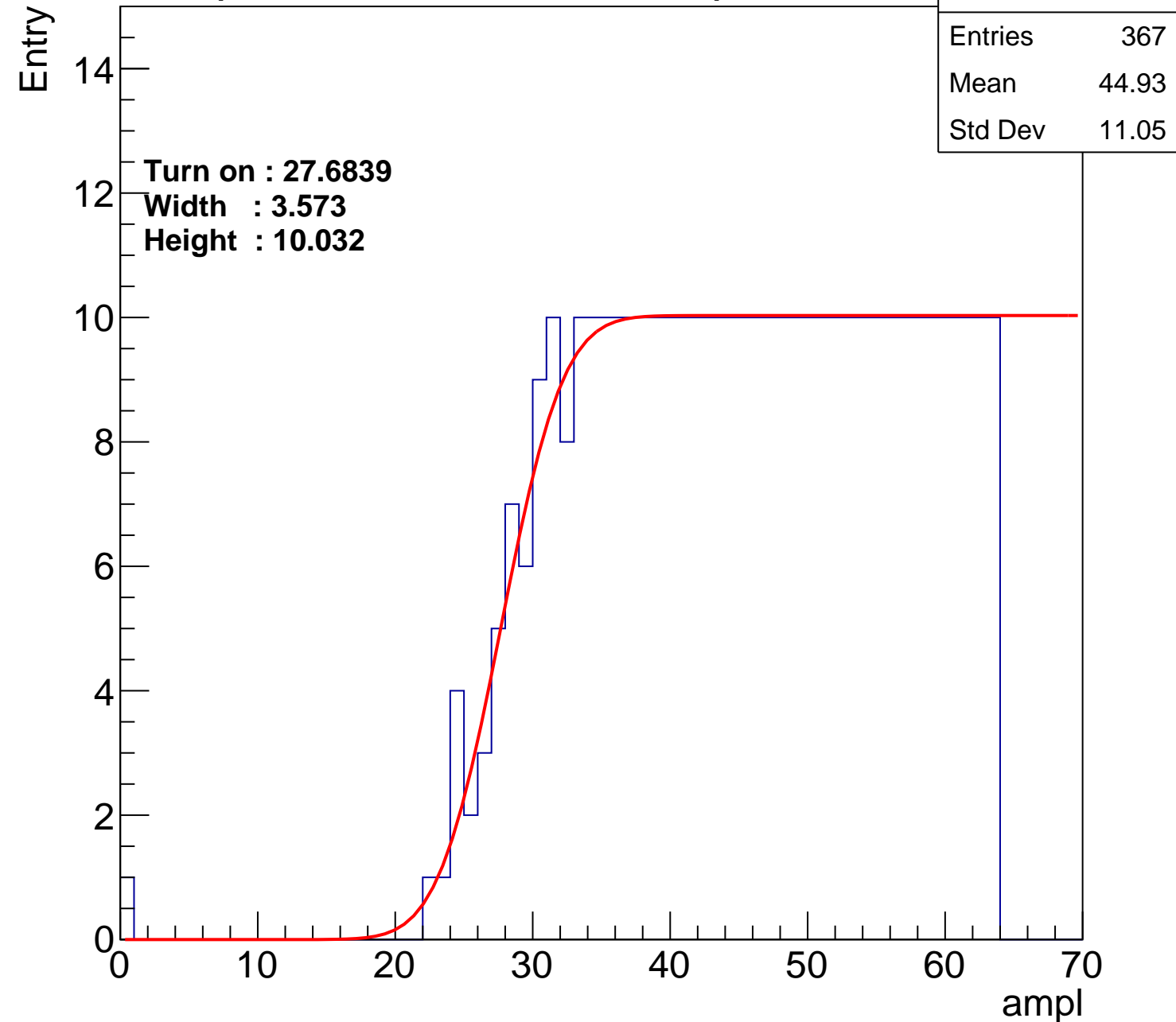
Width : 3.573

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.4968

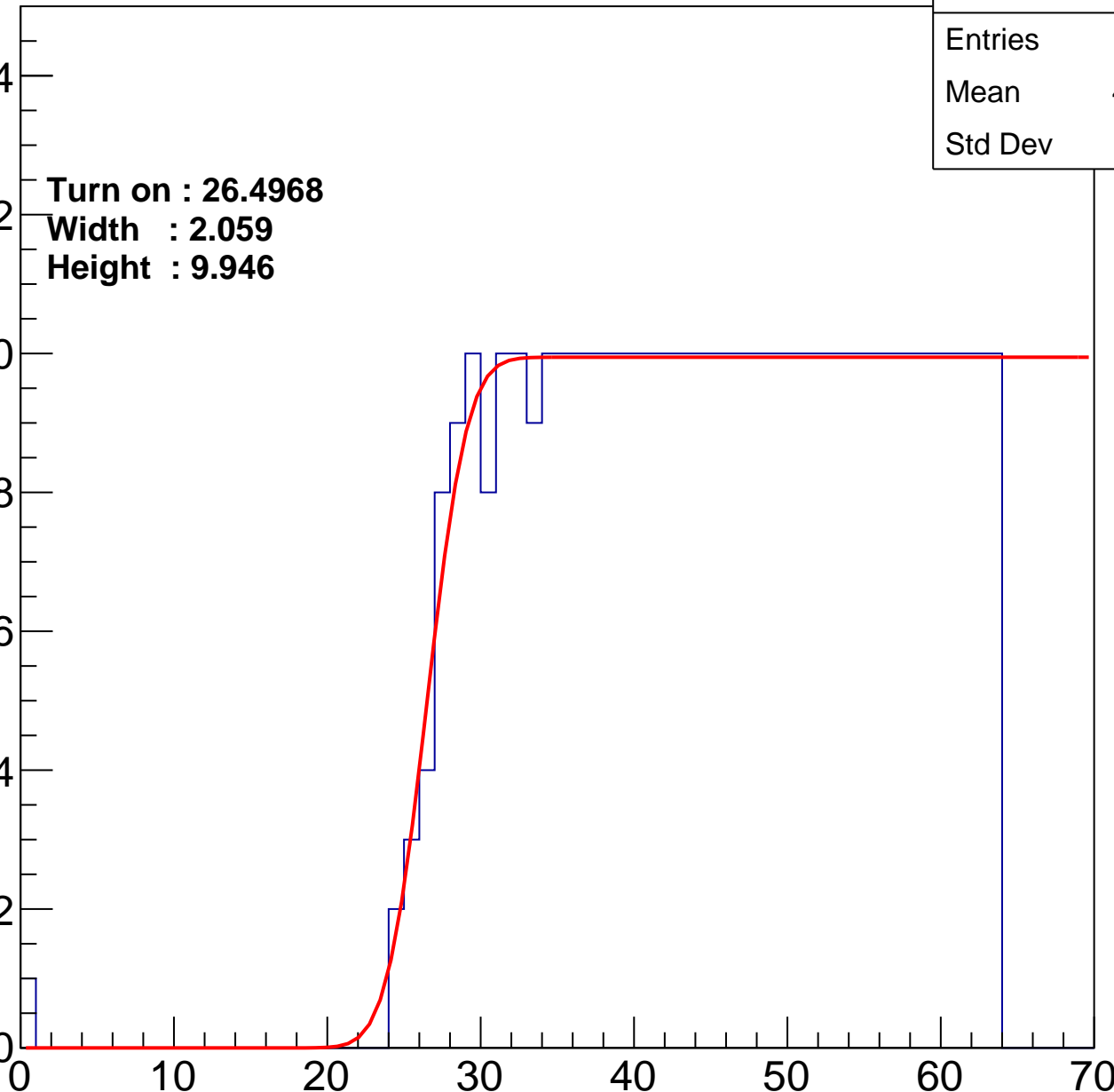
Width : 2.059

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 27.6156

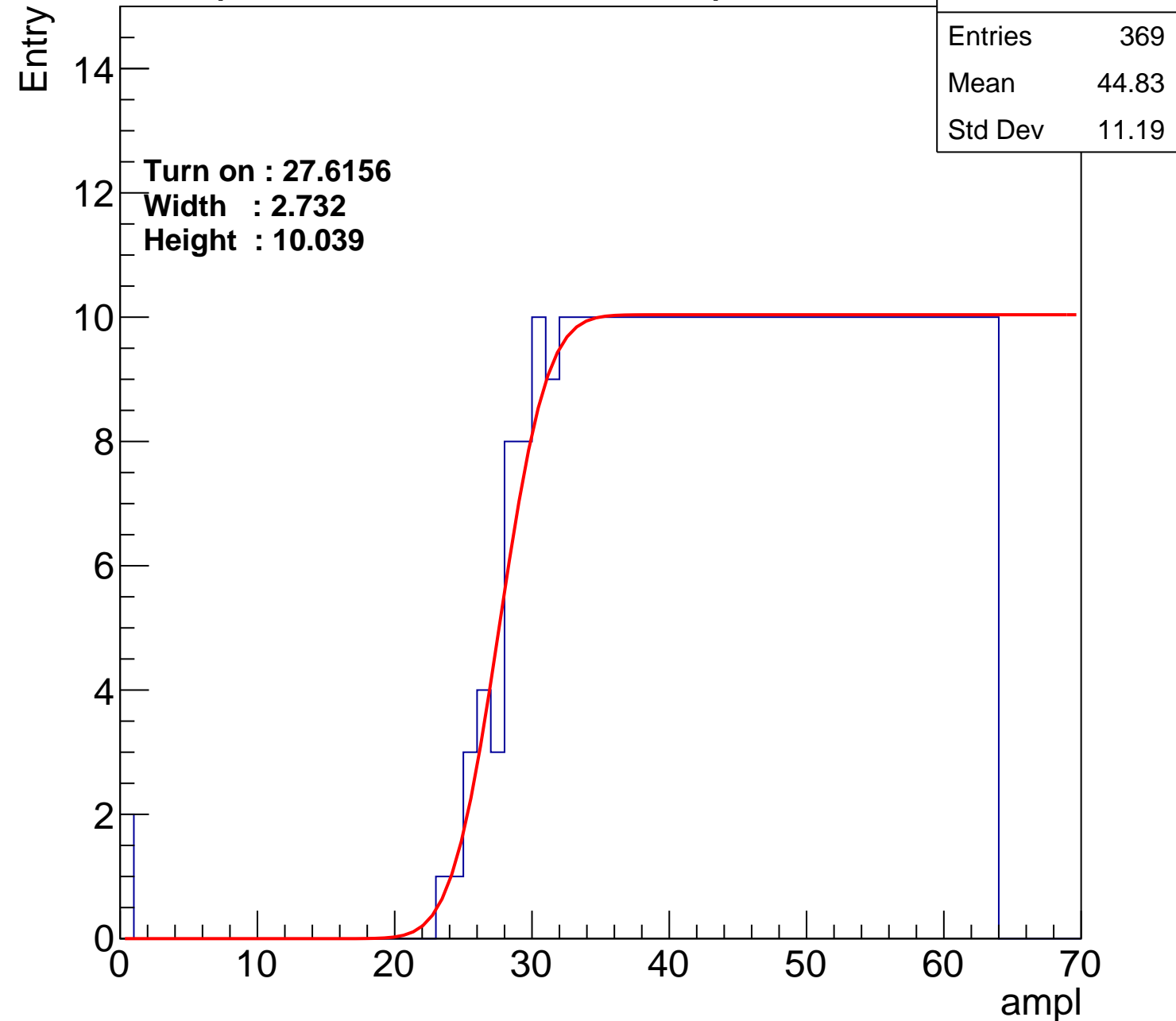
Width : 2.732

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch125

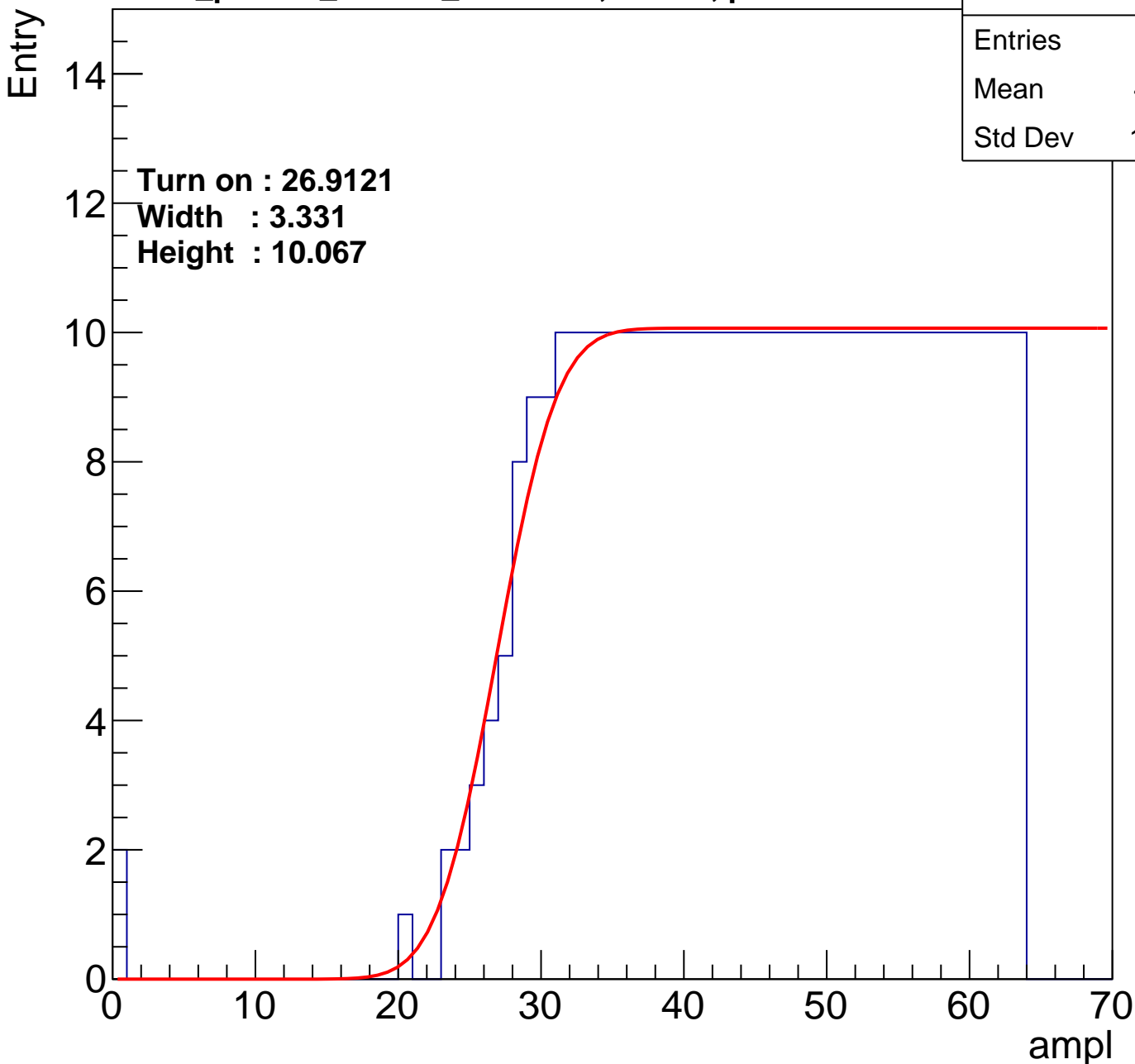
calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.9121

Width : 3.331

Height : 10.067



B1L003S, U23-ch126

calib_packv5_042523_0143.root, FC#13, port D2

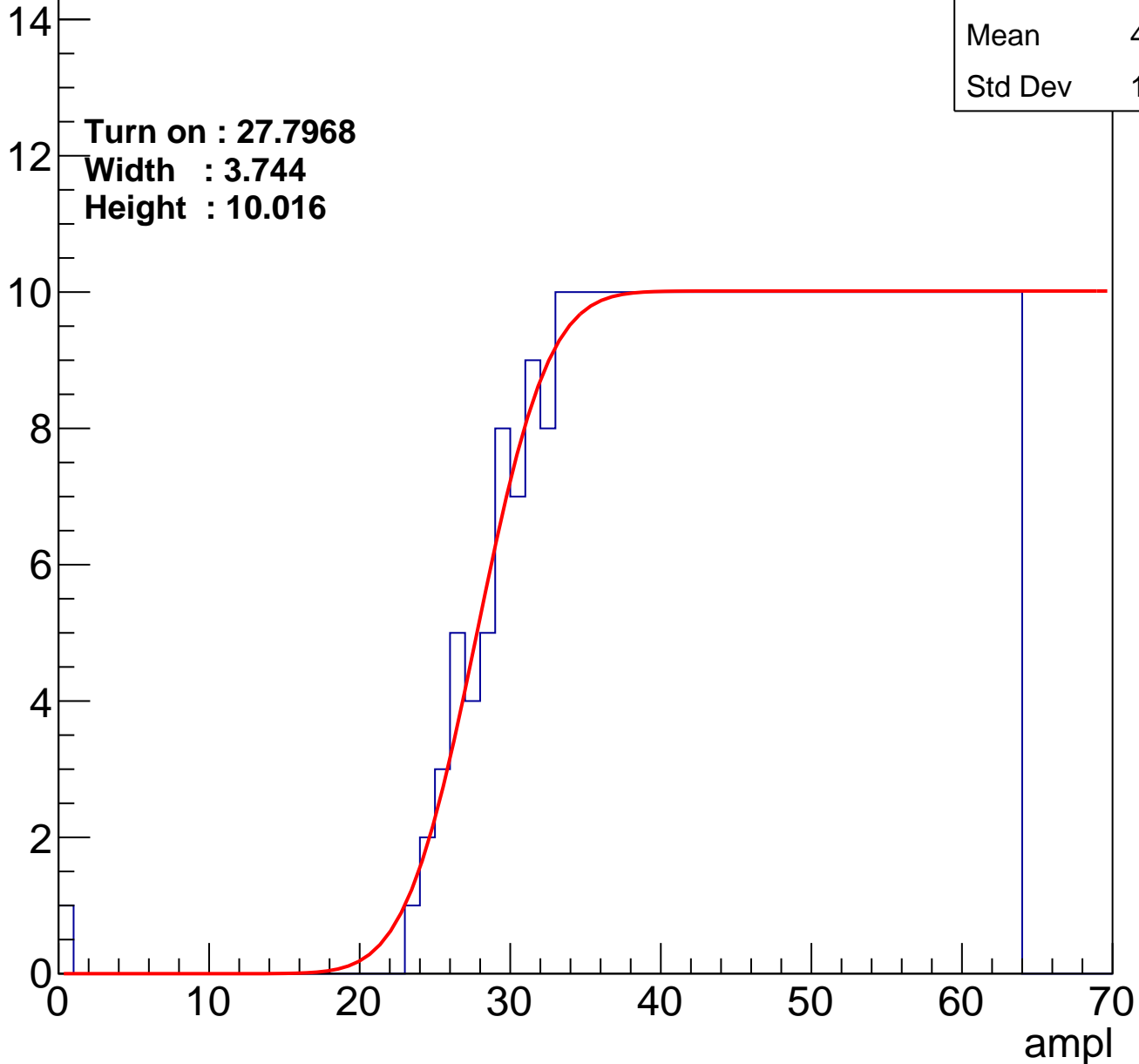
Entries	363
Mean	45.13
Std Dev	10.94

Turn on : 27.7968

Width : 3.744

Height : 10.016

Entry



B1L003S, U23-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.79
Std Dev	11.46

Turn on : 27.9452

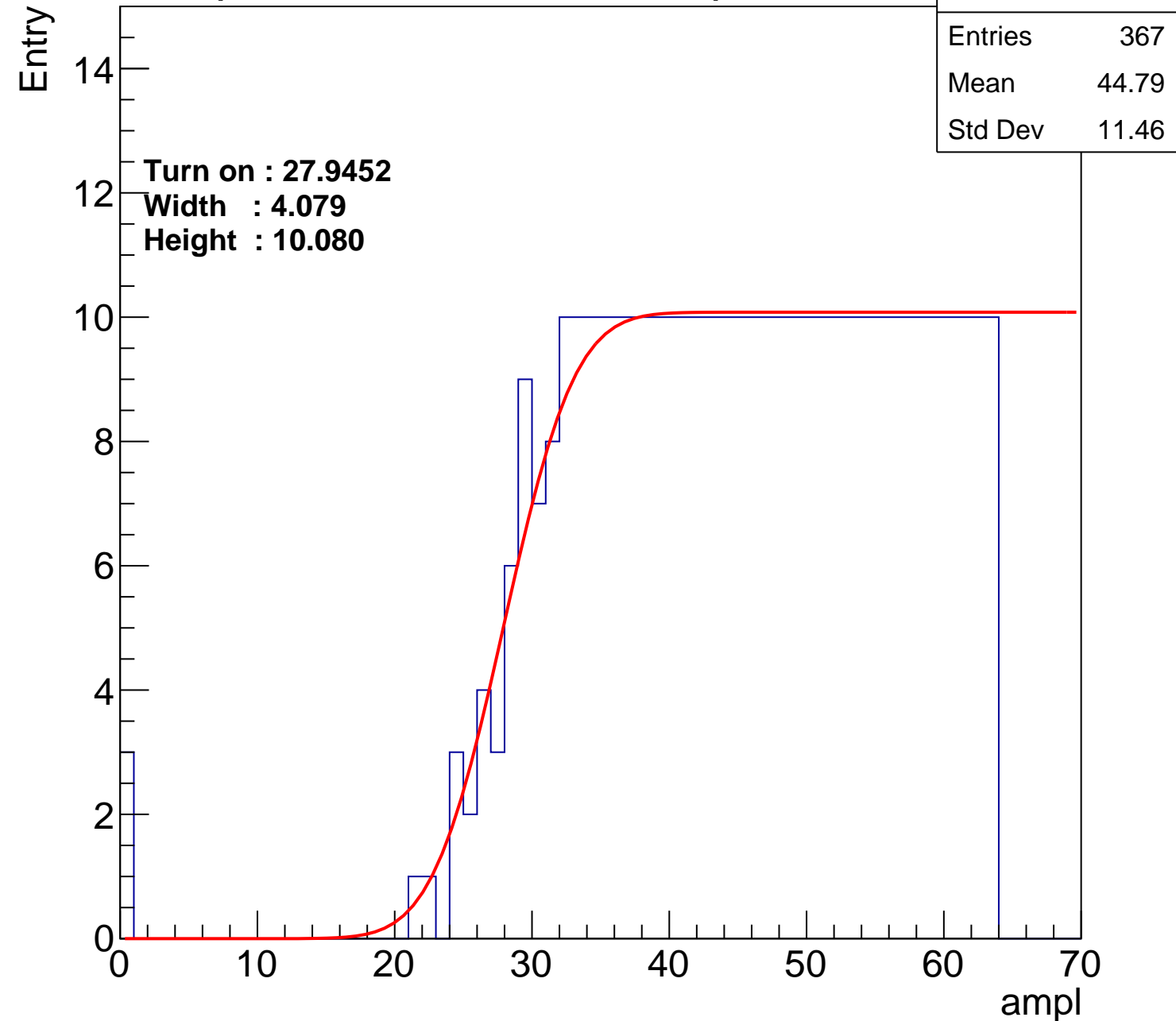
Width : 4.079

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U23-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.79
Std Dev	11.46

Turn on : 27.9452

Width : 4.079

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl

