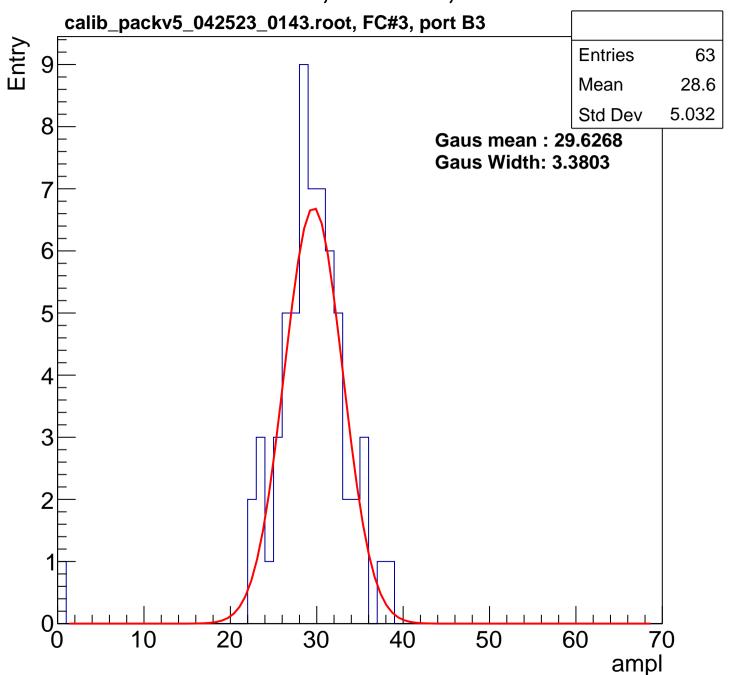
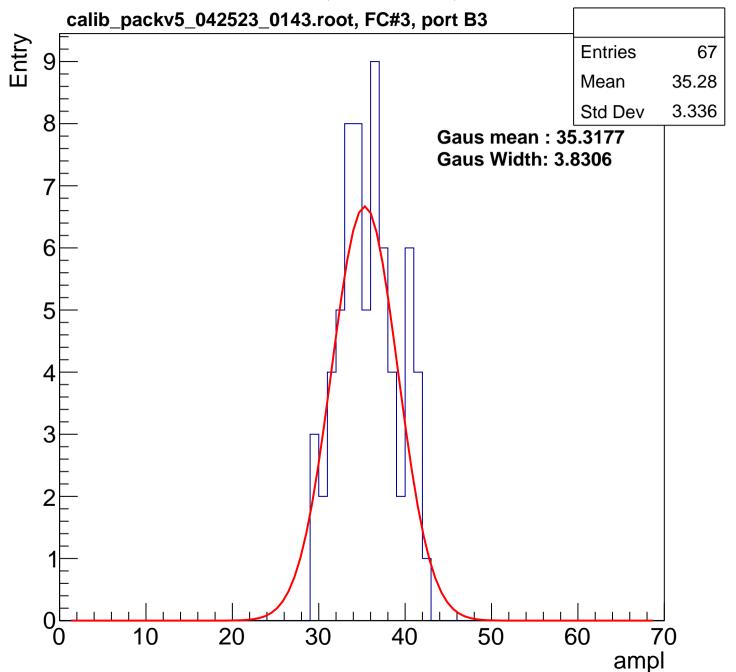
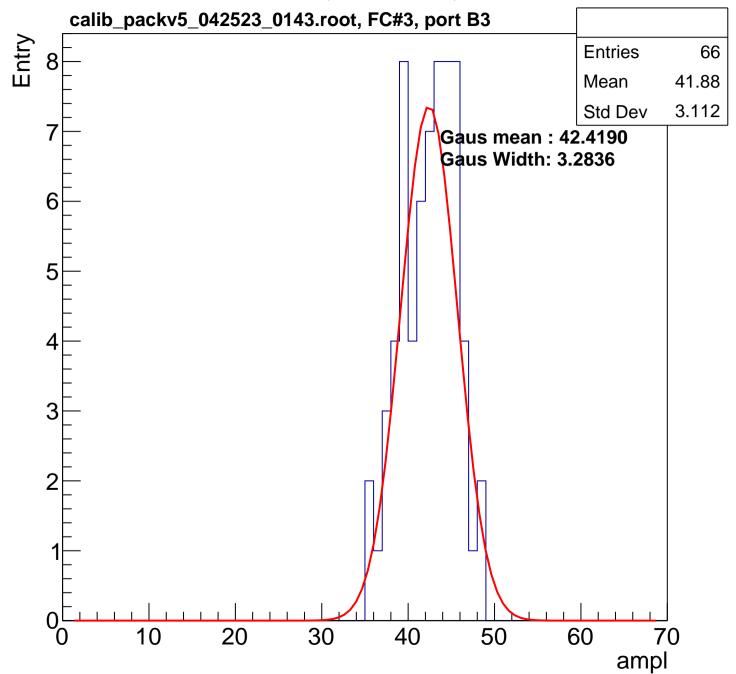
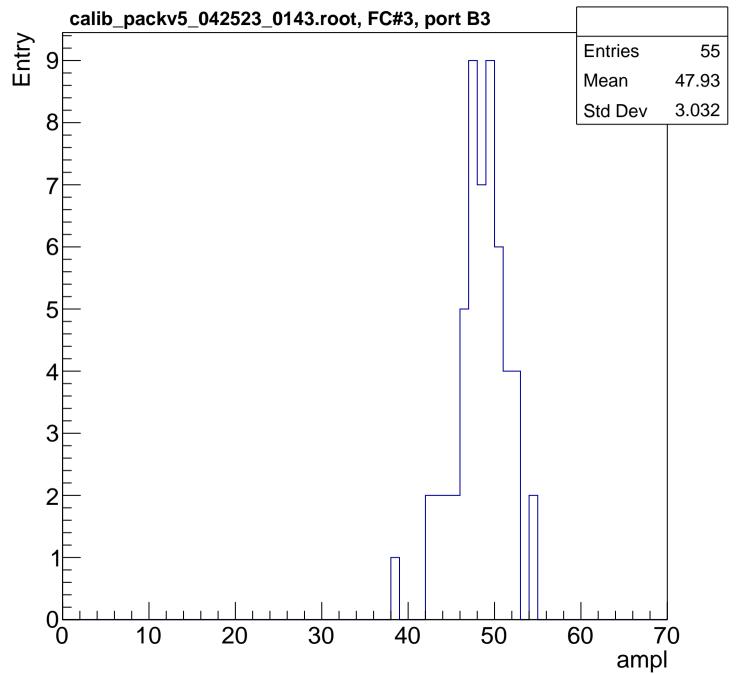


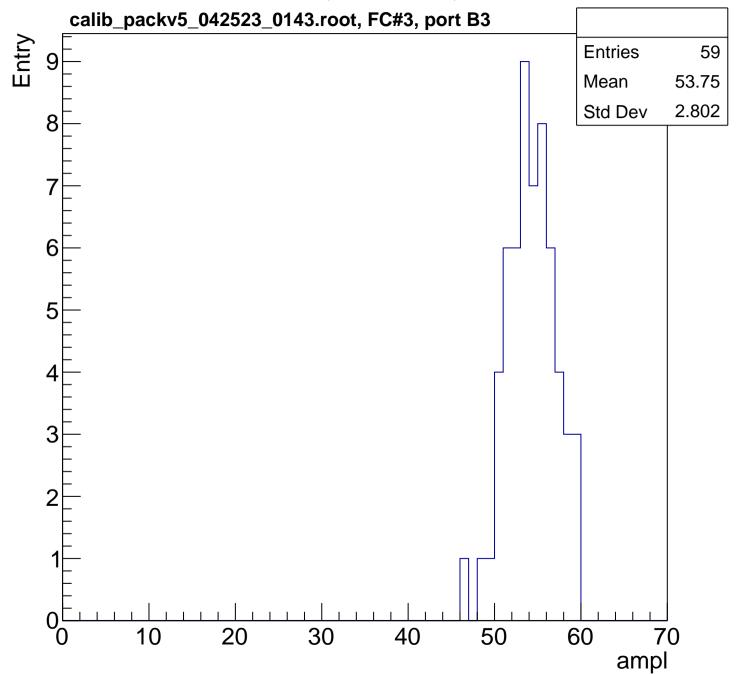
B1L000S, U9-ch1, adc7 calib\_packv5\_042523\_0143.root, FC#3, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

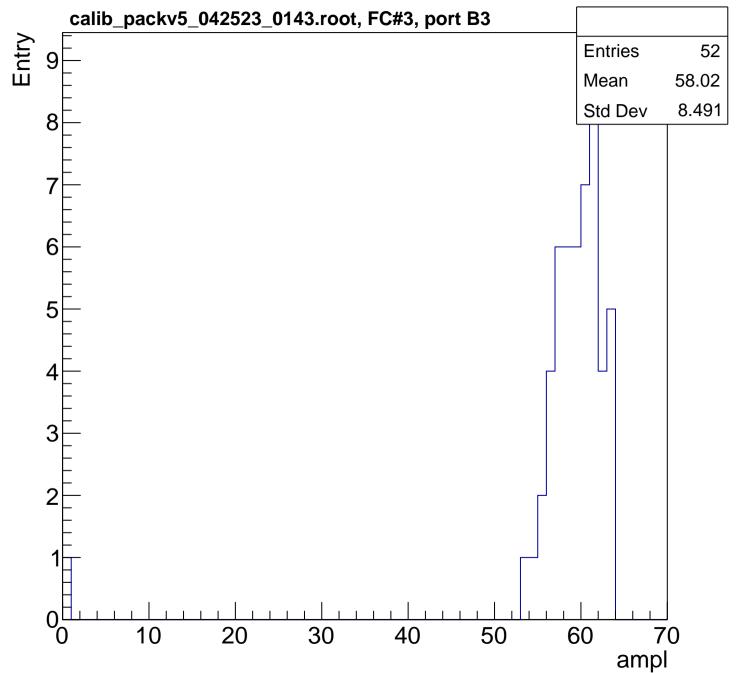


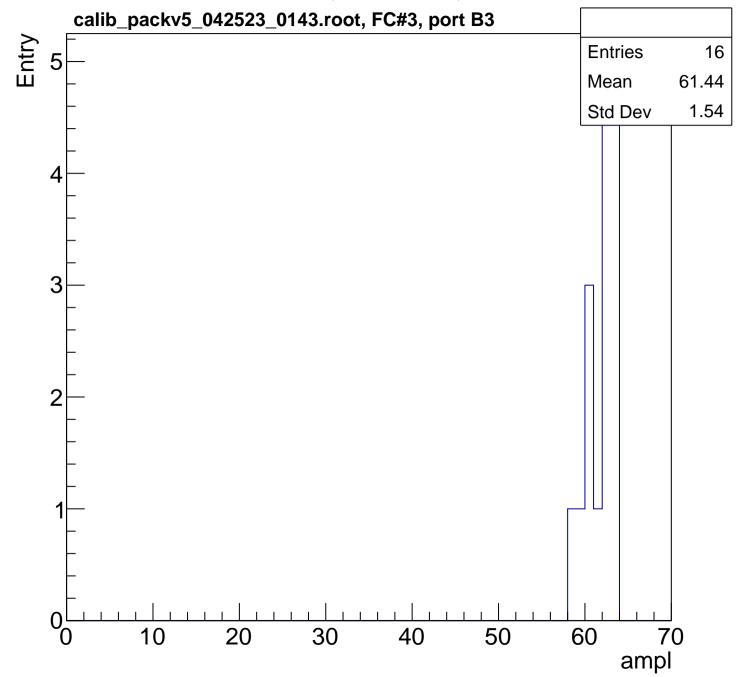


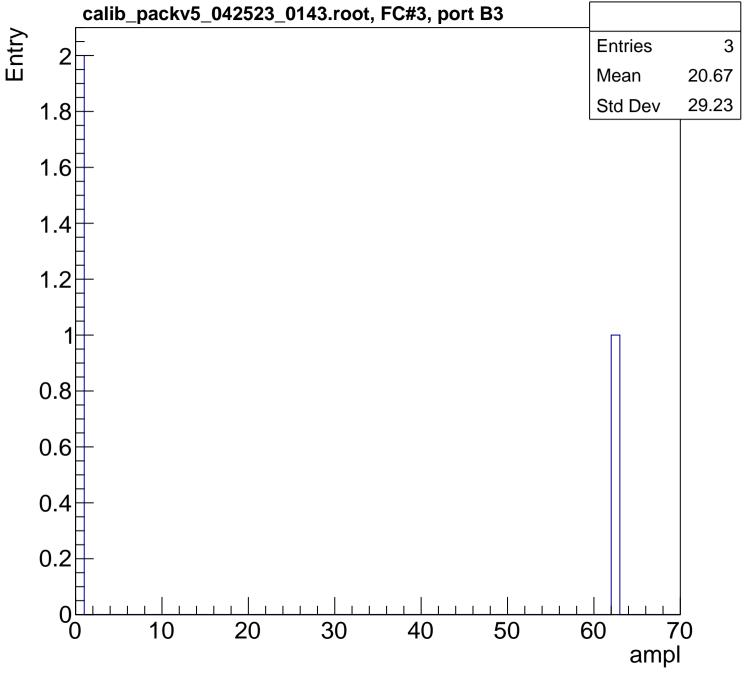


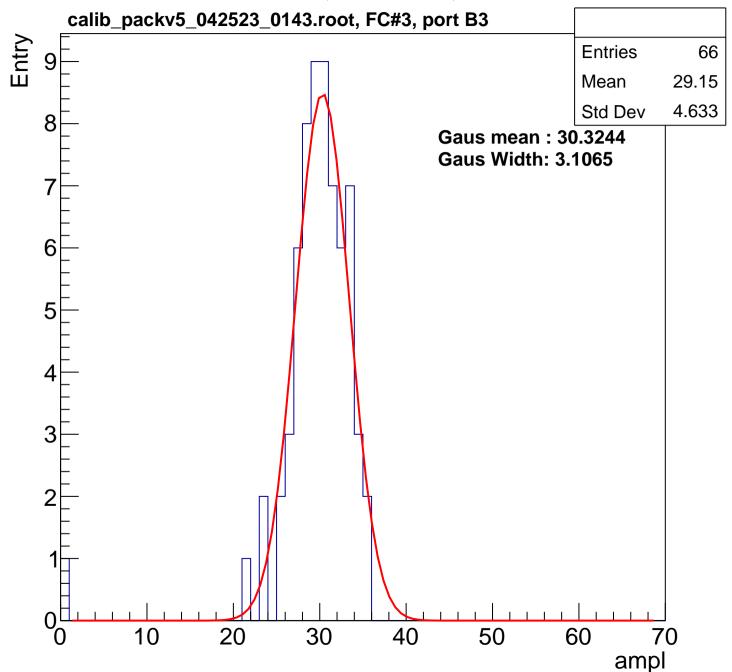


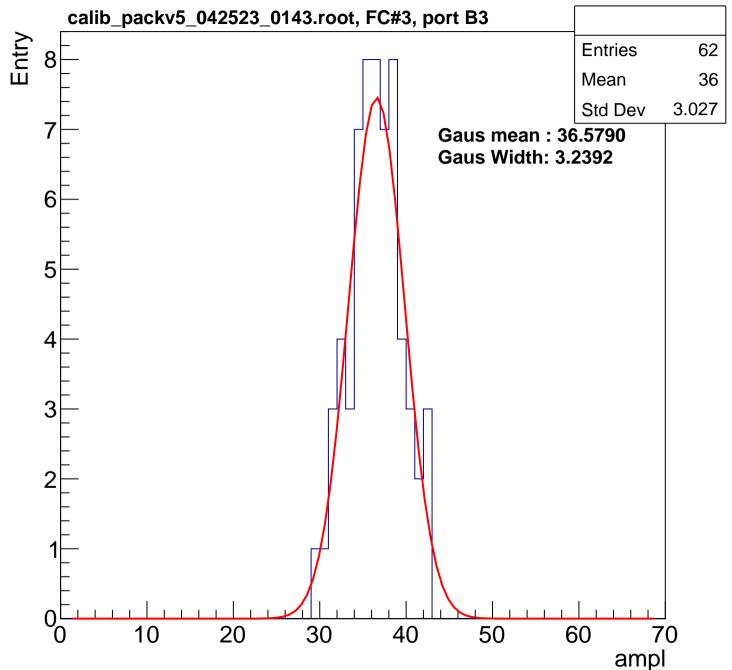


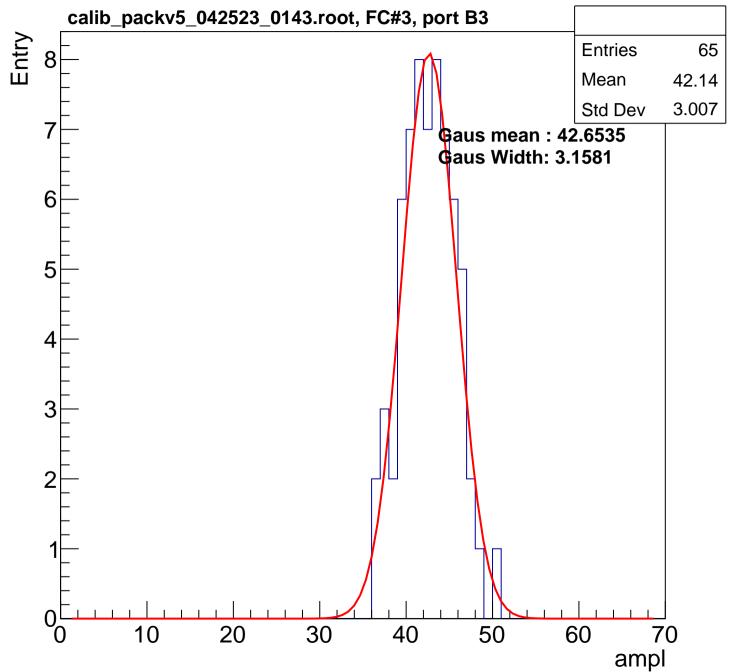


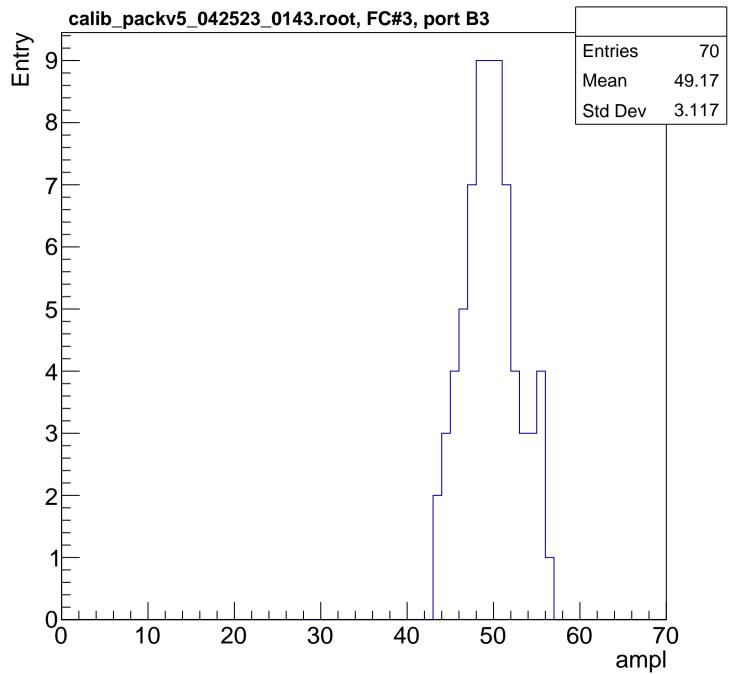


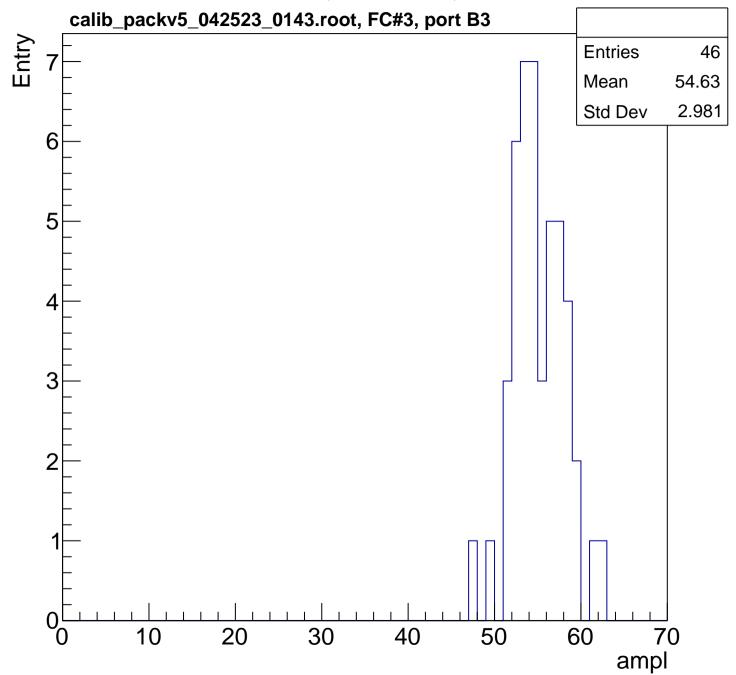


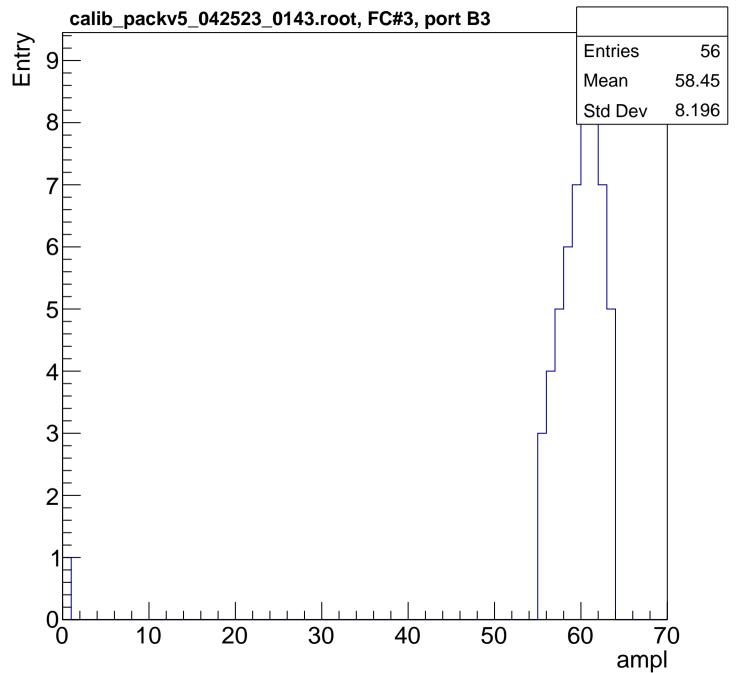


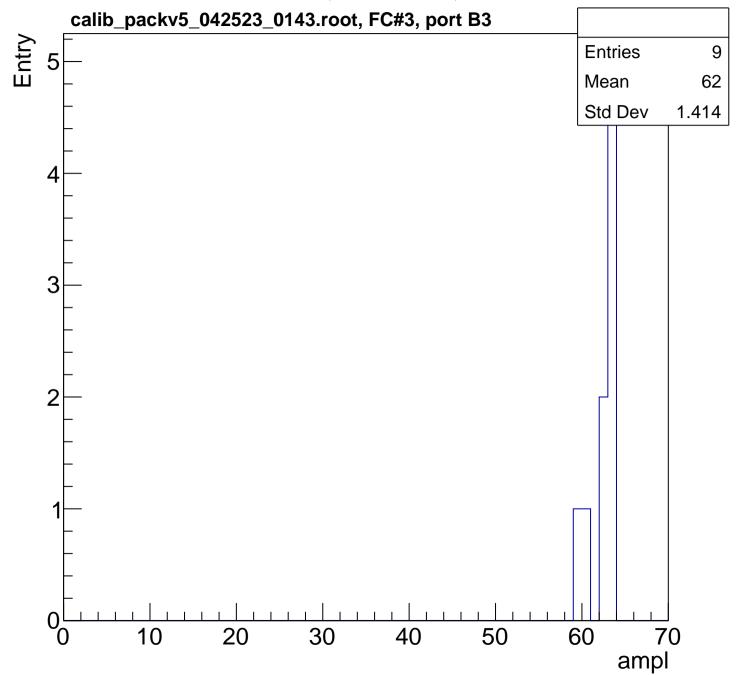


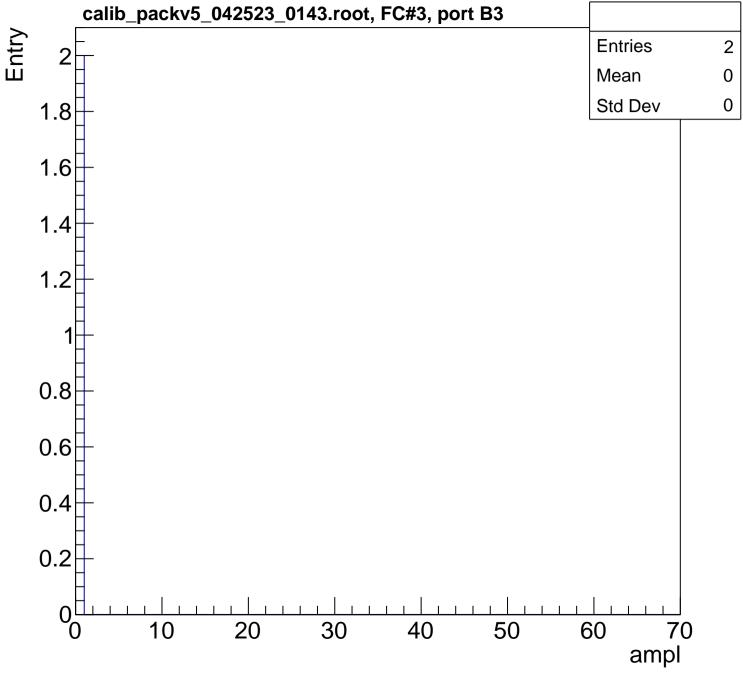


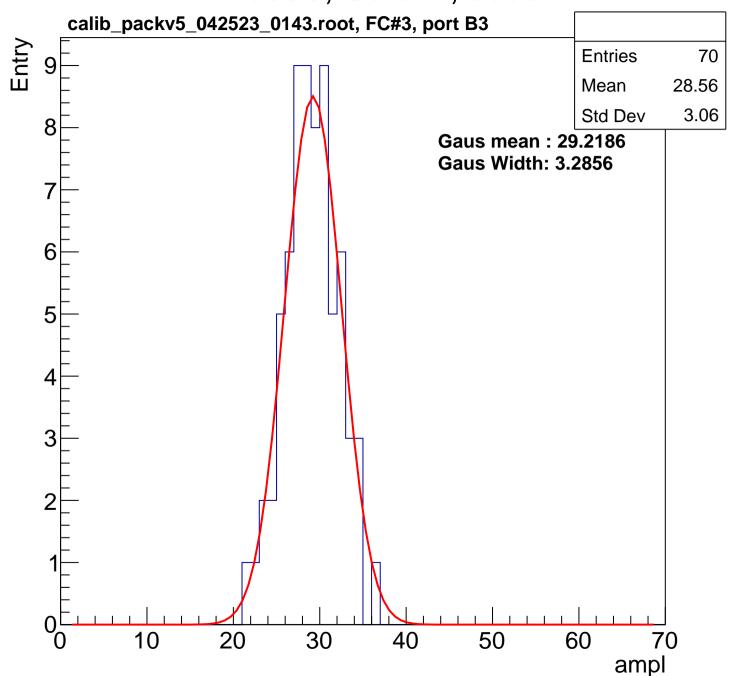


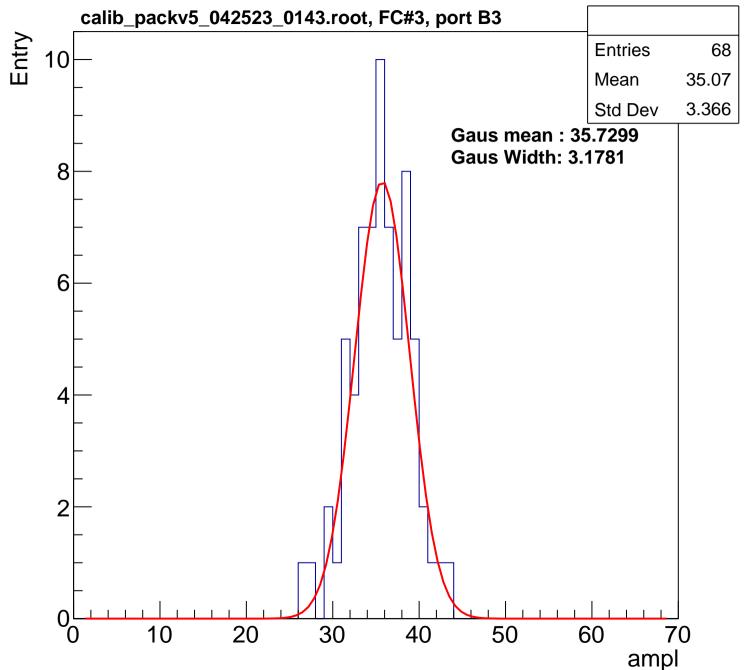


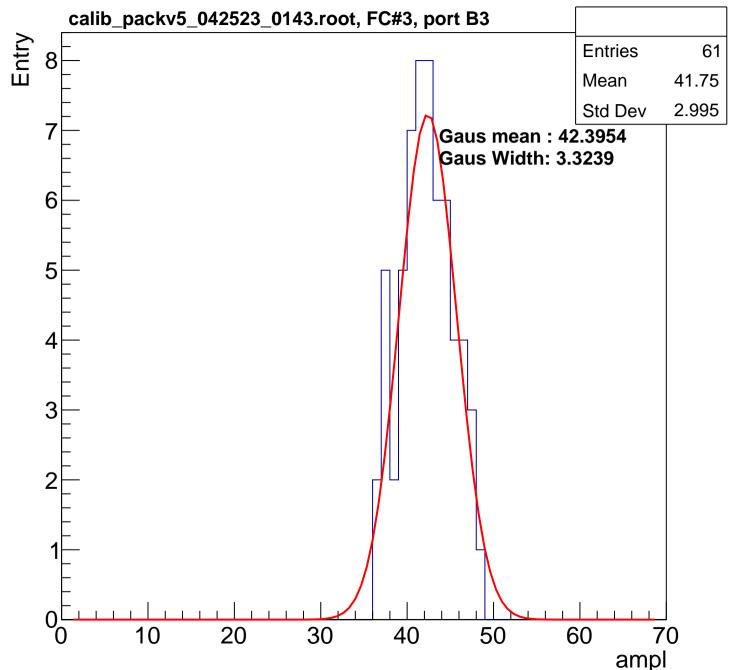


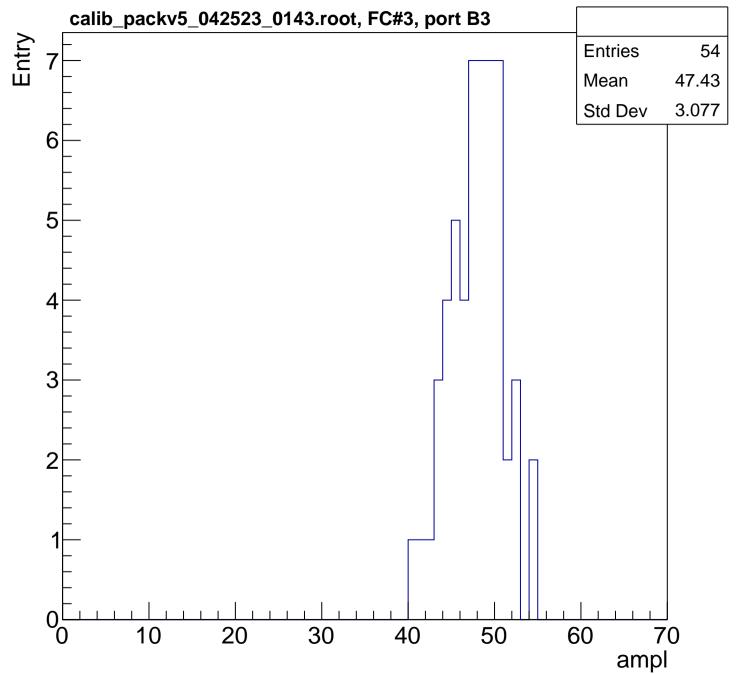


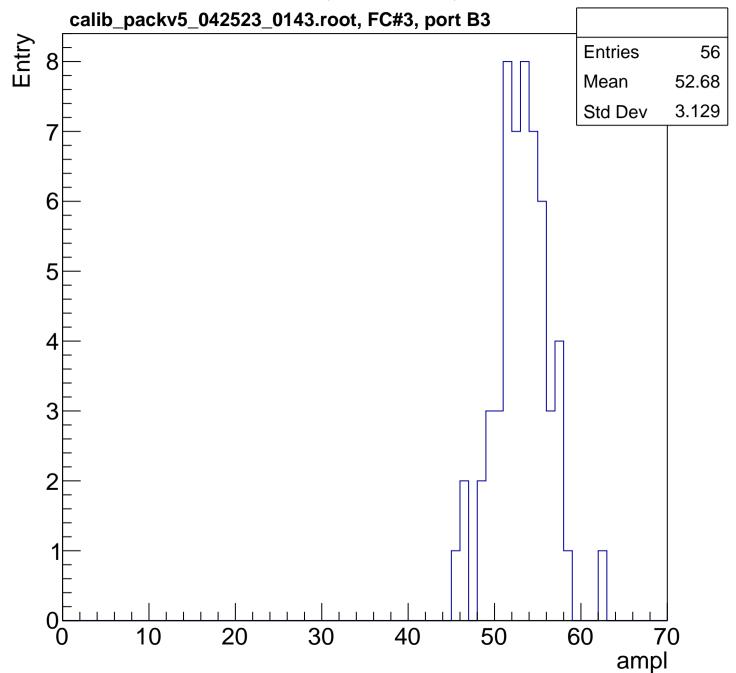


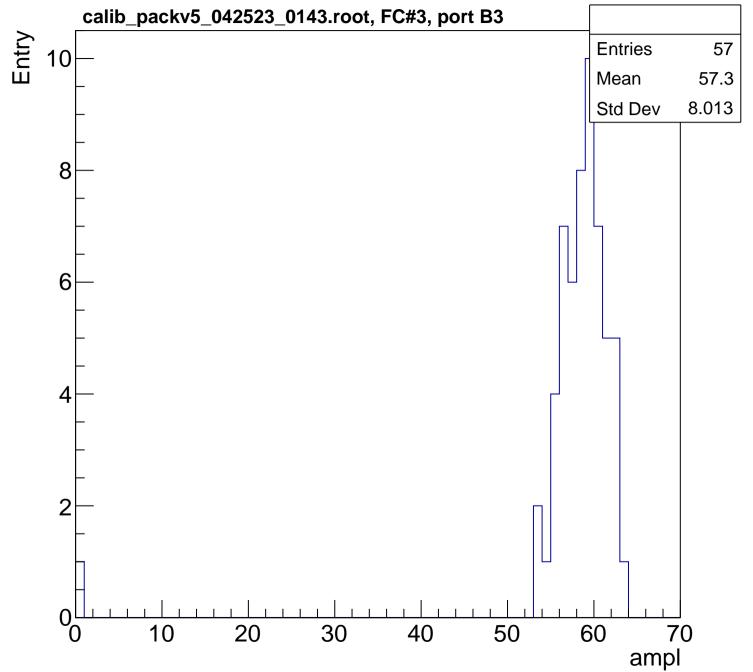


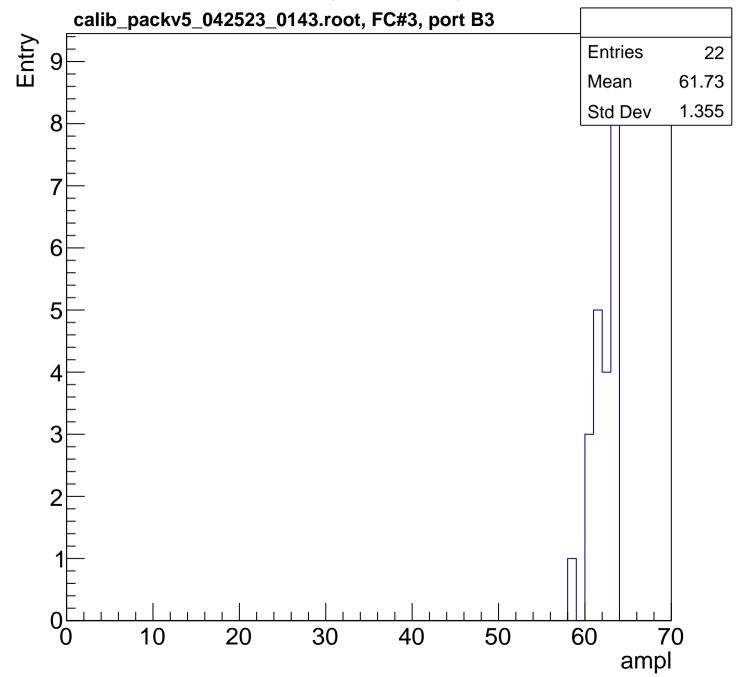




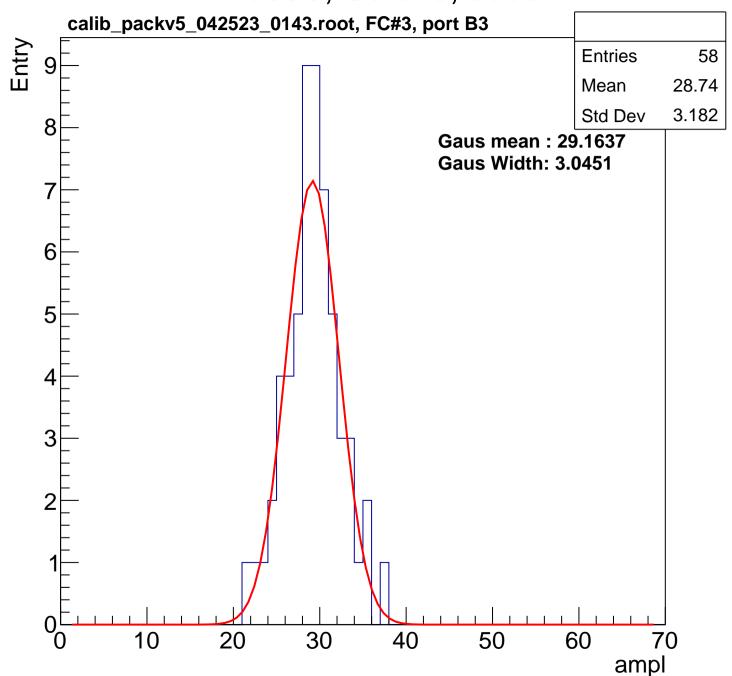


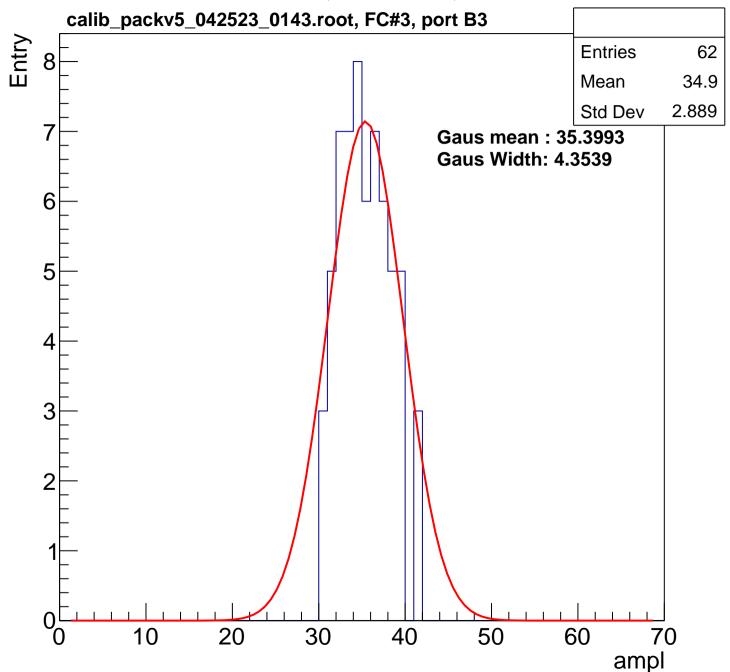


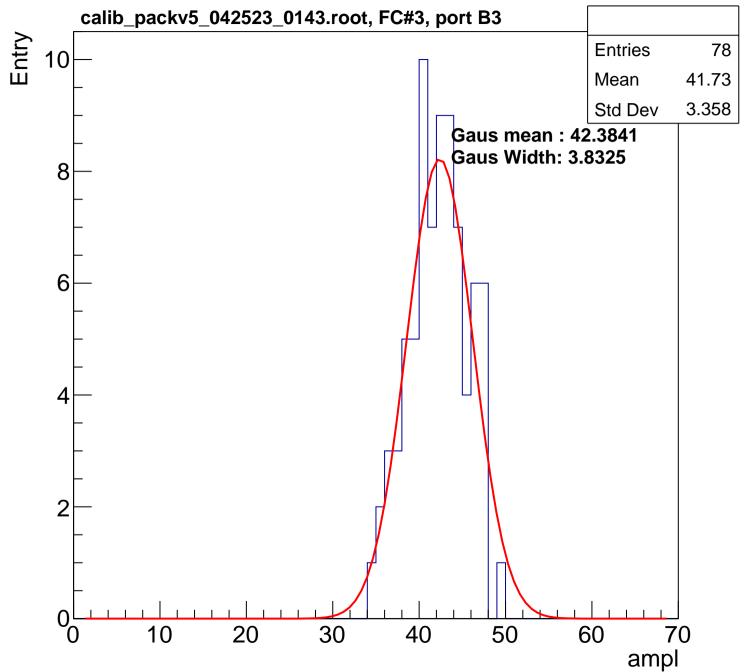


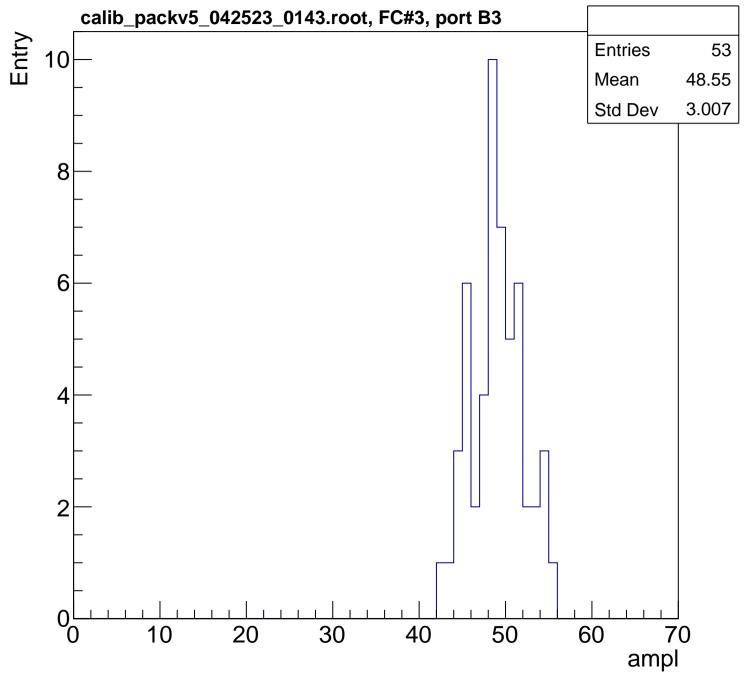


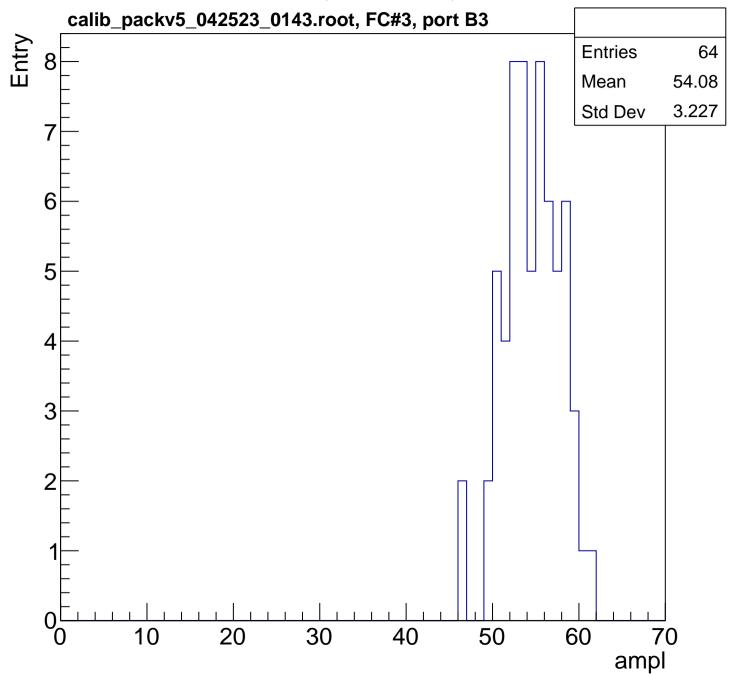
B1L000S, U9-ch4, adc7 calib\_packv5\_042523\_0143.root, FC#3, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

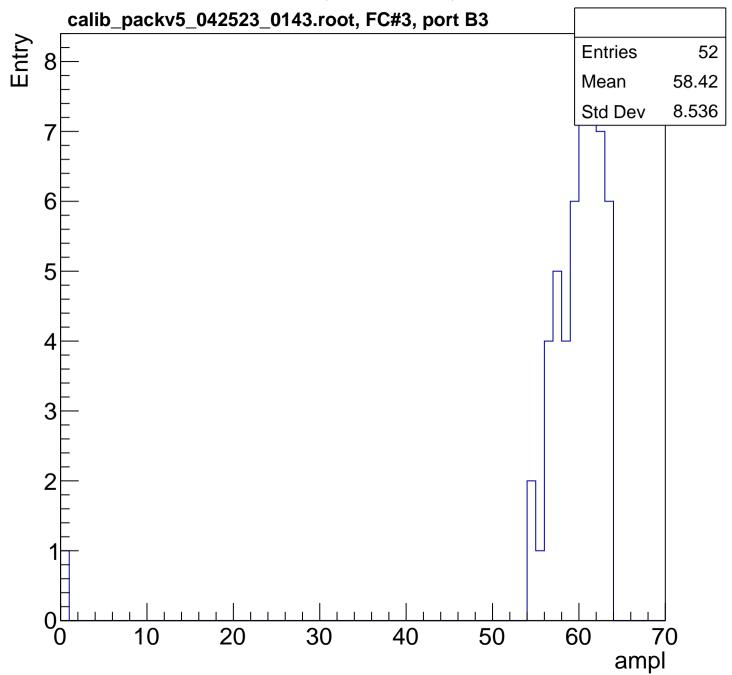


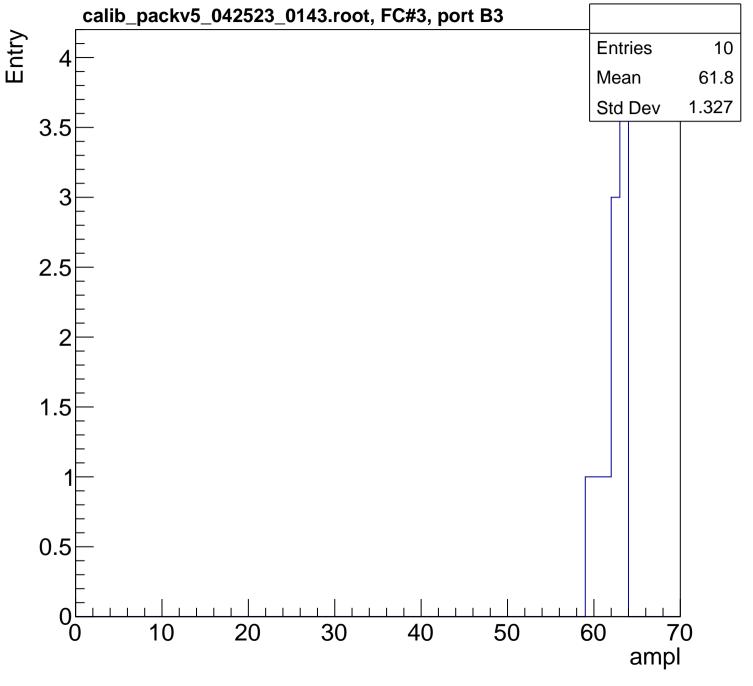




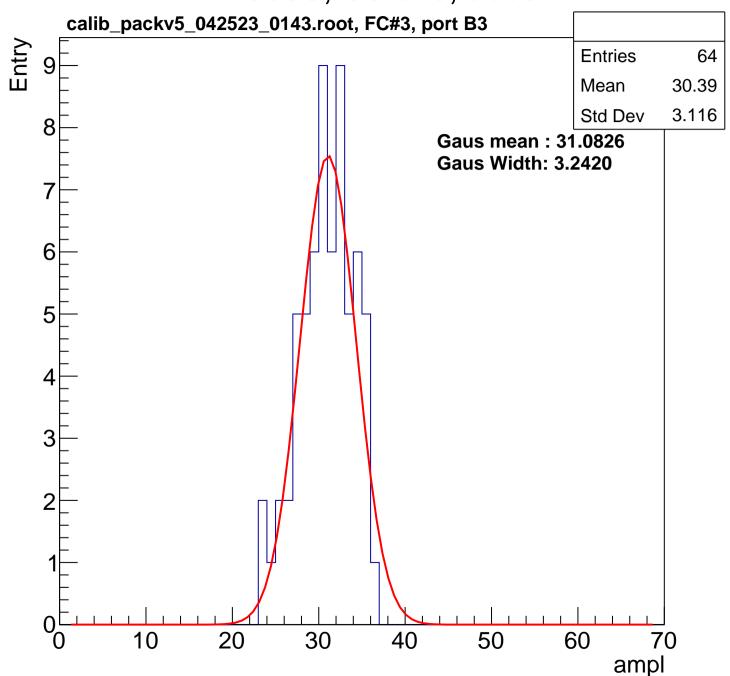


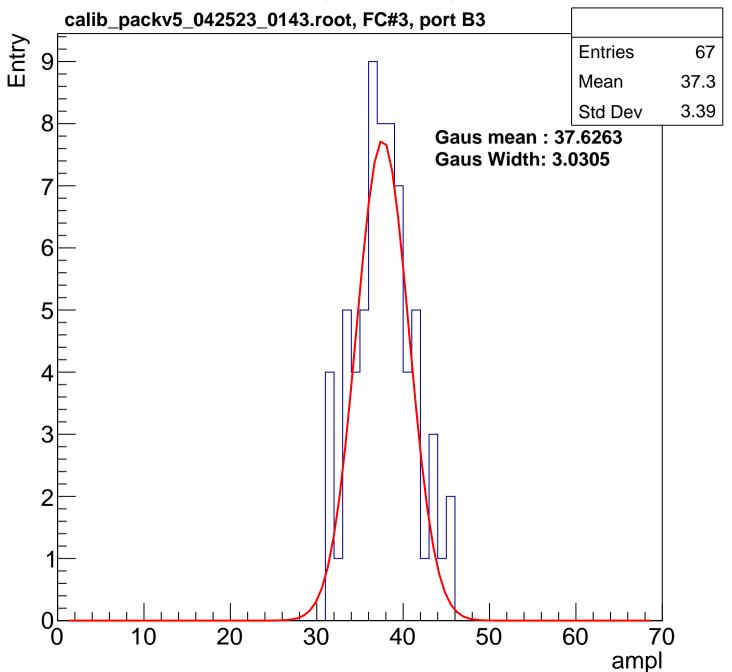


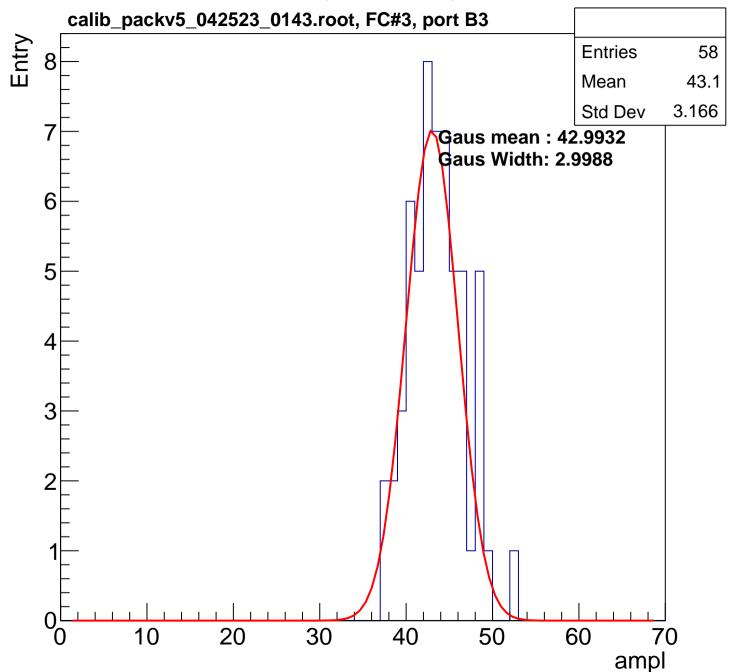


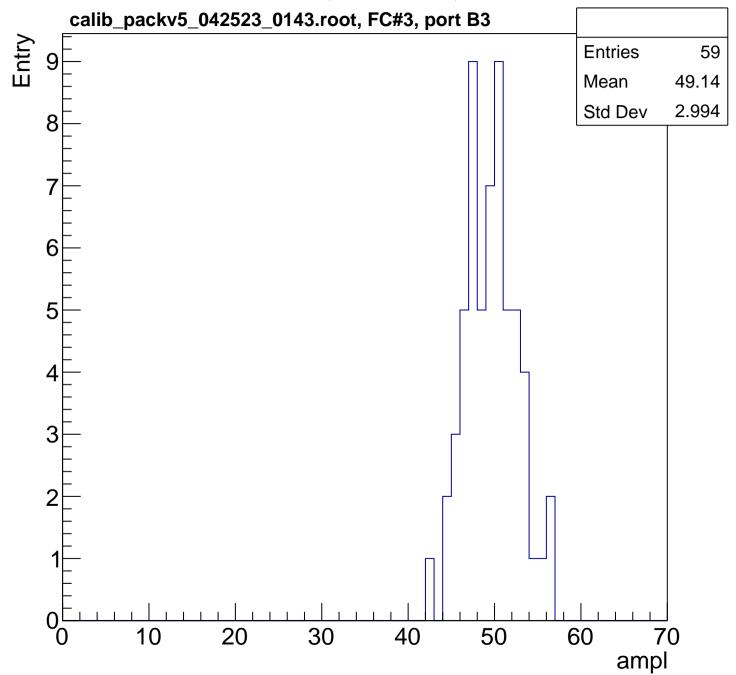


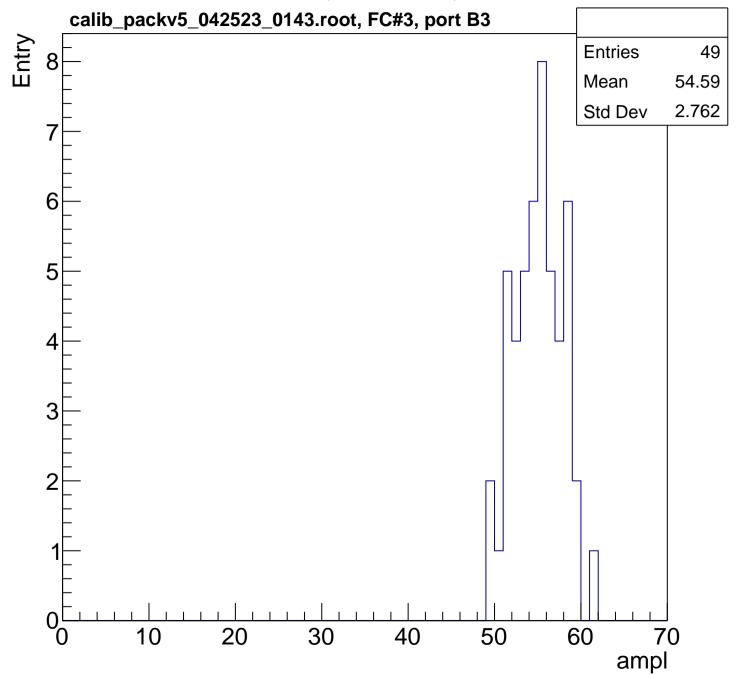
B1L000S, U9-ch5, adc7 calib\_packv5\_042523\_0143.root, FC#3, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

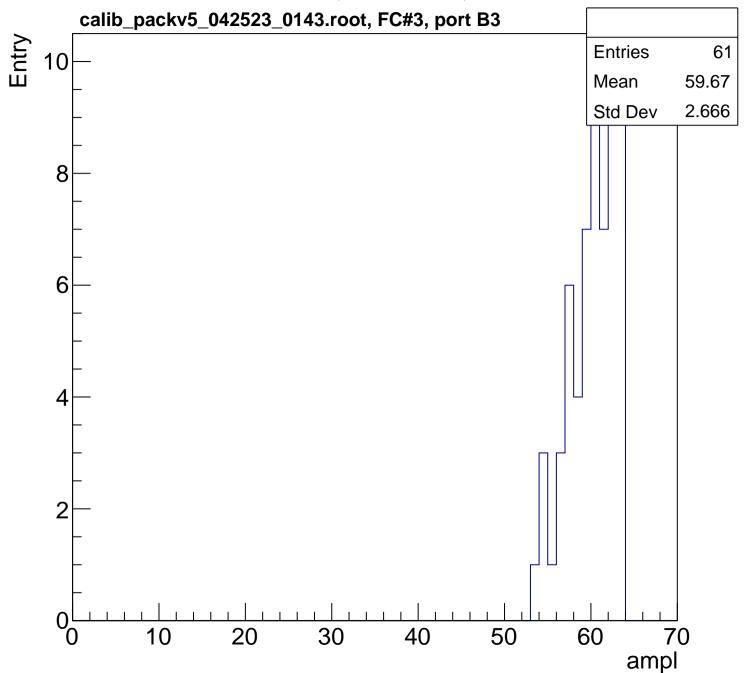


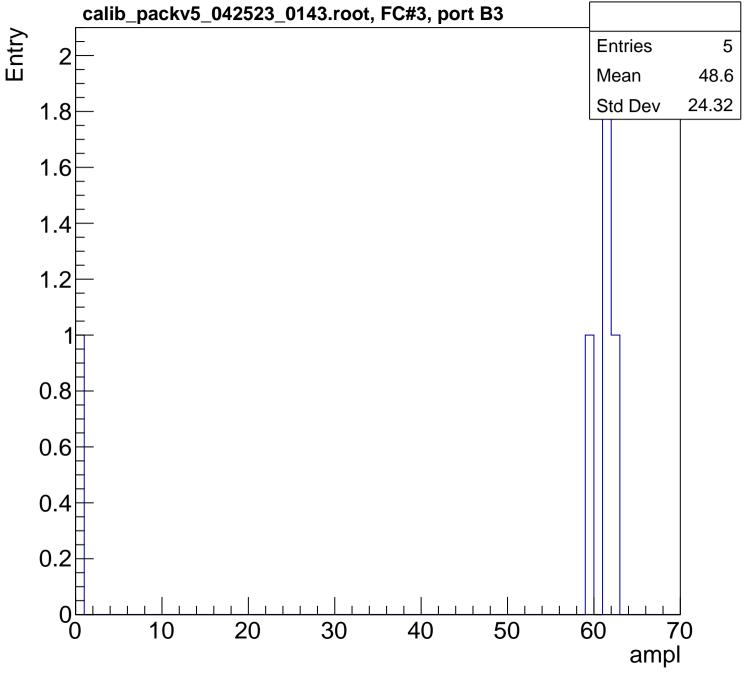


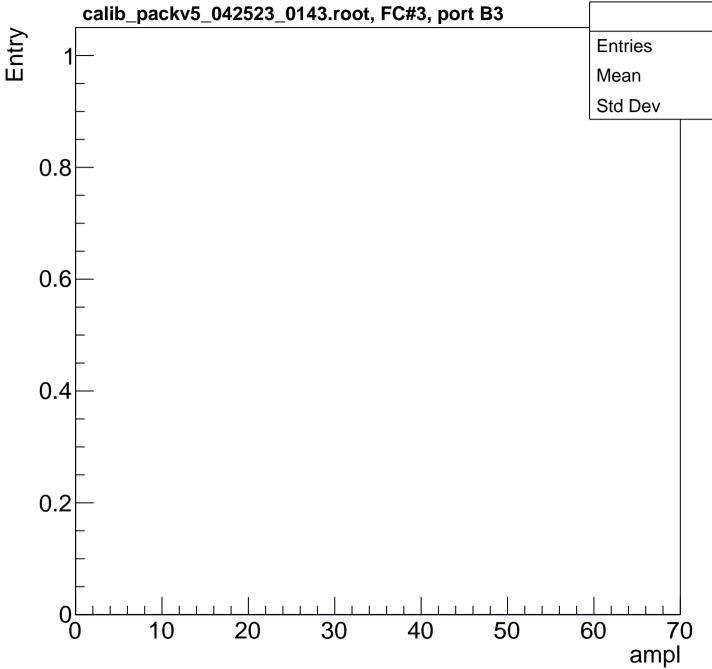


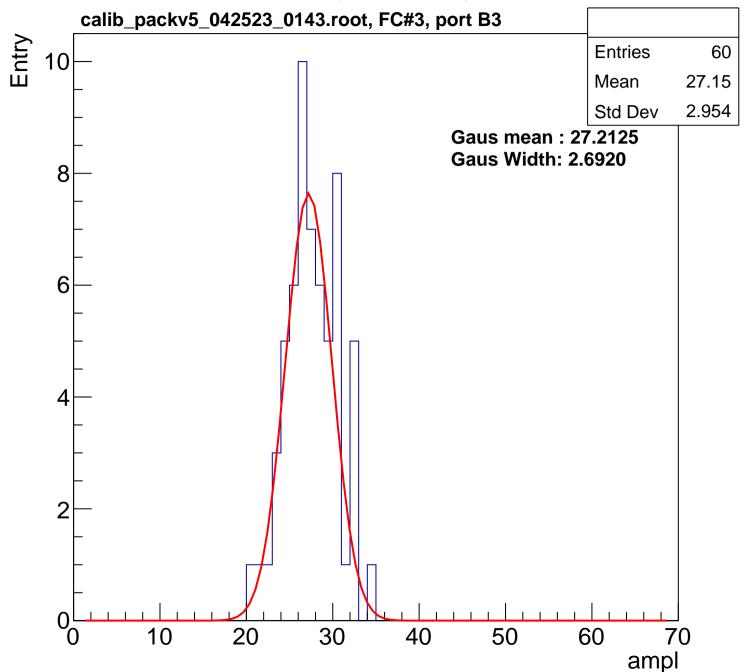


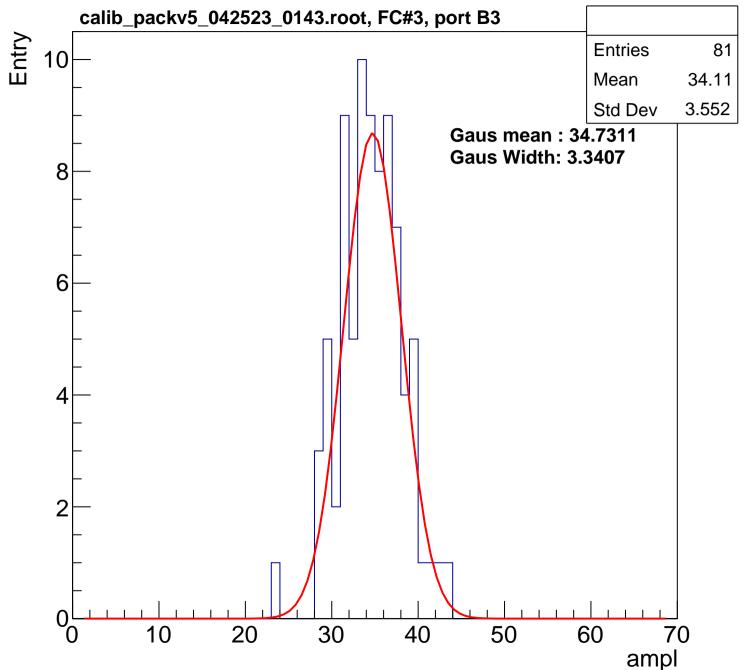


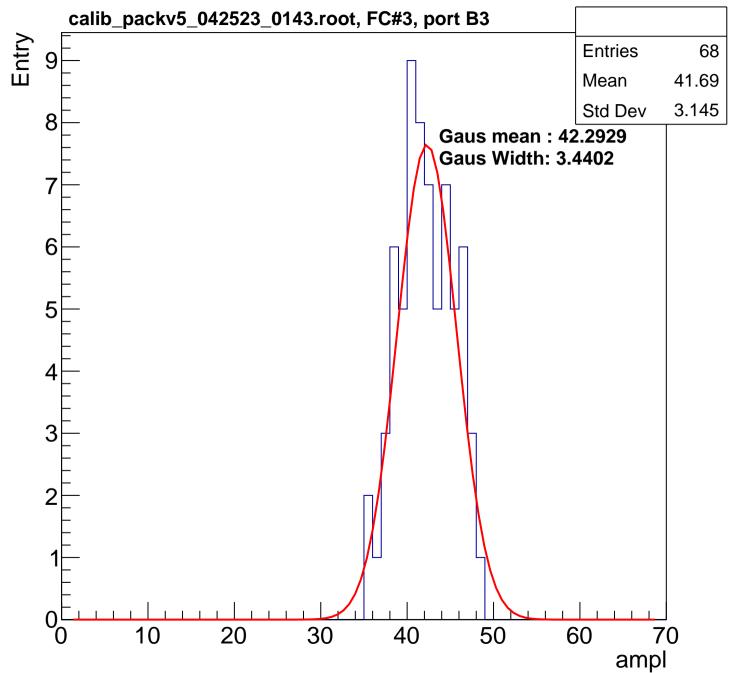


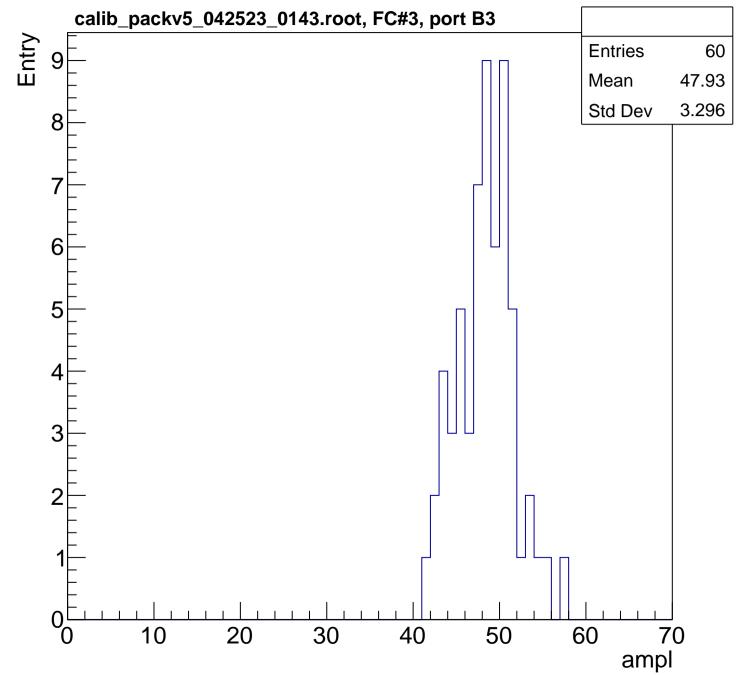


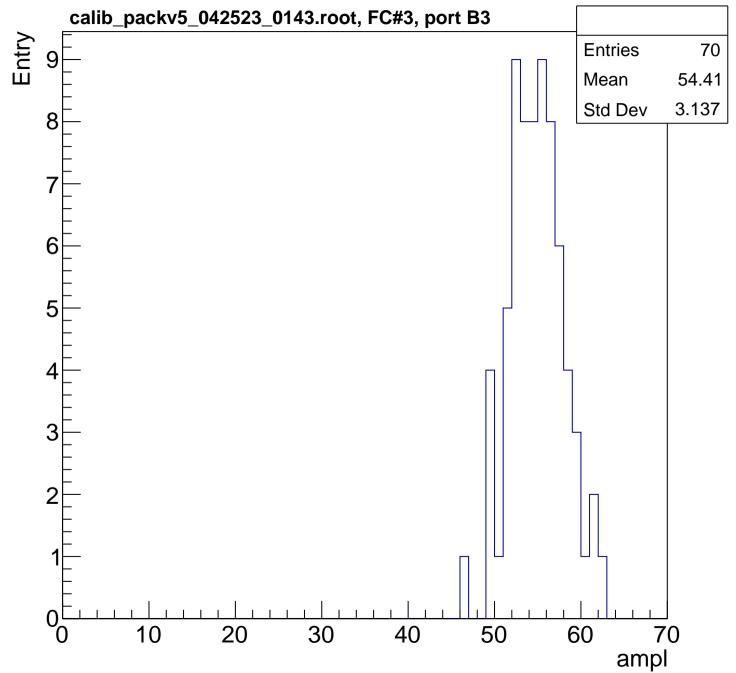


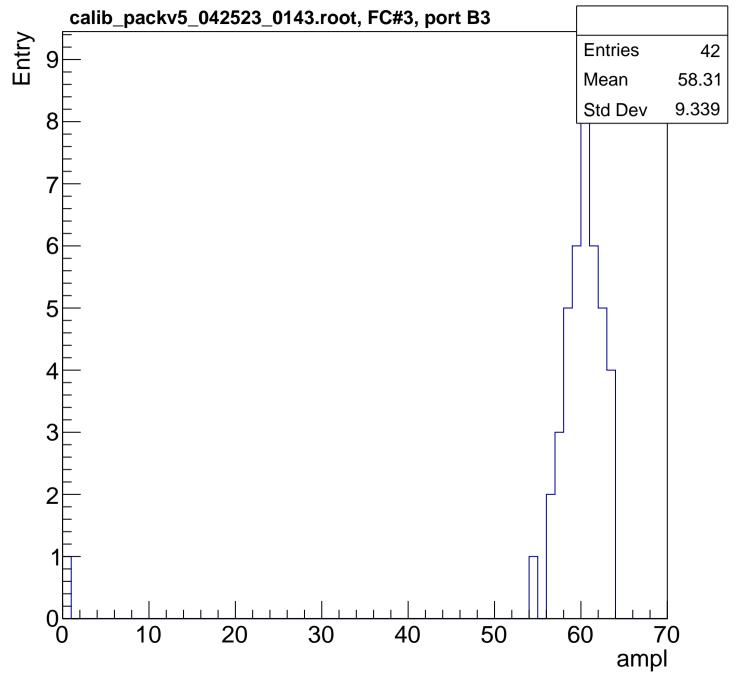


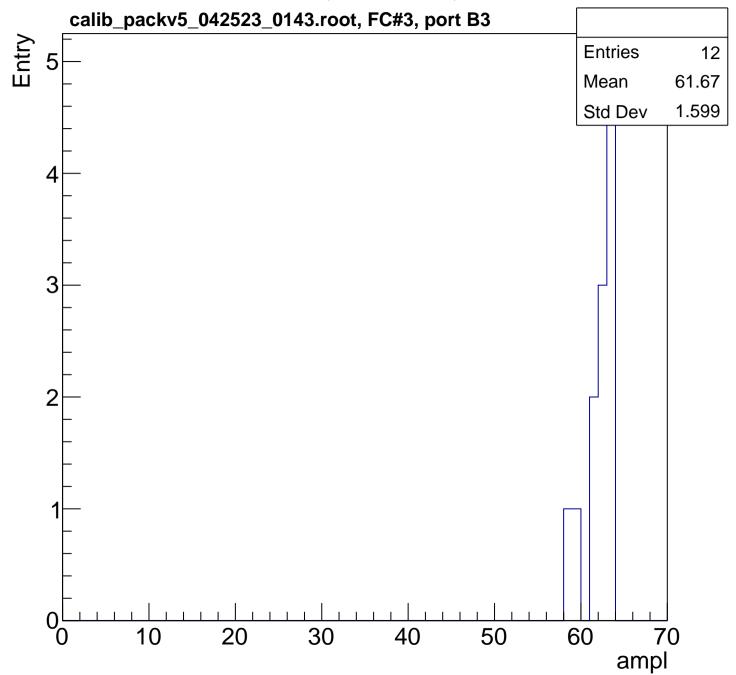


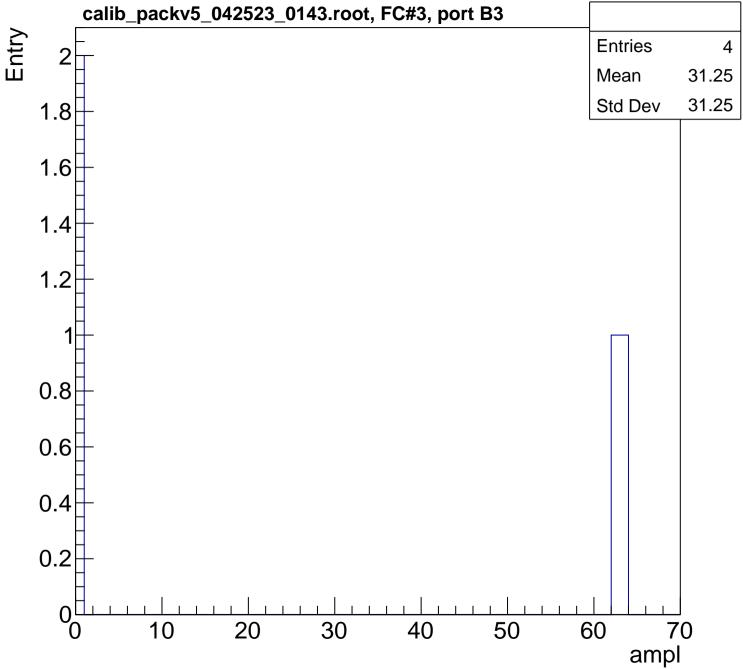


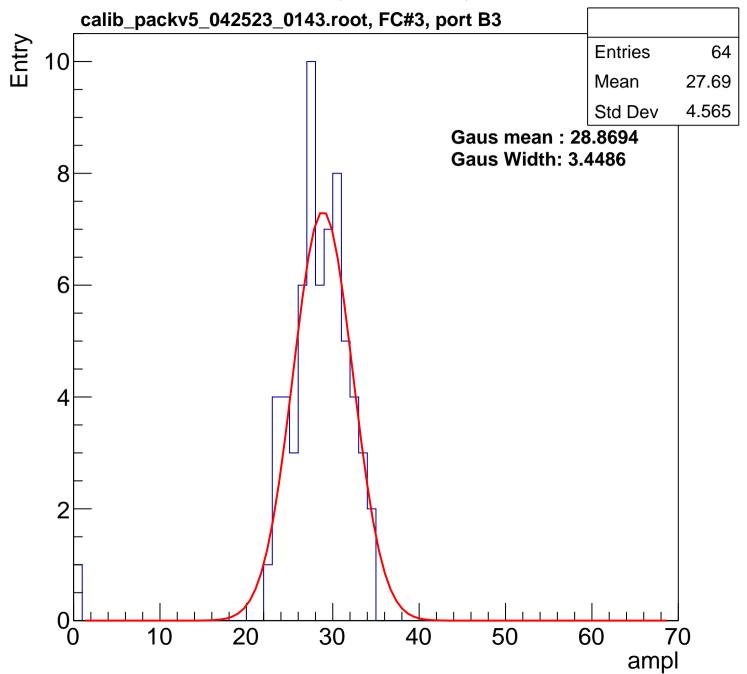


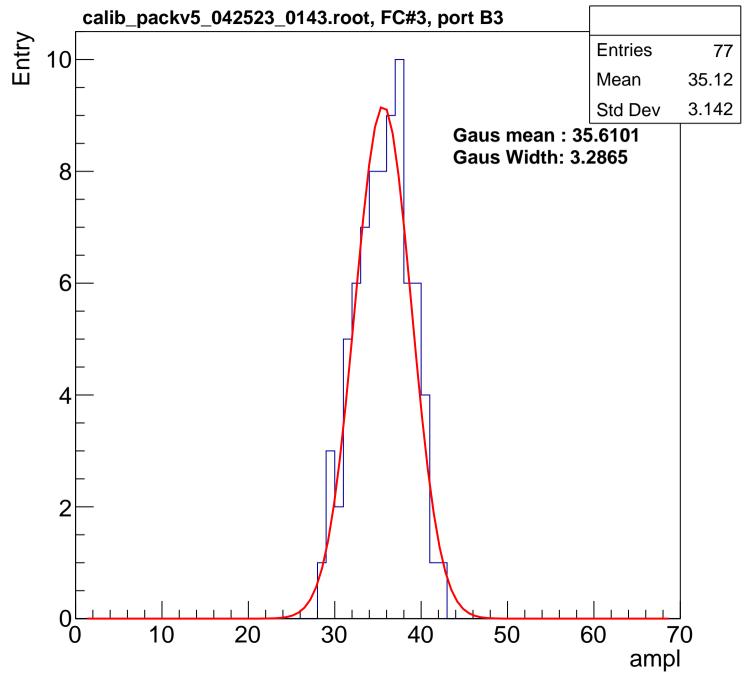


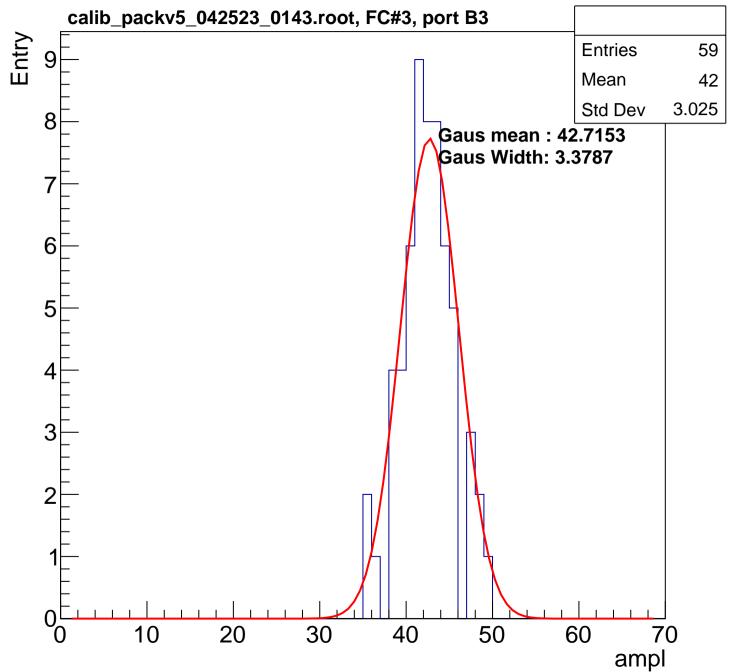


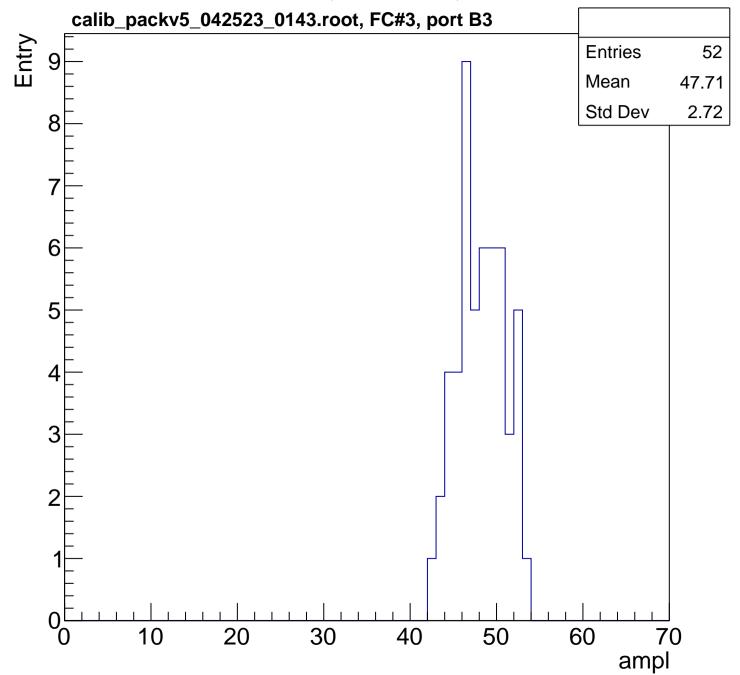


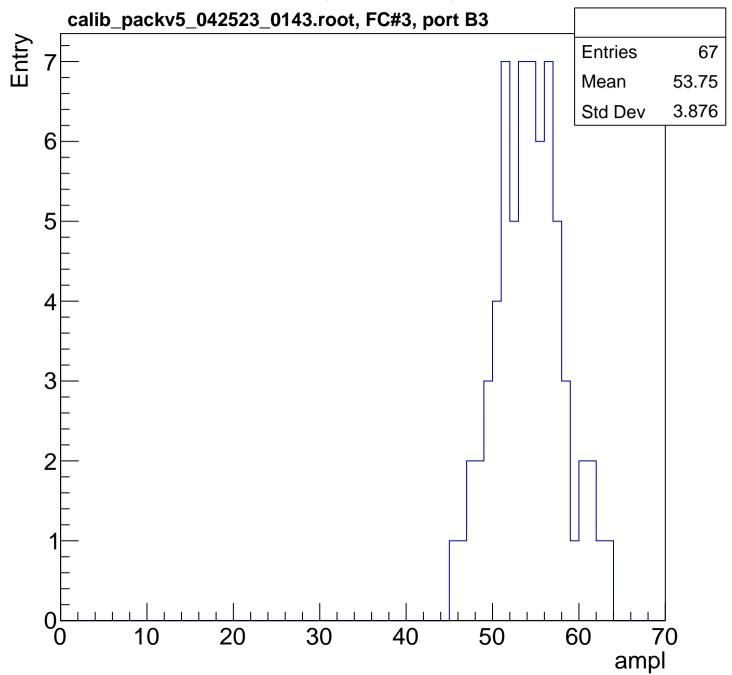


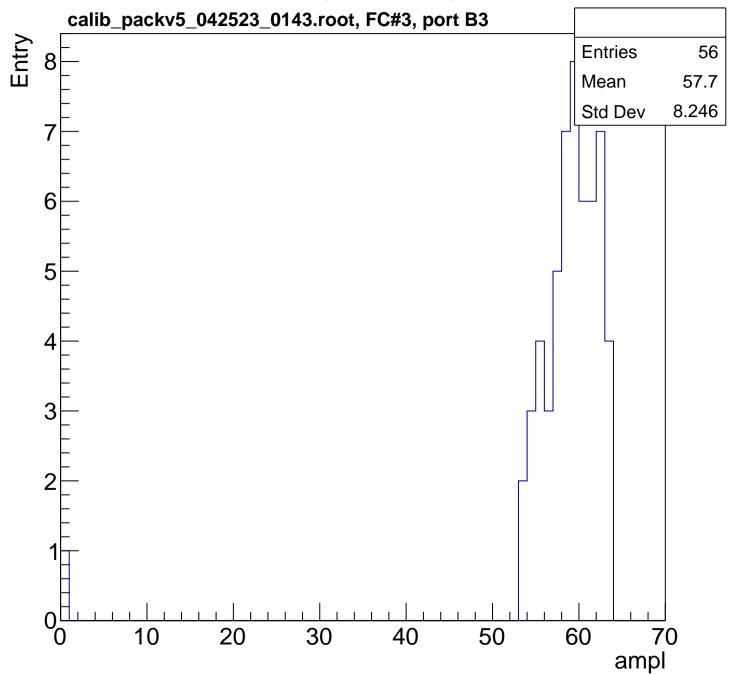


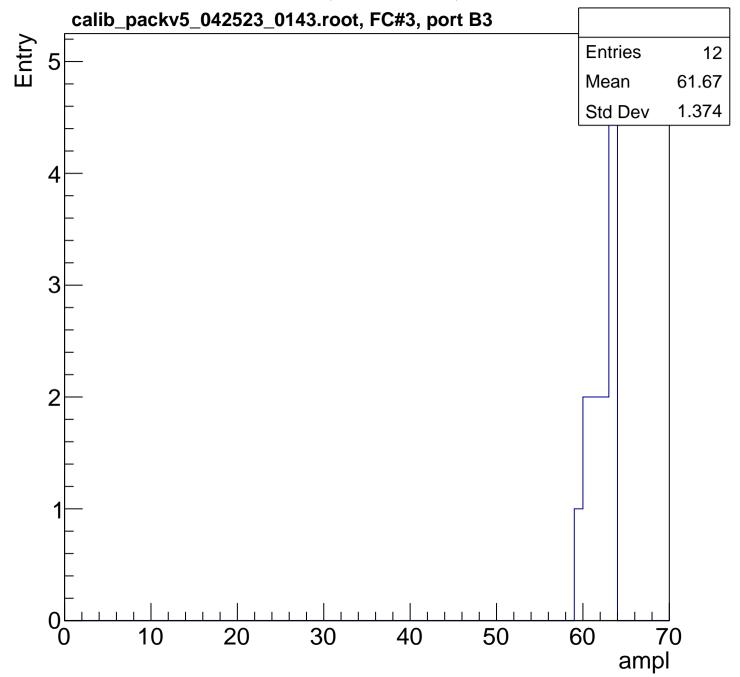












B1L000S, U9-ch8, adc7 calib\_packv5\_042523\_0143.root, FC#3, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

