

B1L003S, U25-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.83
Std Dev	11.2

Turn on : 27.6860

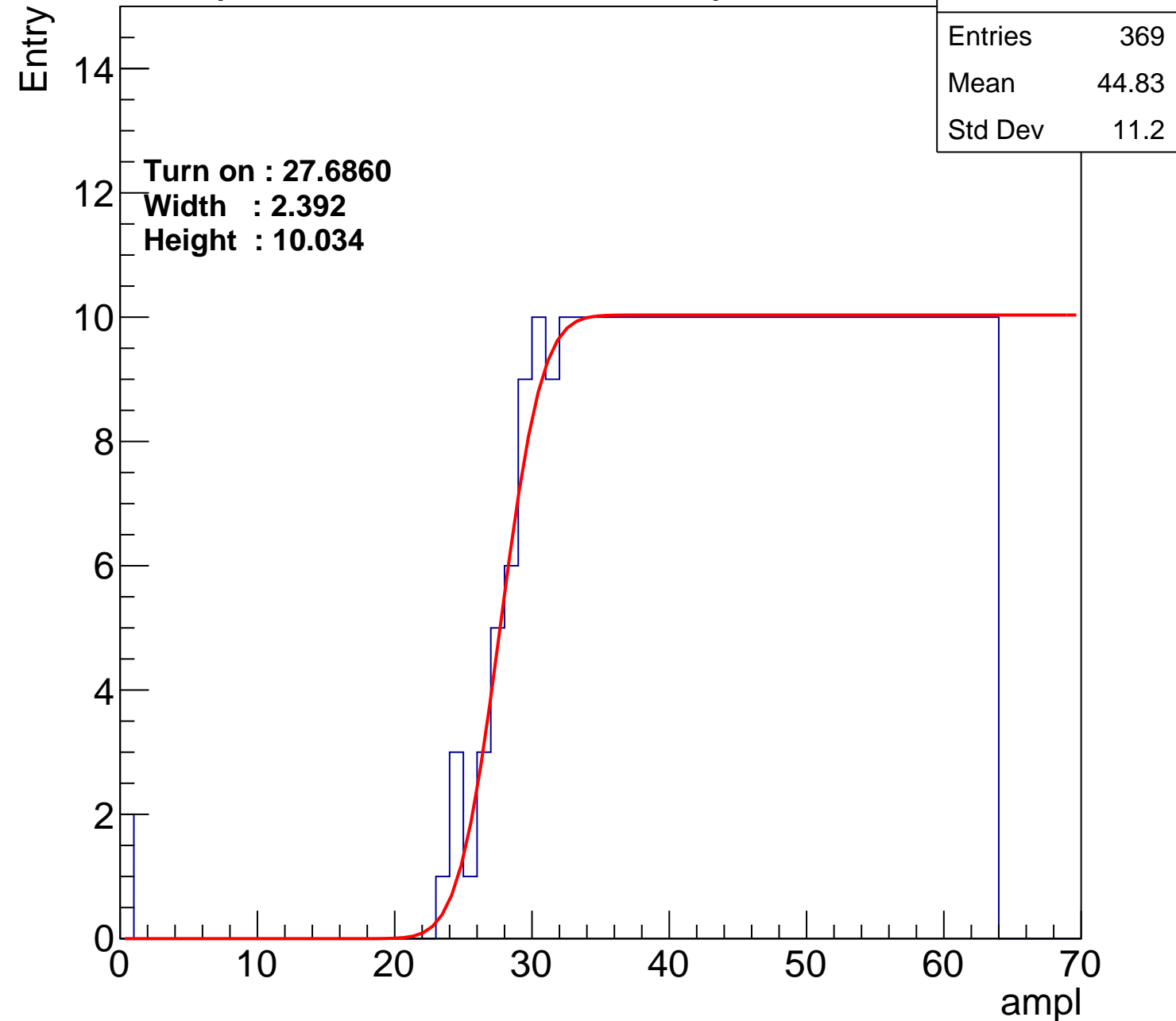
Width : 2.392

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.98
Std Dev	11.16

Turn on : 28.2332

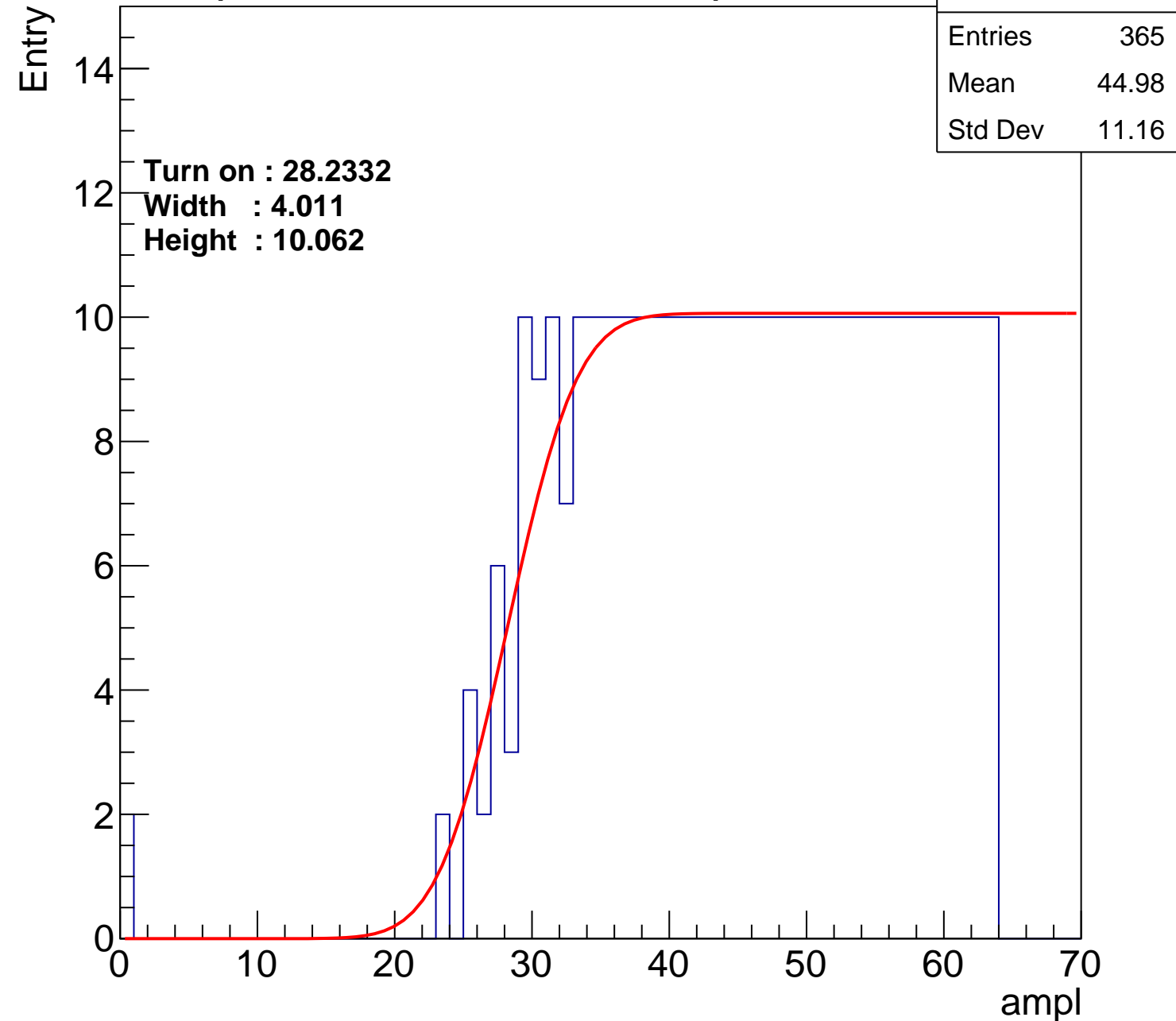
Width : 4.011

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.08
Std Dev	12.02

Turn on : 26.3236

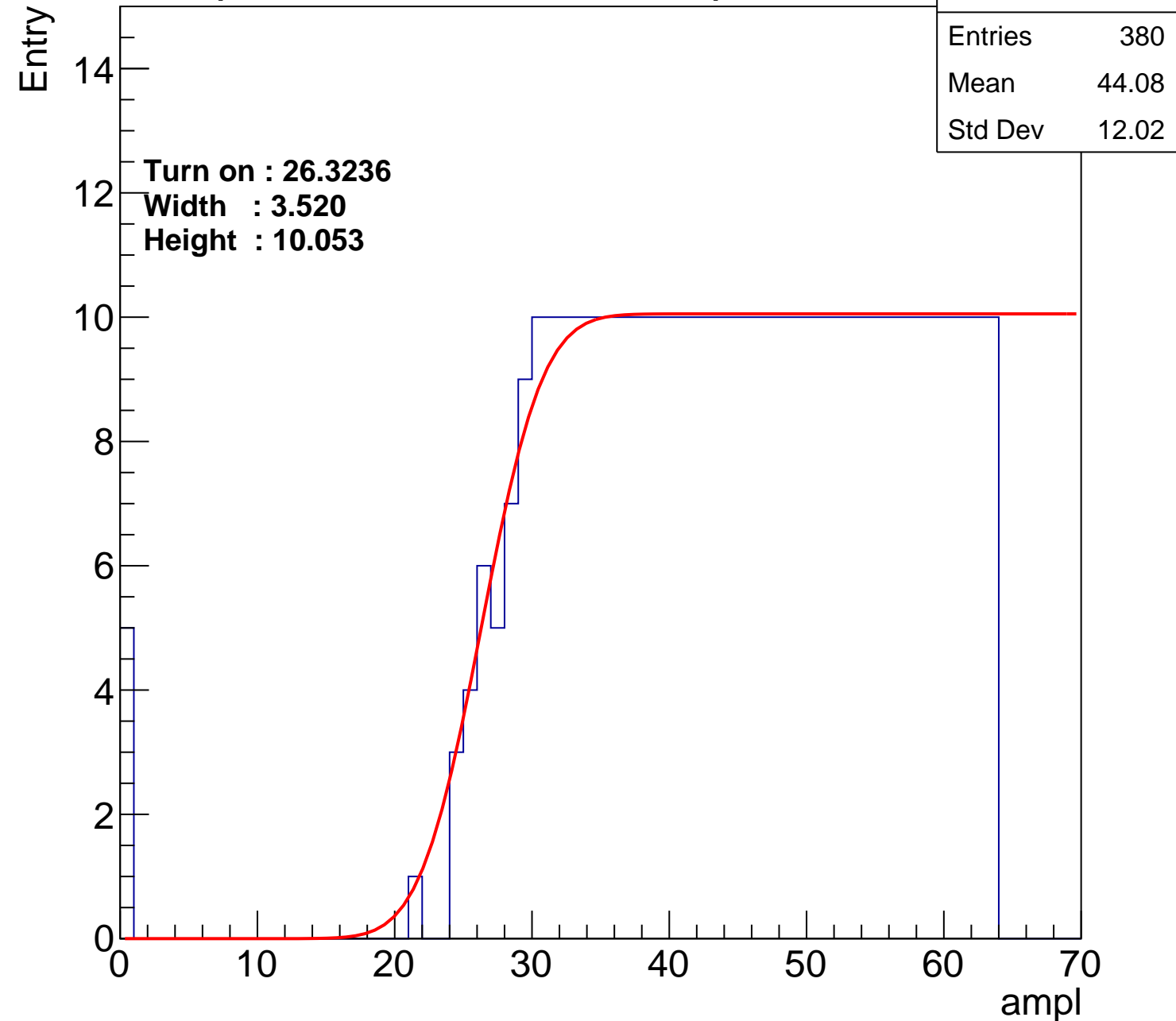
Width : 3.520

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.9685

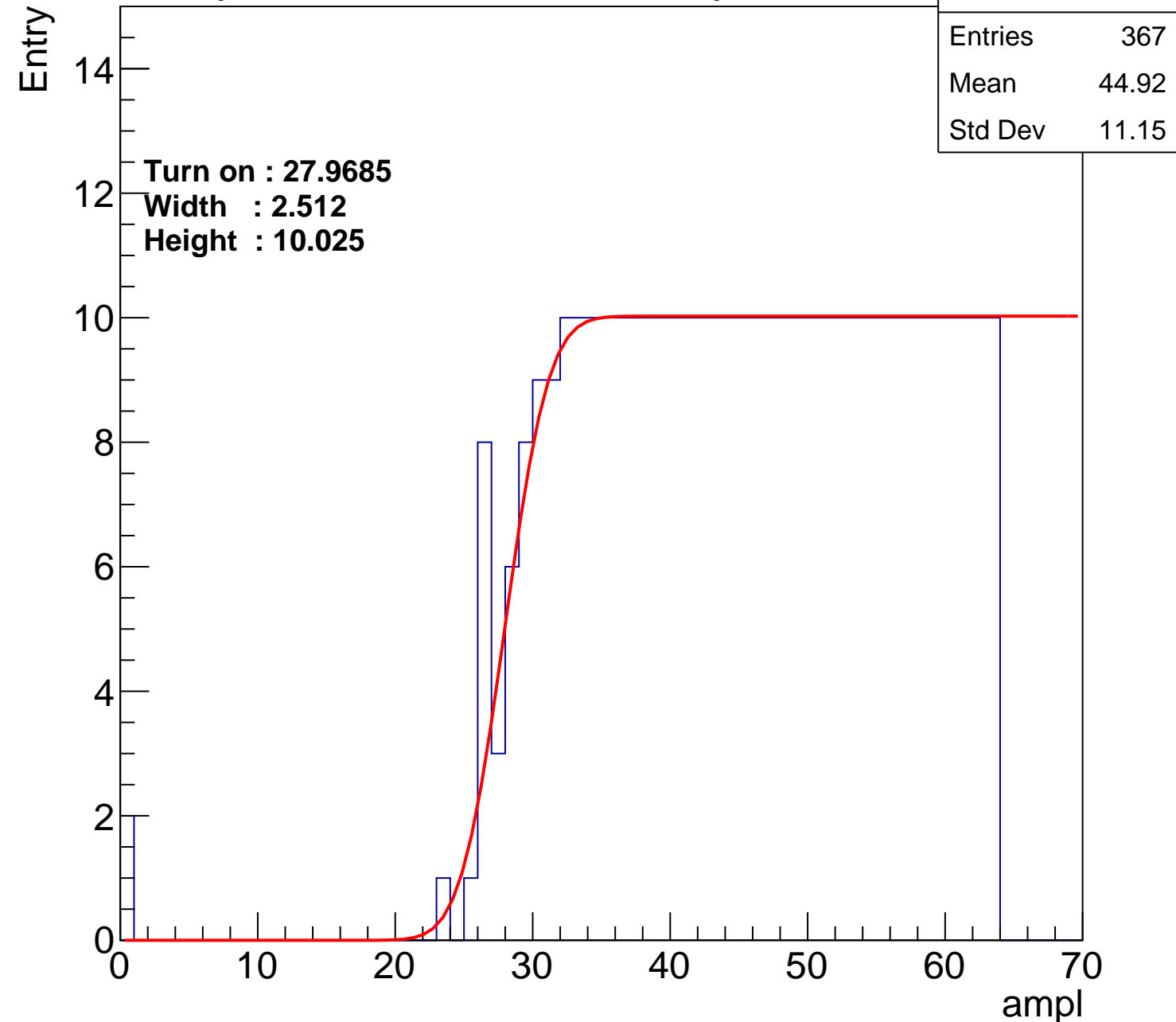
Width : 2.512

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch4

calib_packv5_042523_0143.root, FC#13, port D2

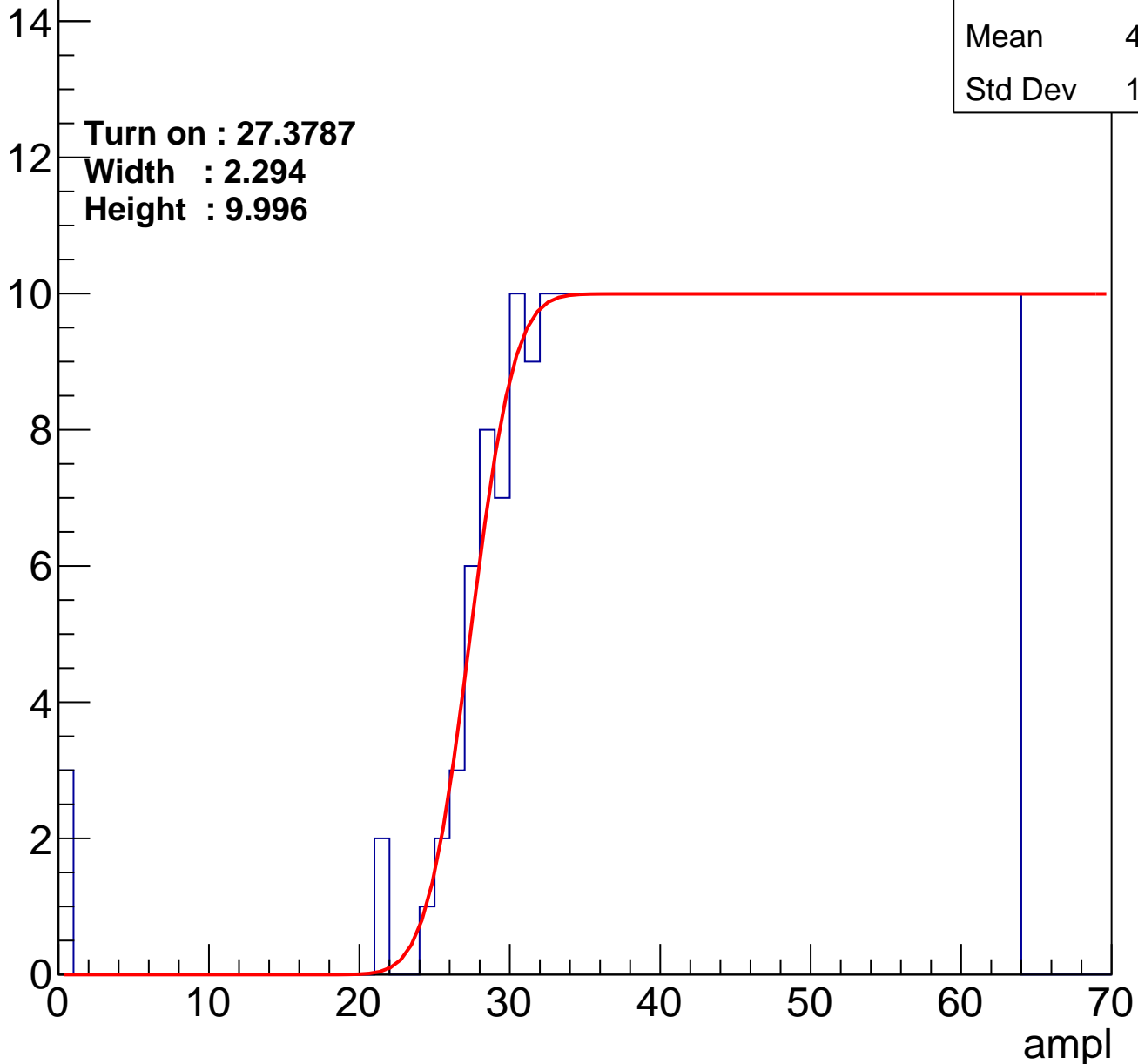
Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.3787

Width : 2.294

Height : 9.996

Entry



B1L003S, U25-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
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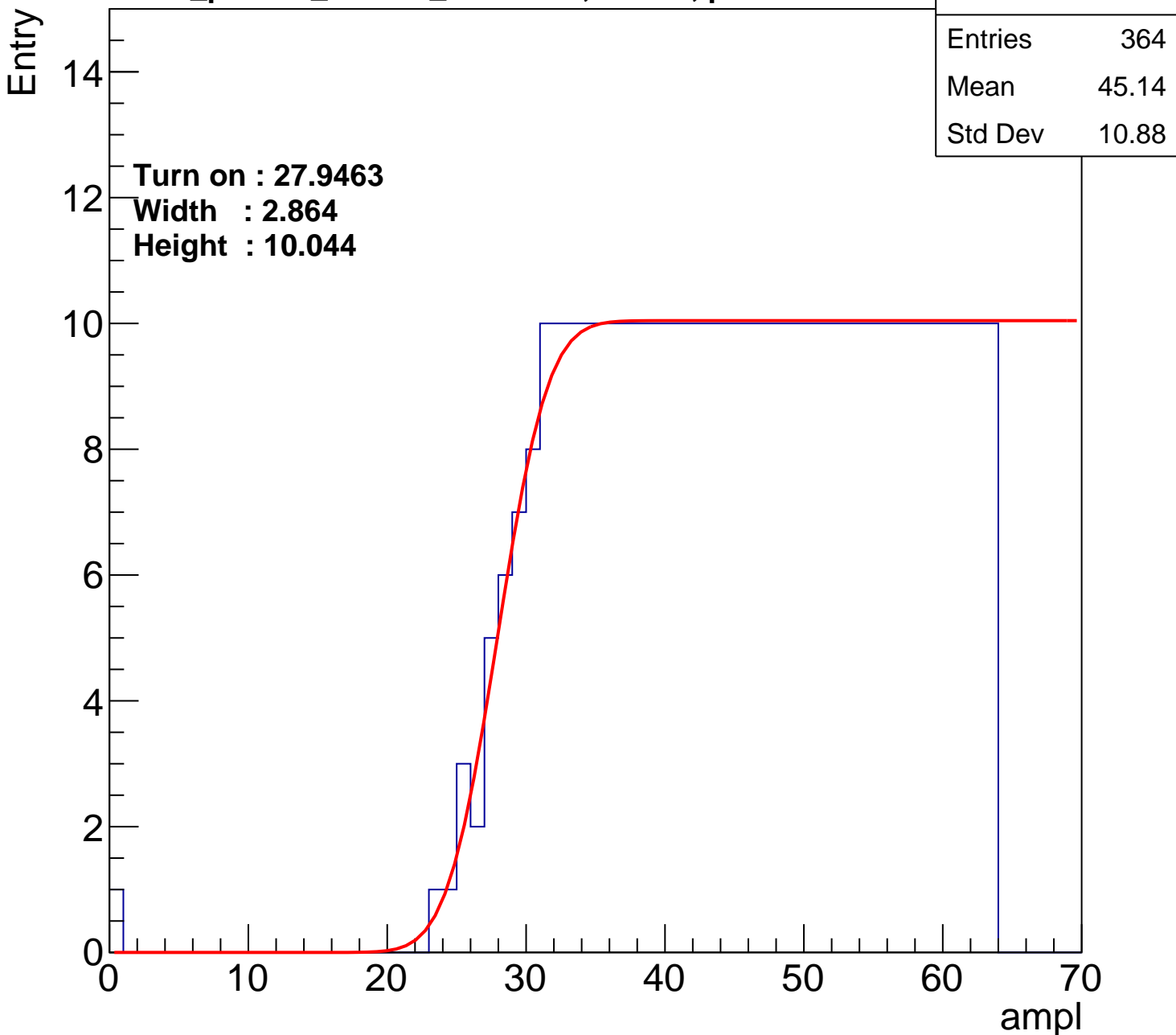
Mean	45.14
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Std Dev	10.88
---------	-------

Turn on : 27.9463

Width : 2.864

Height : 10.044



B1L003S, U25-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.24
Std Dev	10.86

Turn on : 28.4186

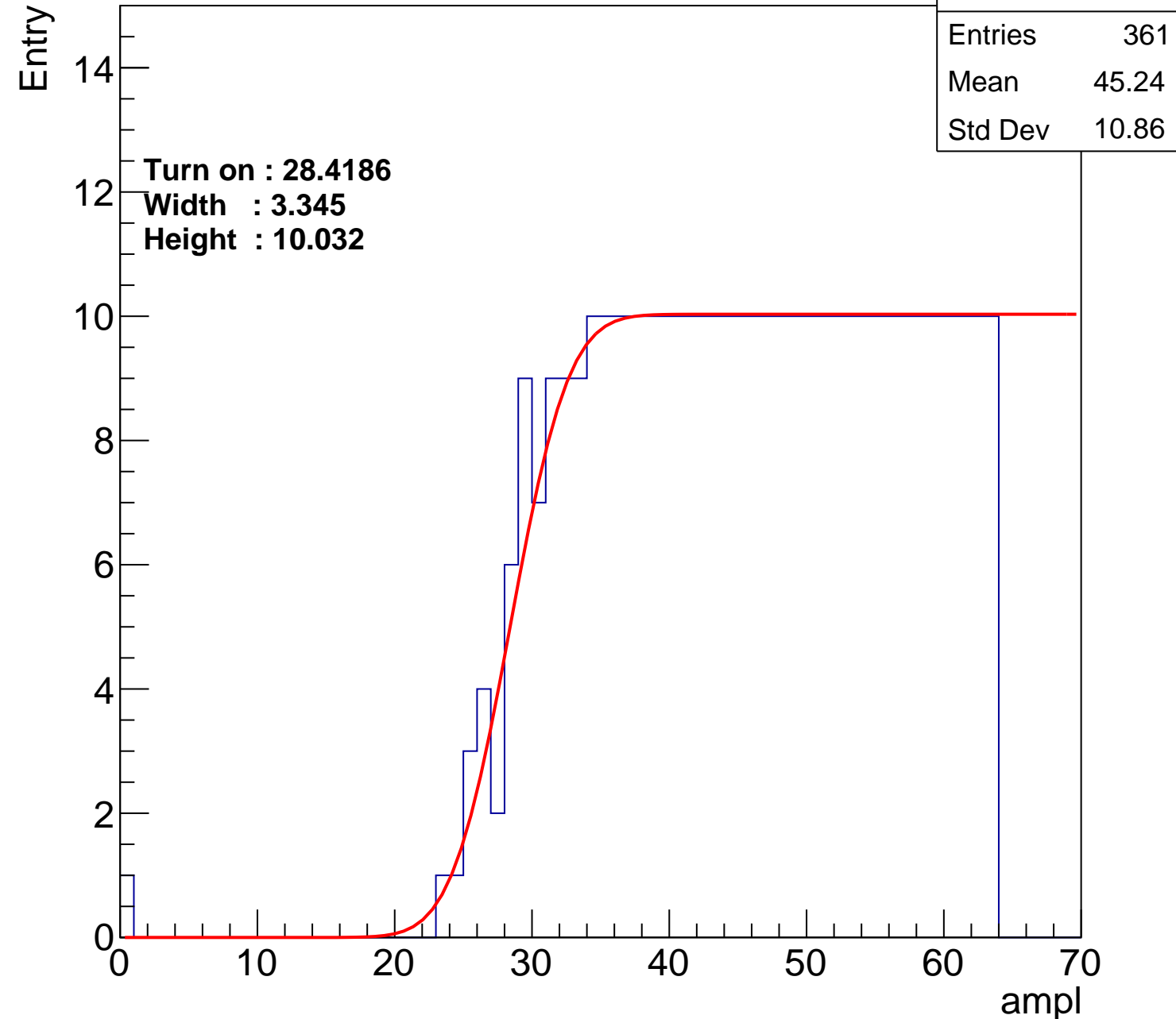
Width : 3.345

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.76
Std Dev	11.11

Turn on : 27.1822

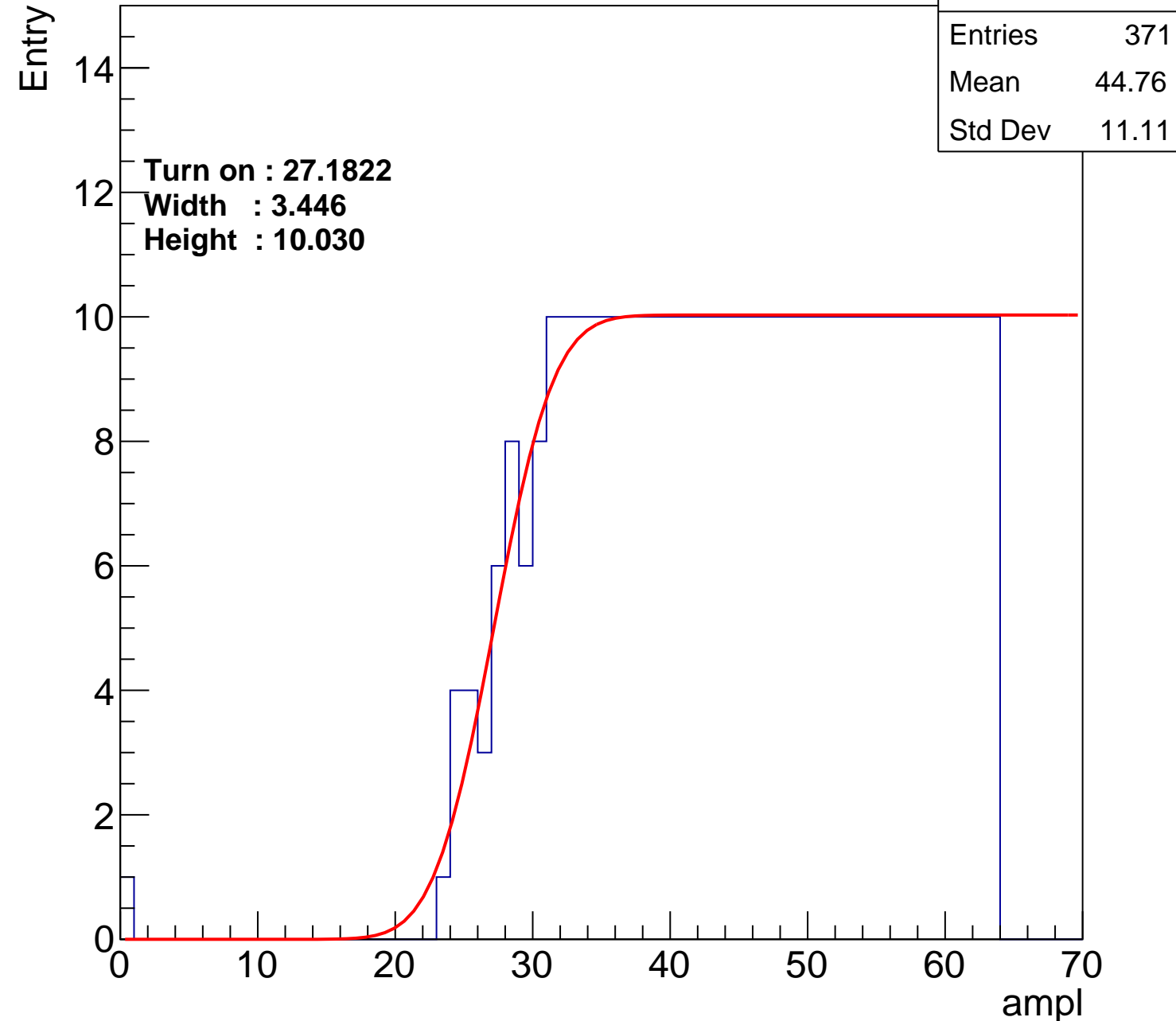
Width : 3.446

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.67
Std Dev	11.16

Turn on : 27.0732

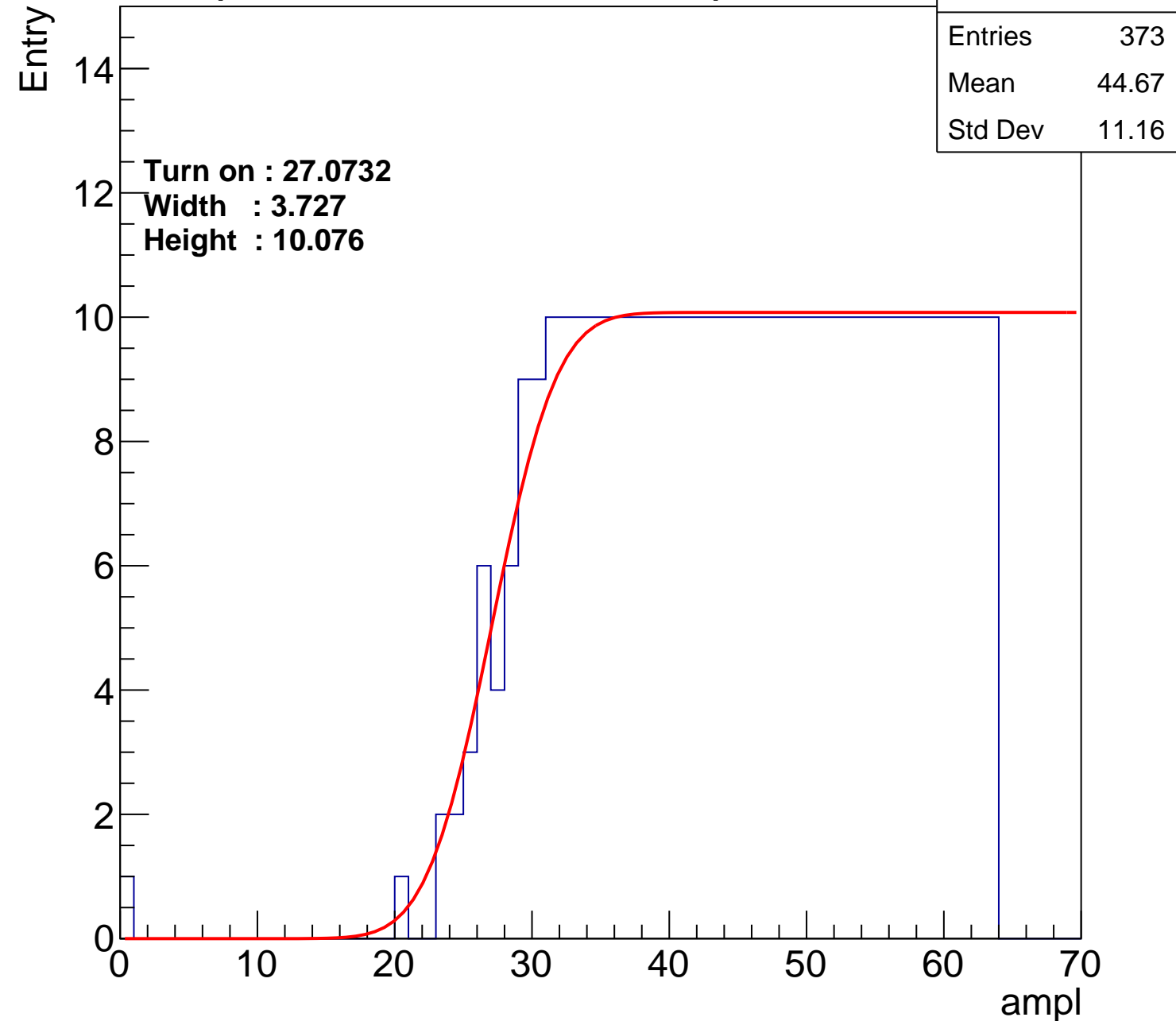
Width : 3.727

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.81
Std Dev	10.53

Turn on : 29.1016

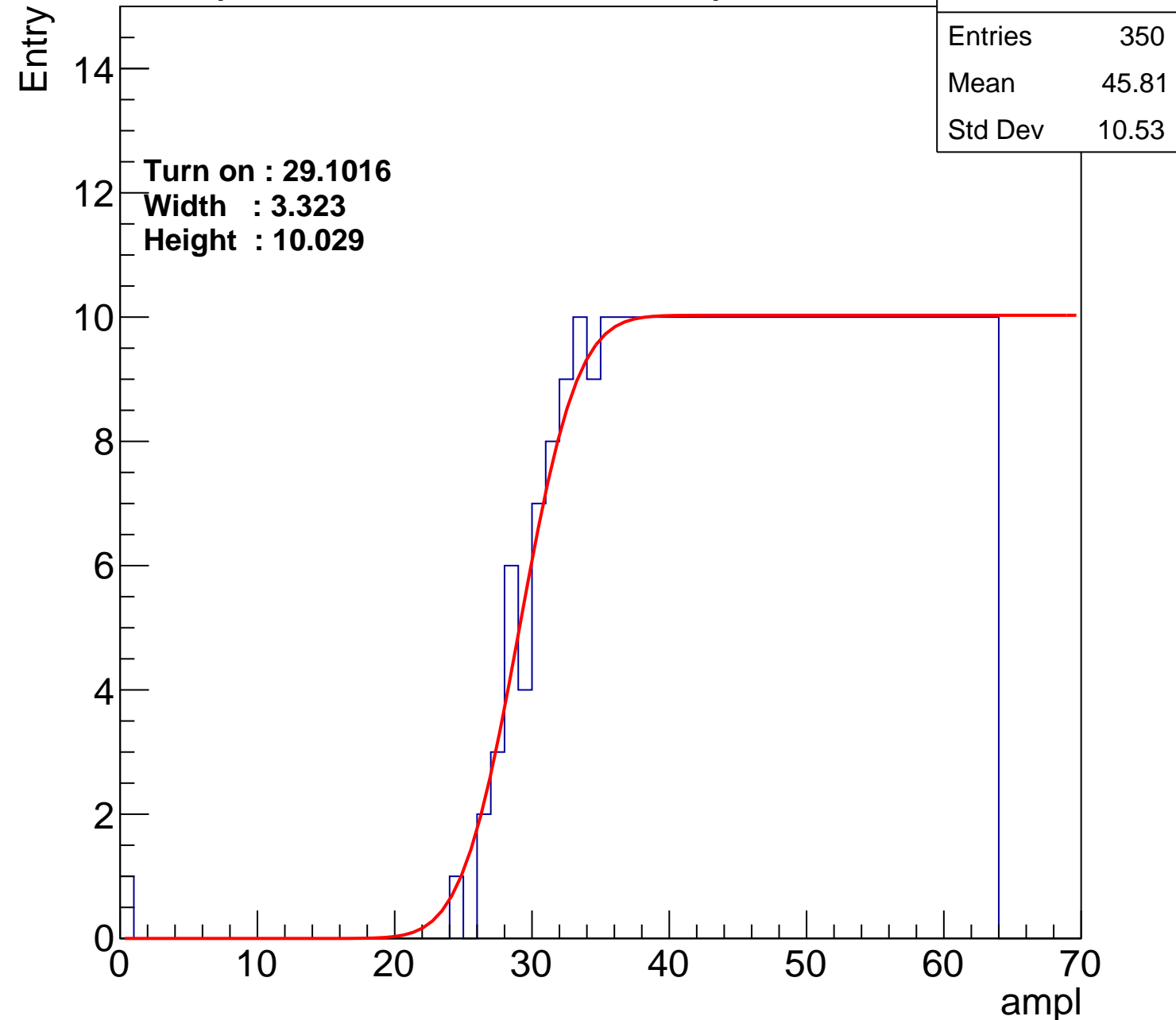
Width : 3.323

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.53
Std Dev	11.68

Turn on : 27.5815

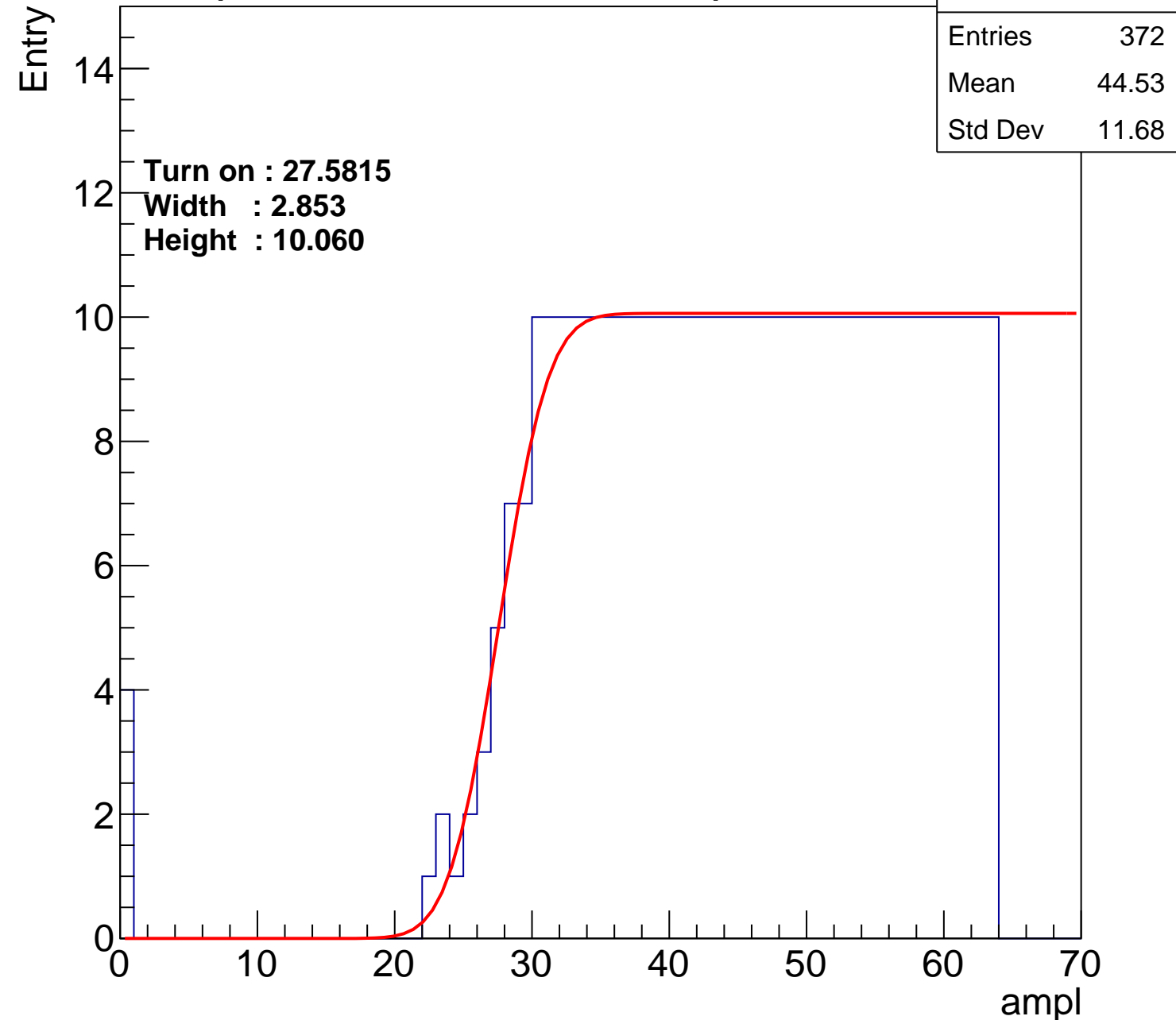
Width : 2.853

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	396
Mean	43.37
Std Dev	12.21

Turn on : 24.8918

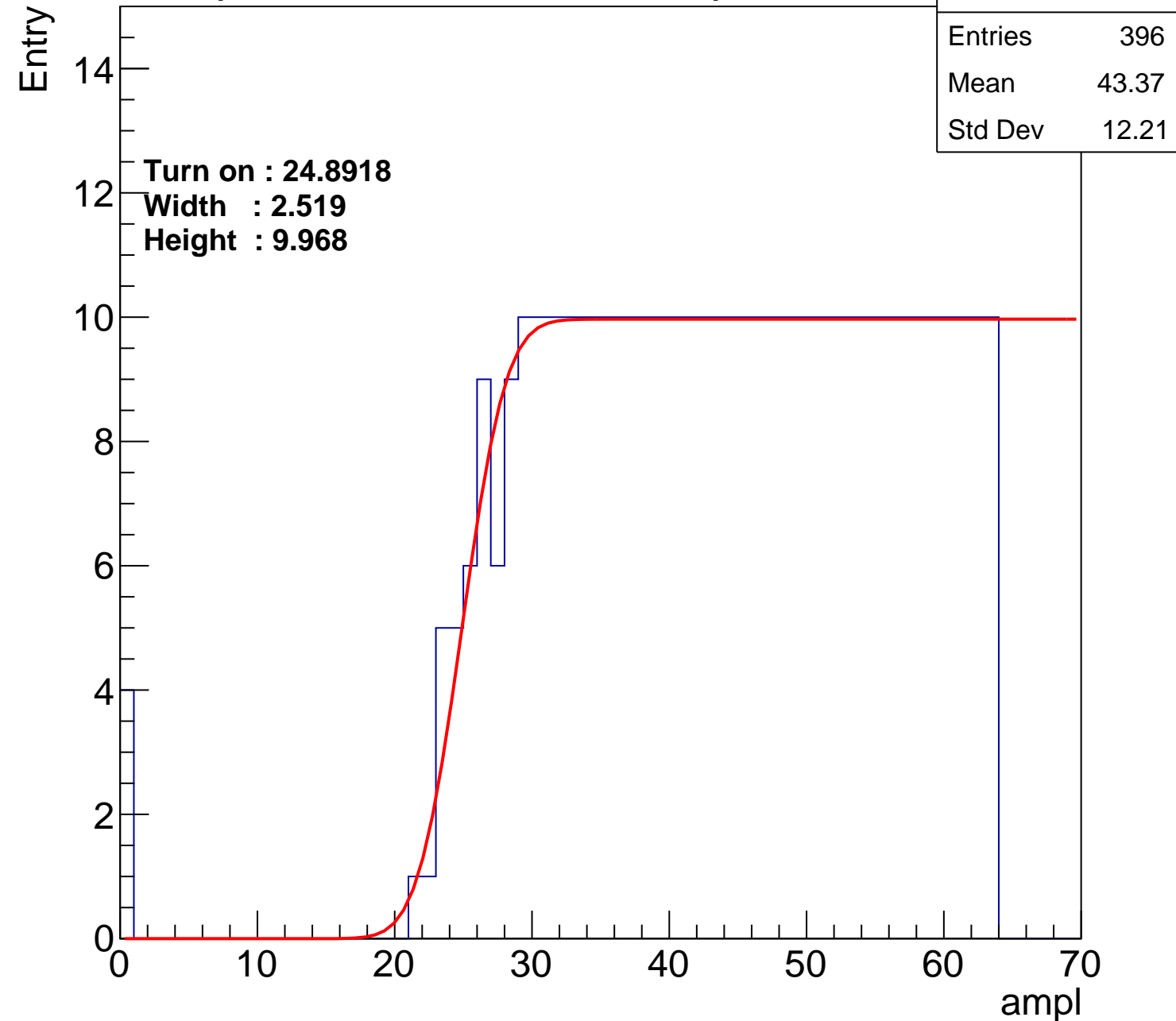
Width : 2.519

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch12

calib_packv5_042523_0143.root, FC#13, port D2

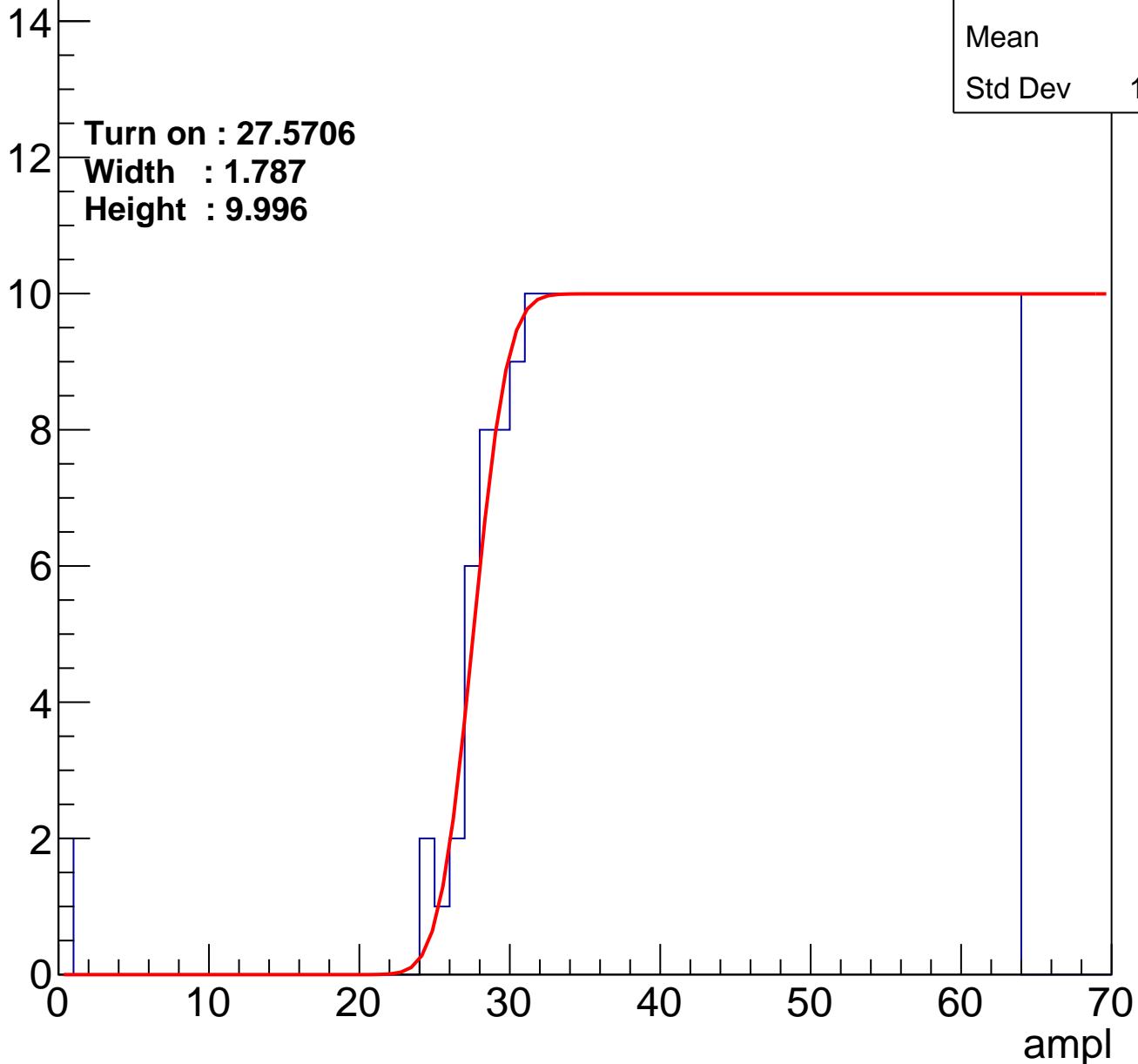
Entries	368
Mean	44.9
Std Dev	11.13

Turn on : 27.5706

Width : 1.787

Height : 9.996

Entry



B1L003S, U25-ch13

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.03
Std Dev	11.13

Turn on : 29.0373

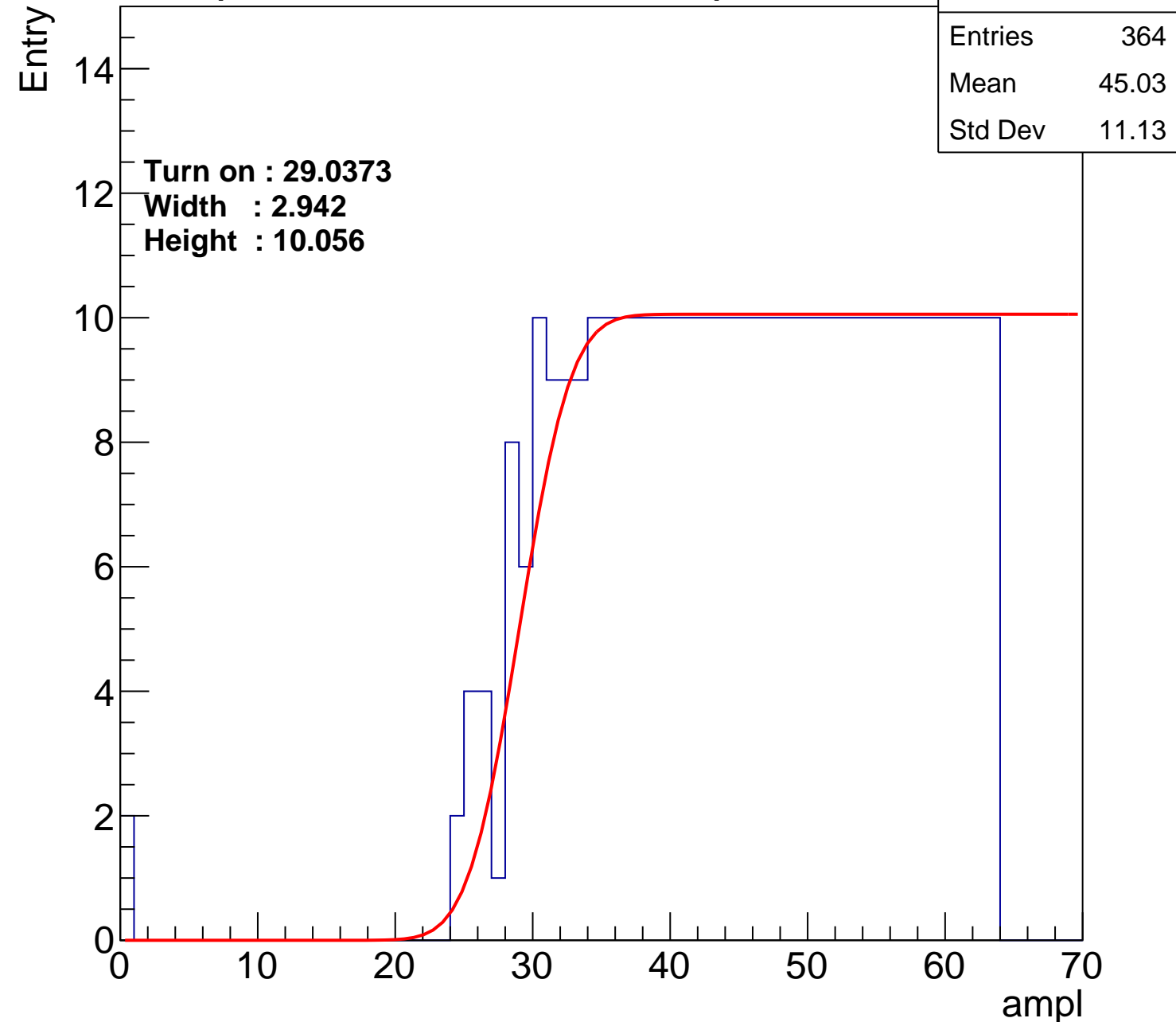
Width : 2.942

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.1741

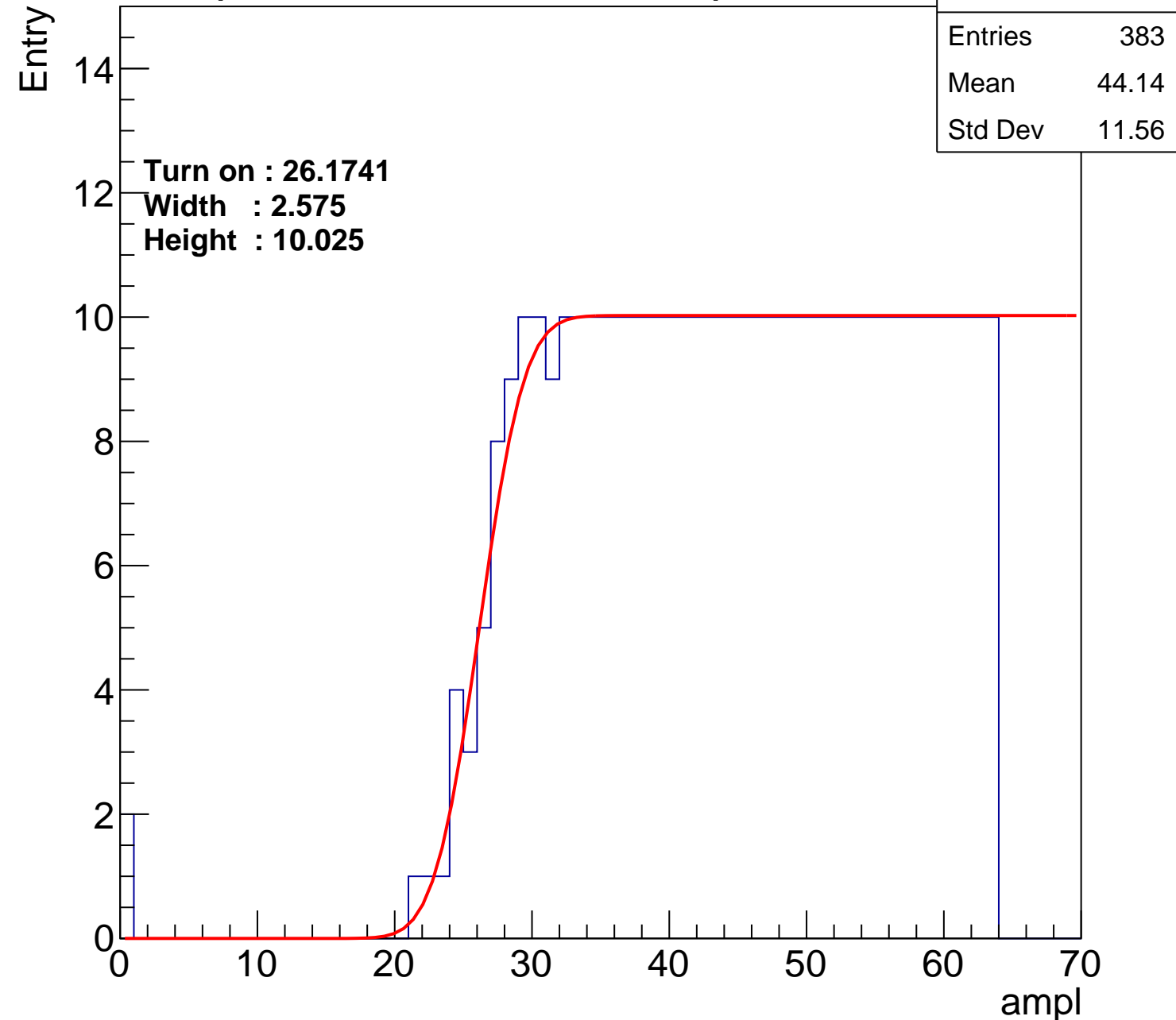
Width : 2.575

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.49
Std Dev	10.67

Turn on : 28.4938

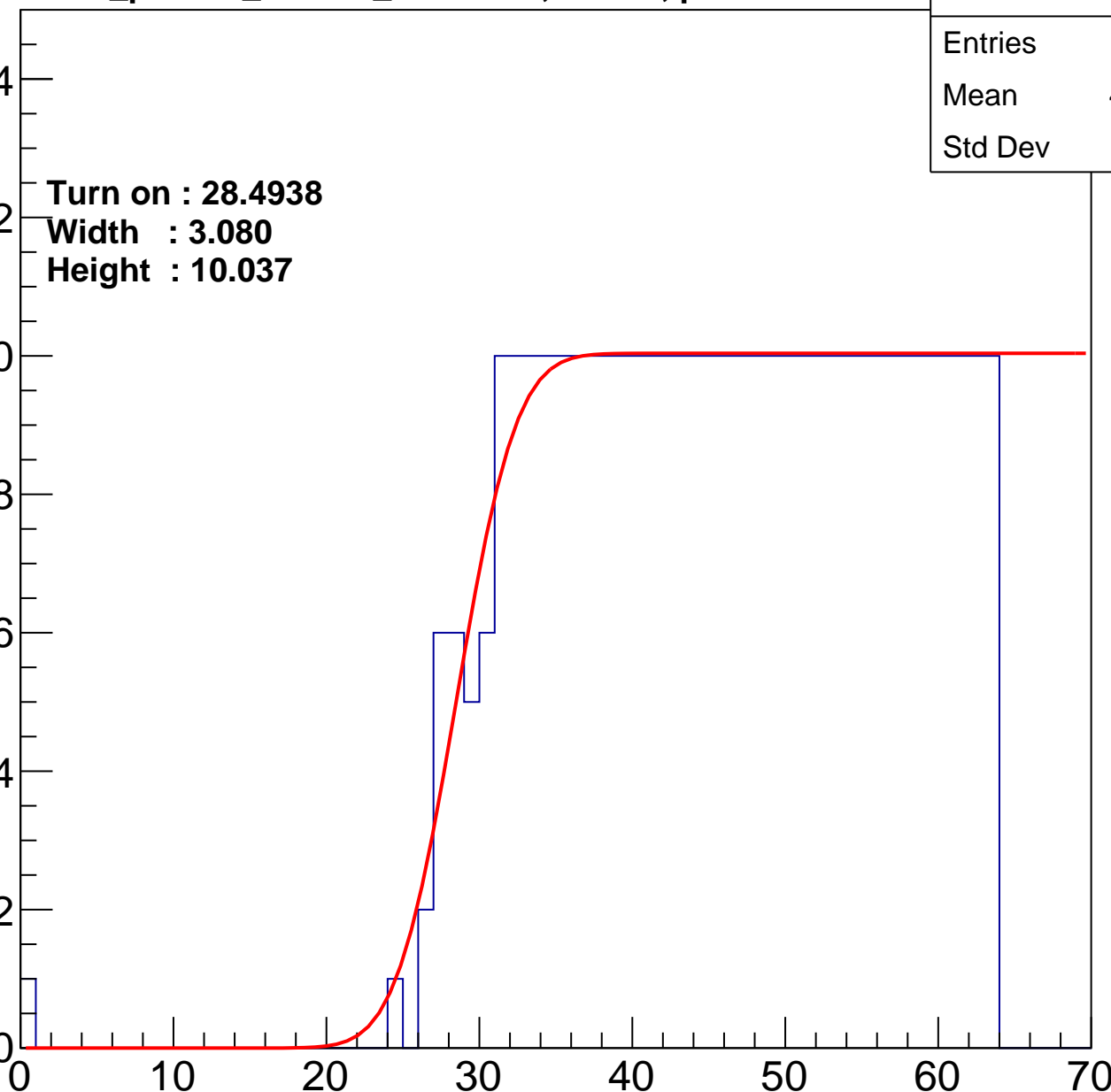
Width : 3.080

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch16

calib_packv5_042523_0143.root, FC#13, port D2

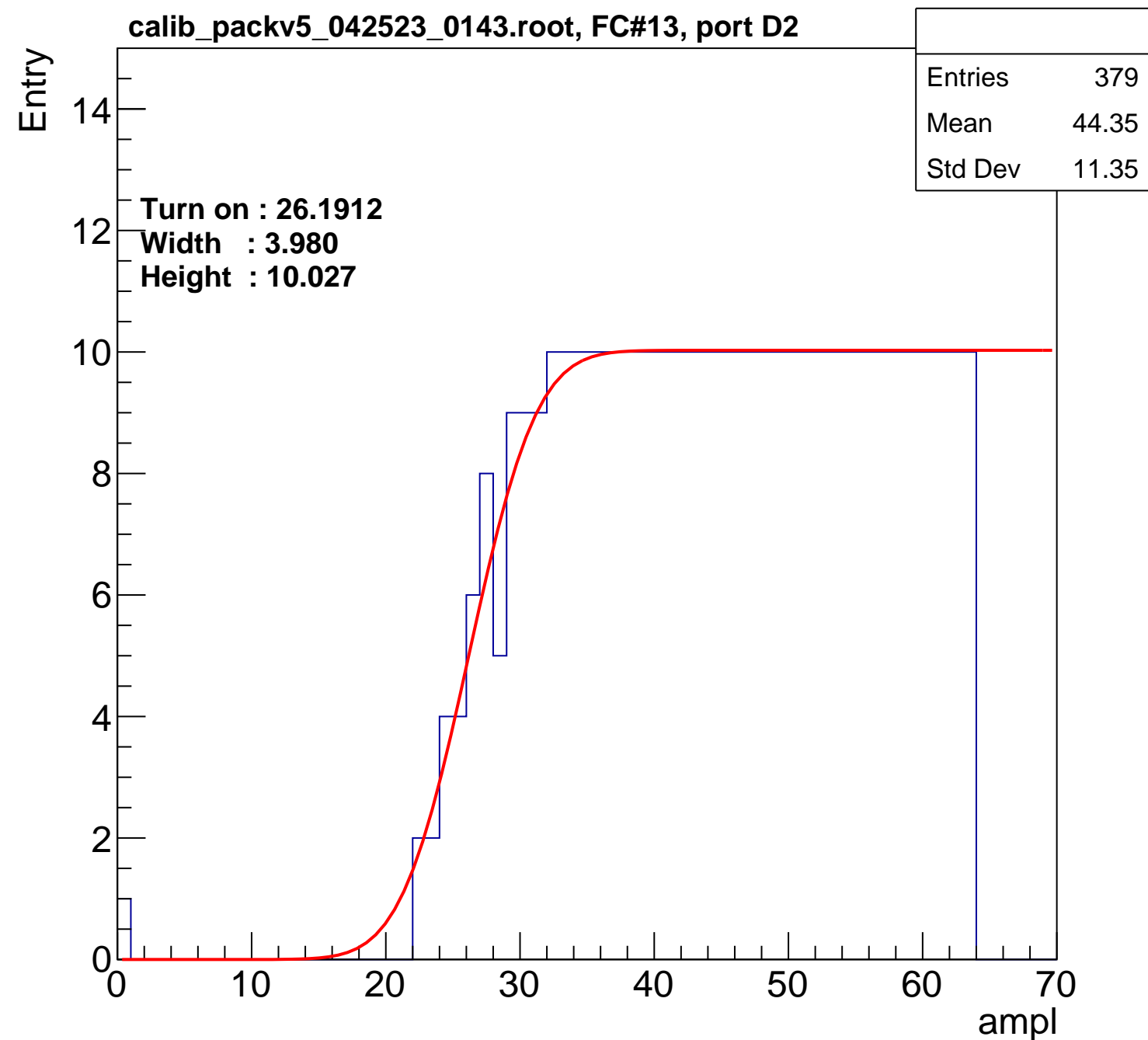
Entry

14
12
10
8
6
4
2
0

Turn on : 26.1912
Width : 3.980
Height : 10.027

Entries	379
Mean	44.35
Std Dev	11.35

ampl



B1L003S, U25-ch17

calib_packv5_042523_0143.root, FC#13, port D2

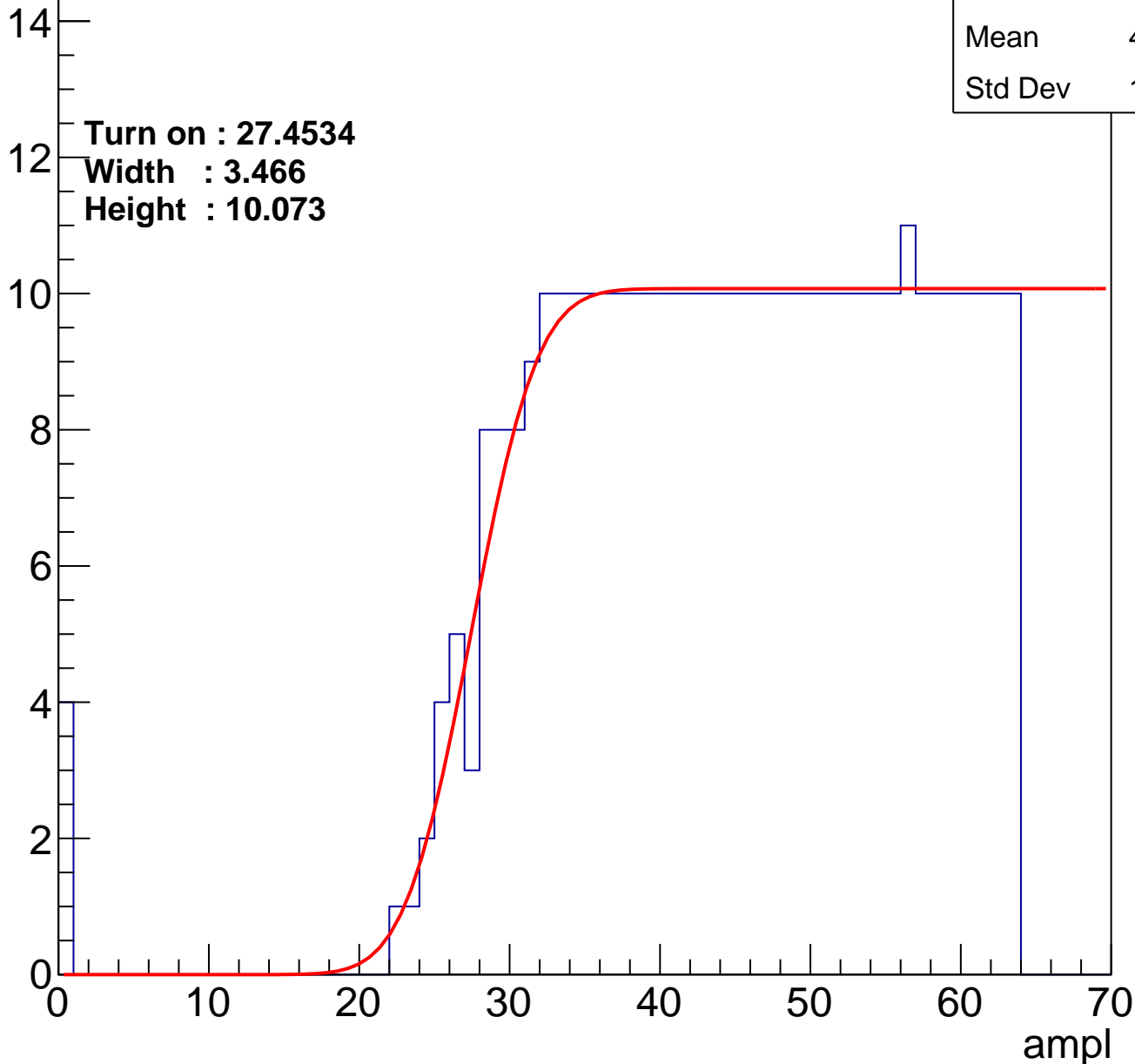
Entries	374
Mean	44.48
Std Dev	11.75

Turn on : 27.4534

Width : 3.466

Height : 10.073

Entry



B1L003S, U25-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.2692

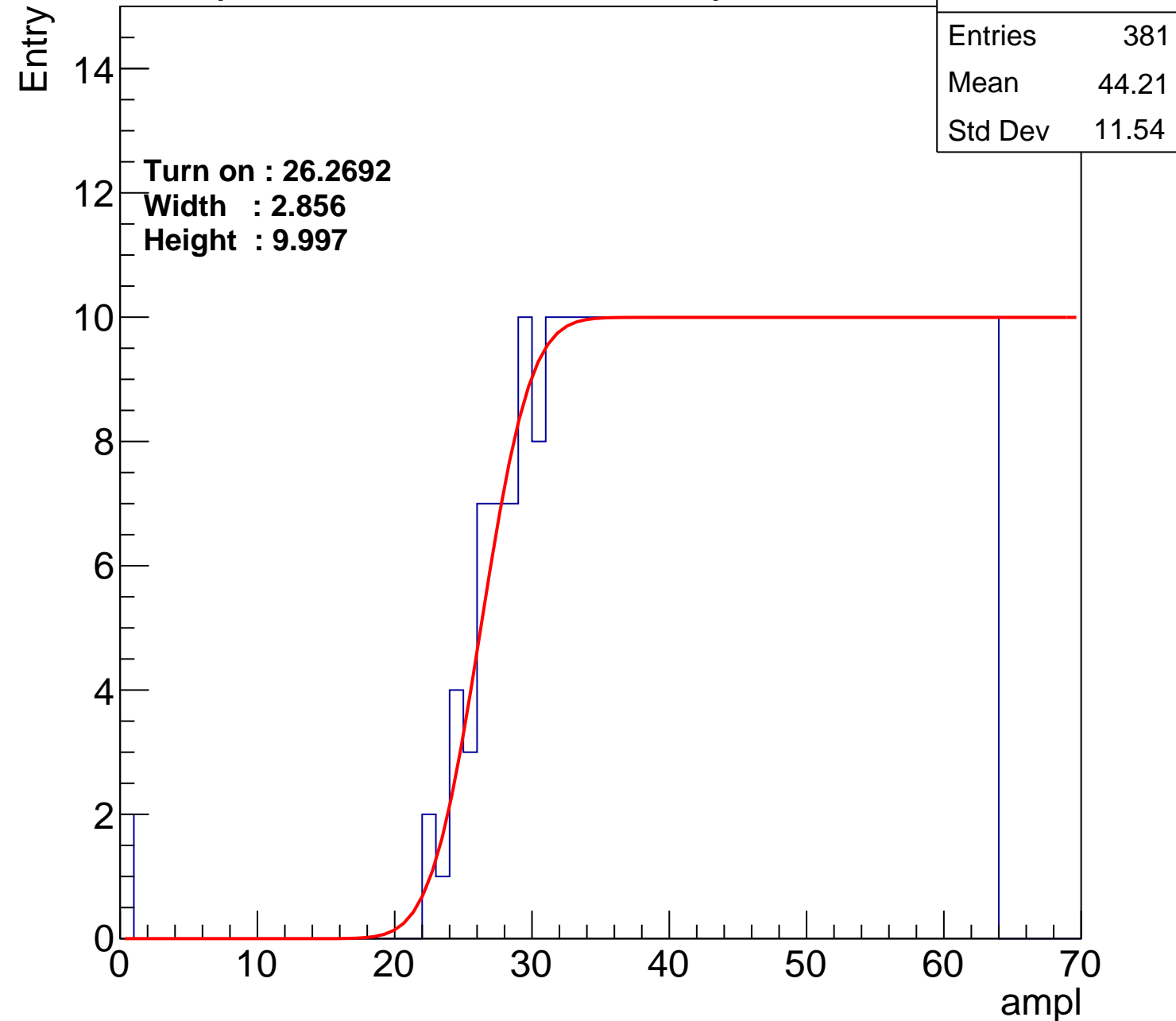
Width : 2.856

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch19

calib_packv5_042523_0143.root, FC#13, port D2

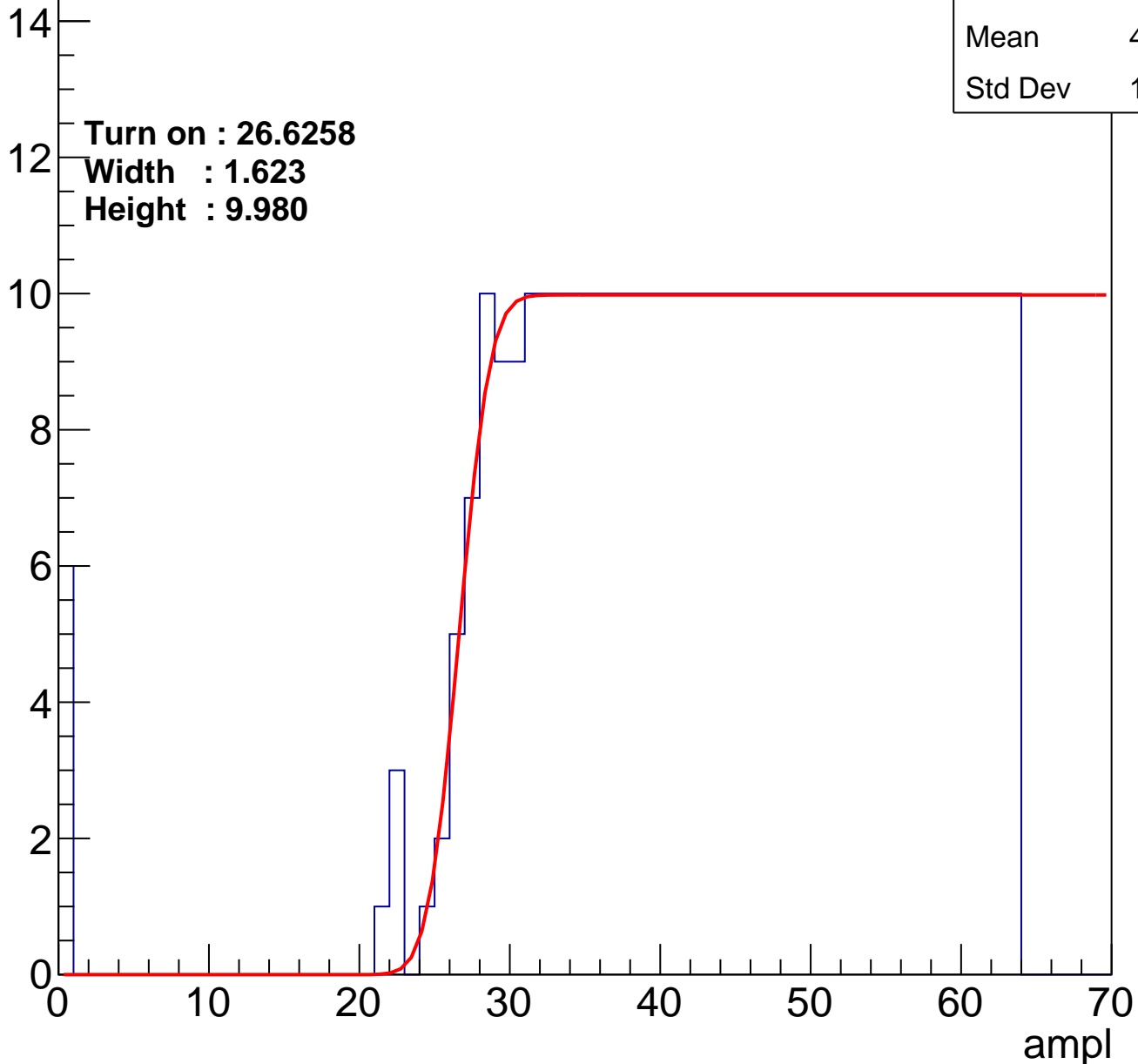
Entries	383
Mean	43.87
Std Dev	12.26

Turn on : 26.6258

Width : 1.623

Height : 9.980

Entry



B1L003S, U25-ch20

calib_packv5_042523_0143.root, FC#13, port D2

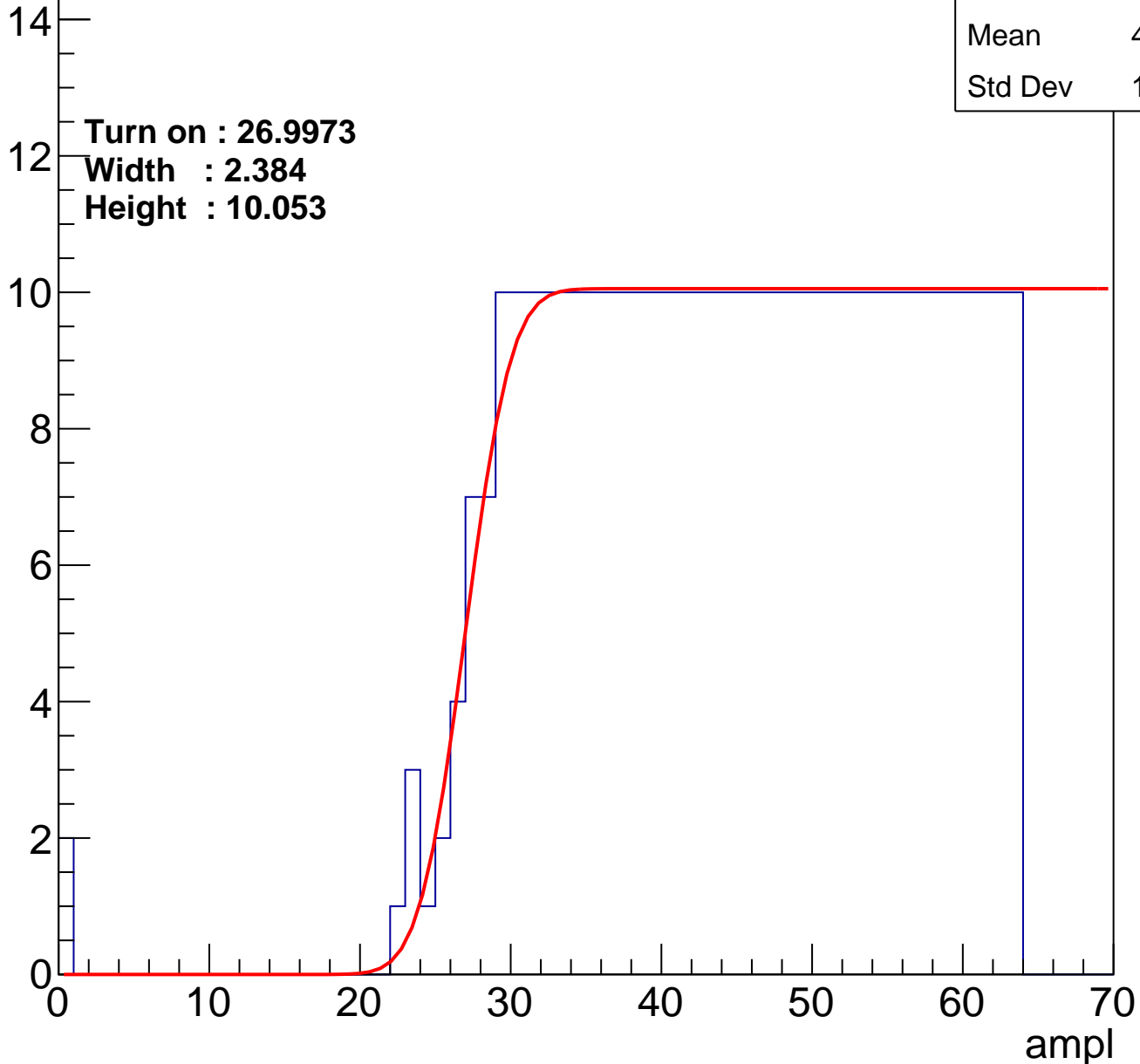
Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.9973

Width : 2.384

Height : 10.053

Entry



B1L003S, U25-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.97
Std Dev	11.86

Turn on : 26.5748

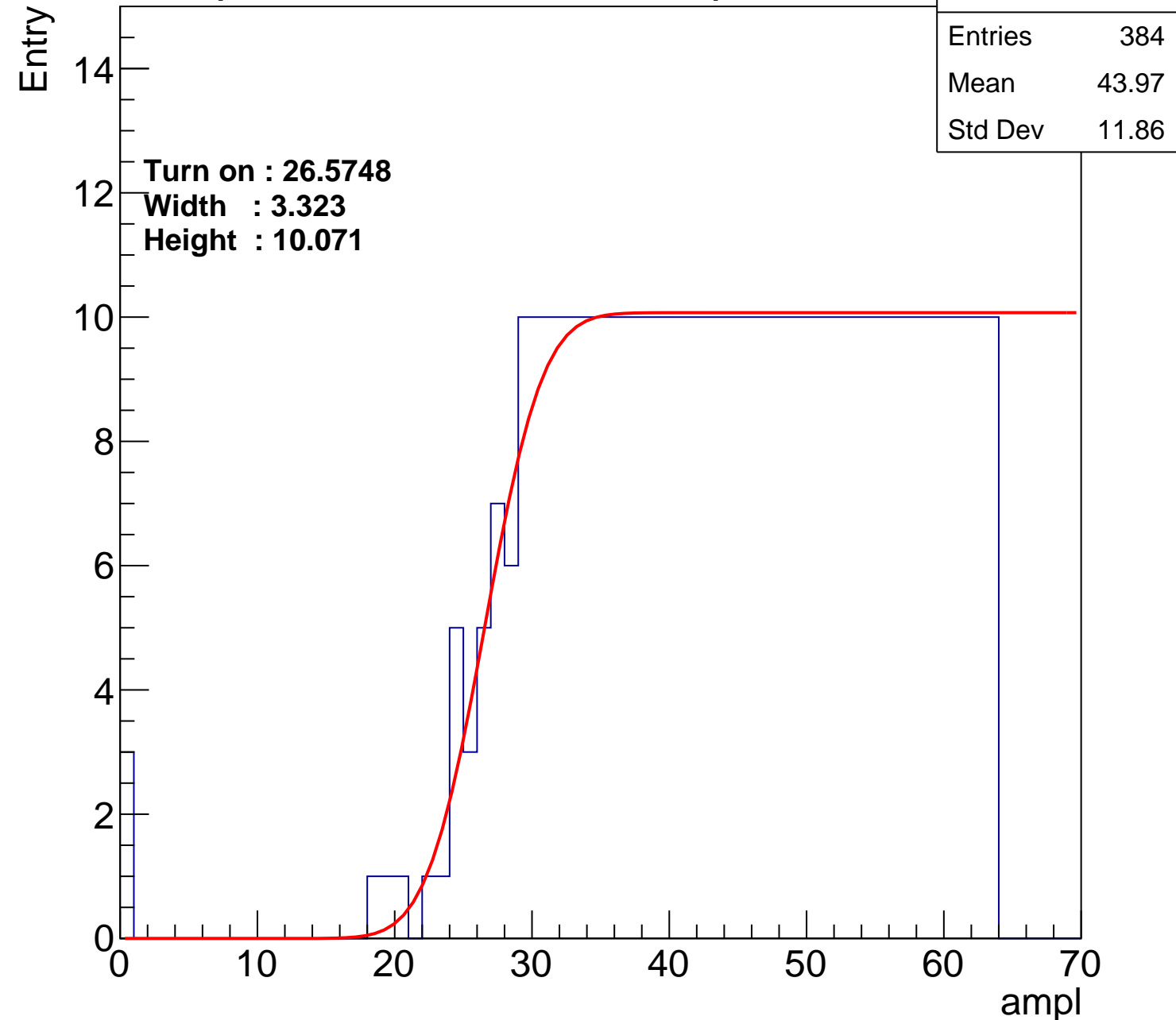
Width : 3.323

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.3144

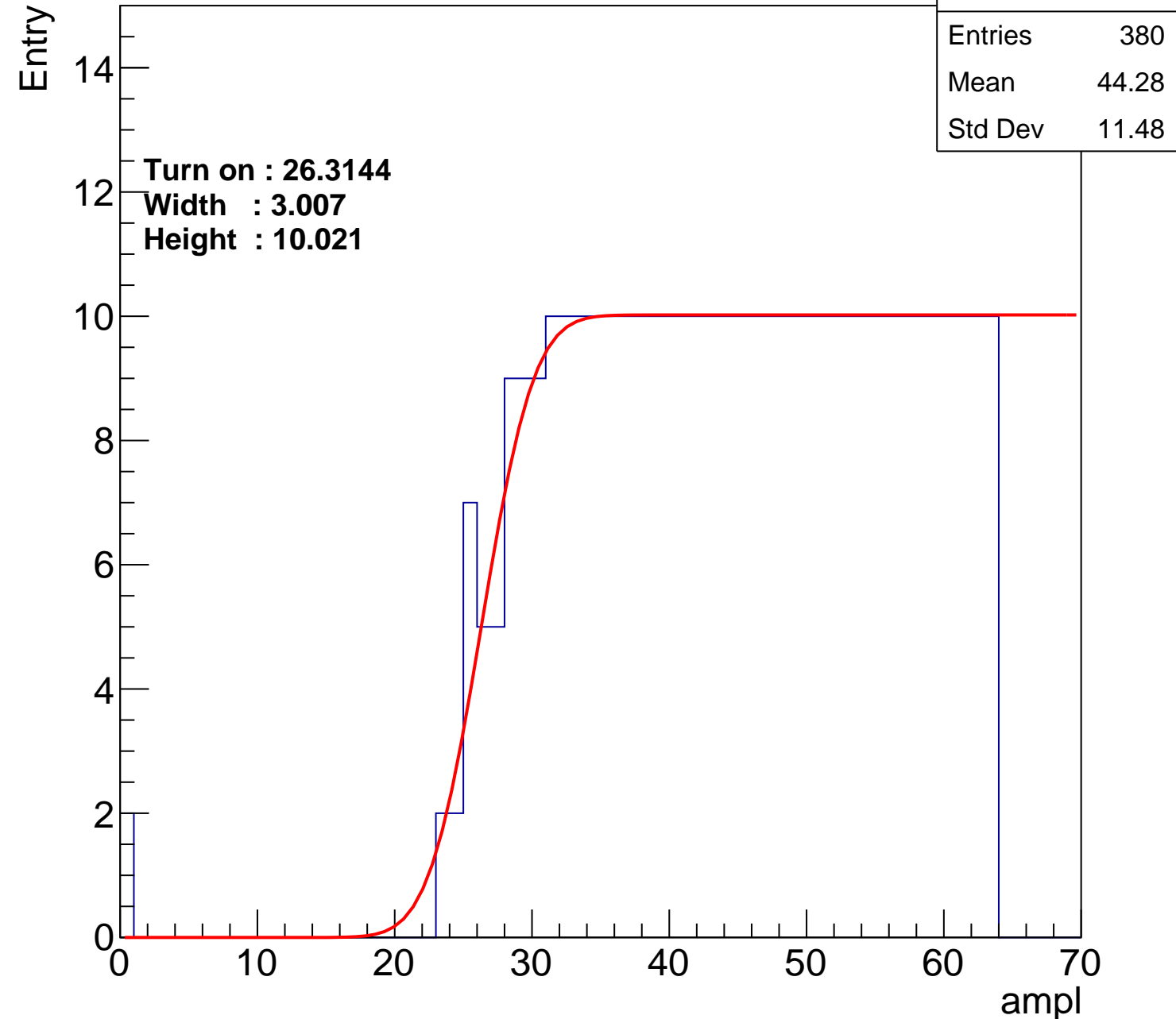
Width : 3.007

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.7866

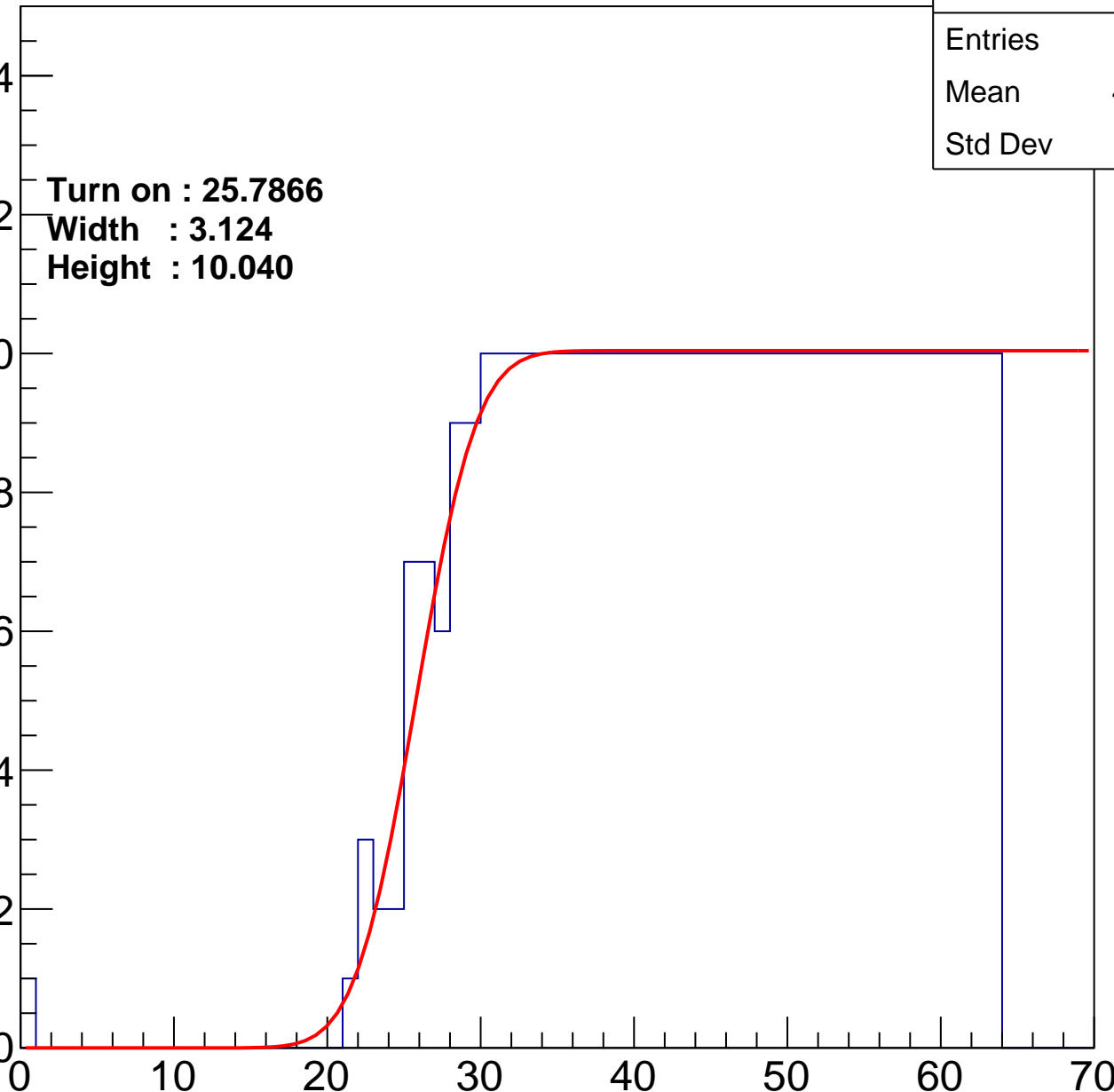
Width : 3.124

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch24

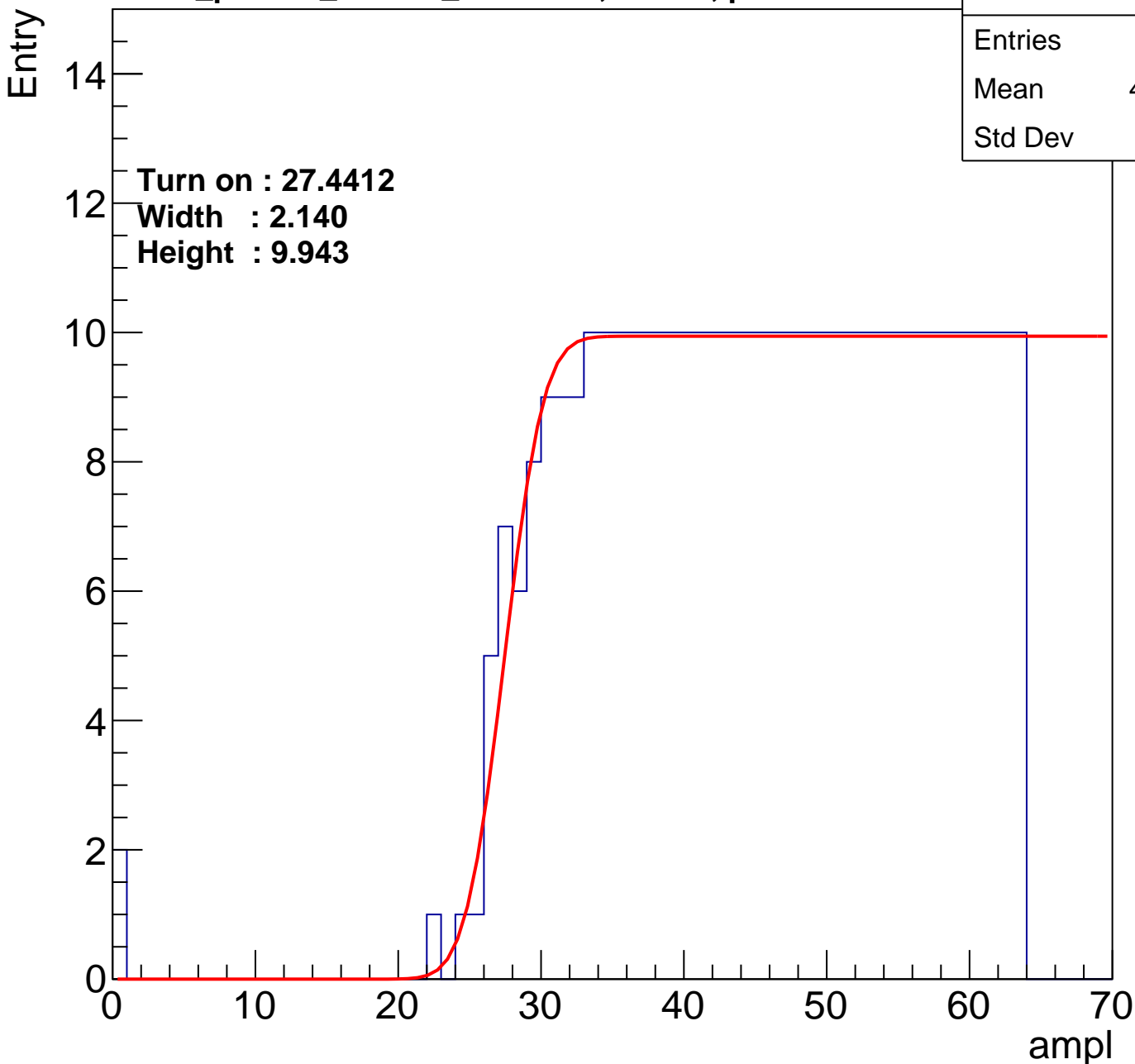
calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.4412

Width : 2.140

Height : 9.943



B1L003S, U25-ch25

calib_packv5_042523_0143.root, FC#13, port D2

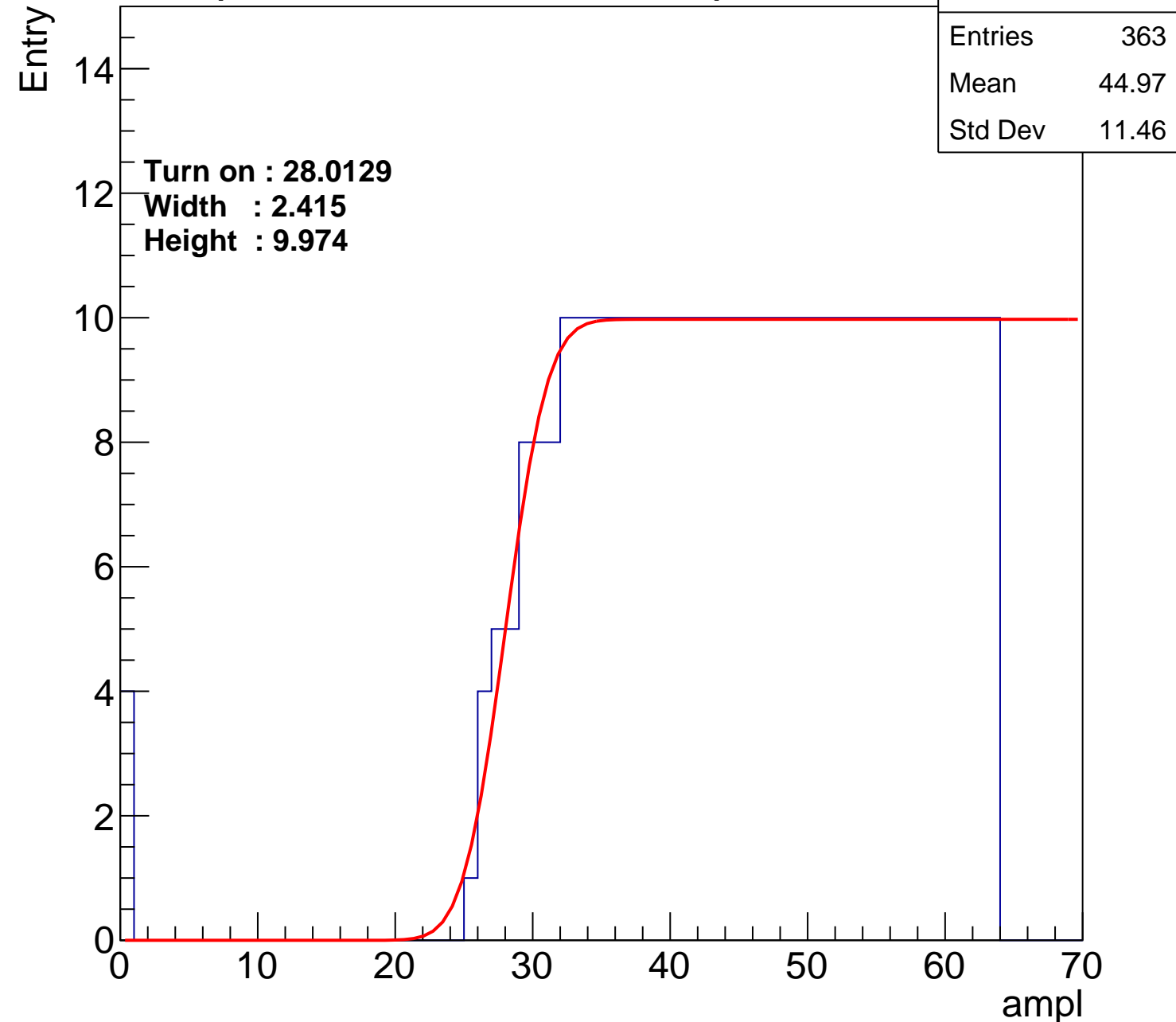
Entry

14
12
10
8
6
4
2
0

Turn on : 28.0129
Width : 2.415
Height : 9.974

Entries	363
Mean	44.97
Std Dev	11.46

ampl



B1L003S, U25-ch26

calib_packv5_042523_0143.root, FC#13, port D2

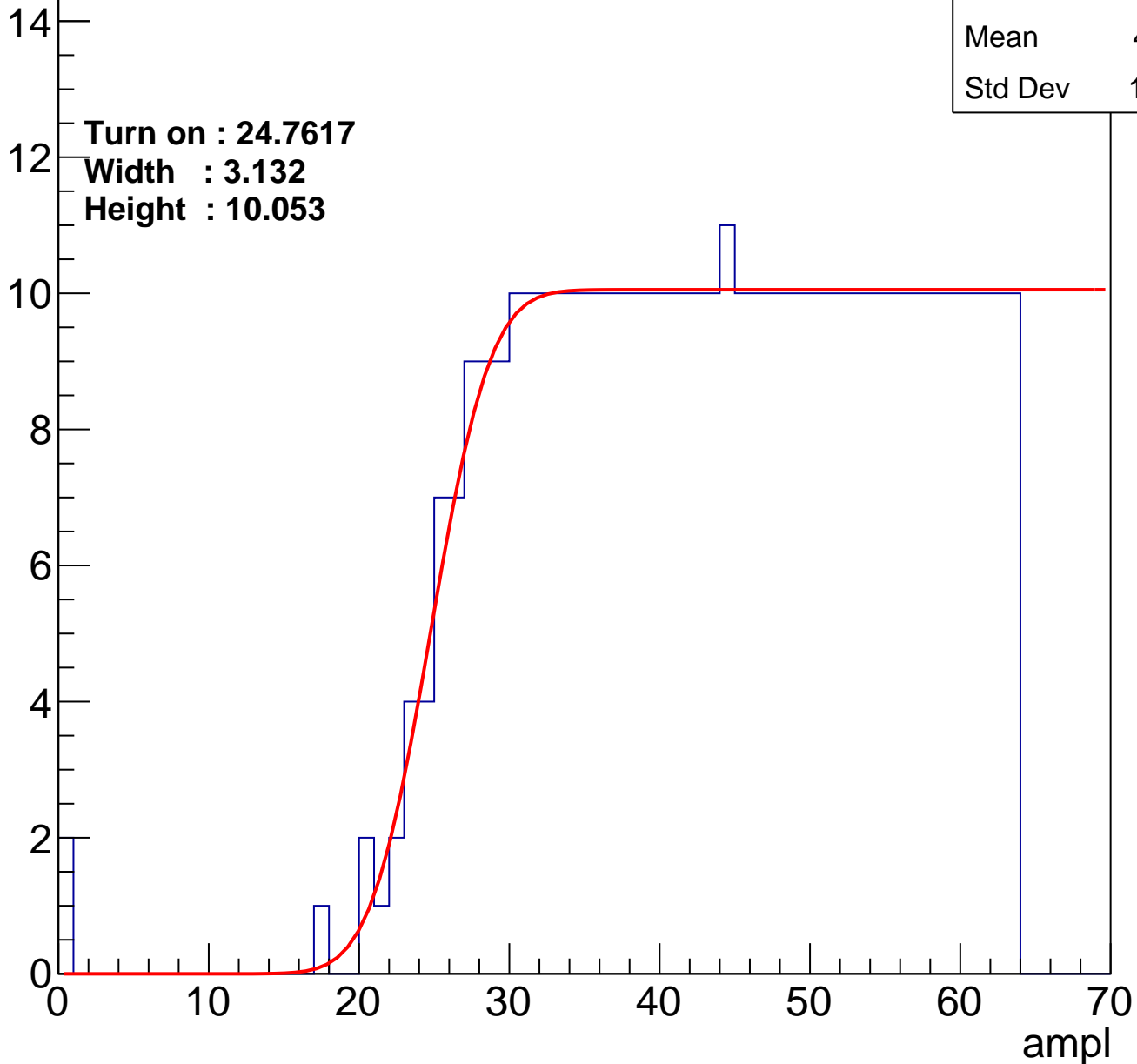
Entries	398
Mean	43.41
Std Dev	11.97

Turn on : 24.7617

Width : 3.132

Height : 10.053

Entry



B1L003S, U25-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.96
Std Dev	11.49

Turn on : 28.5926

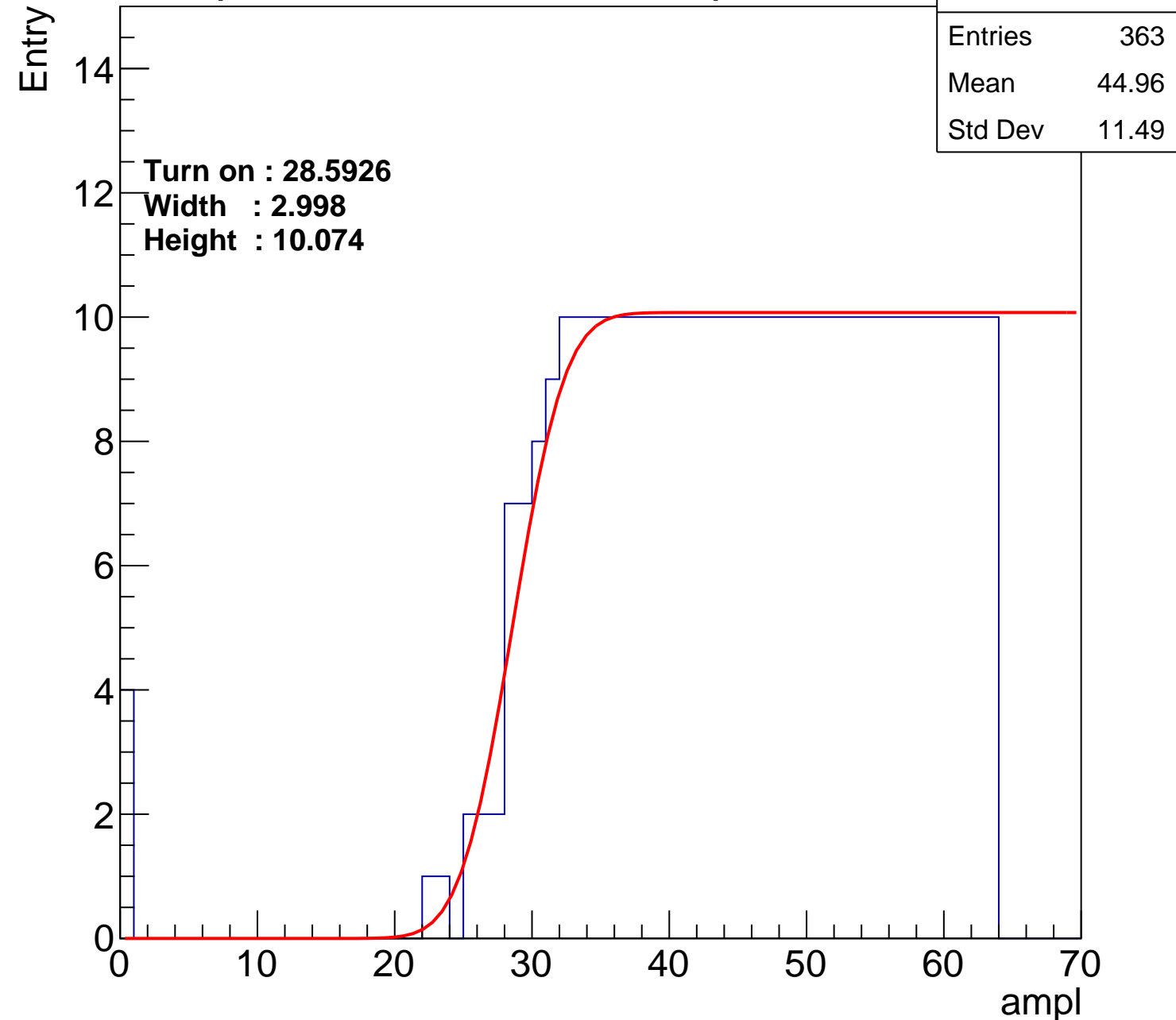
Width : 2.998

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.48
Std Dev	11.55

Turn on : 26.8243

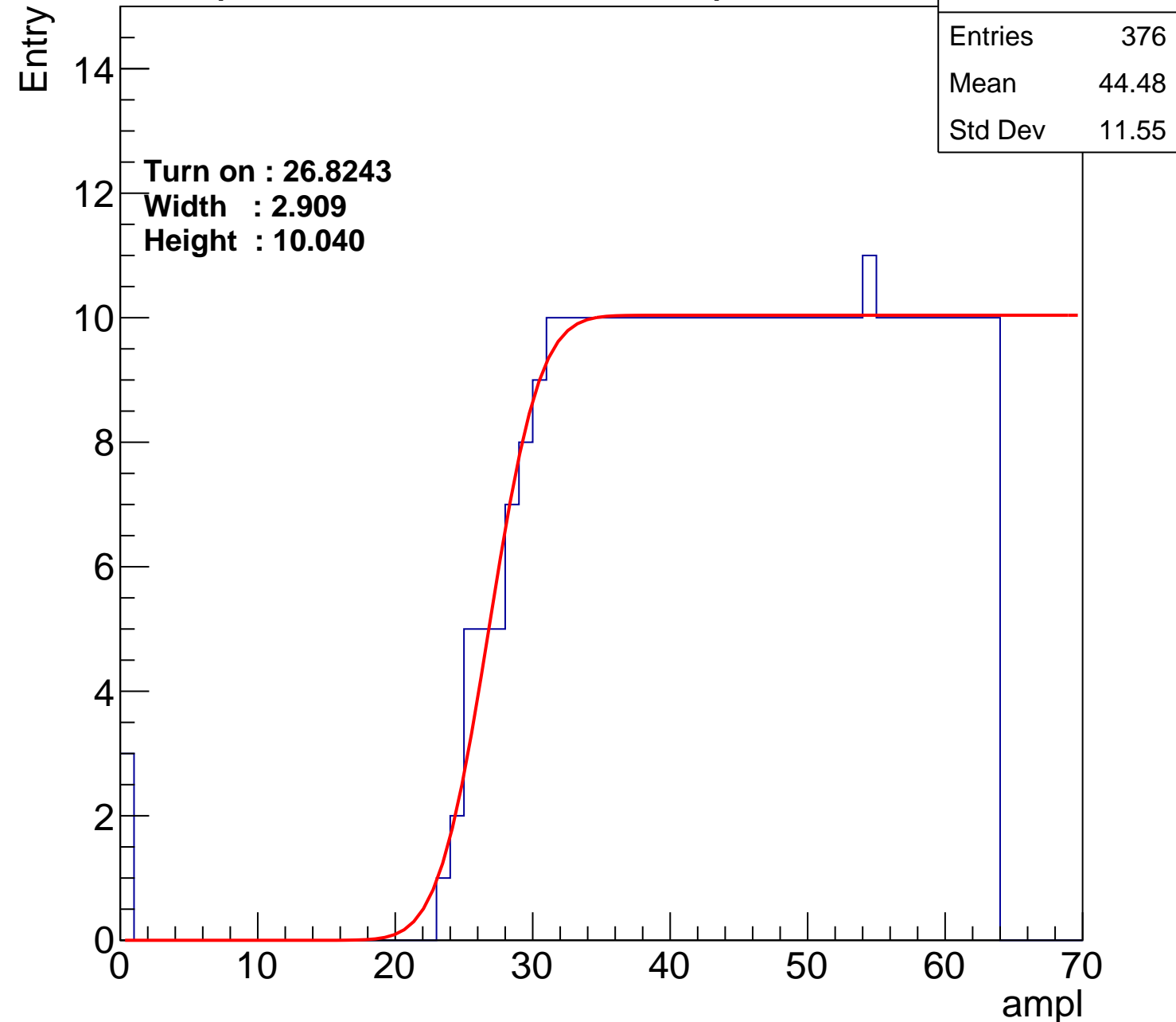
Width : 2.909

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.7
Std Dev	10.55

Turn on : 28.8530

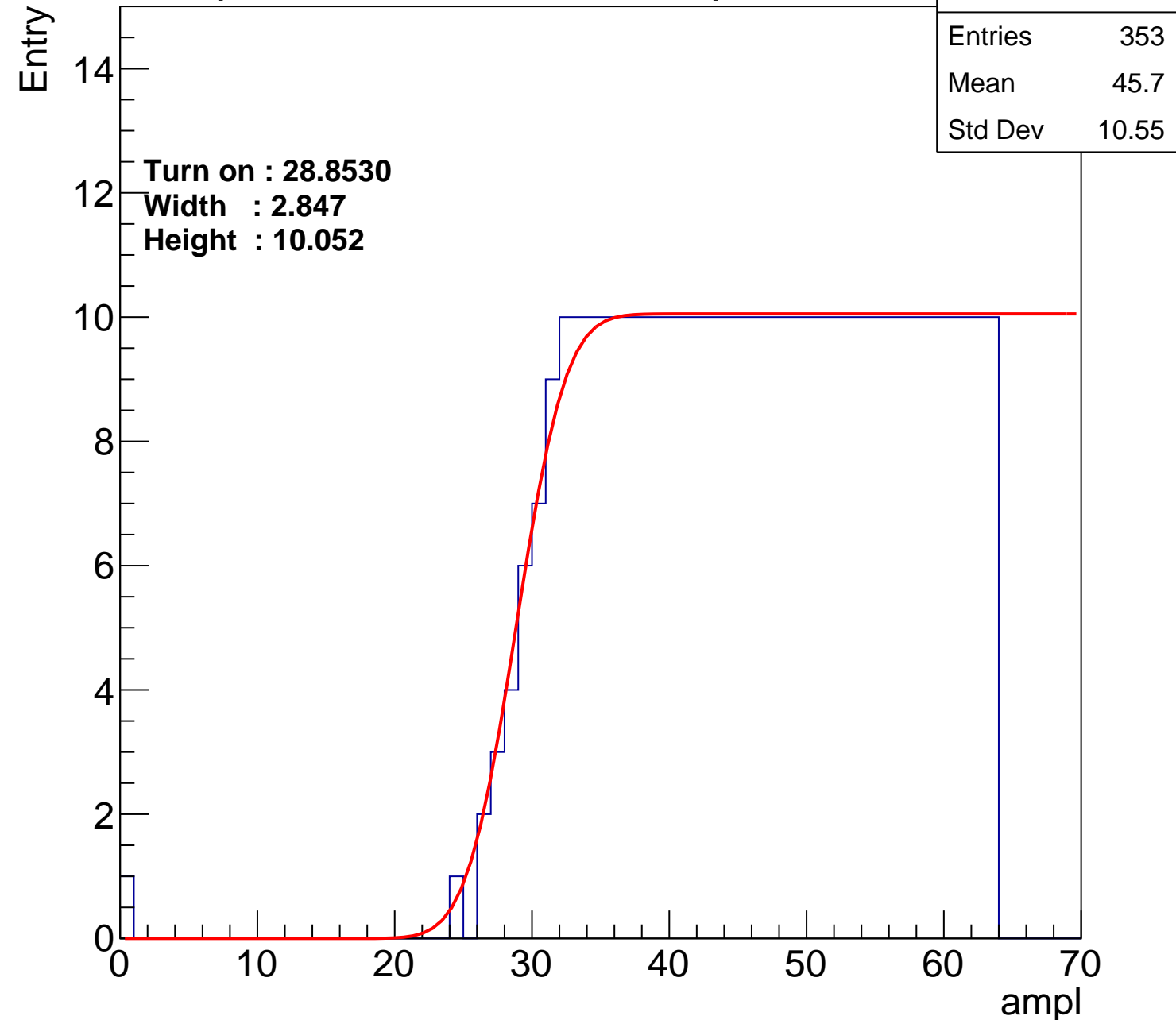
Width : 2.847

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch30

calib_packv5_042523_0143.root, FC#13, port D2

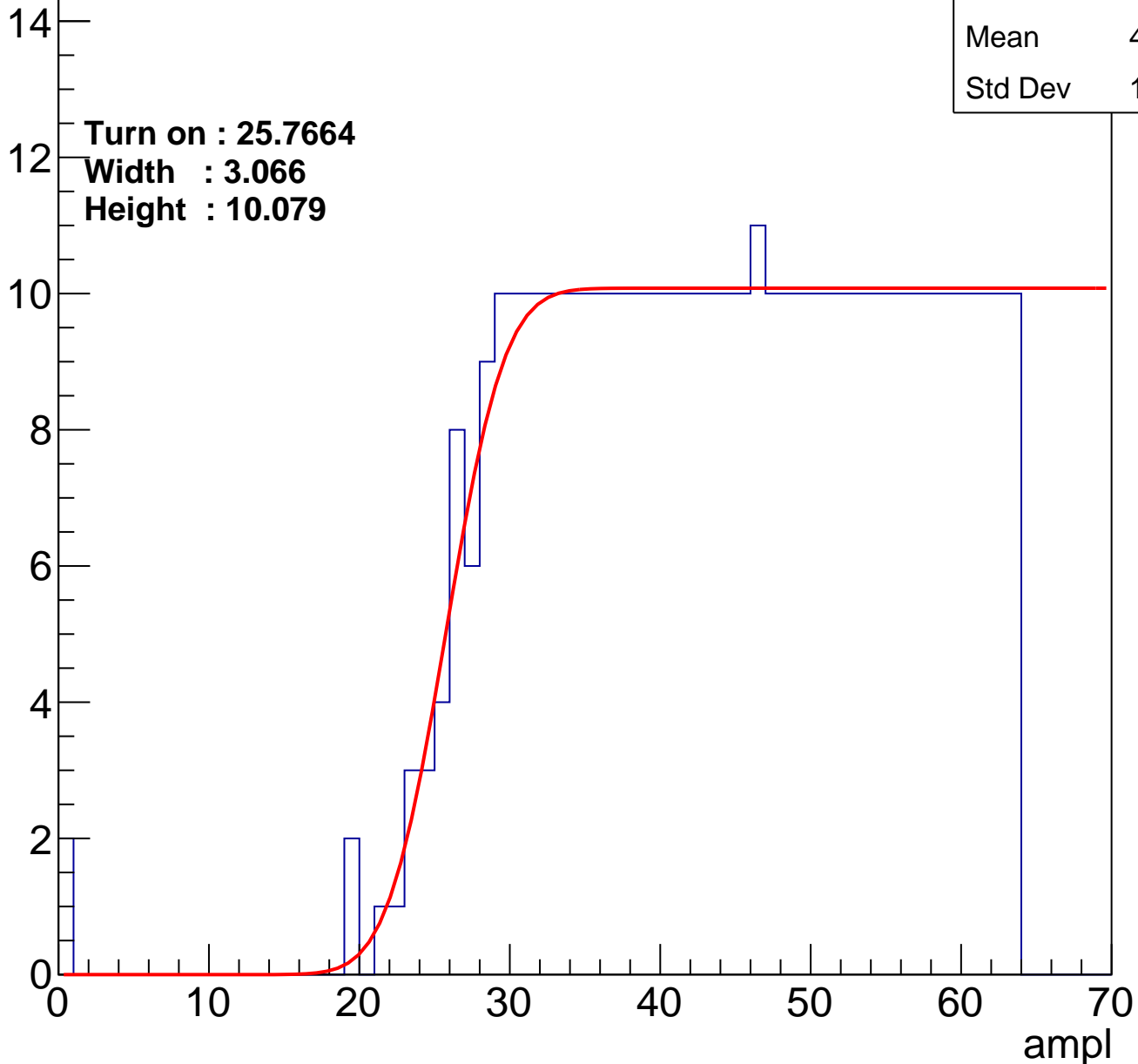
Entries	390
Mean	43.82
Std Dev	11.75

Turn on : 25.7664

Width : 3.066

Height : 10.079

Entry



B1L003S, U25-ch31

calib_packv5_042523_0143.root, FC#13, port D2

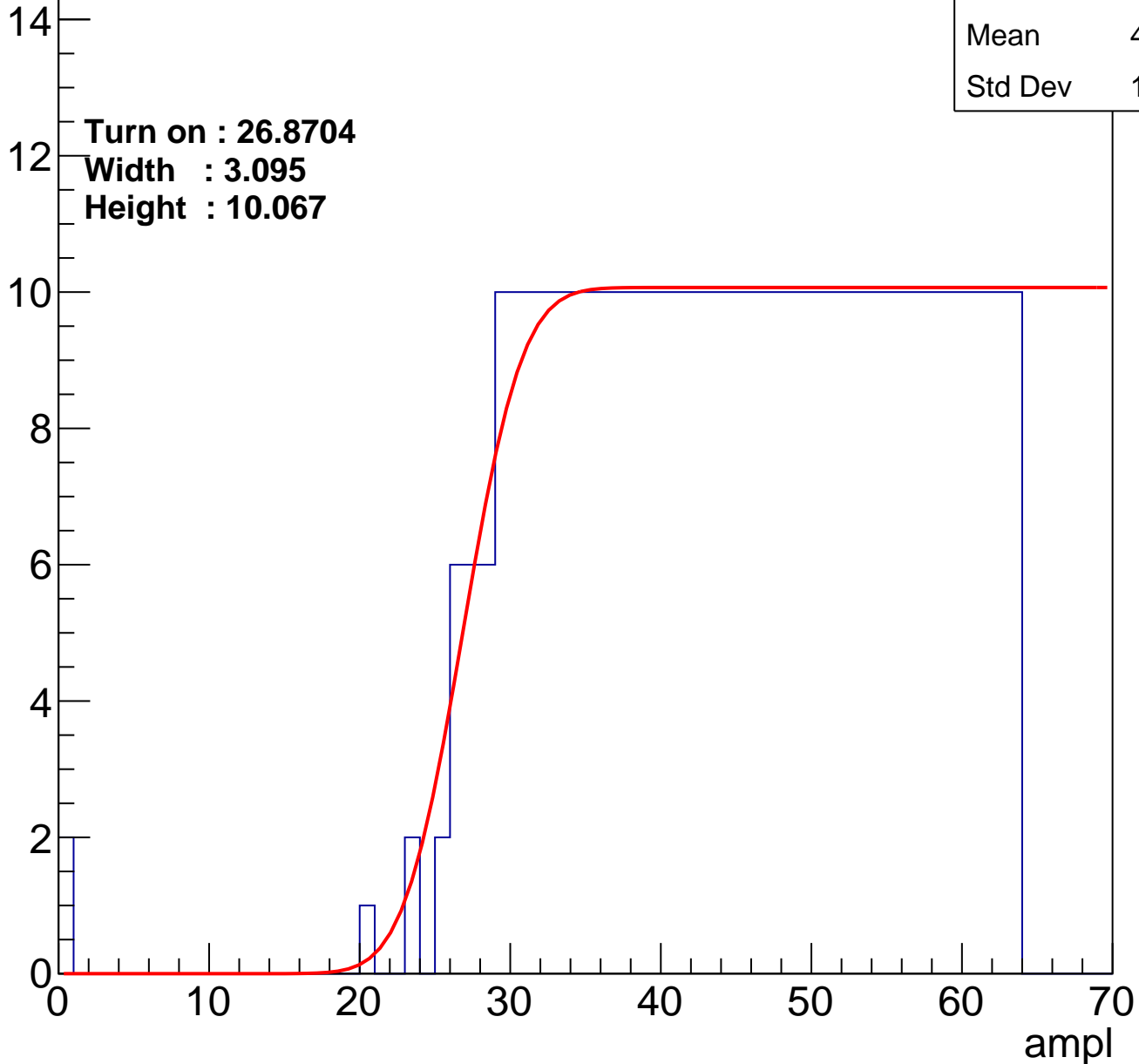
Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.8704

Width : 3.095

Height : 10.067

Entry



B1L003S, U25-ch32

calib_packv5_042523_0143.root, FC#13, port D2

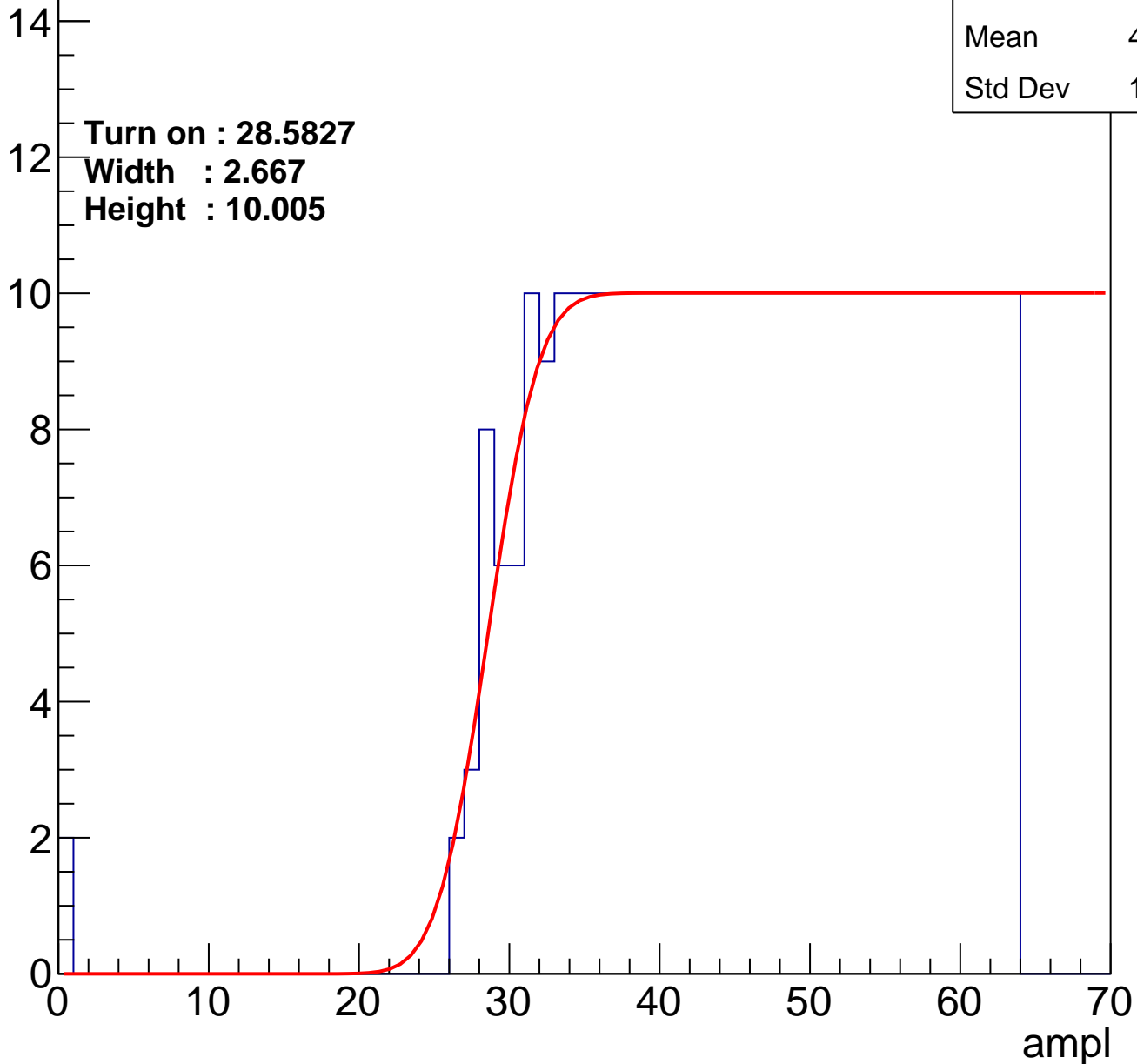
Entries	356
Mean	45.47
Std Dev	10.85

Turn on : 28.5827

Width : 2.667

Height : 10.005

Entry



B1L003S, U25-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.1
Std Dev	11.23

Turn on : 28.4253

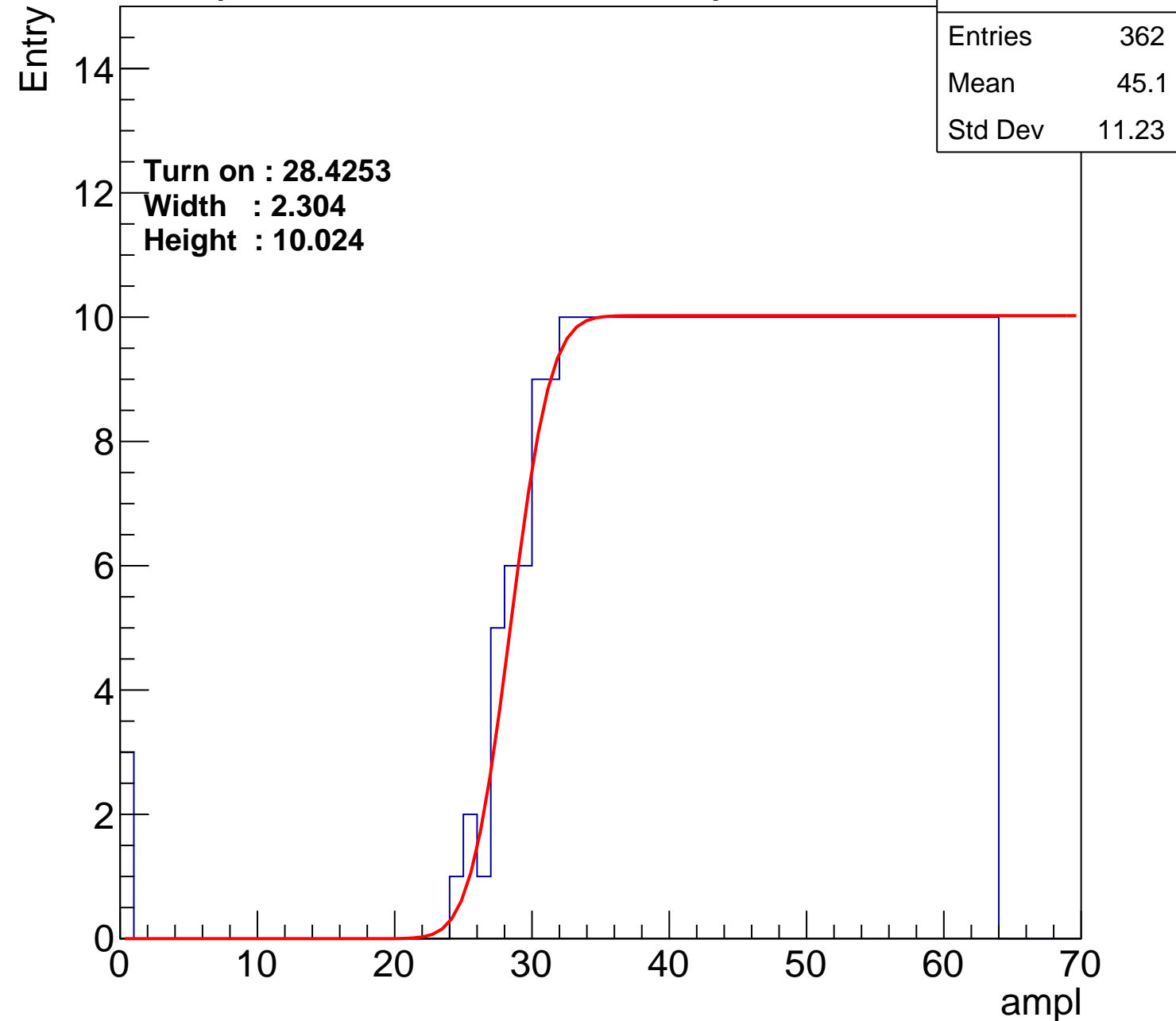
Width : 2.304

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.27
Std Dev	10.82

Turn on : 27.8746

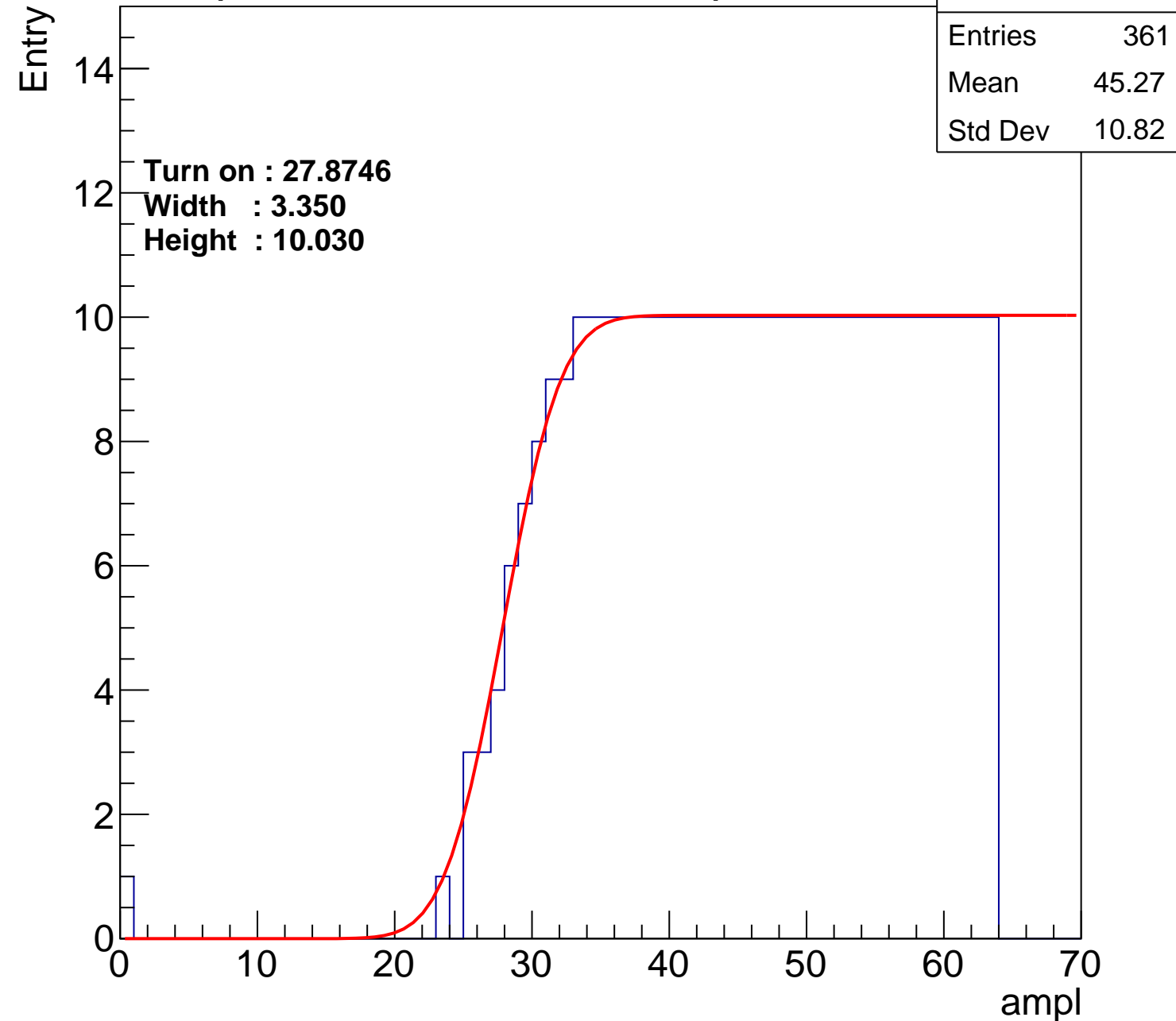
Width : 3.350

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.53
Std Dev	11.17

Turn on : 26.4054

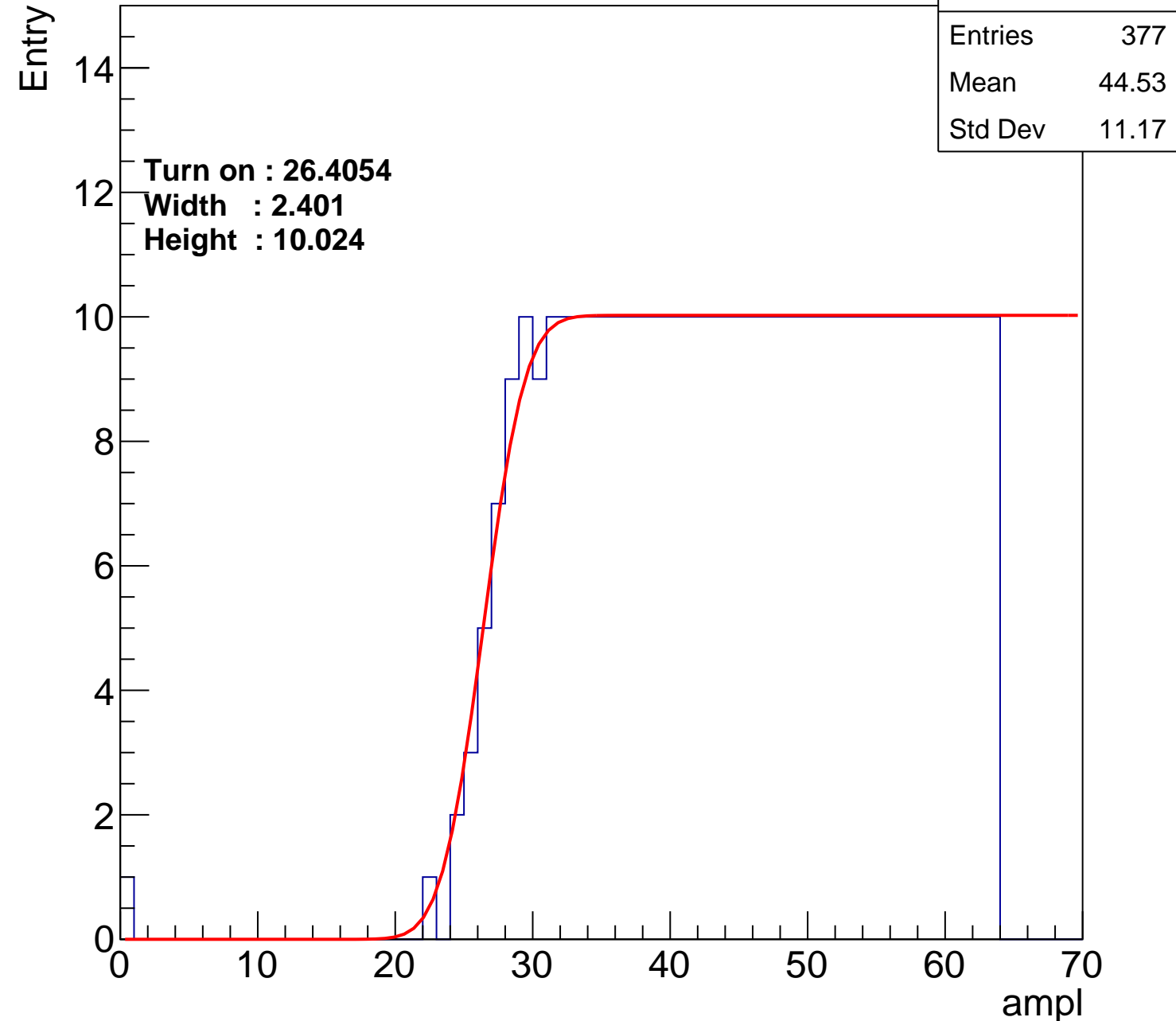
Width : 2.401

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch36

calib_packv5_042523_0143.root, FC#13, port D2

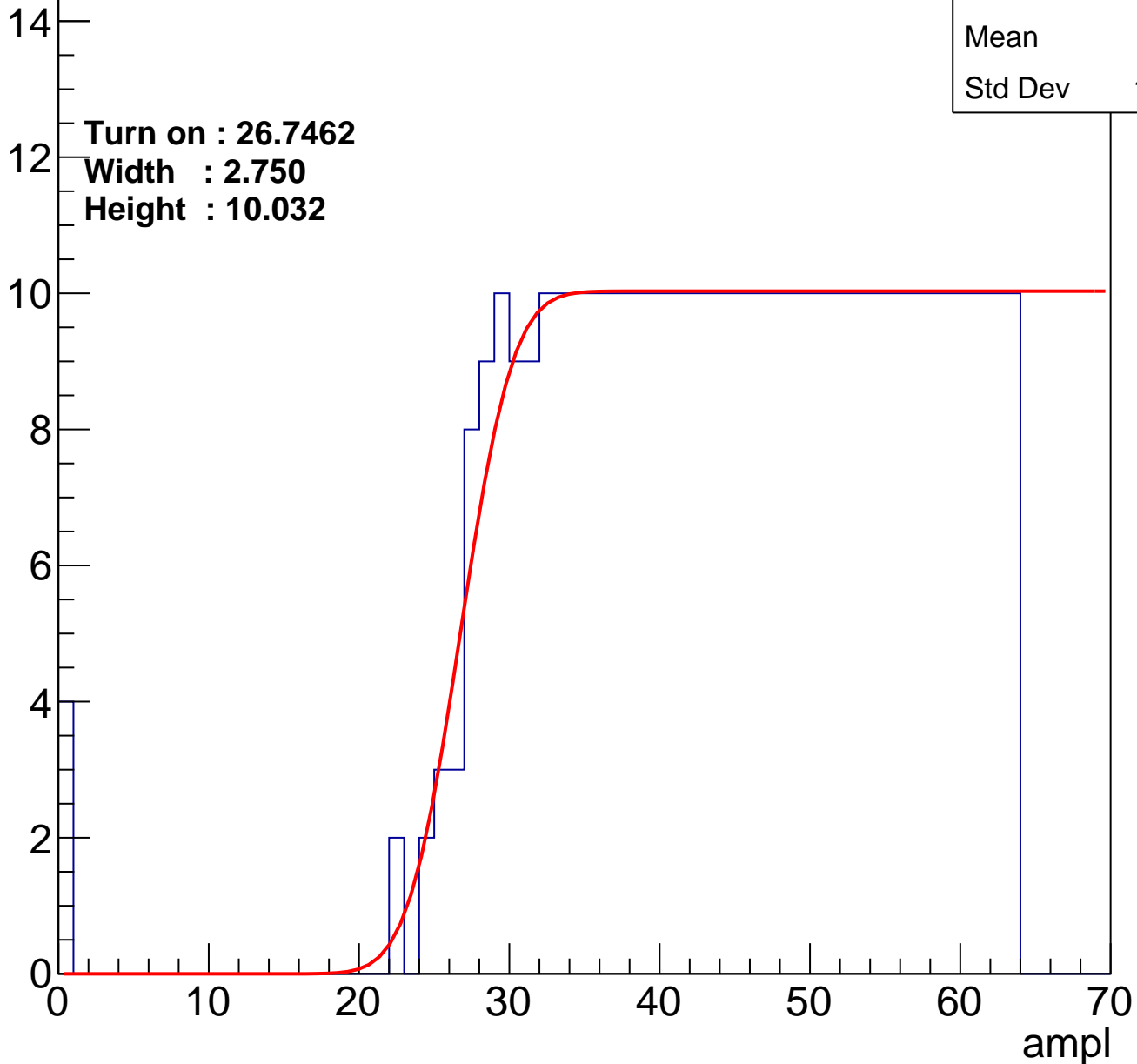
Entries	379
Mean	44.2
Std Dev	11.81

Turn on : 26.7462

Width : 2.750

Height : 10.032

Entry



B1L003S, U25-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.83
Std Dev	11.57

Turn on : 27.8248

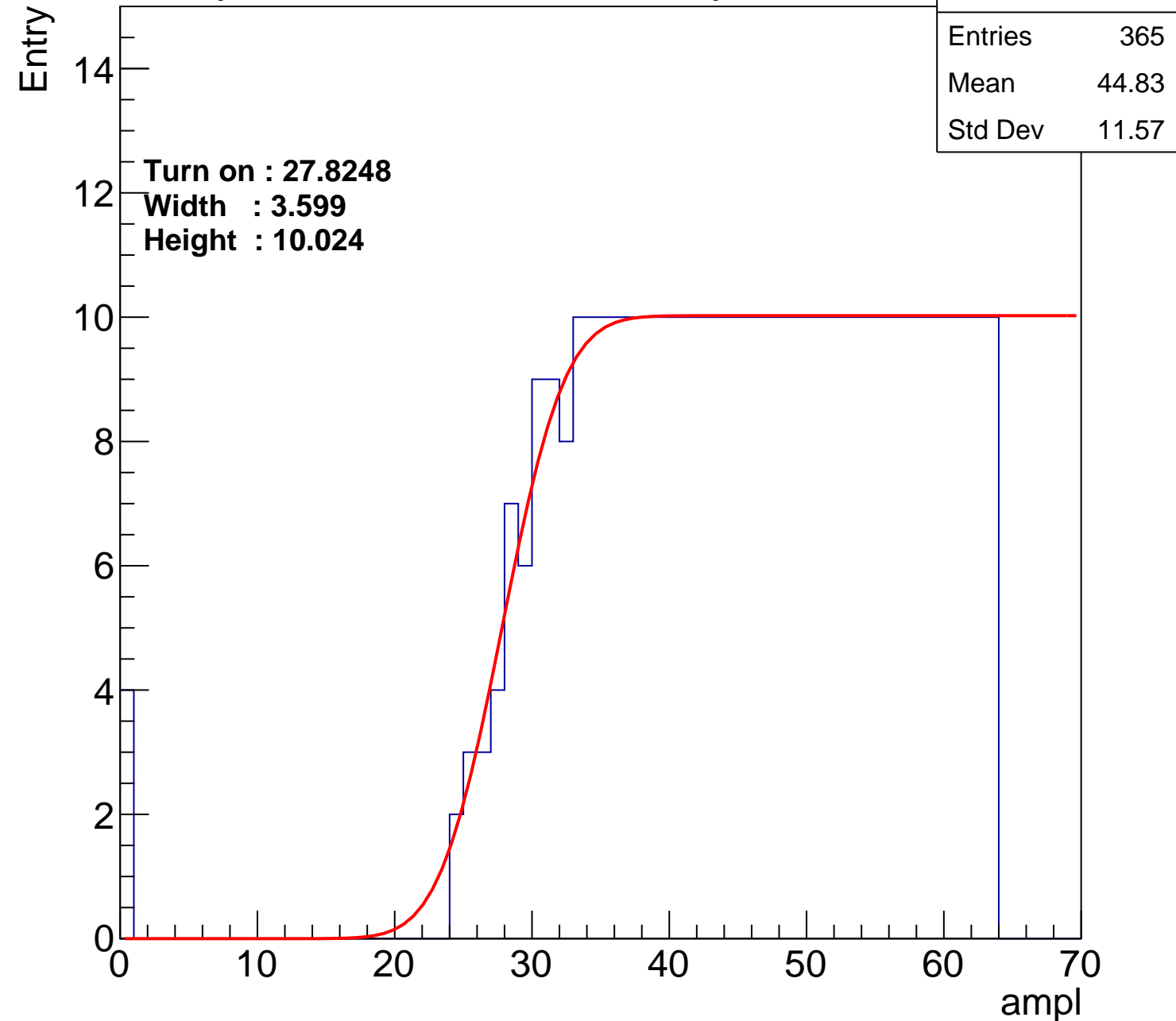
Width : 3.599

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch38

calib_packv5_042523_0143.root, FC#13, port D2

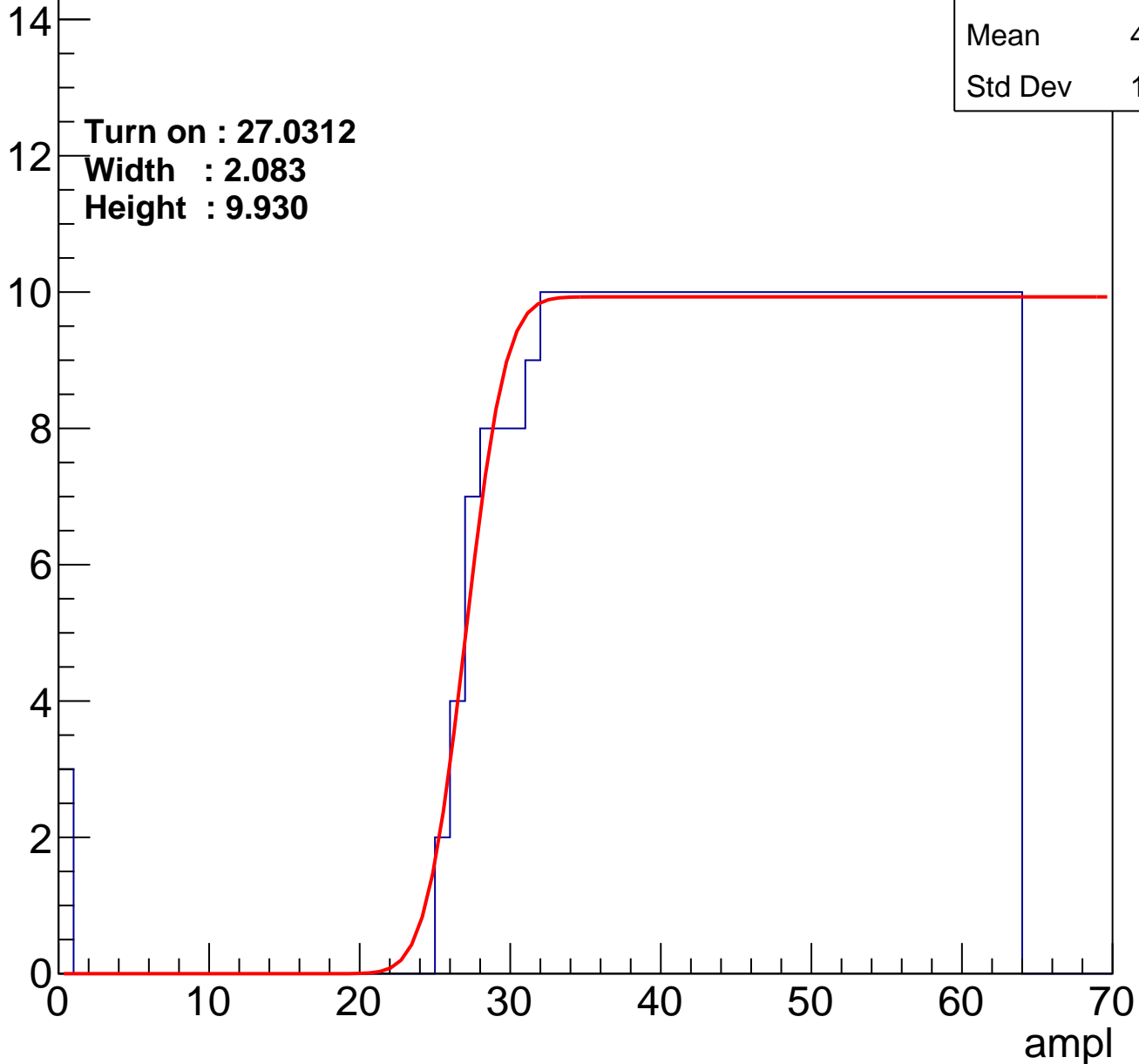
Entries	369
Mean	44.76
Std Dev	11.38

Turn on : 27.0312

Width : 2.083

Height : 9.930

Entry



B1L003S, U25-ch39

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.97
Std Dev	10.98

Turn on : 27.3912

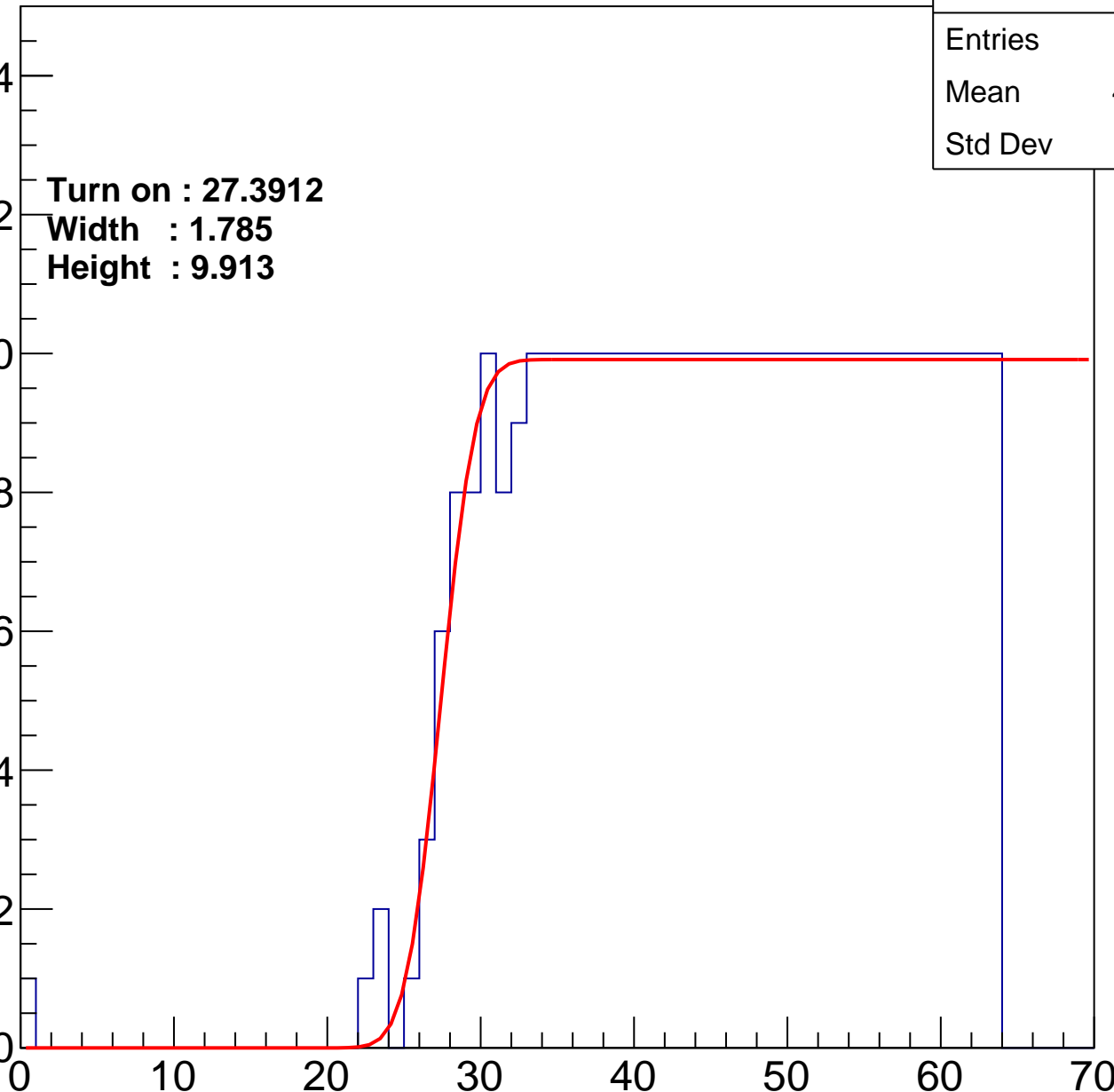
Width : 1.785

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.15
Std Dev	10.91

Turn on : 28.3089

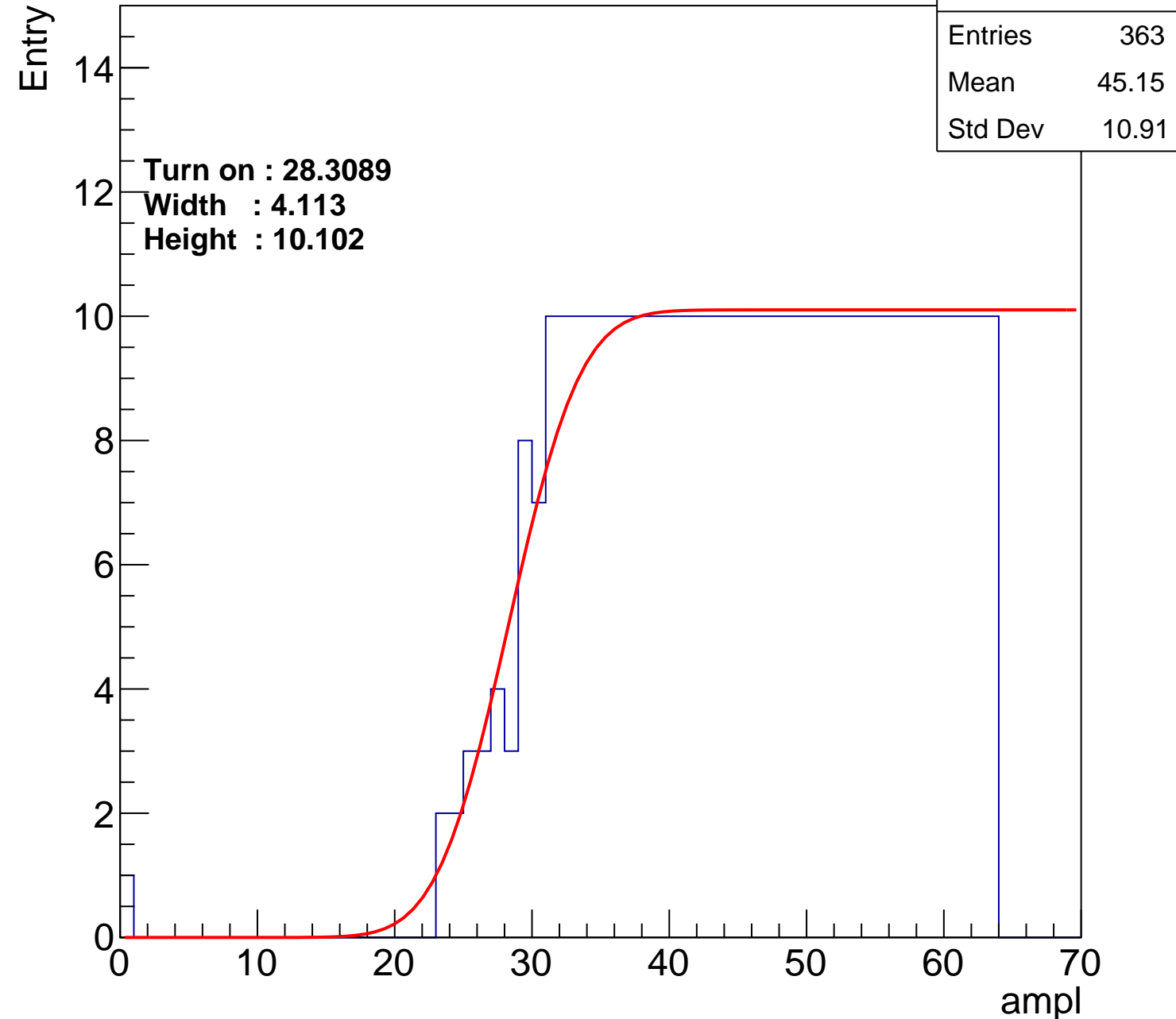
Width : 4.113

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.51
Std Dev	11.58

Turn on : 27.6072

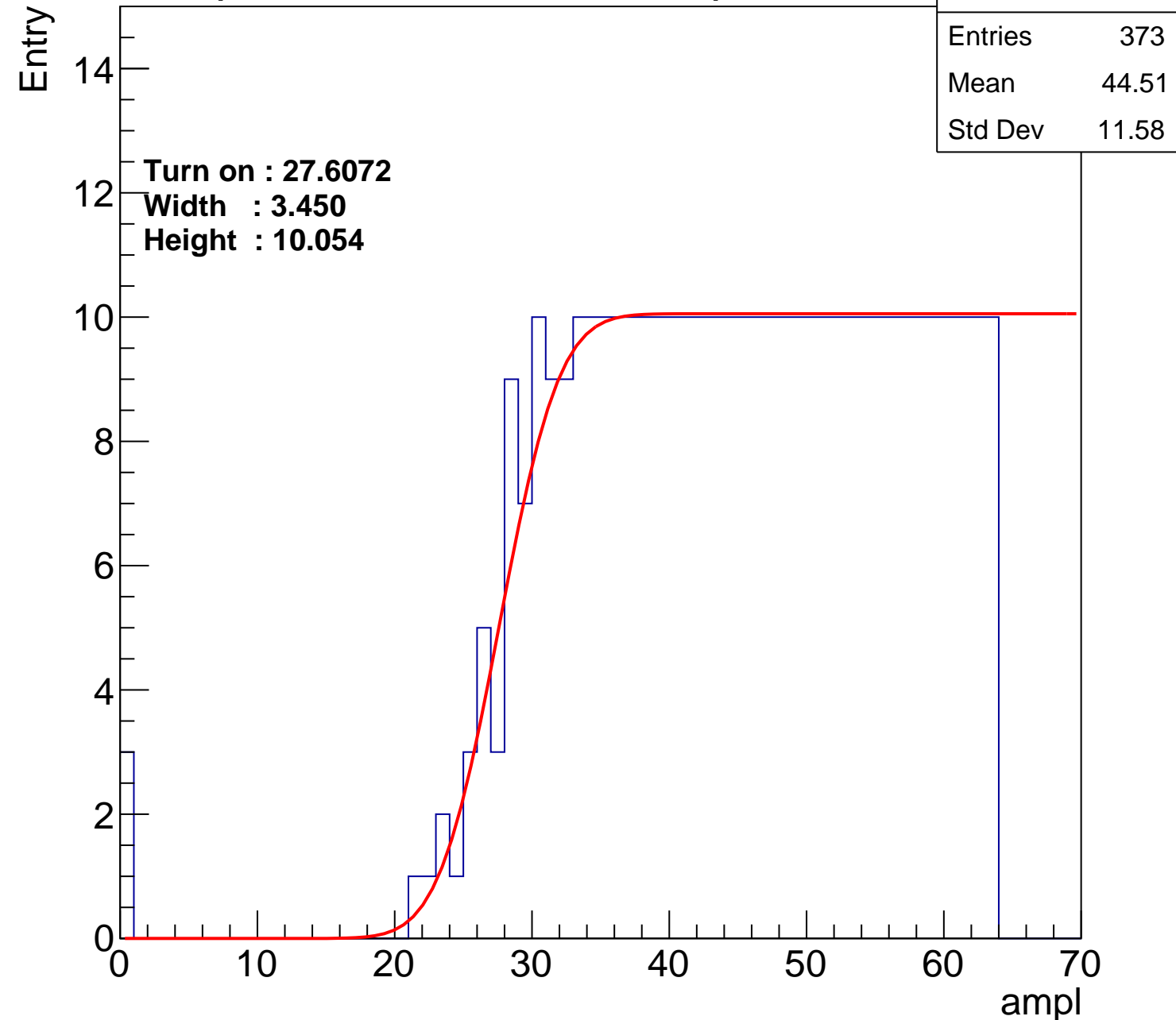
Width : 3.450

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.34
Std Dev	10.92

Turn on : 28.2846

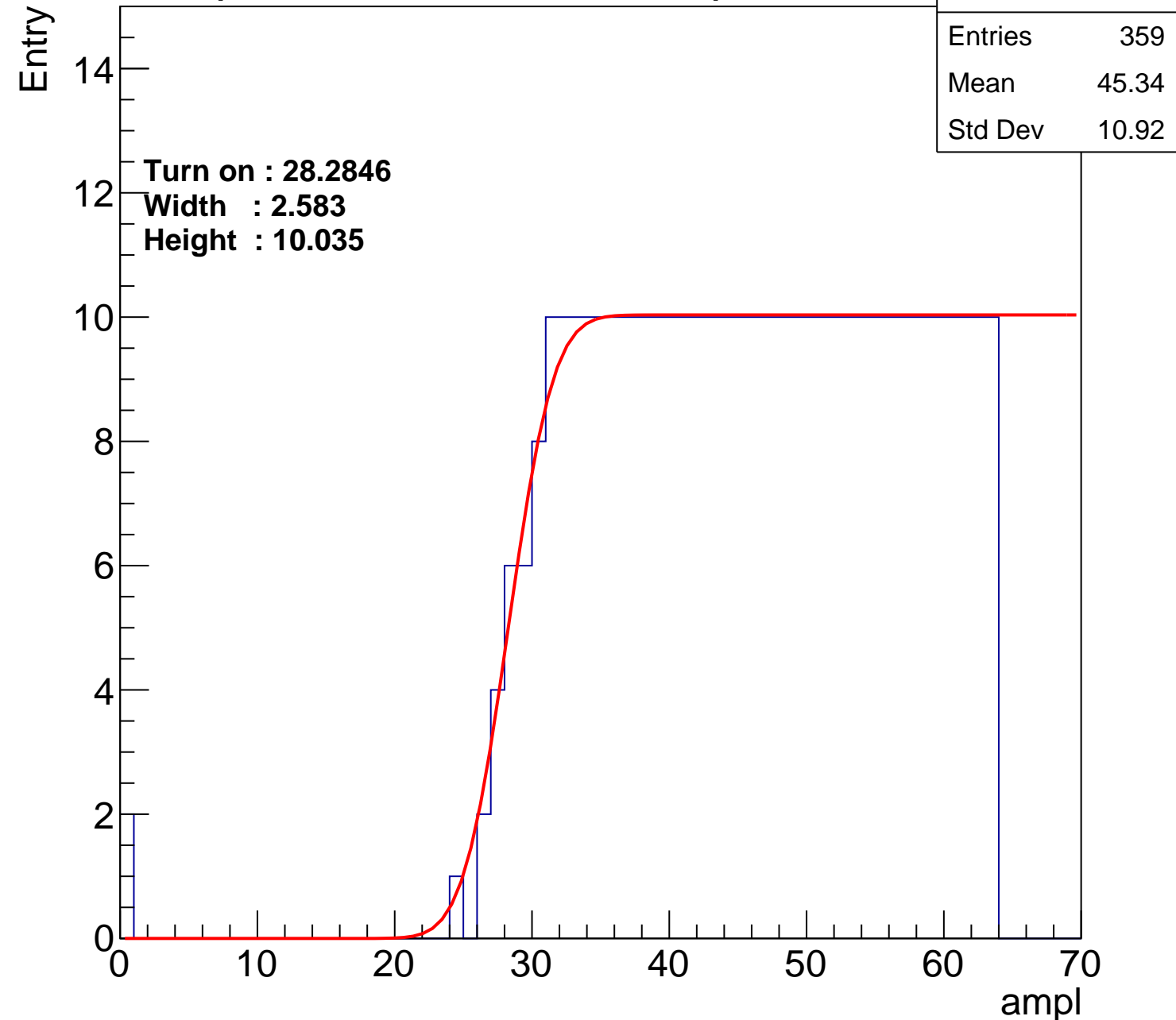
Width : 2.583

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch43

calib_packv5_042523_0143.root, FC#13, port D2

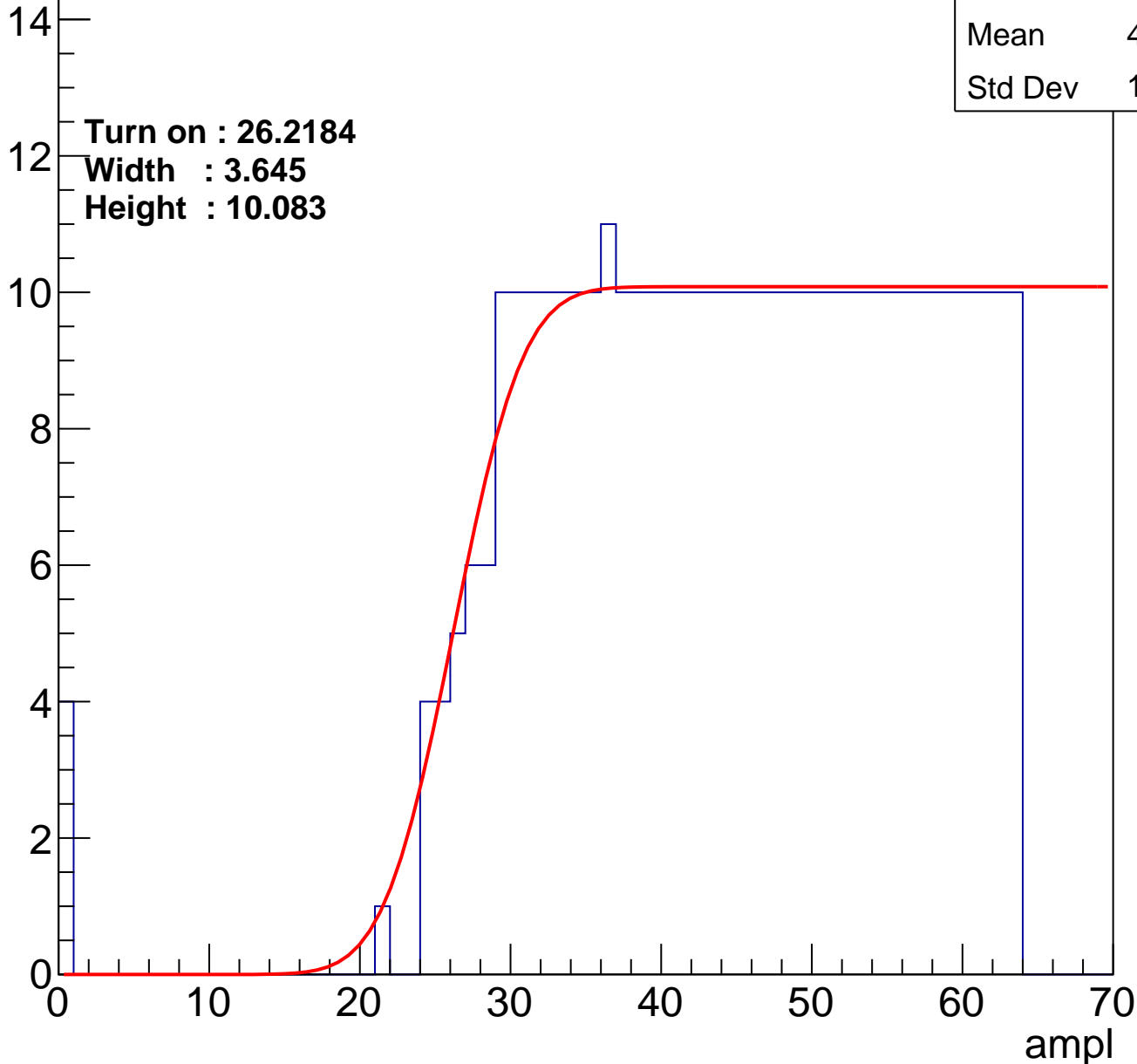
Entries	381
Mean	44.13
Std Dev	11.83

Turn on : 26.2184

Width : 3.645

Height : 10.083

Entry



B1L003S, U25-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.0472

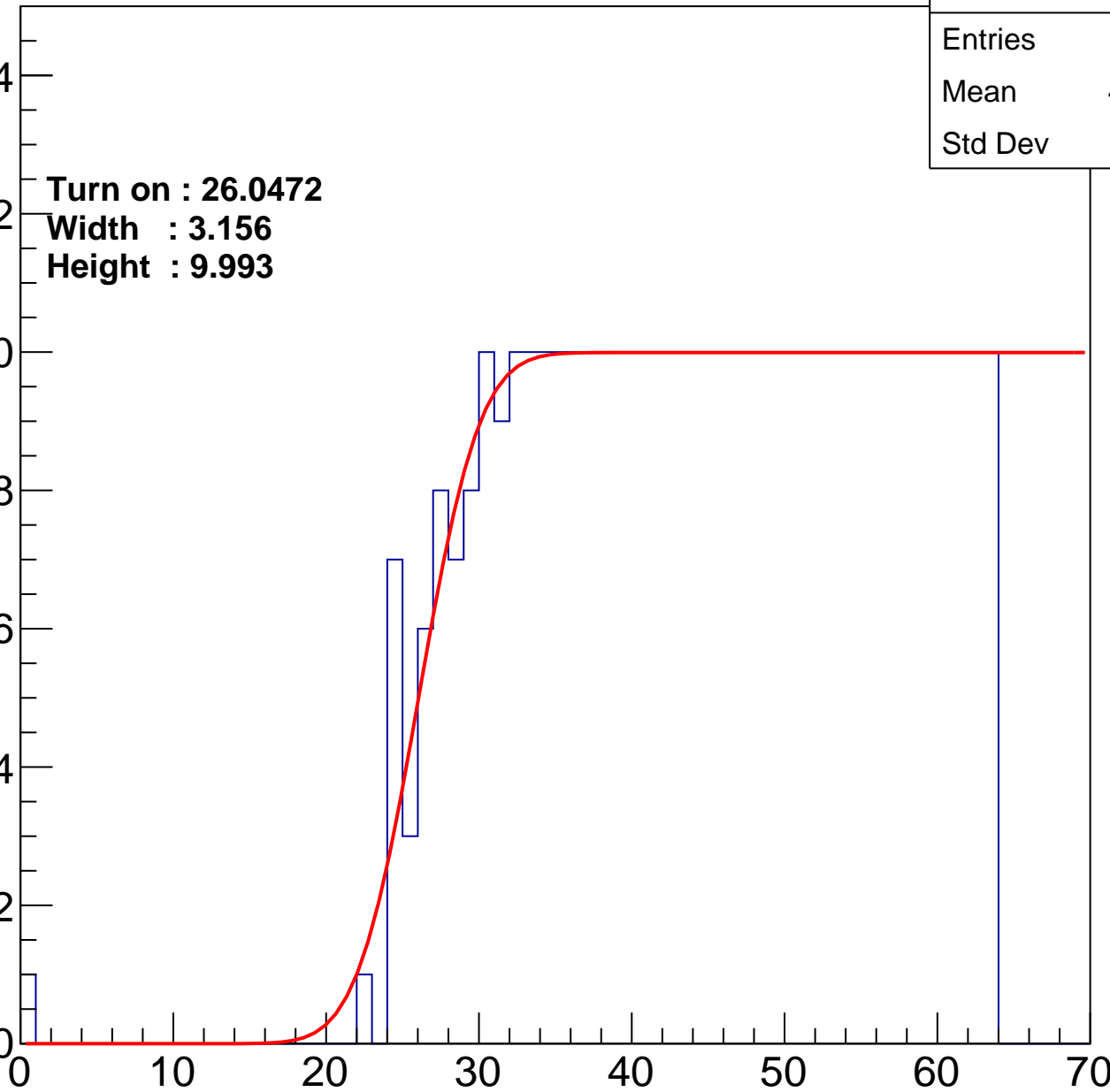
Width : 3.156

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	394
Mean	43.45
Std Dev	12.2

Turn on : 25.2483

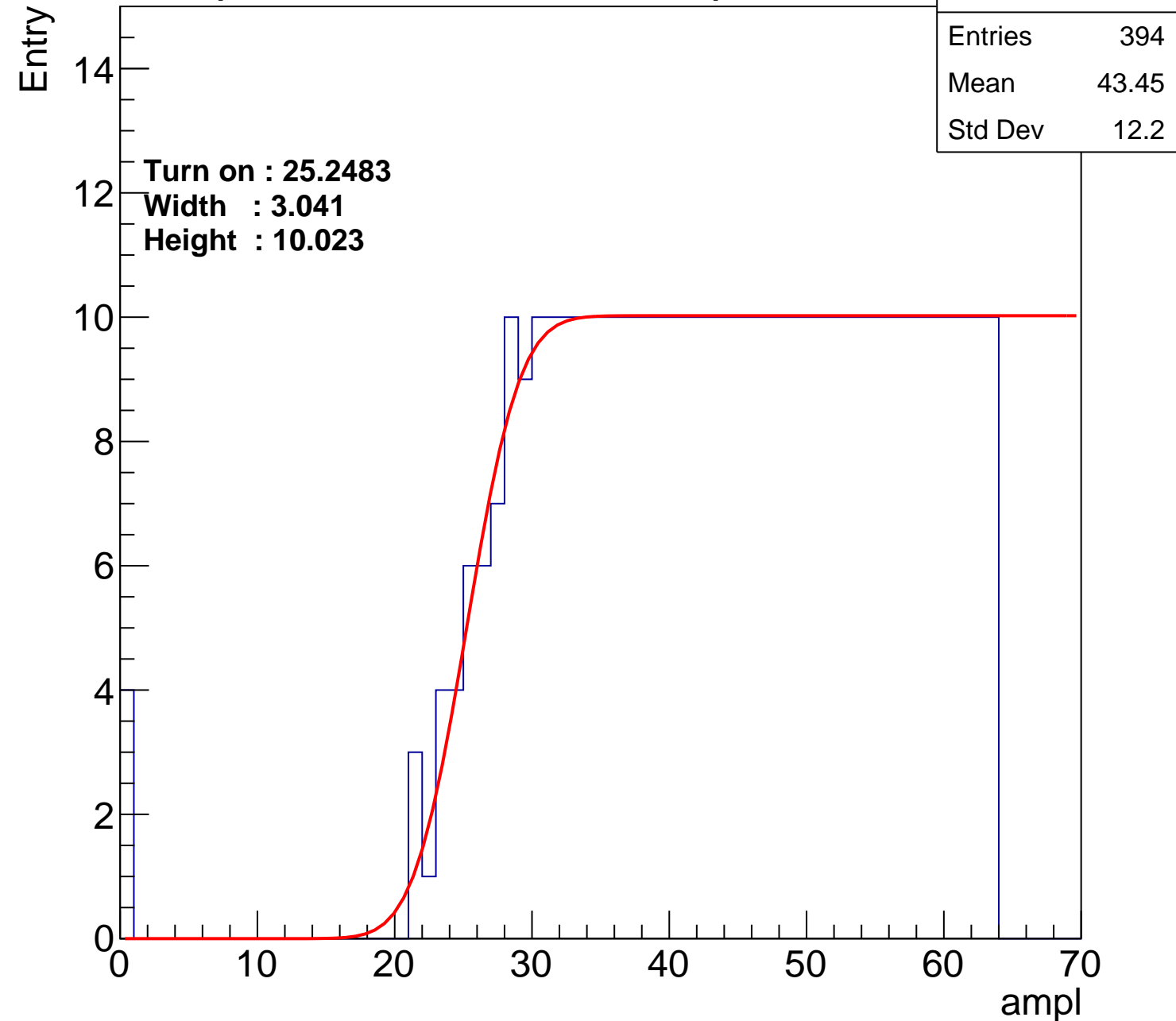
Width : 3.041

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.75
Std Dev	11.59

Turn on : 28.0331

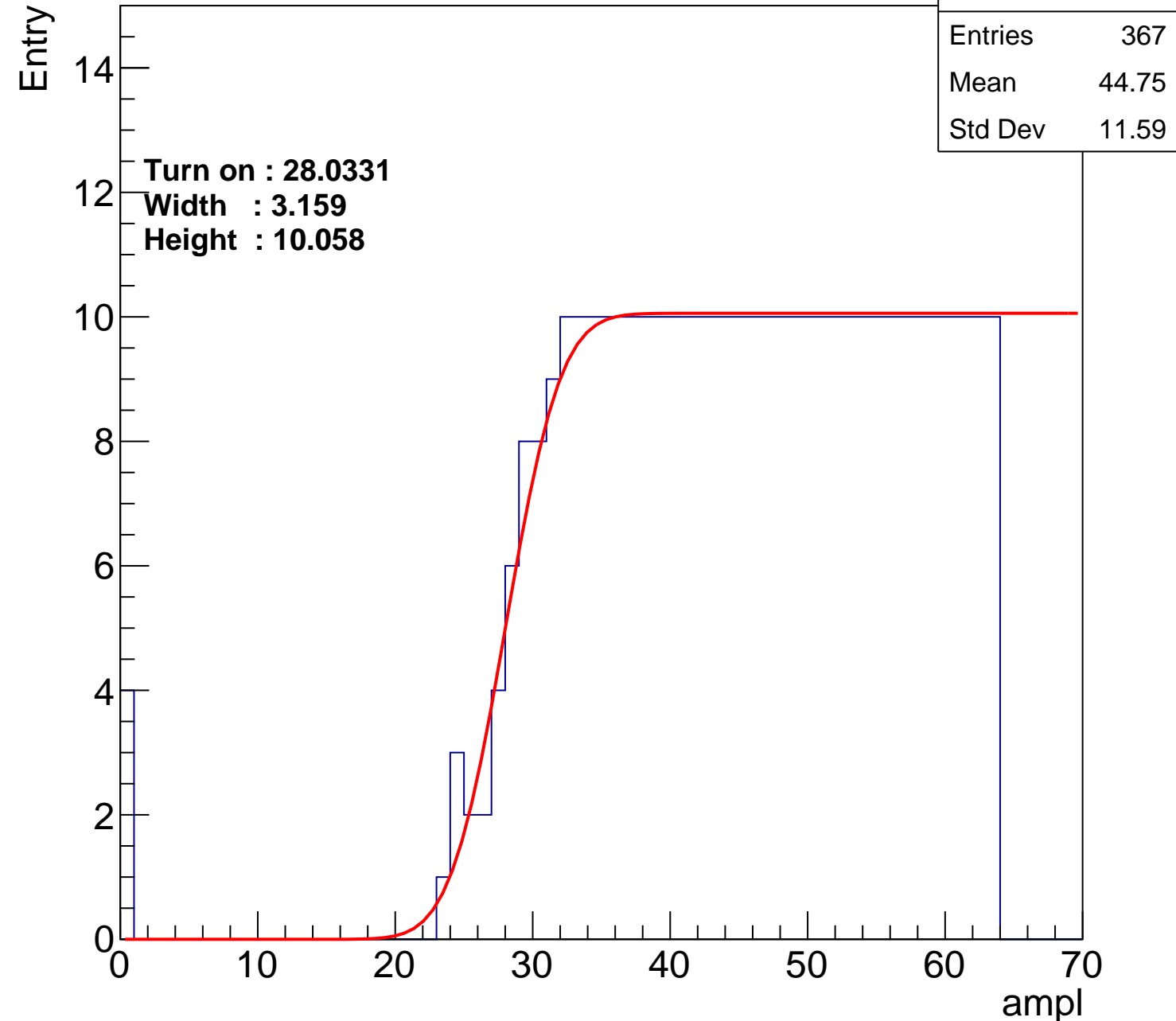
Width : 3.159

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch47

calib_packv5_042523_0143.root, FC#13, port D2

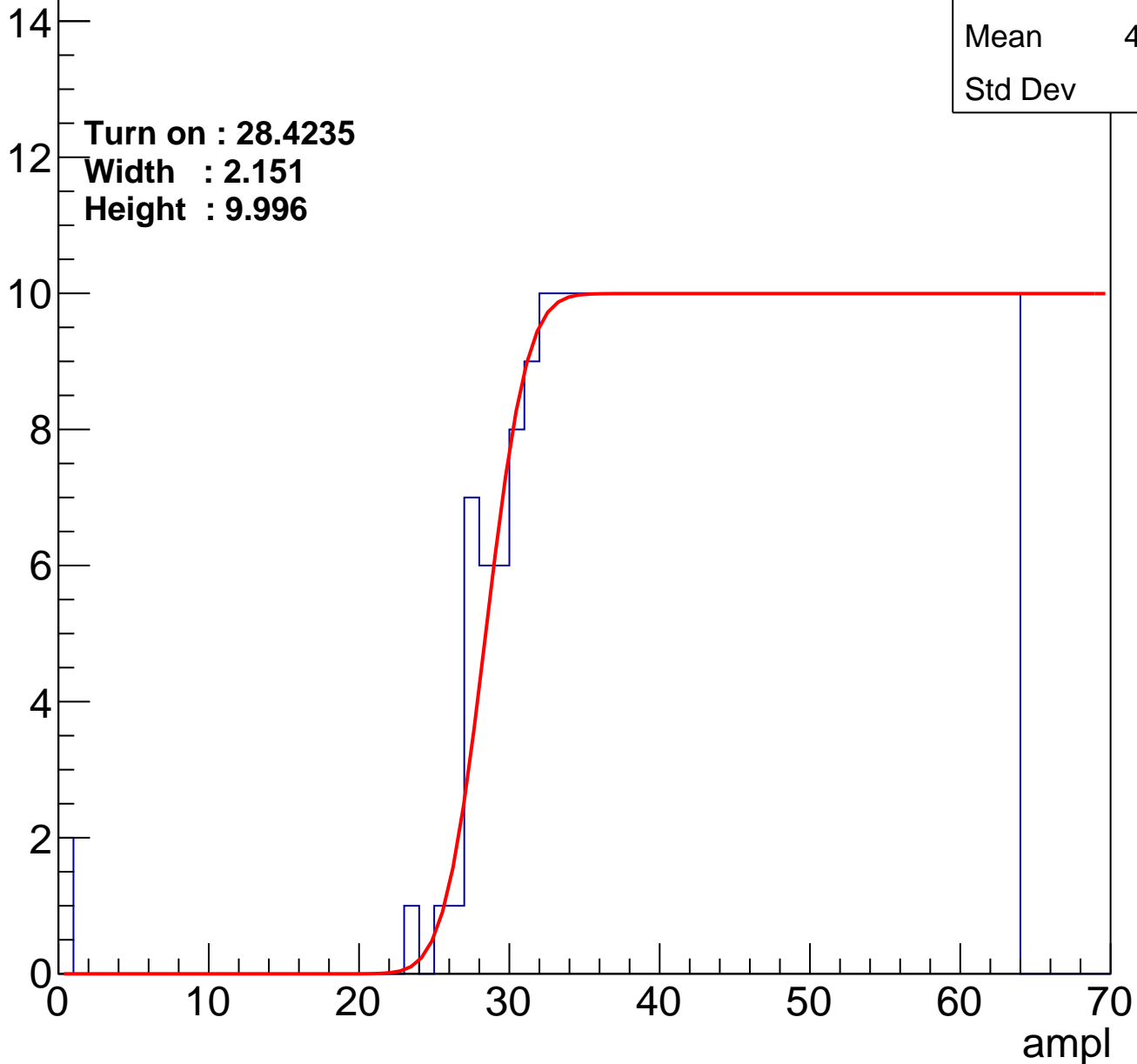
Entries	361
Mean	45.22
Std Dev	11

Turn on : 28.4235

Width : 2.151

Height : 9.996

Entry



B1L003S, U25-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.96
Std Dev	11.34

Turn on : 28.1141

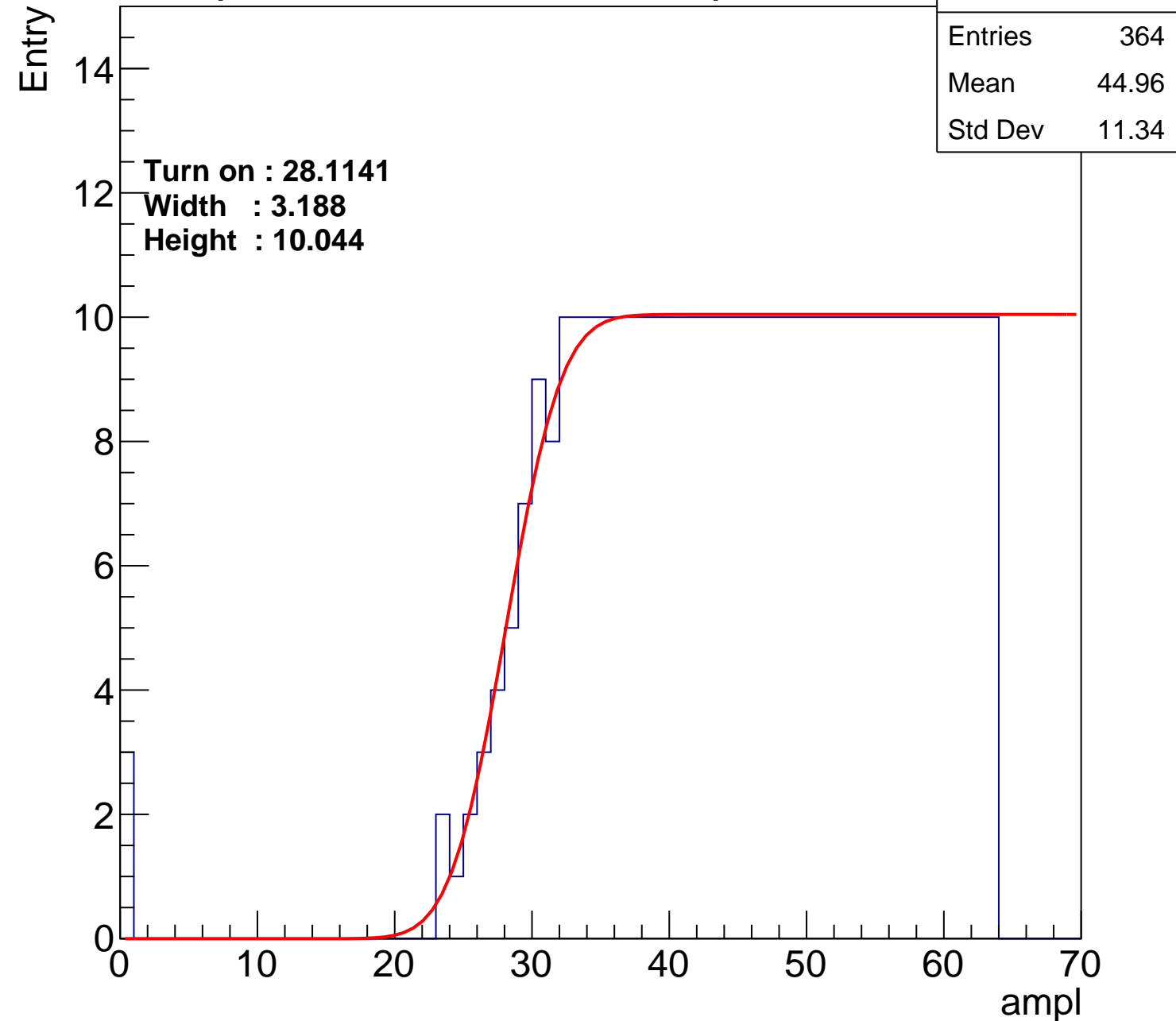
Width : 3.188

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	402
Mean	43.21
Std Dev	12.03

Turn on : 24.0798

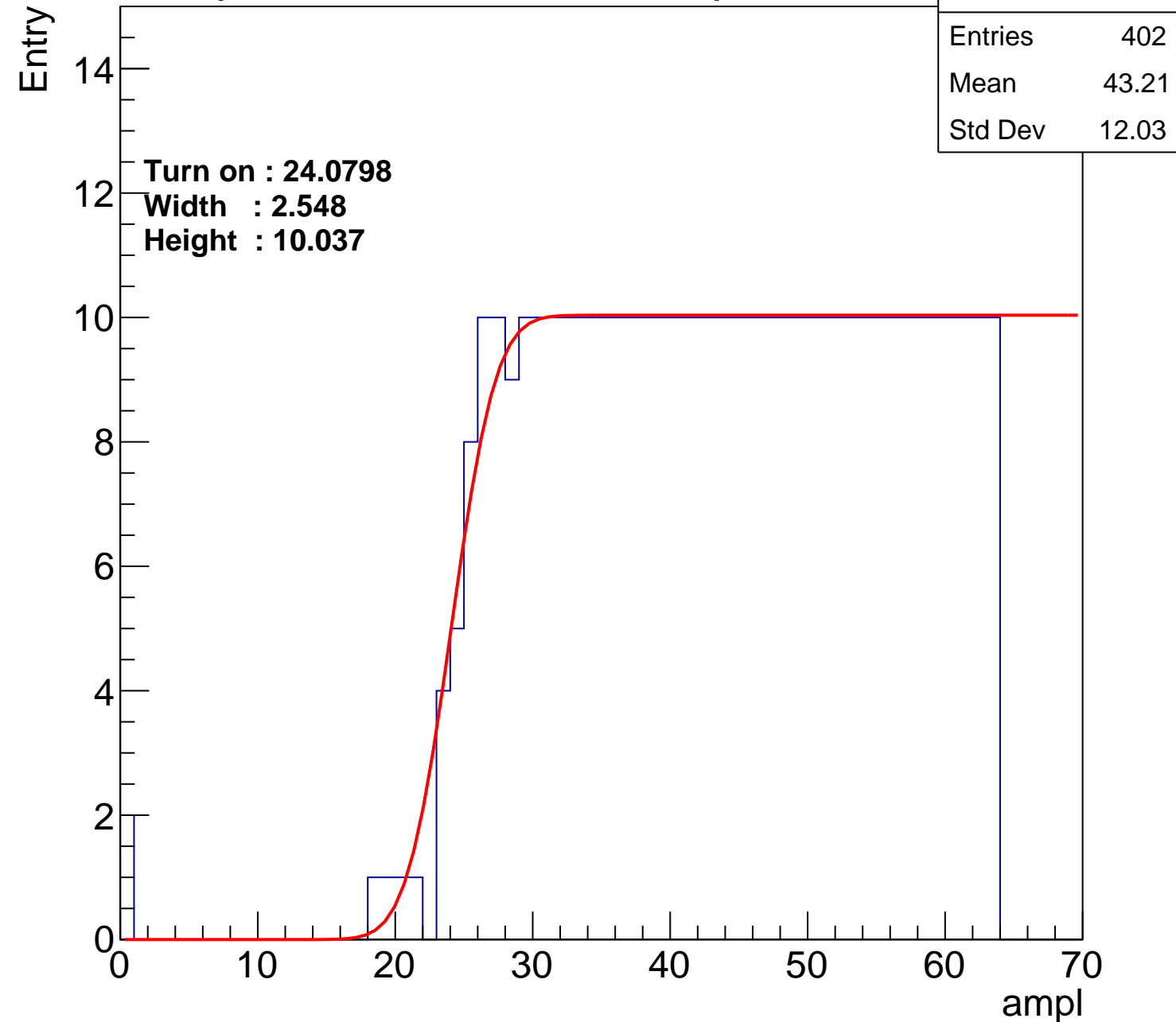
Width : 2.548

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch50

calib_packv5_042523_0143.root, FC#13, port D2

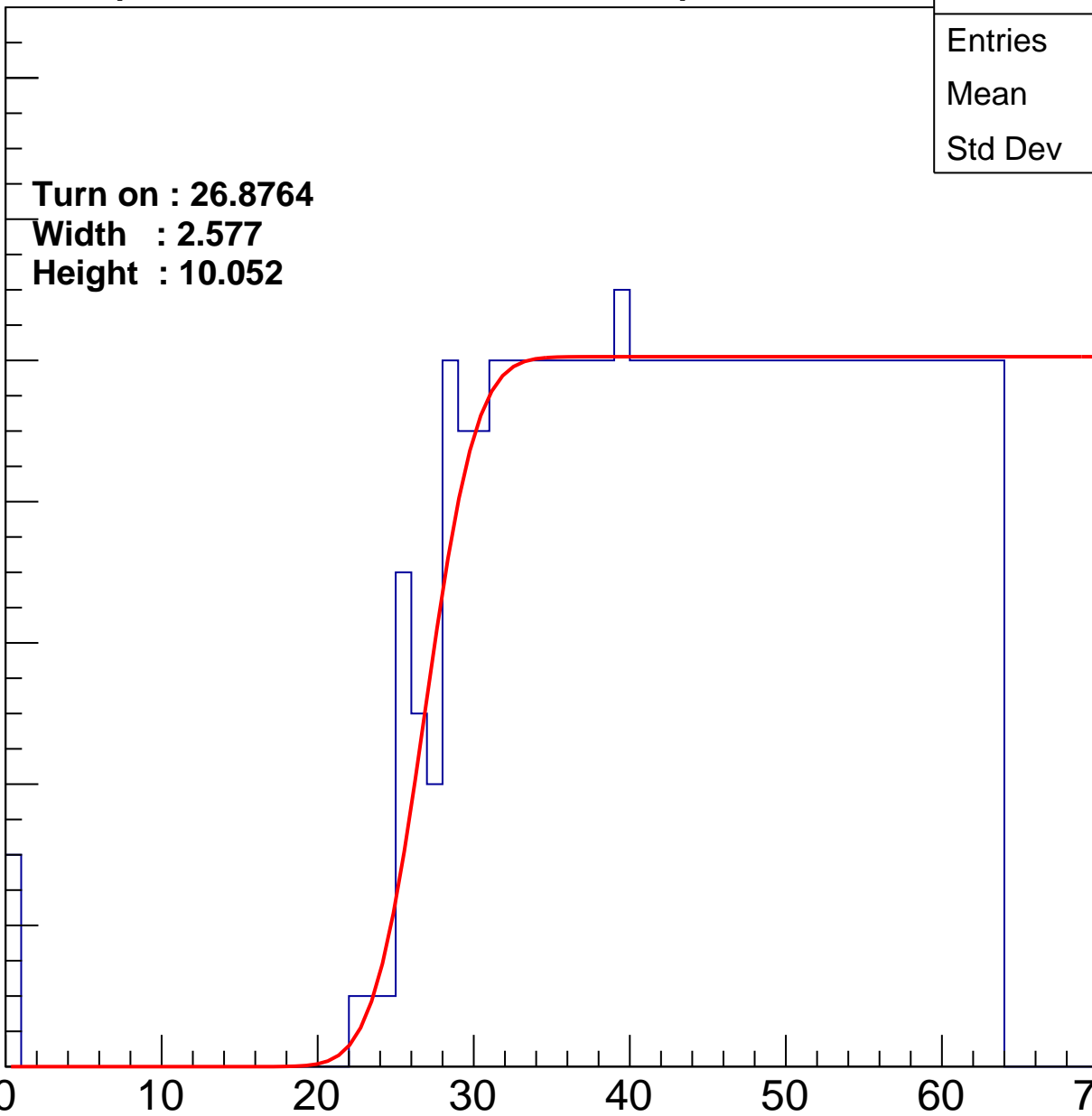
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8764
Width : 2.577
Height : 10.052

Entries	381
Mean	44.2
Std Dev	11.65

ampl



B1L003S, U25-ch51

calib_packv5_042523_0143.root, FC#13, port D2

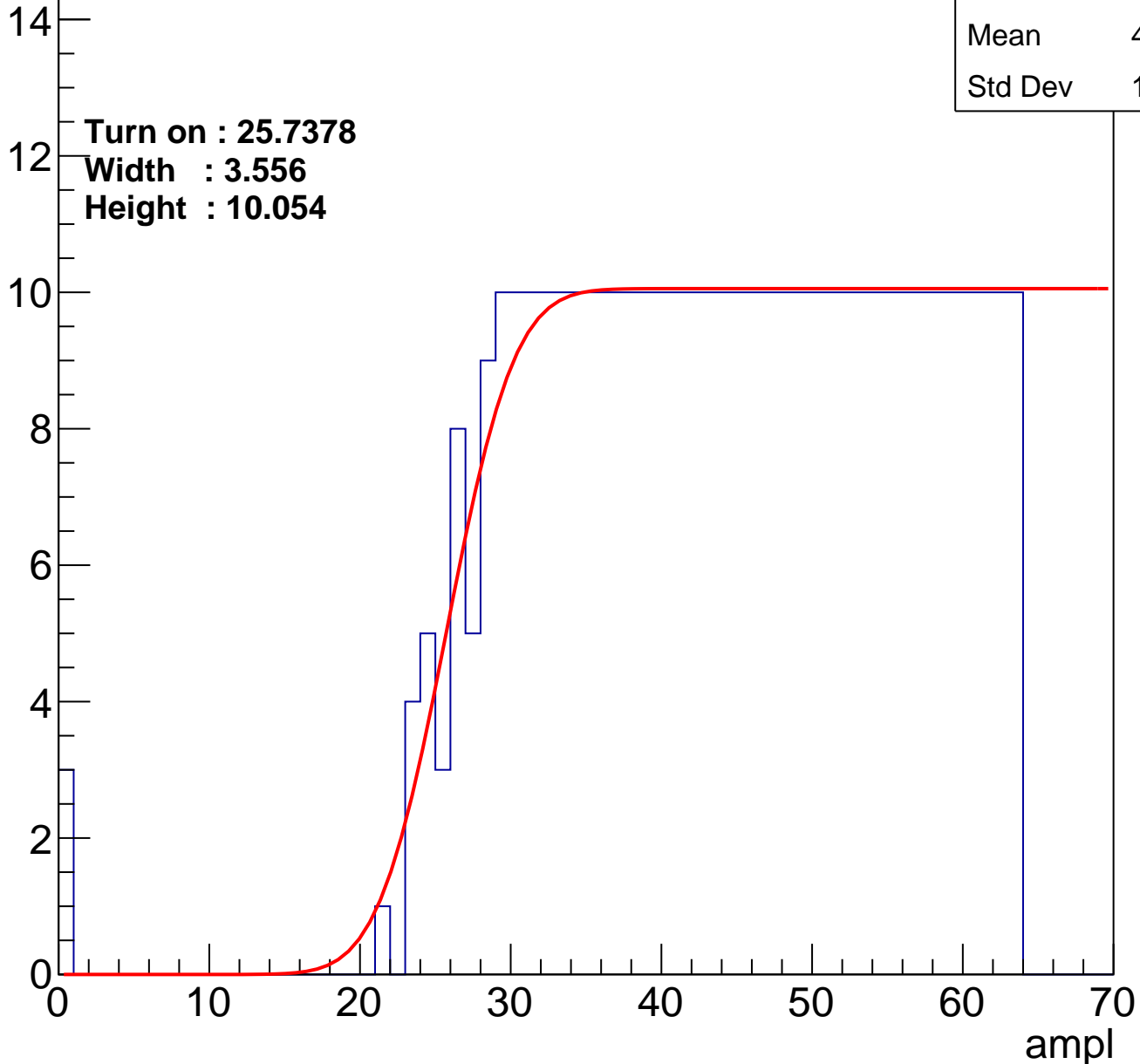
Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 25.7378

Width : 3.556

Height : 10.054

Entry



B1L003S, U25-ch52

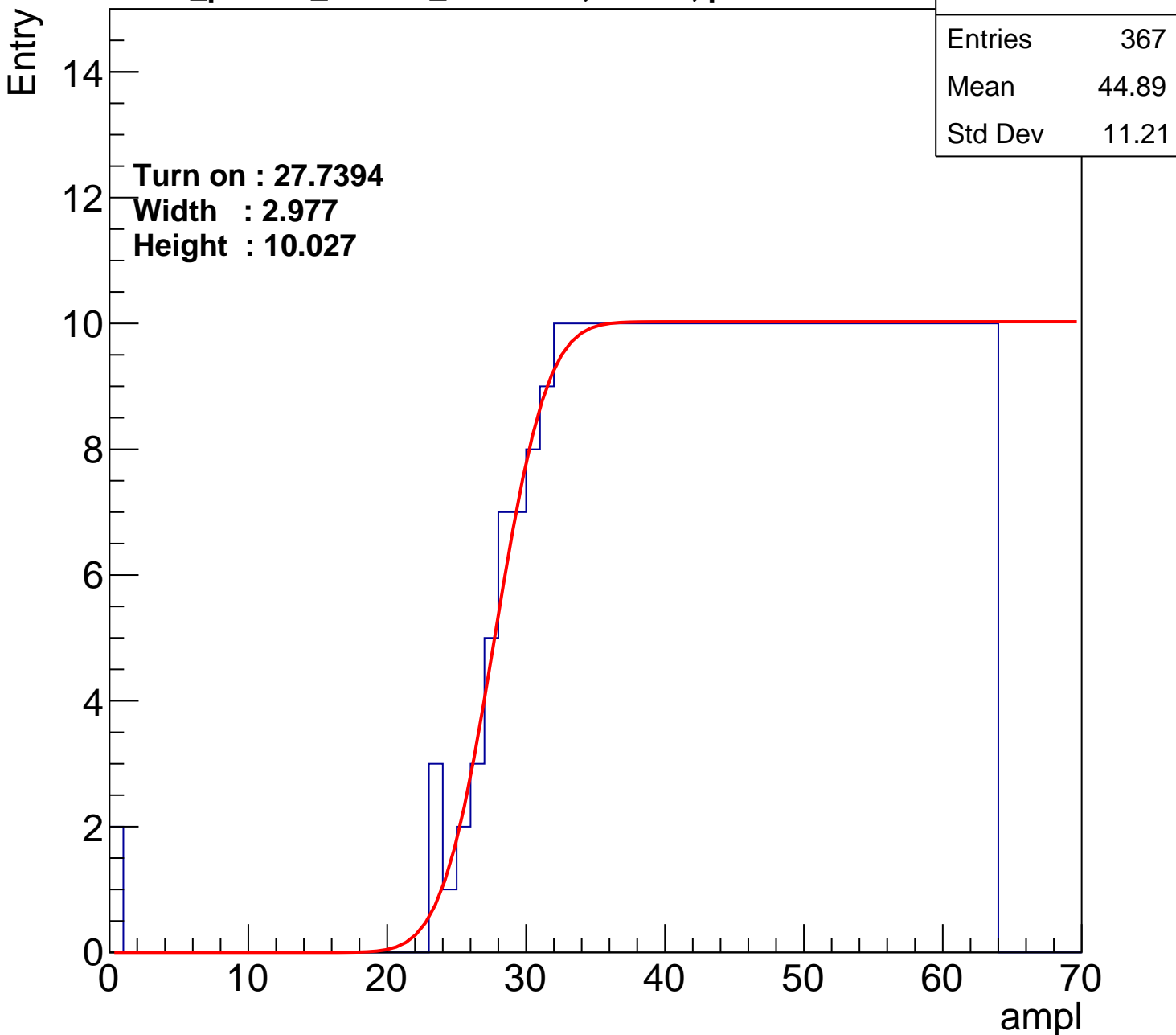
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.7394

Width : 2.977

Height : 10.027

Entries	367
Mean	44.89
Std Dev	11.21



B1L003S, U25-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 26.0839

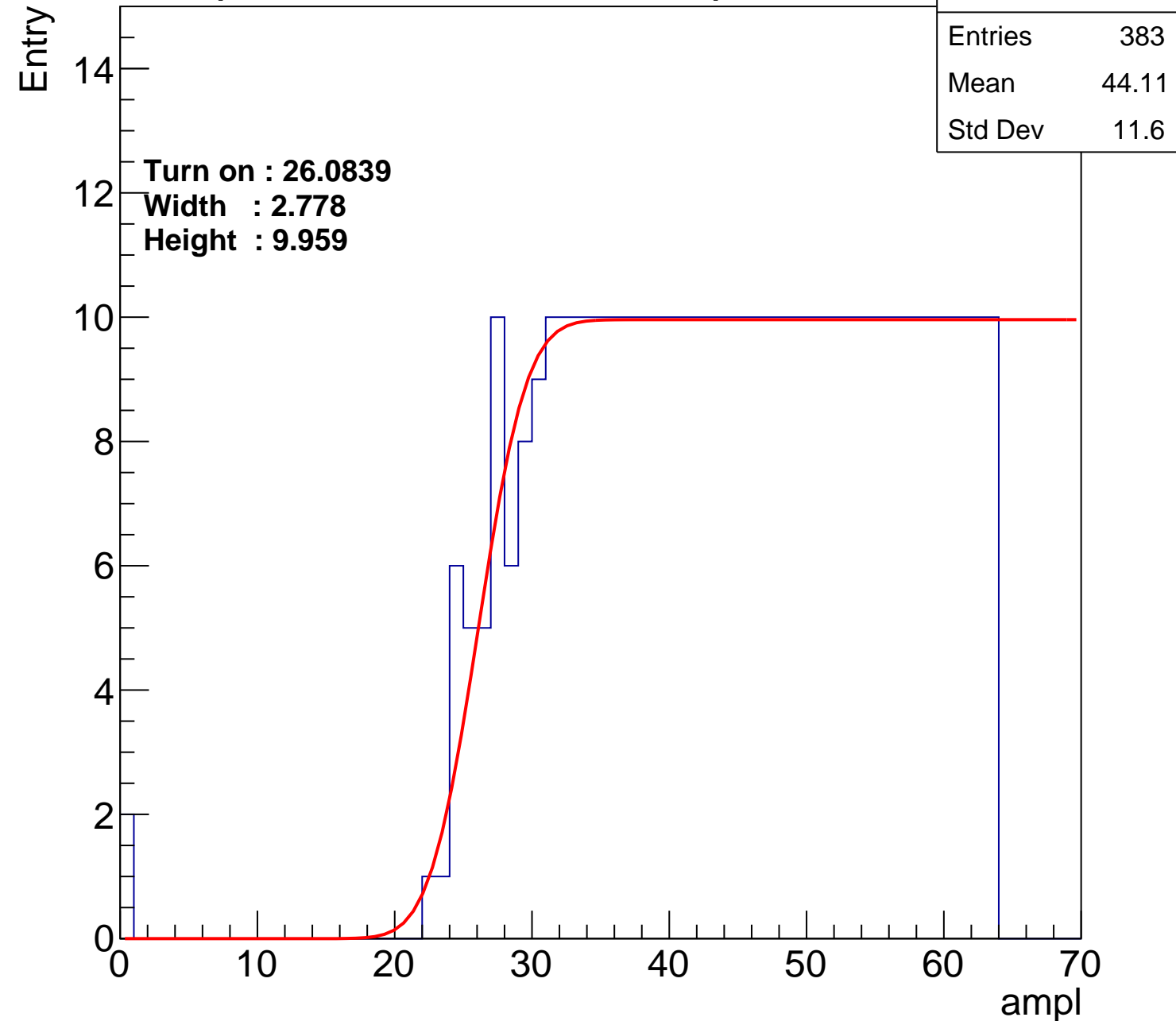
Width : 2.778

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.56
Std Dev	10.88

Turn on : 28.9982

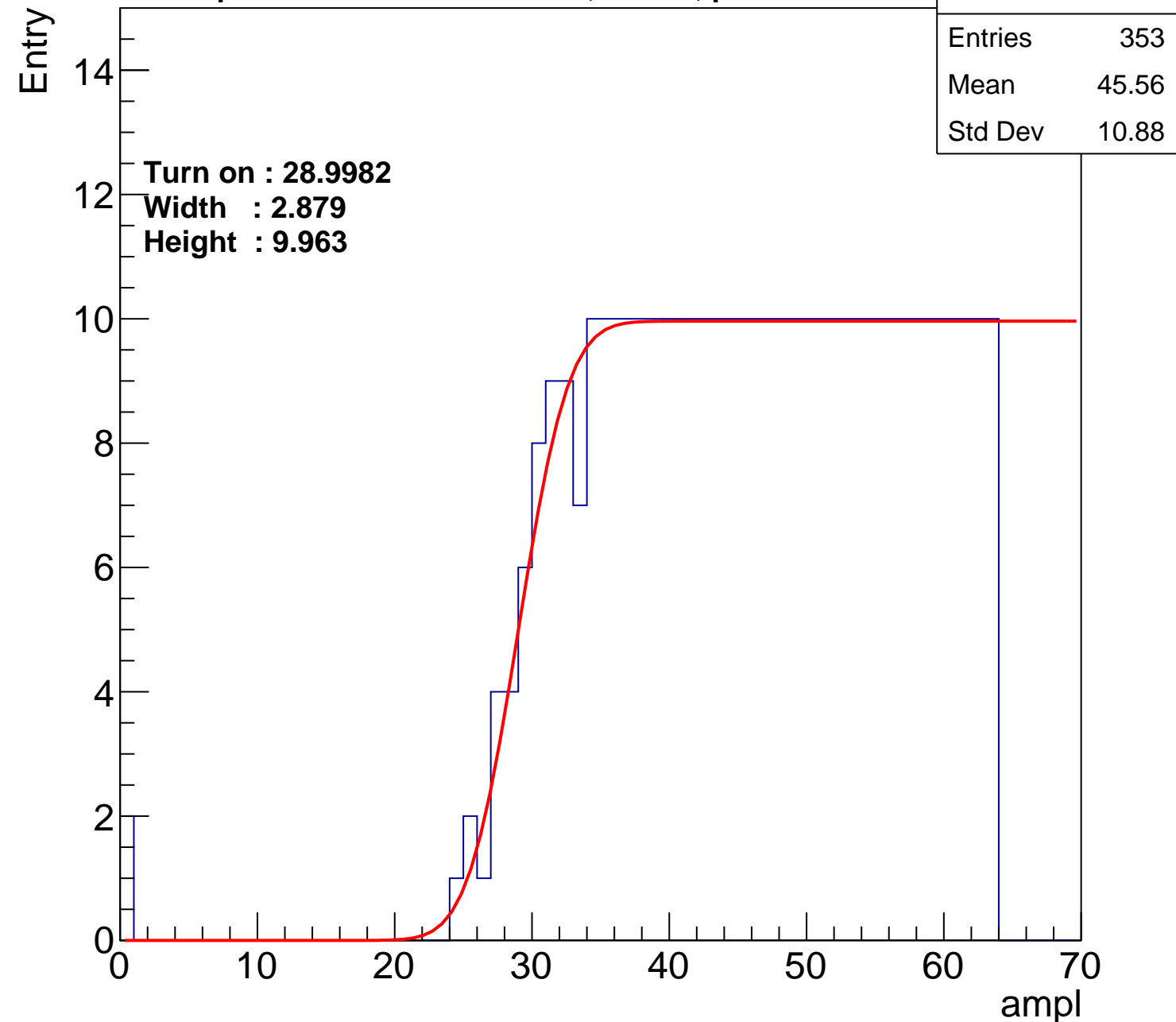
Width : 2.879

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.18
Std Dev	11.21

Turn on : 28.7683

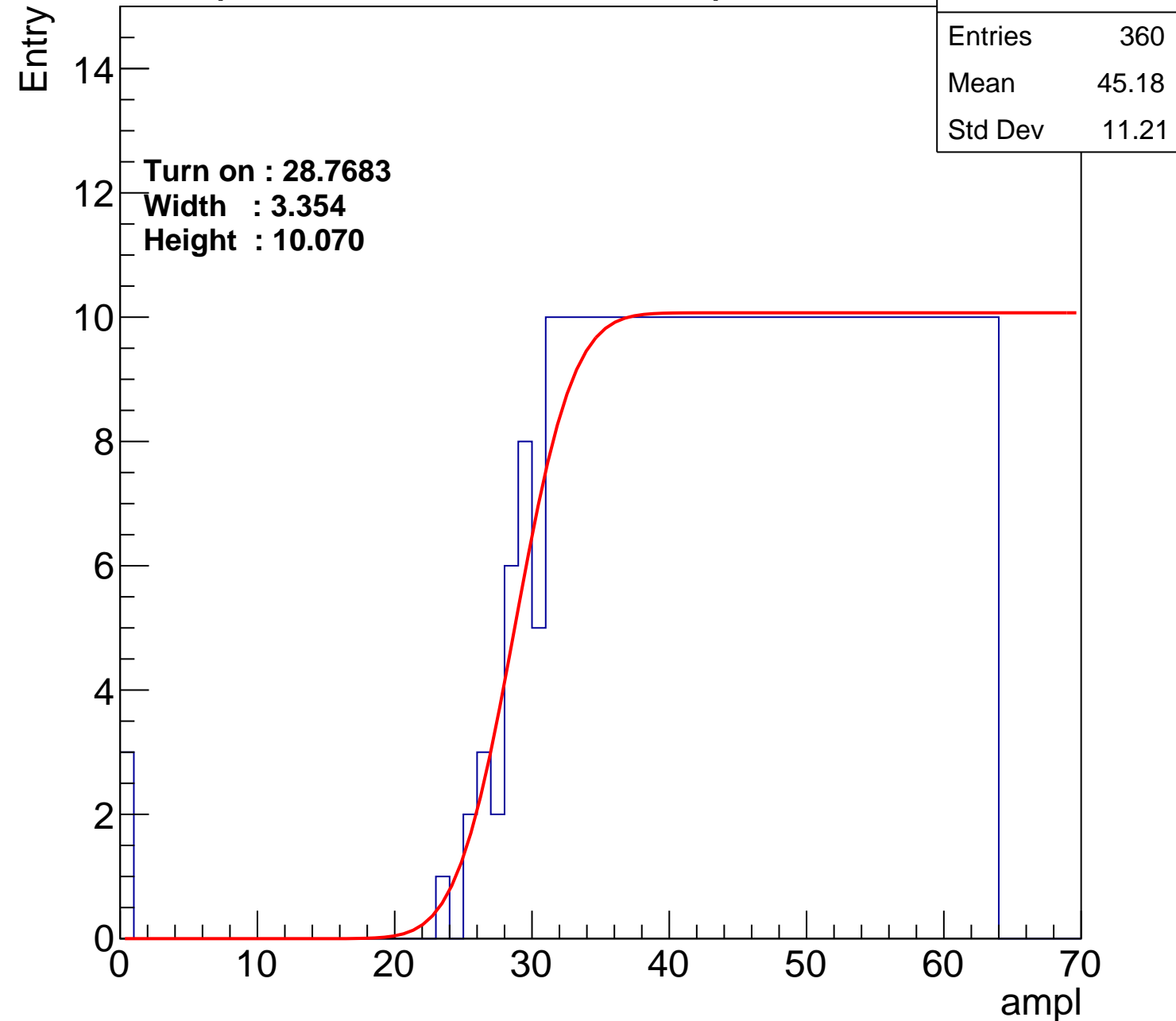
Width : 3.354

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch56

calib_packv5_042523_0143.root, FC#13, port D2

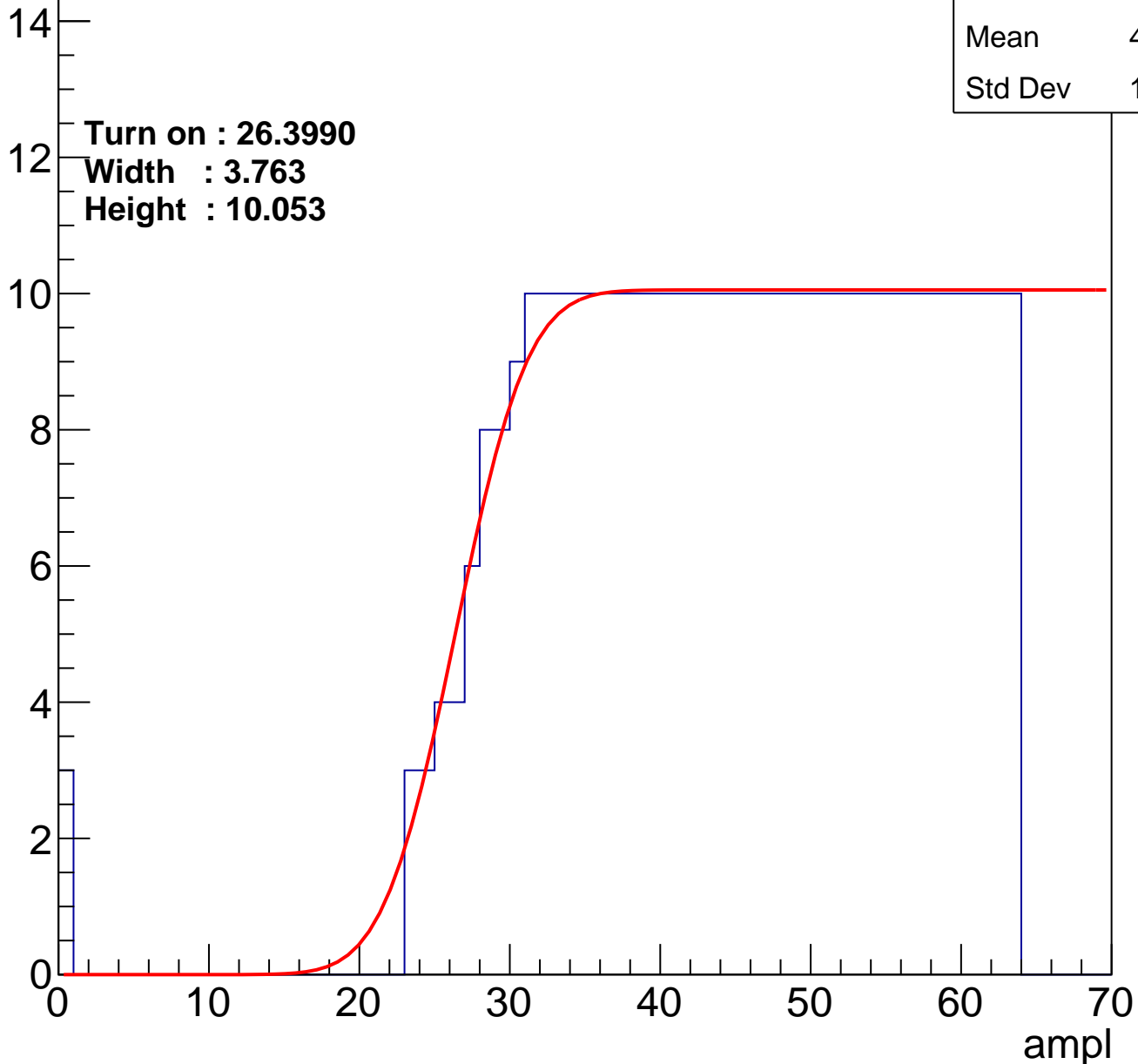
Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.3990

Width : 3.763

Height : 10.053

Entry



B1L003S, U25-ch57

calib_packv5_042523_0143.root, FC#13, port D2

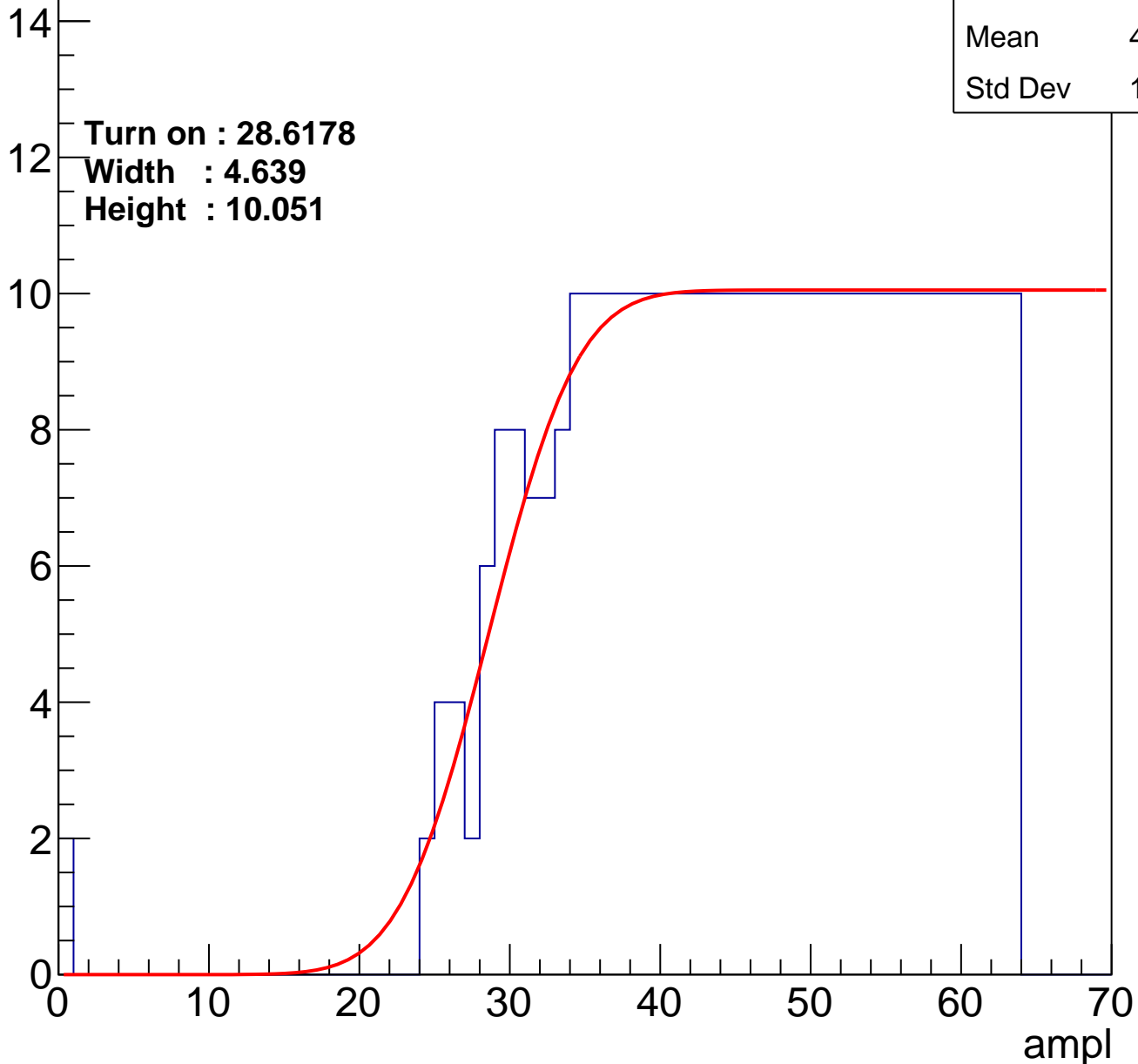
Entries	358
Mean	45.25
Std Dev	11.09

Turn on : 28.6178

Width : 4.639

Height : 10.051

Entry



B1L003S, U25-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.7
Std Dev	11.15

Turn on : 26.9842

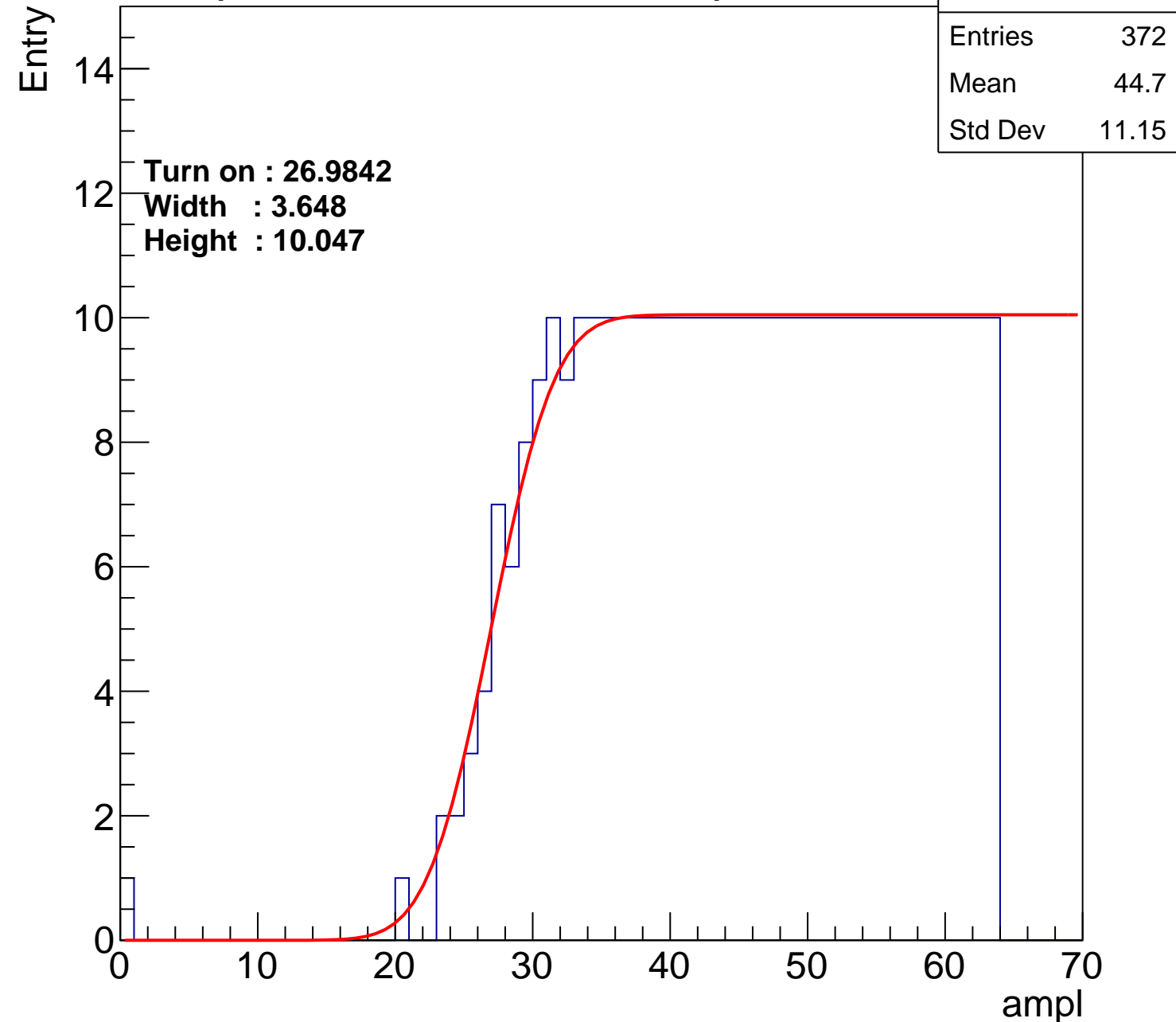
Width : 3.648

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.86
Std Dev	11

Turn on : 26.8730

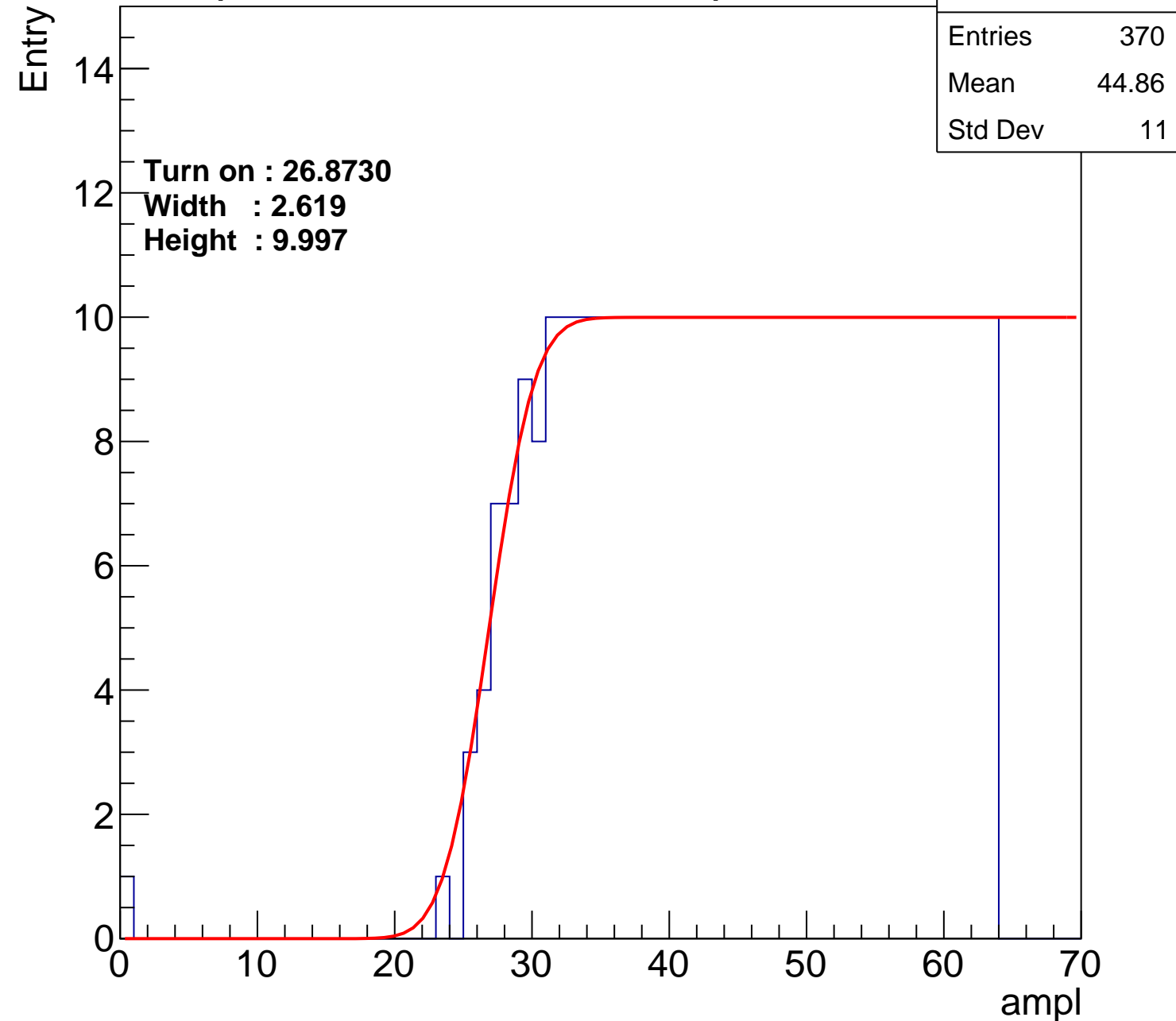
Width : 2.619

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.6399

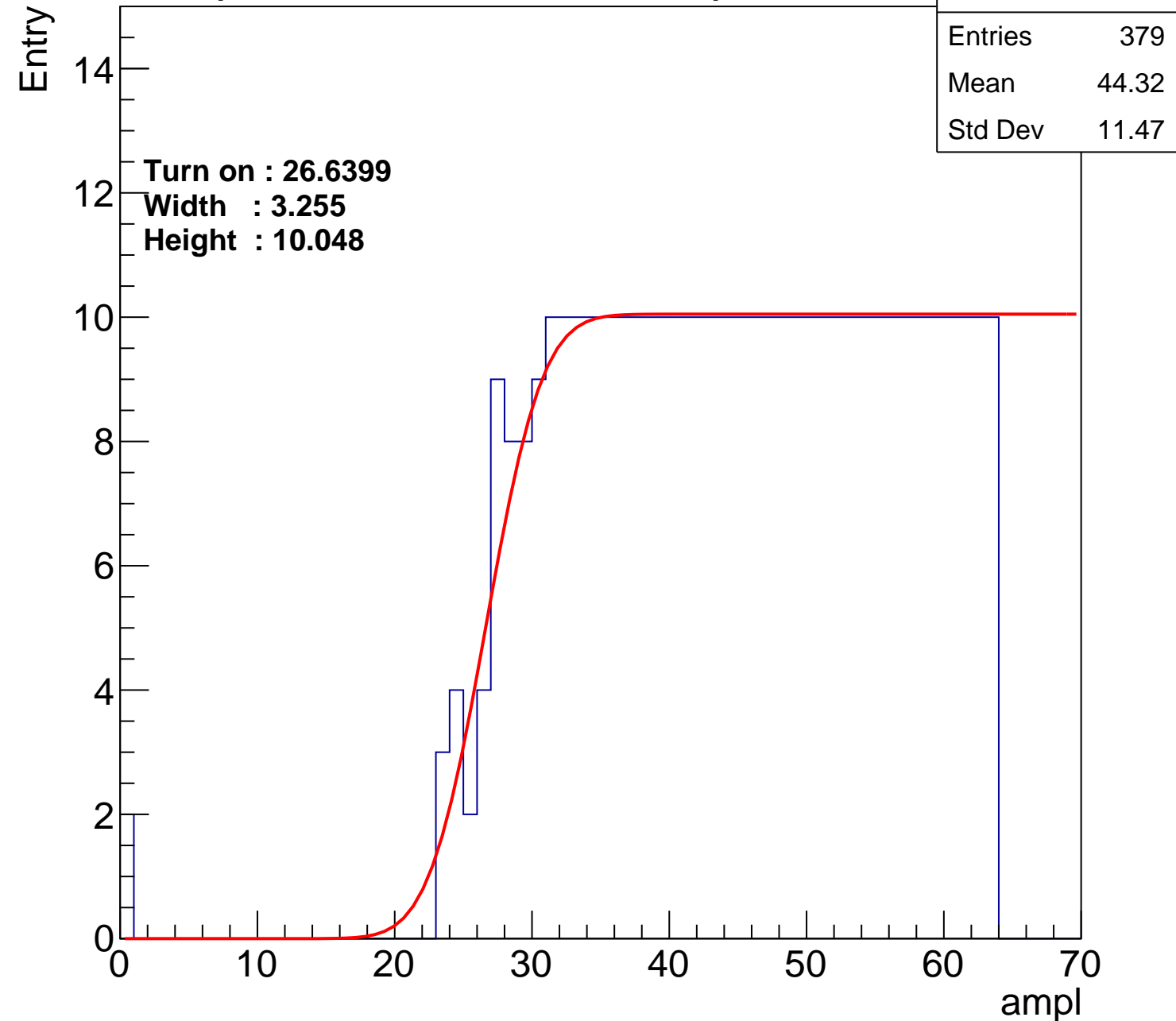
Width : 3.255

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 25.7997

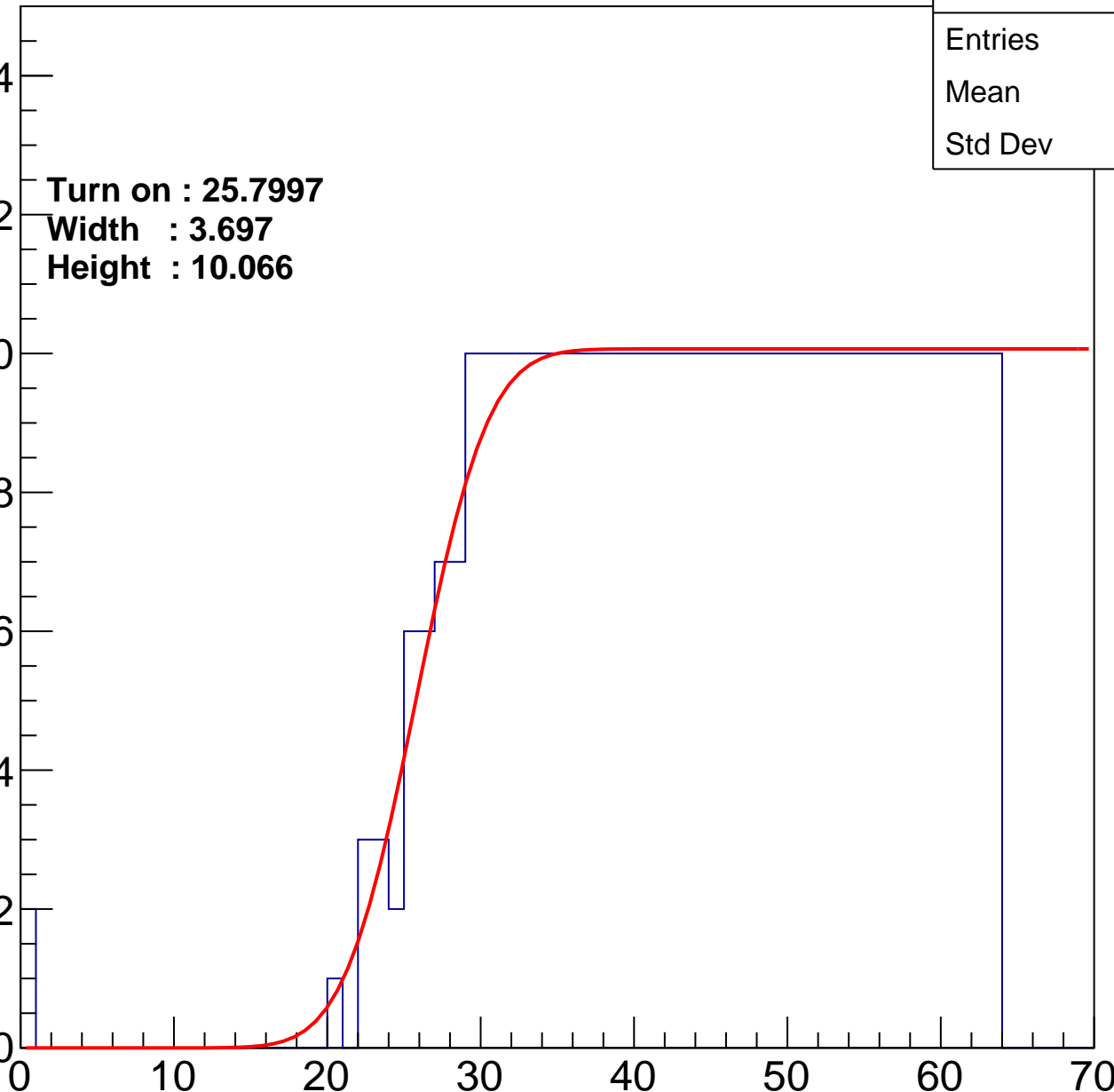
Width : 3.697

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	43.95
Std Dev	12.07

Turn on : 26.3185

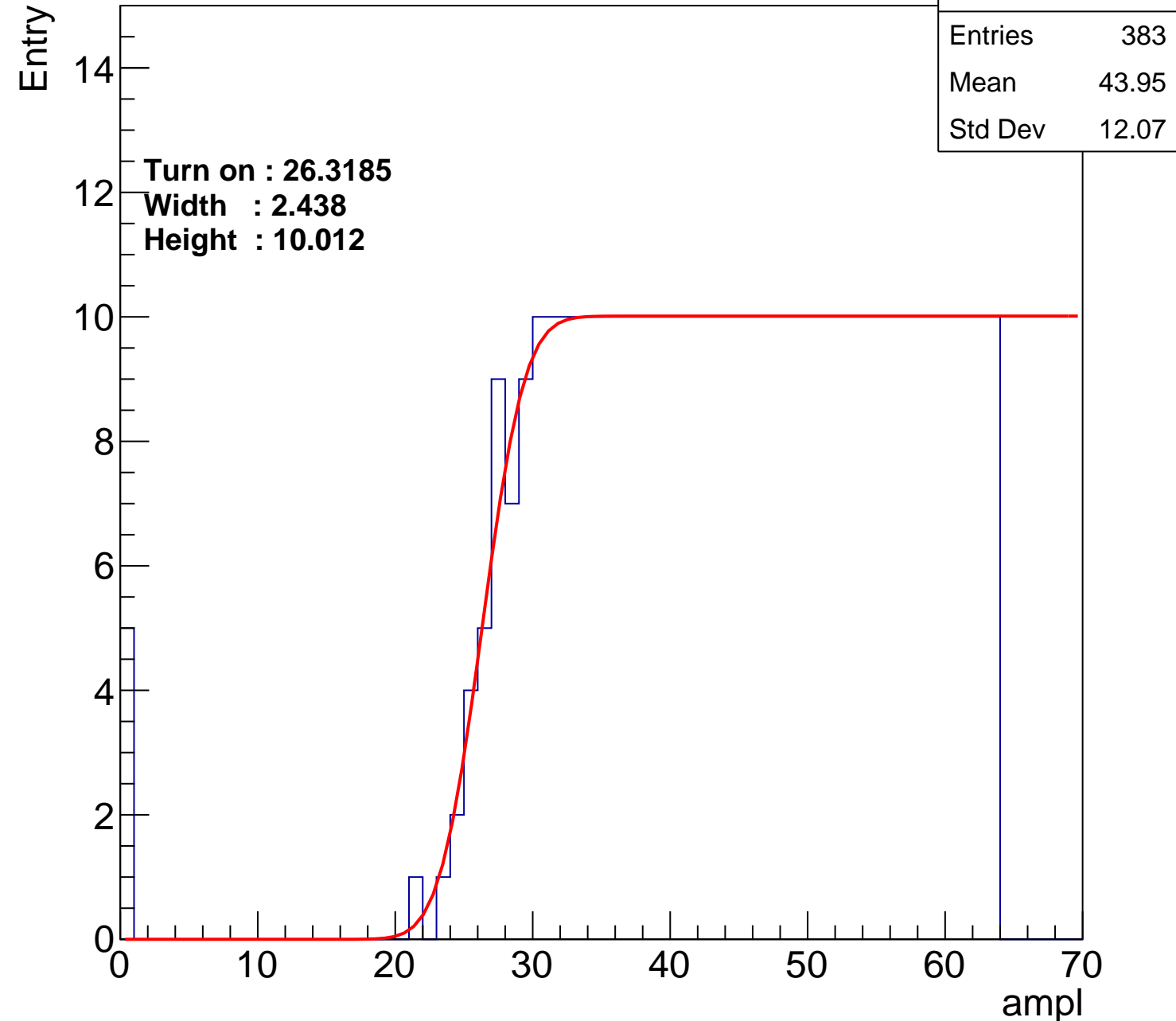
Width : 2.438

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.46
Std Dev	11.08

Turn on : 28.7725

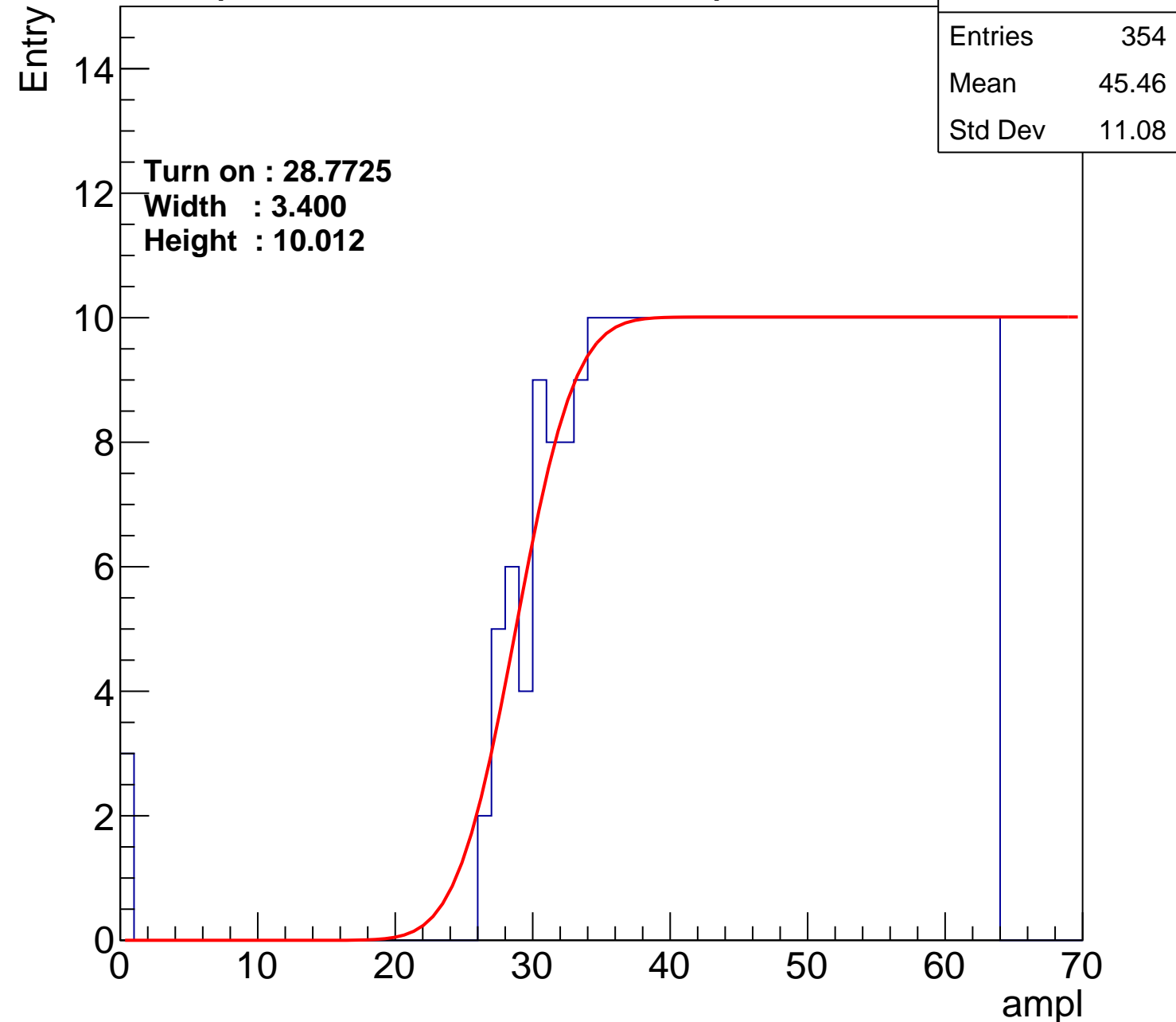
Width : 3.400

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch64

calib_packv5_042523_0143.root, FC#13, port D2

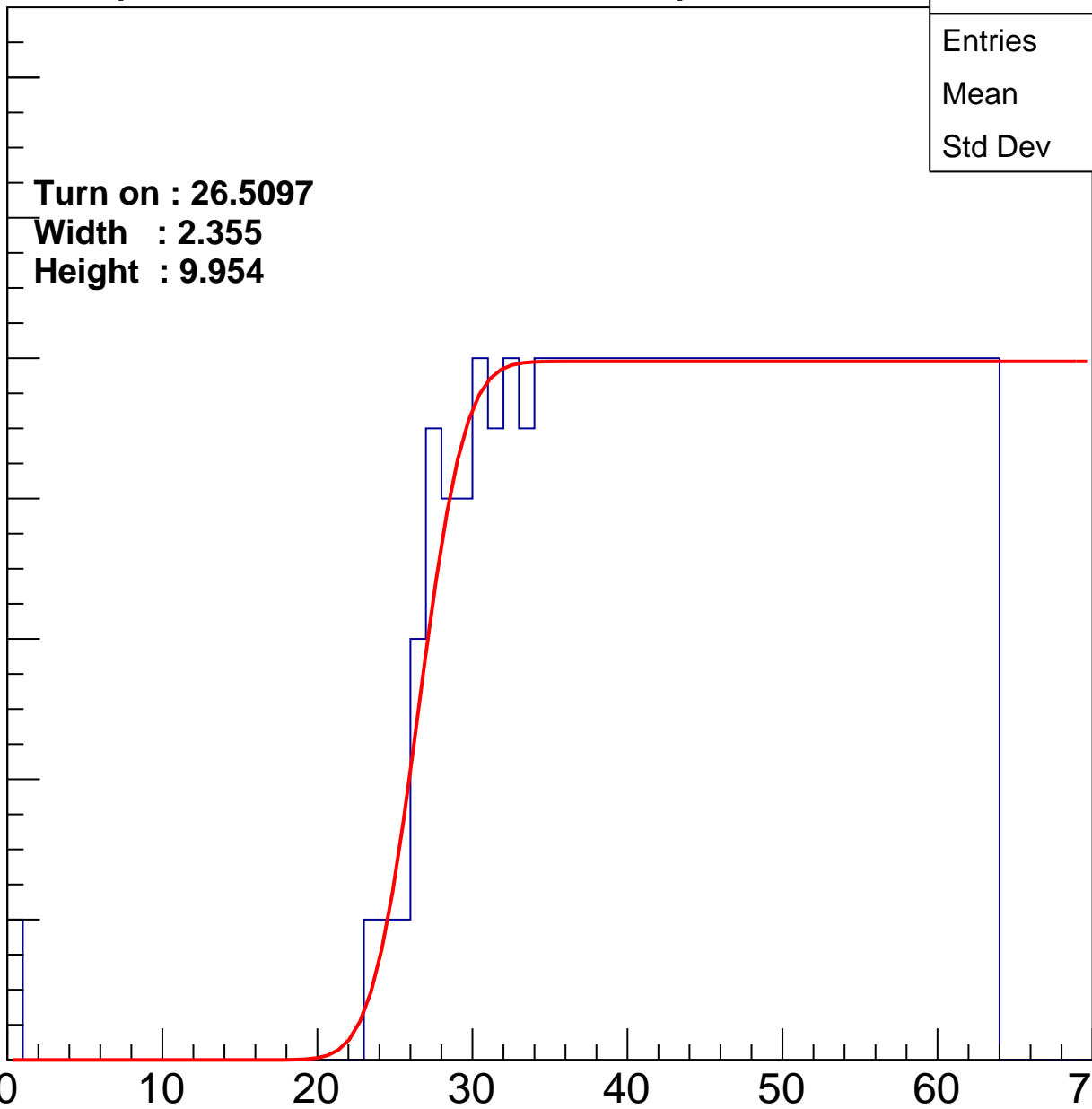
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5097
Width : 2.355
Height : 9.954

Entries	377
Mean	44.42
Std Dev	11.42

ampl



B1L003S, U25-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.65
Std Dev	11.36

Turn on : 26.7500

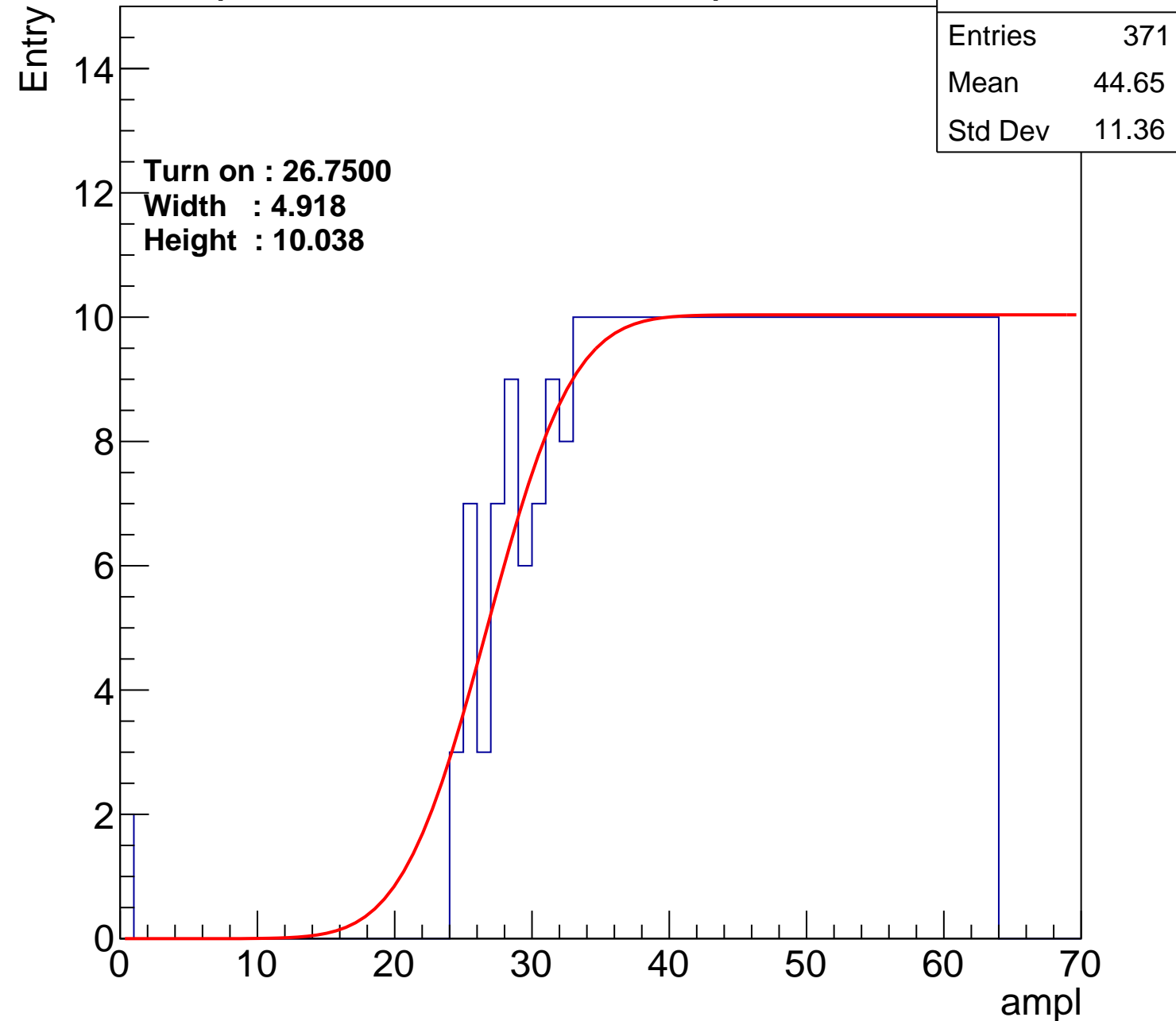
Width : 4.918

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.67
Std Dev	11.48

Turn on : 27.7699

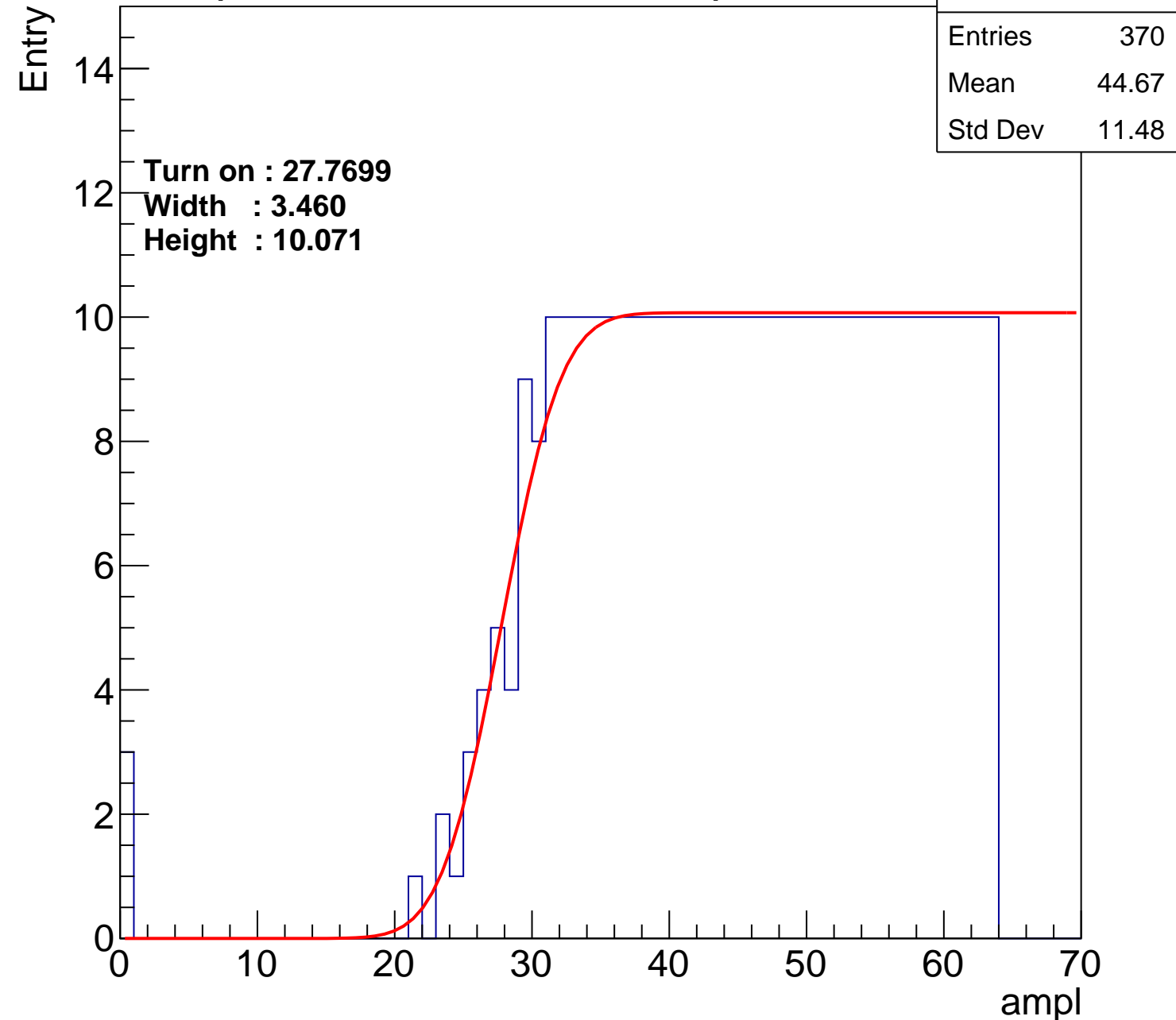
Width : 3.460

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch67

calib_packv5_042523_0143.root, FC#13, port D2

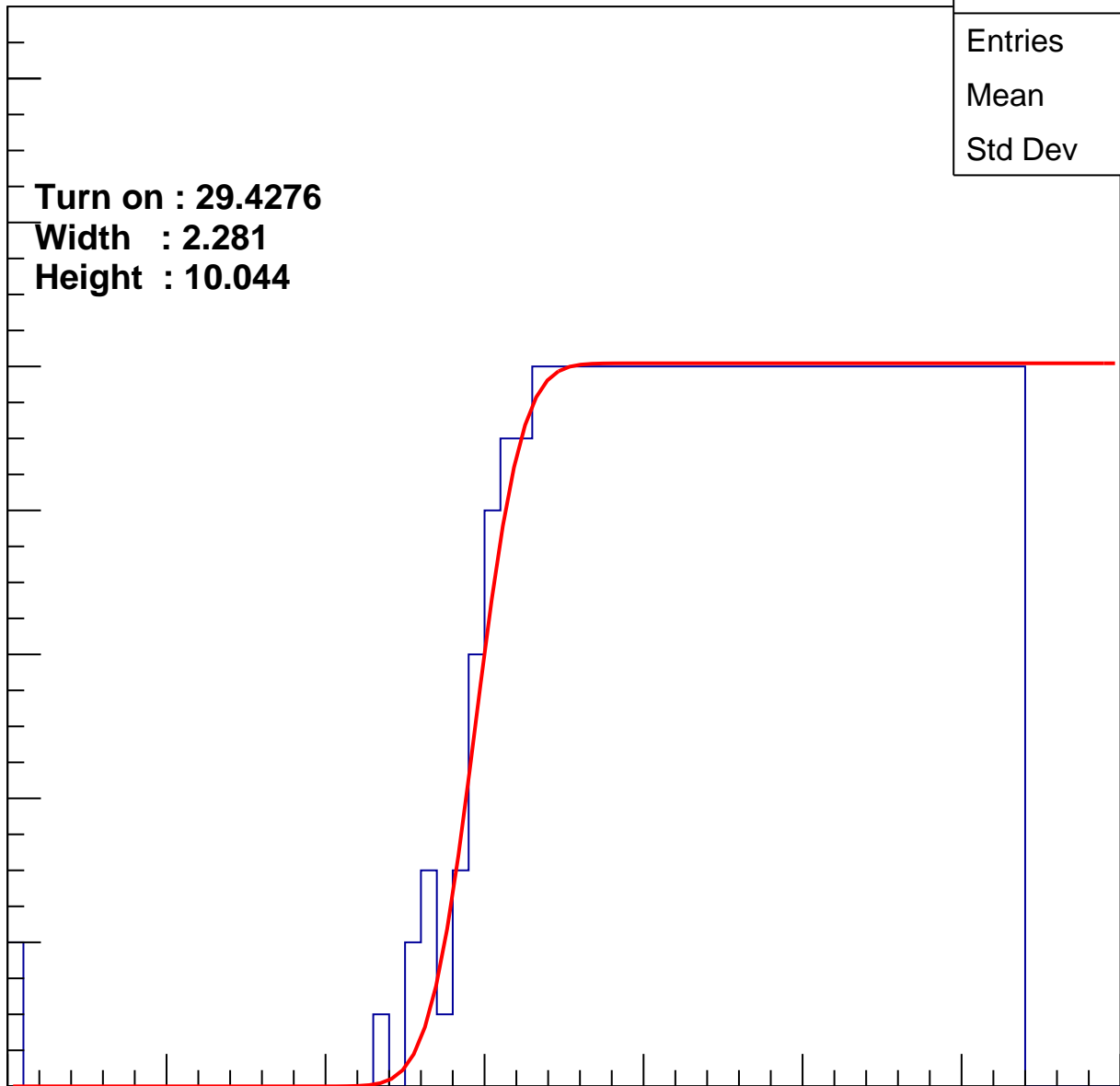
Entry

14
12
10
8
6
4
2
0

Turn on : 29.4276
Width : 2.281
Height : 10.044

Entries	354
Mean	45.55
Std Dev	10.86

ampl



B1L003S, U25-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.57
Std Dev	12.23

Turn on : 25.6171

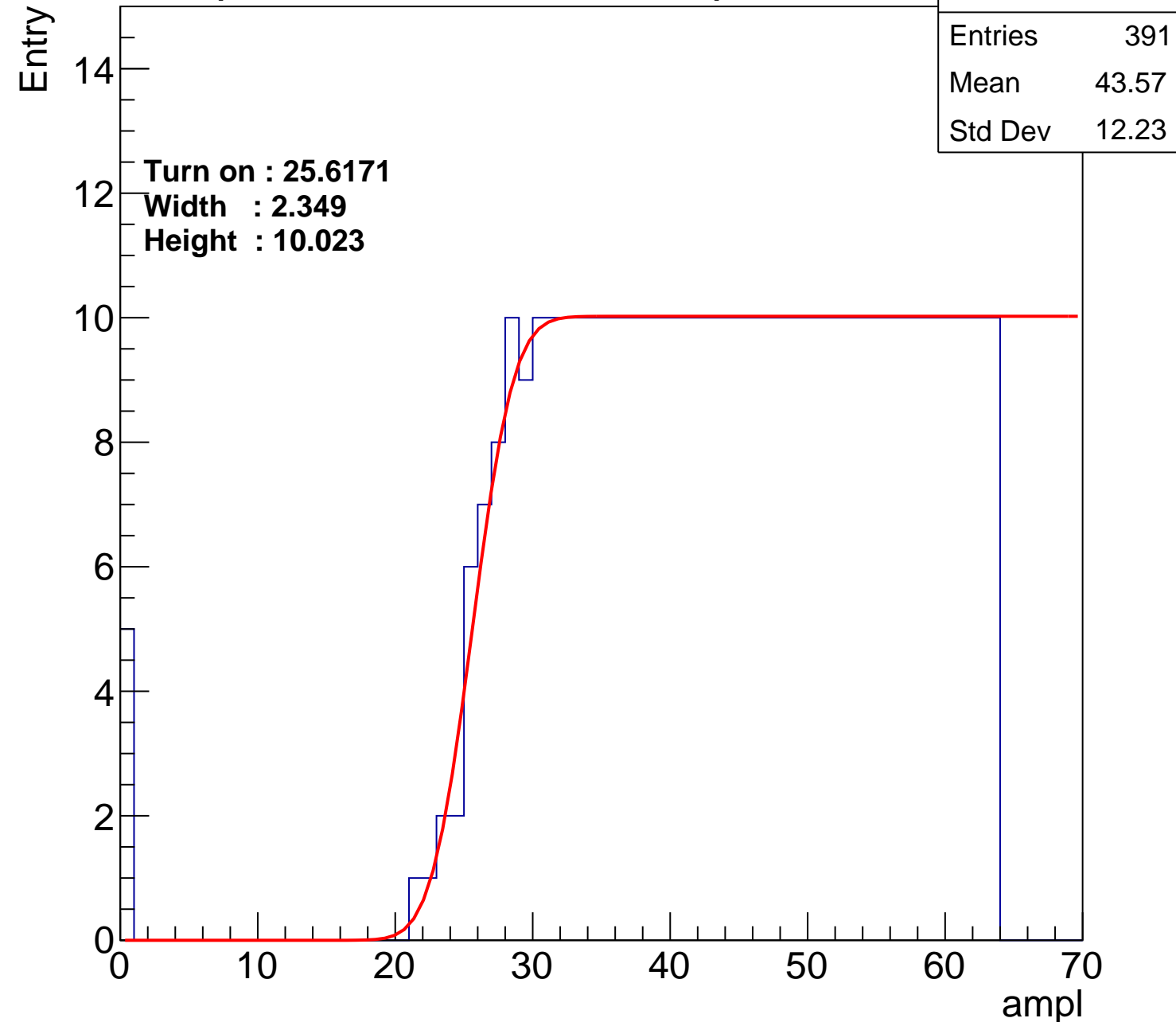
Width : 2.349

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 26.6068

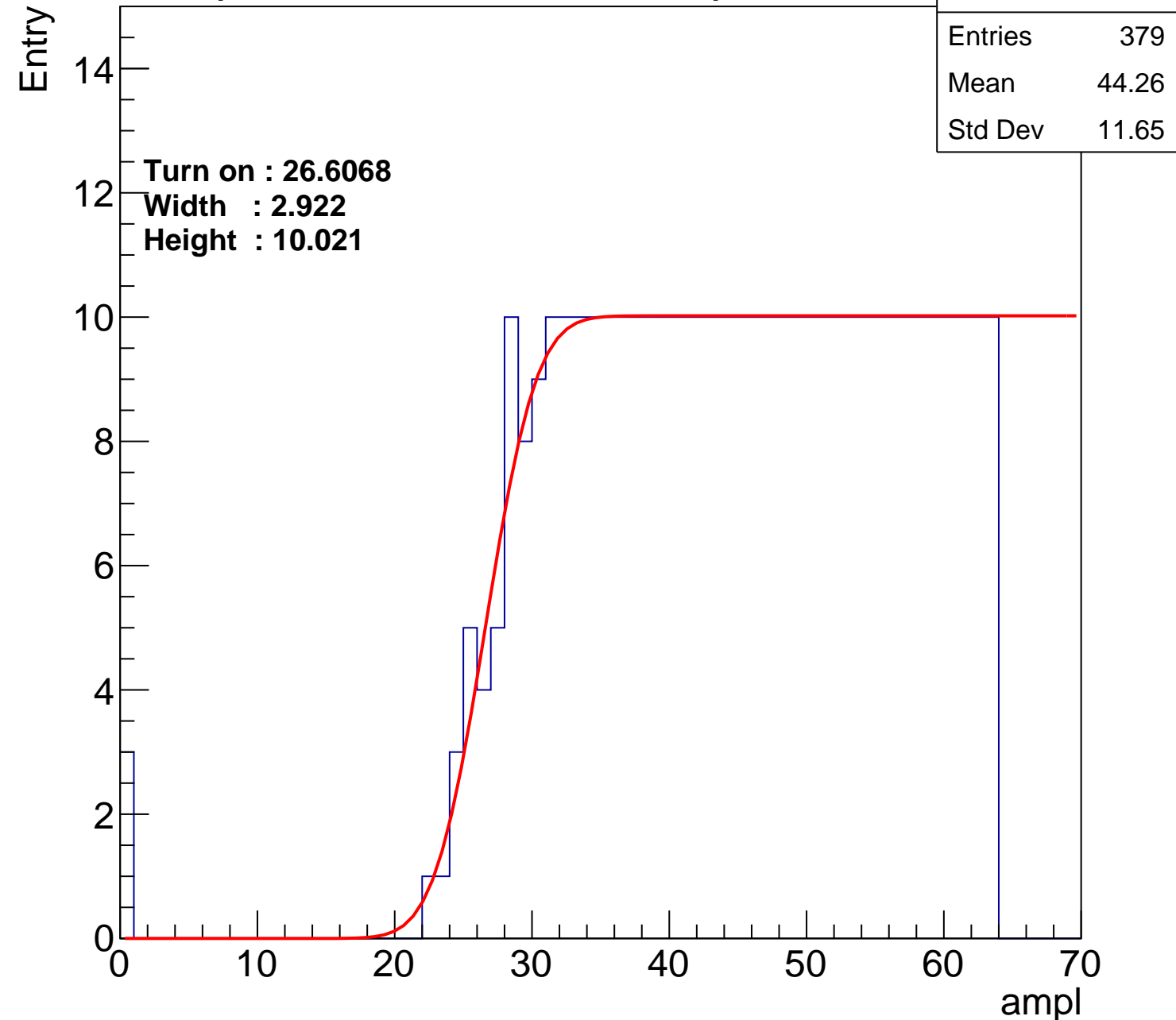
Width : 2.922

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.17
Std Dev	11.57

Turn on : 26.6503

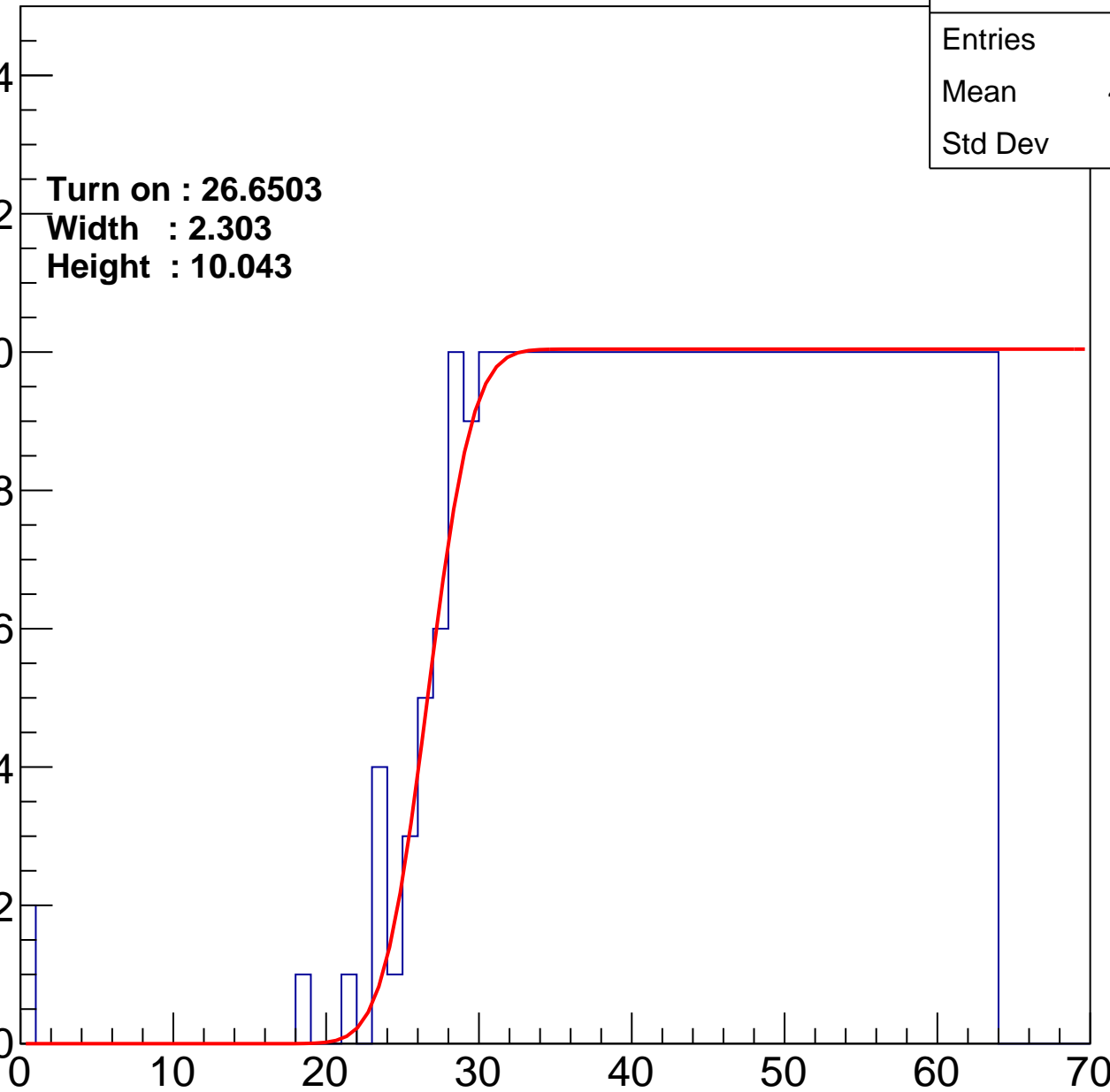
Width : 2.303

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.84
Std Dev	11.22

Turn on : 27.7259

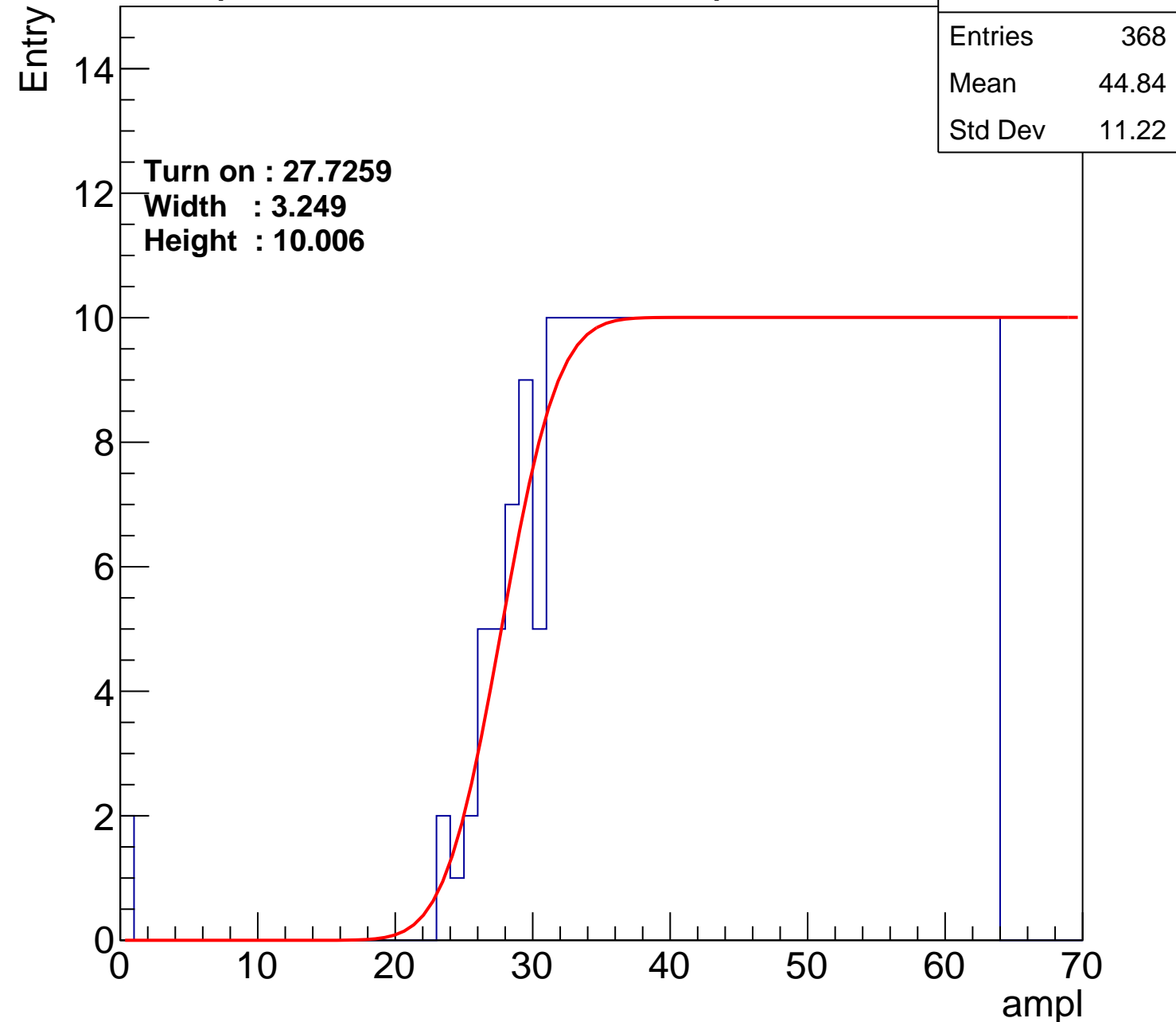
Width : 3.249

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch72

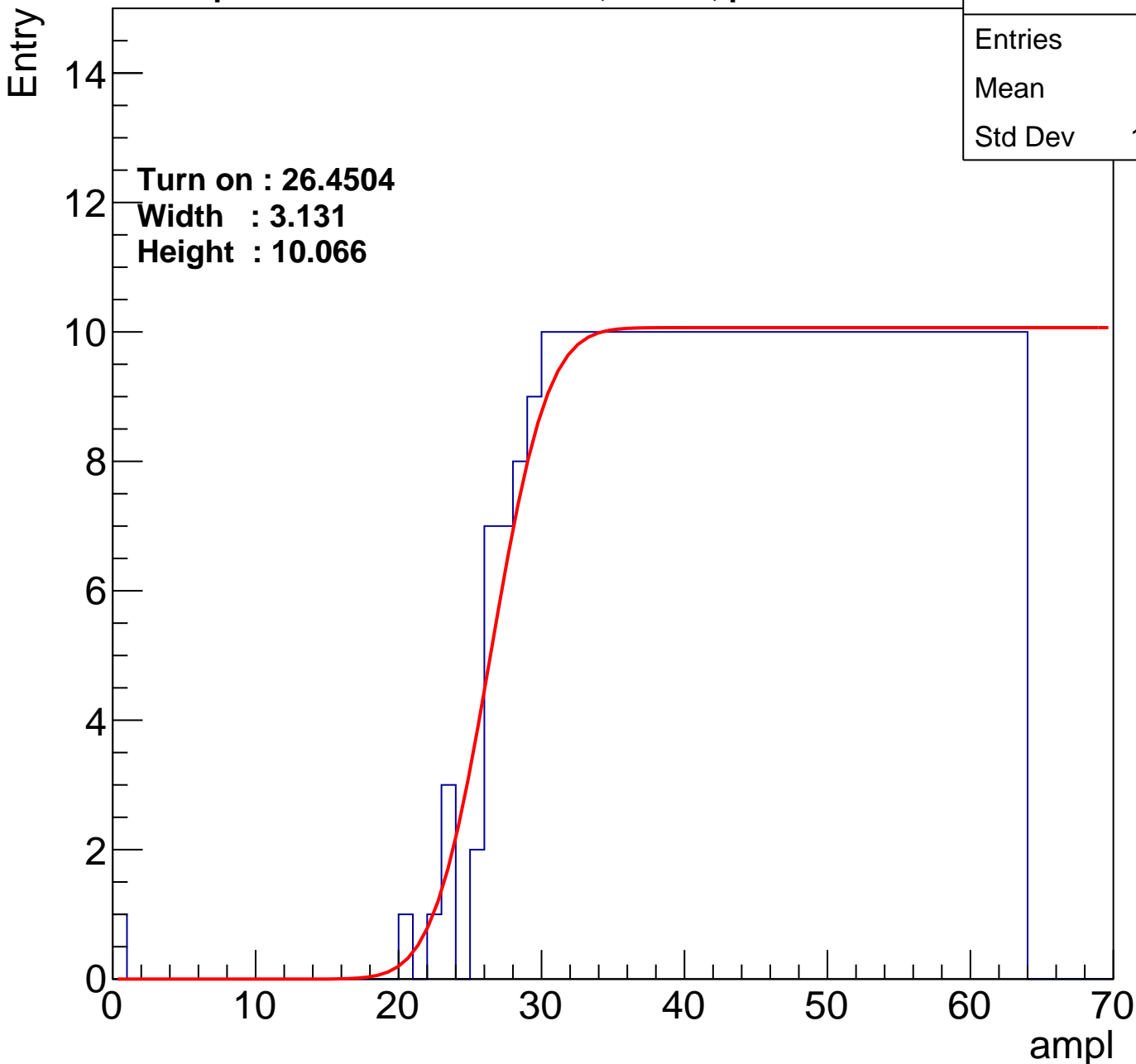
calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.4
Std Dev	11.28

Turn on : 26.4504

Width : 3.131

Height : 10.066



B1L003S, U25-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.05
Std Dev	11.45

Turn on : 28.8299

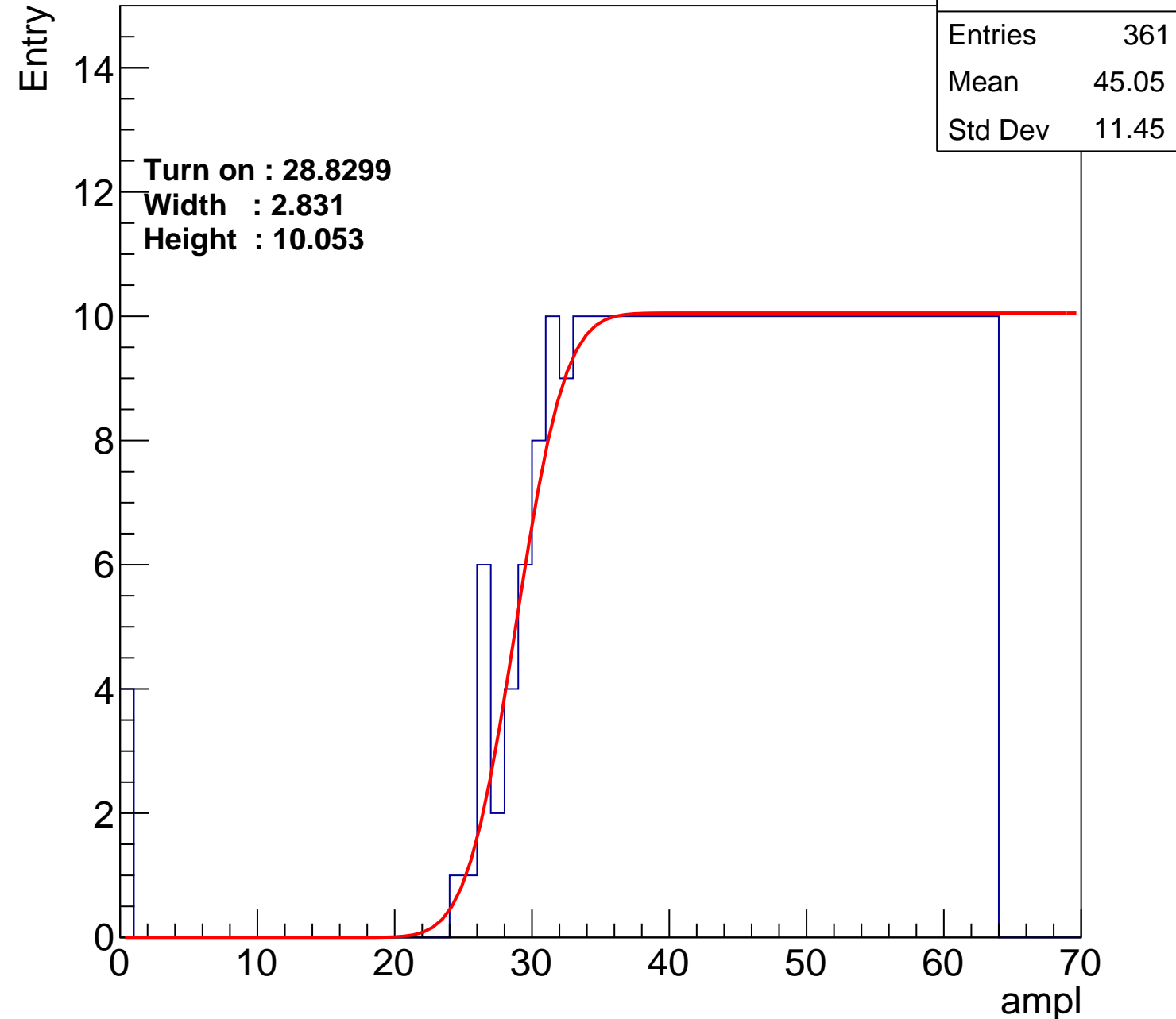
Width : 2.831

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.87
Std Dev	11.19

Turn on : 28.0383

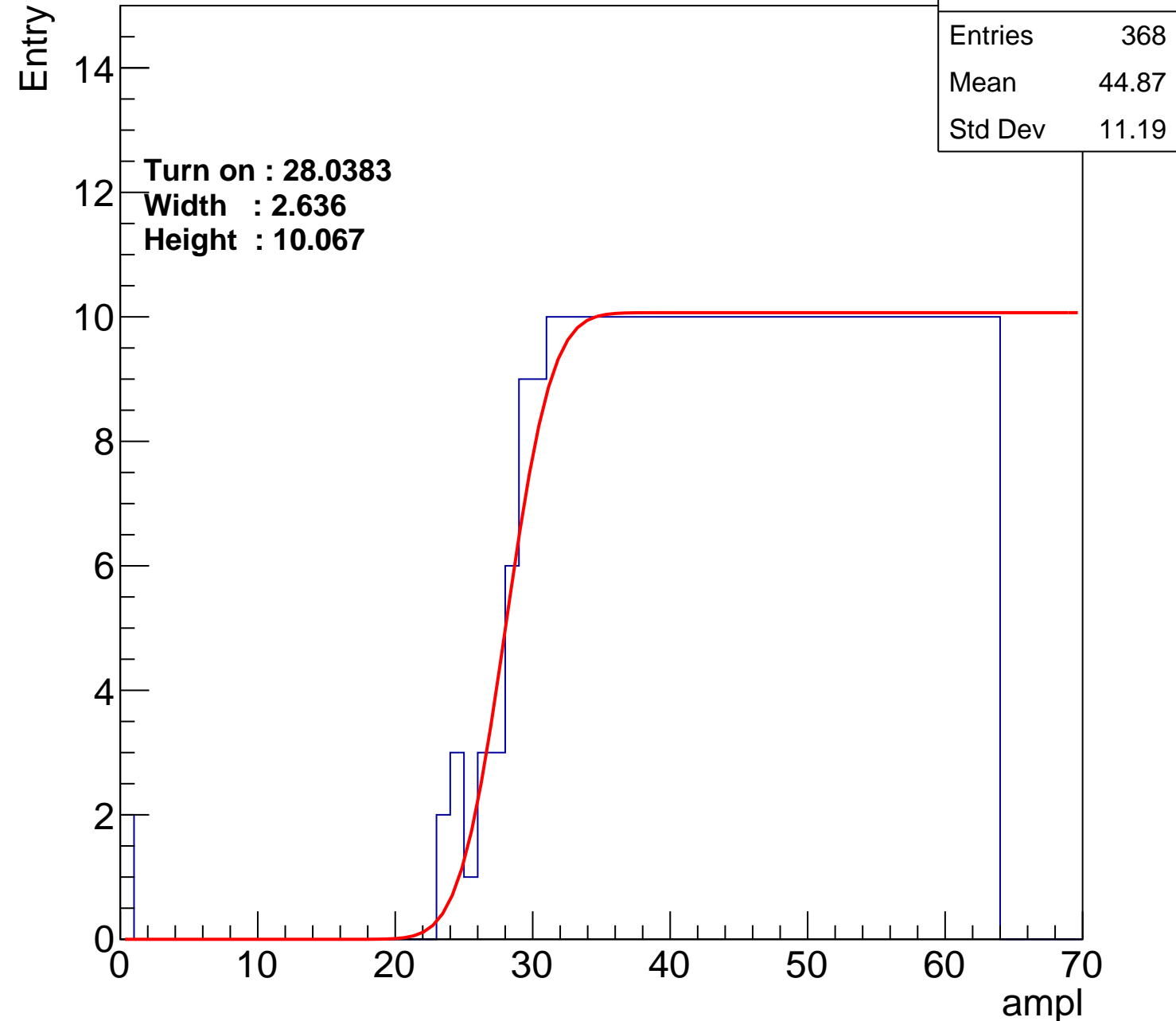
Width : 2.636

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.2412

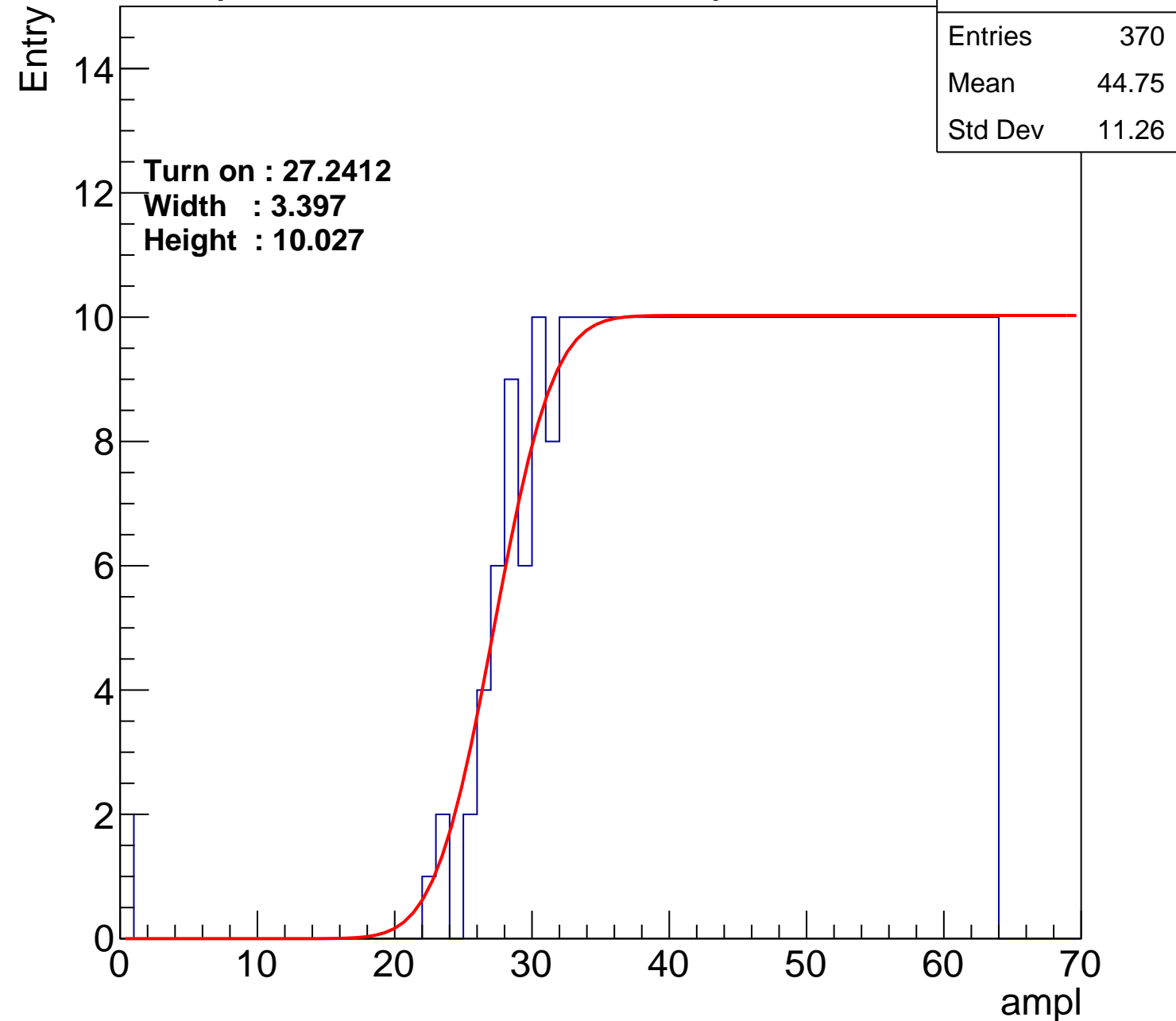
Width : 3.397

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch76

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.98
Std Dev	11.12

Turn on : 26.9310

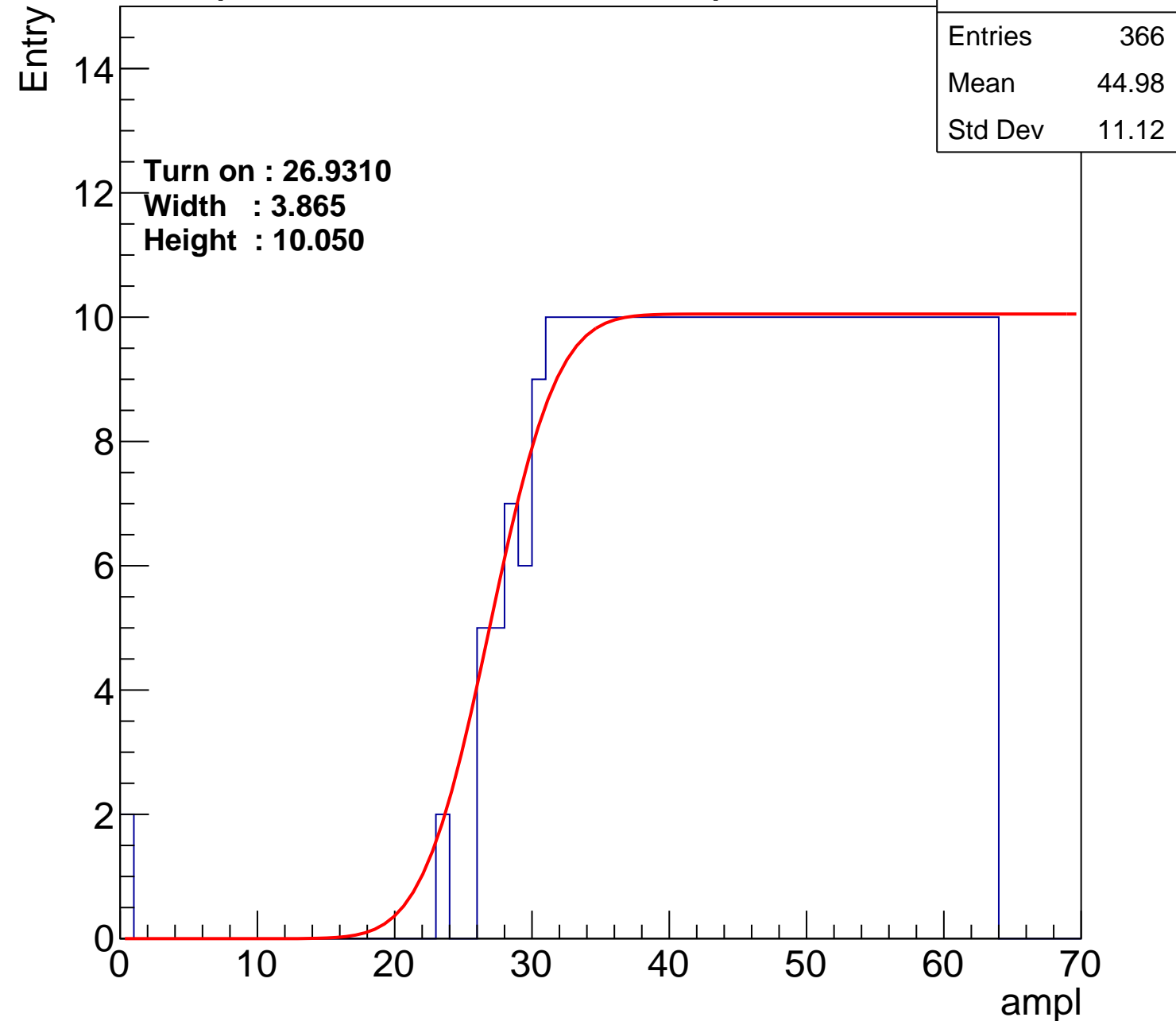
Width : 3.865

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch77

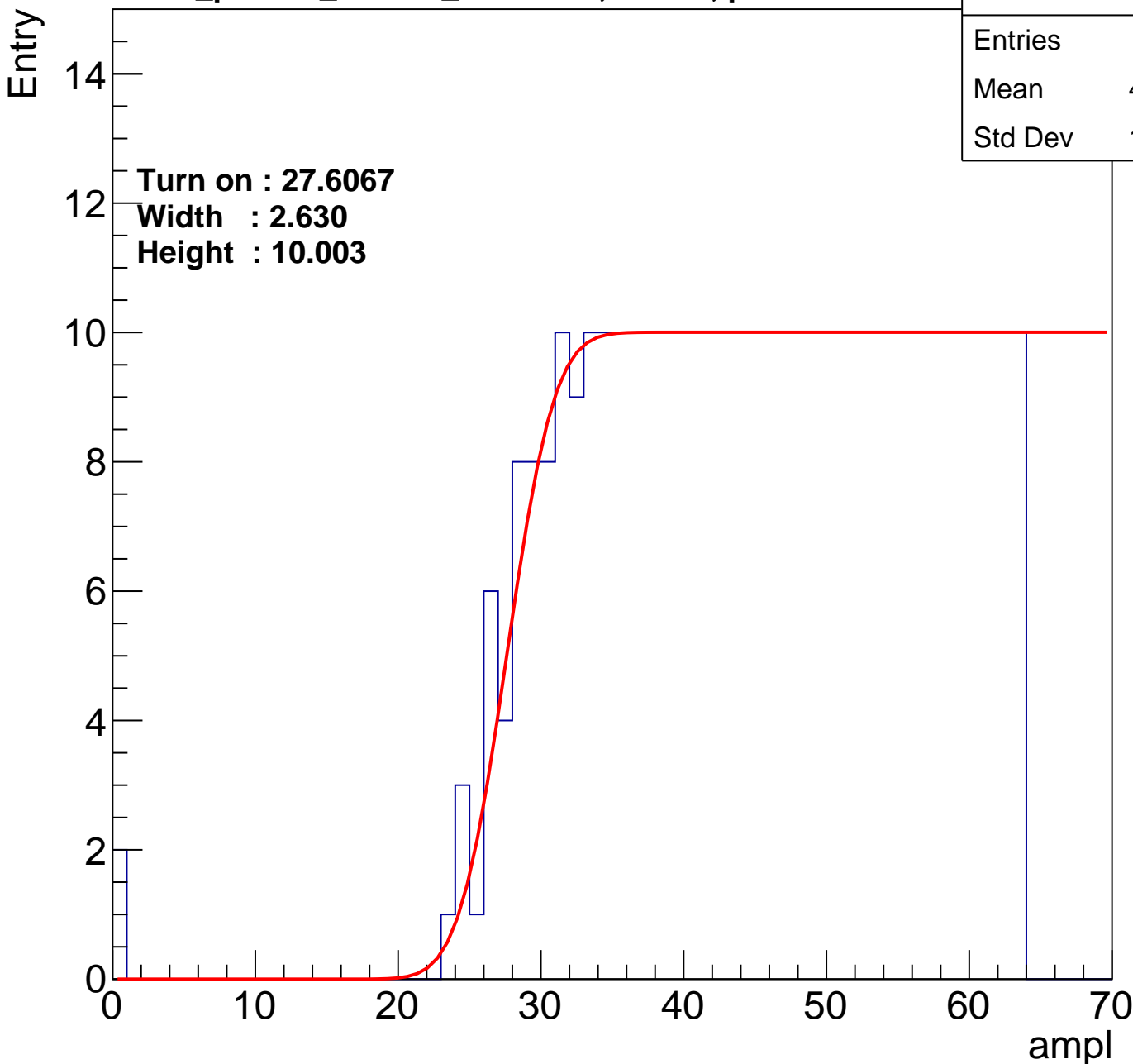
calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.6067

Width : 2.630

Height : 10.003



B1L003S, U25-ch78

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.01
Std Dev	12.08

Turn on : 26.1144

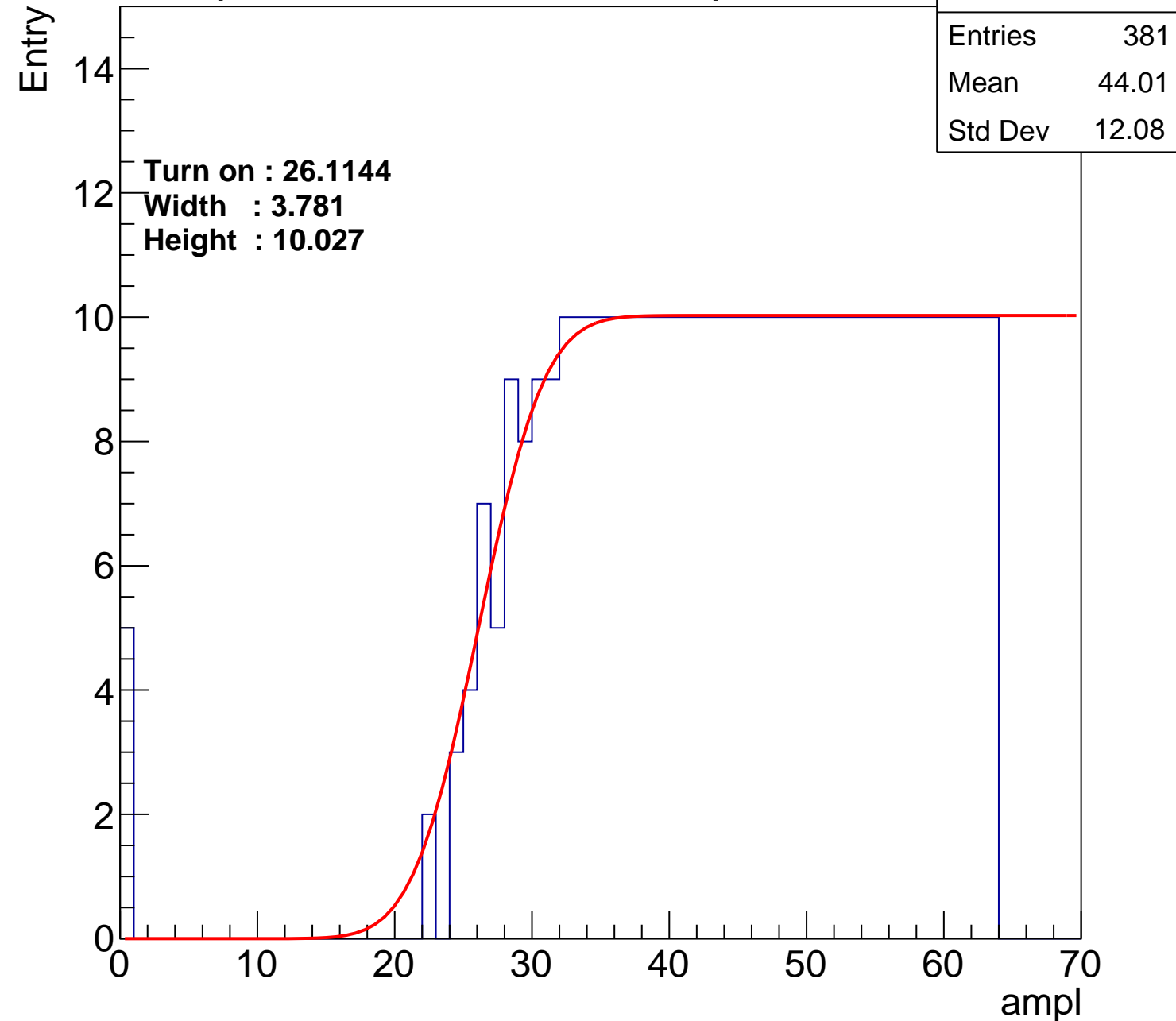
Width : 3.781

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.02
Std Dev	11.29

Turn on : 28.2633

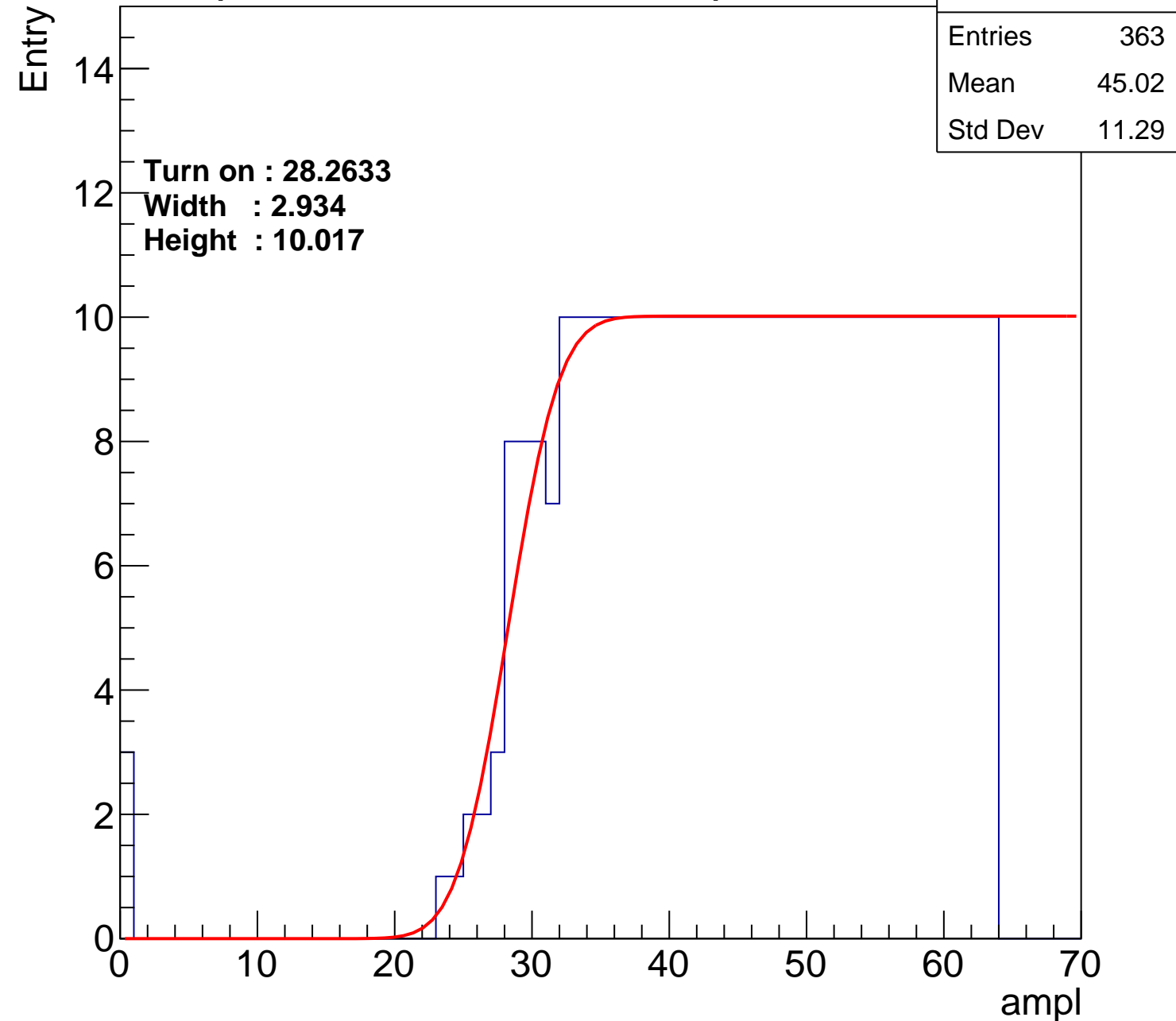
Width : 2.934

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	397
Mean	43.31
Std Dev	12.26

Turn on : 24.6326

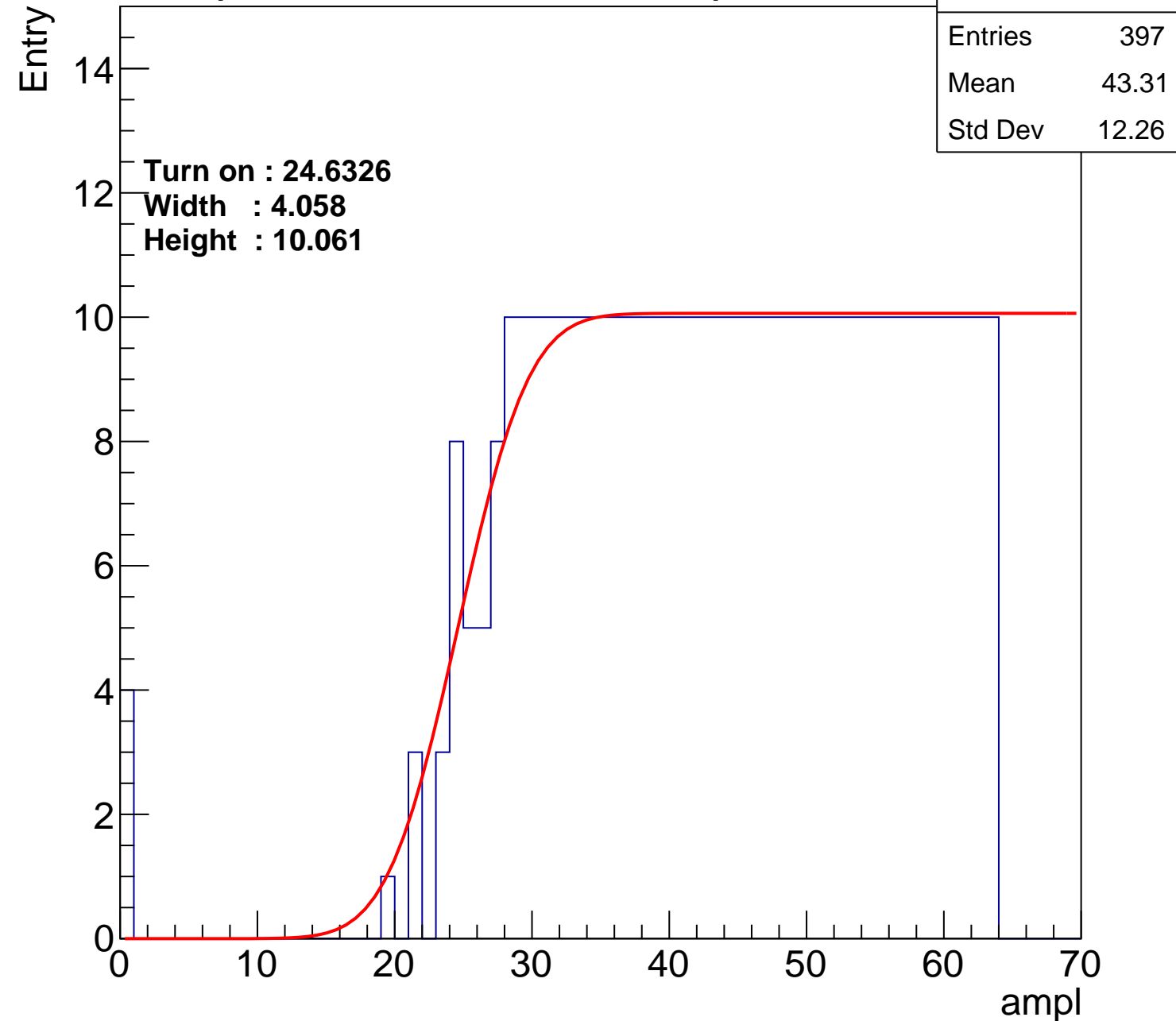
Width : 4.058

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.01
Std Dev	10.99

Turn on : 28.4098

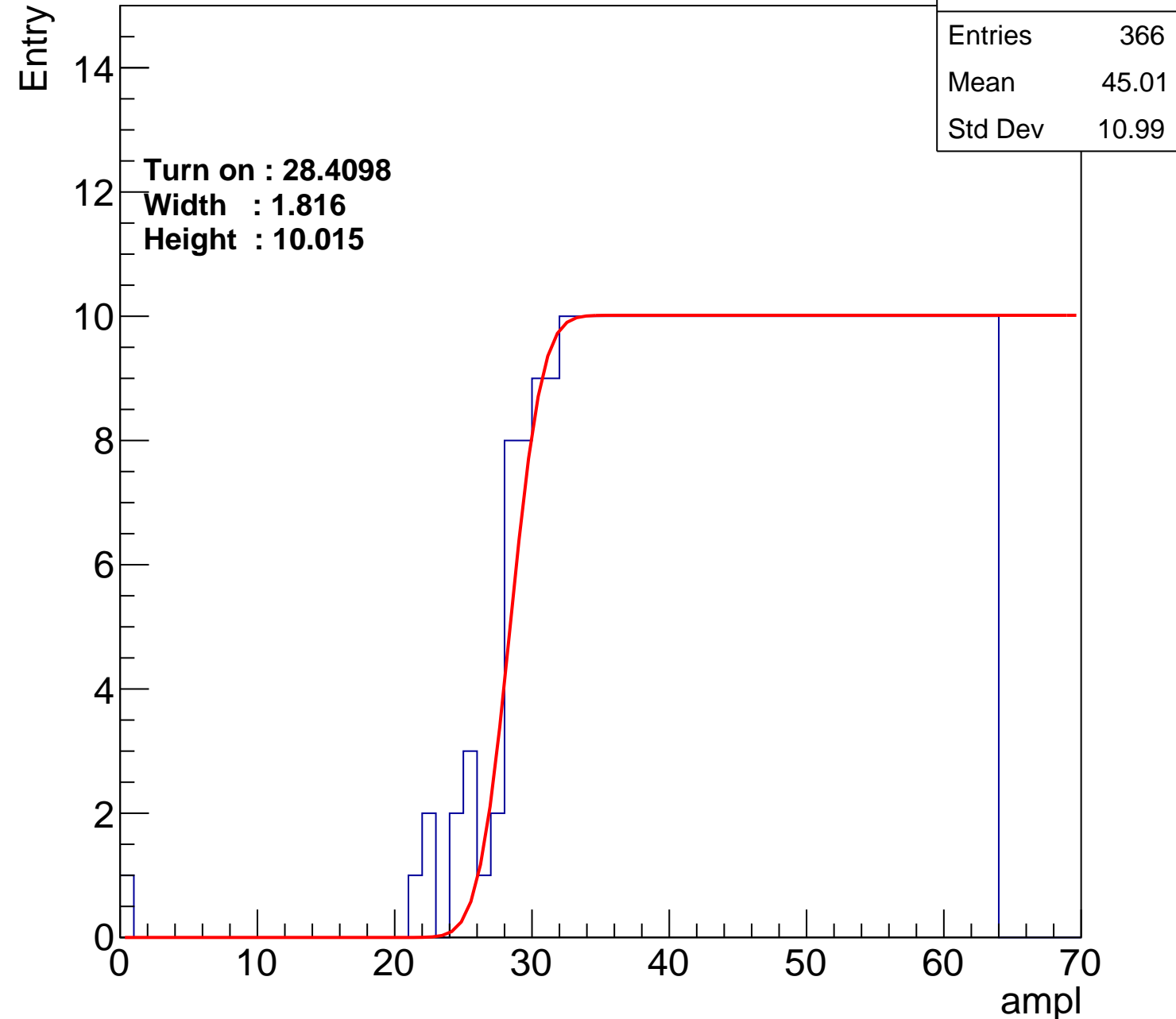
Width : 1.816

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch82

calib_packv5_042523_0143.root, FC#13, port D2

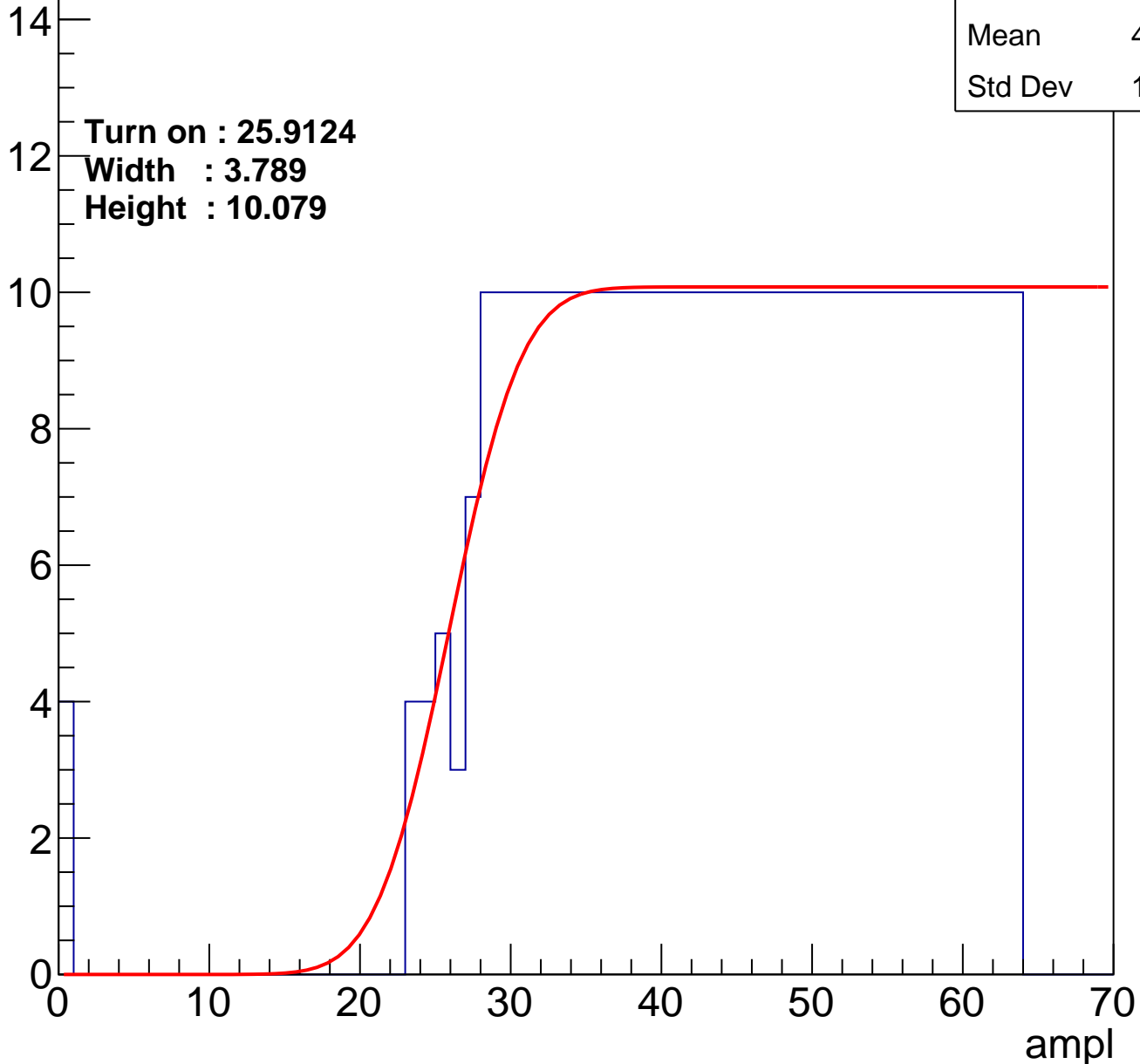
Entries	387
Mean	43.82
Std Dev	11.98

Turn on : 25.9124

Width : 3.789

Height : 10.079

Entry



B1L003S, U25-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.6
Std Dev	11.67

Turn on : 27.4251

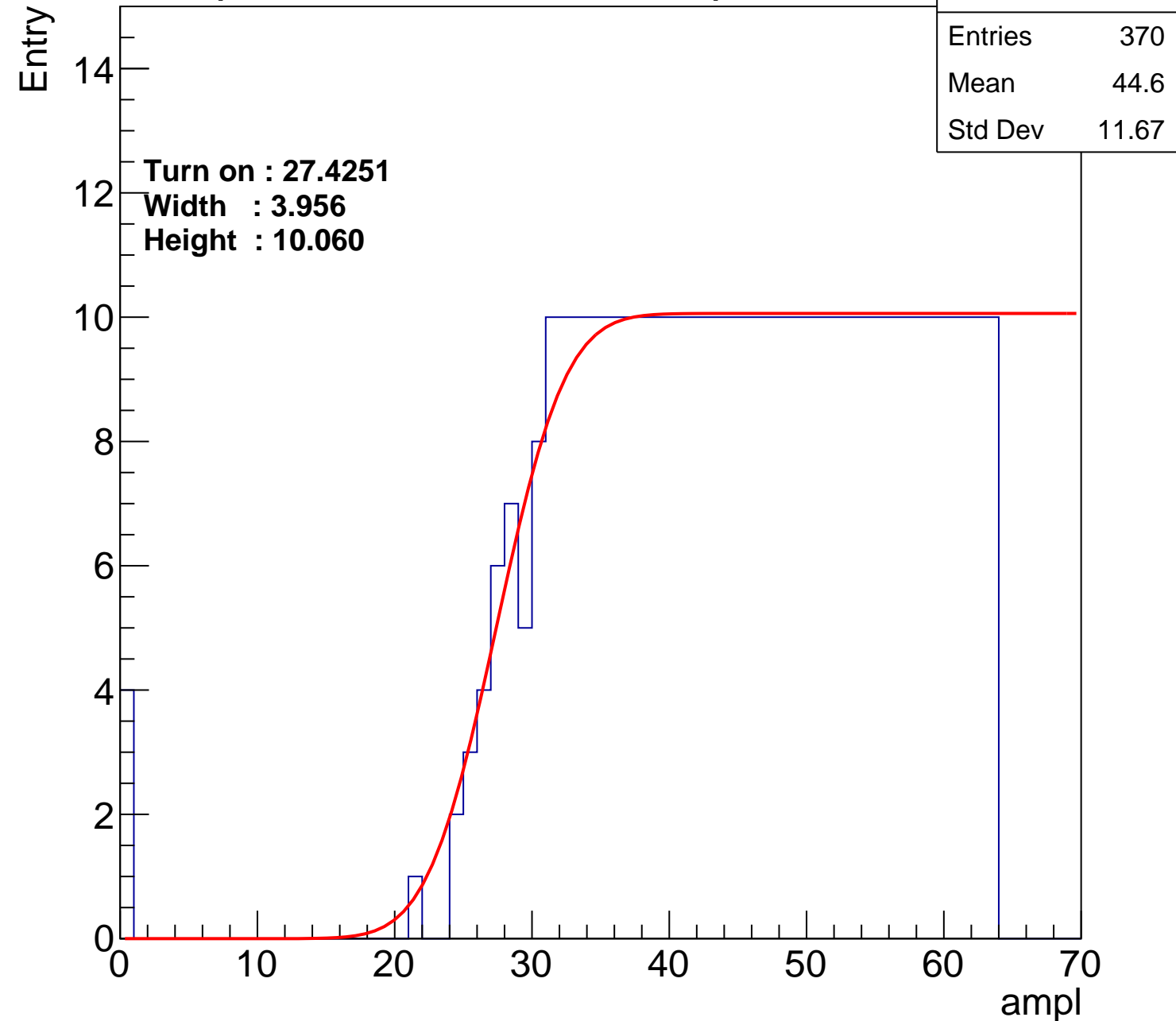
Width : 3.956

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.99
Std Dev	11.01

Turn on : 27.2461

Width : 4.305

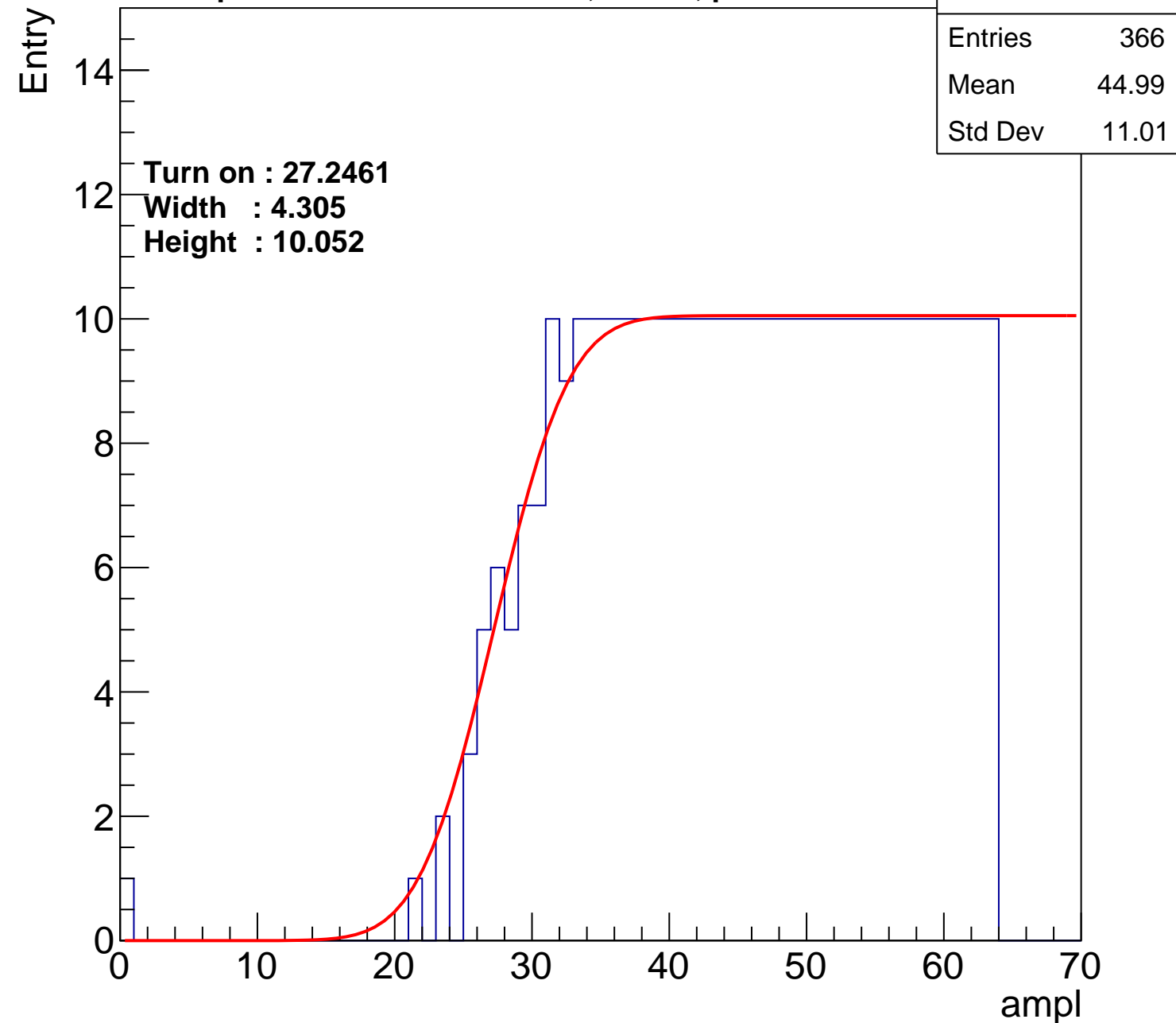
Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U25-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.65
Std Dev	11.28

Turn on : 27.4757

Width : 2.595

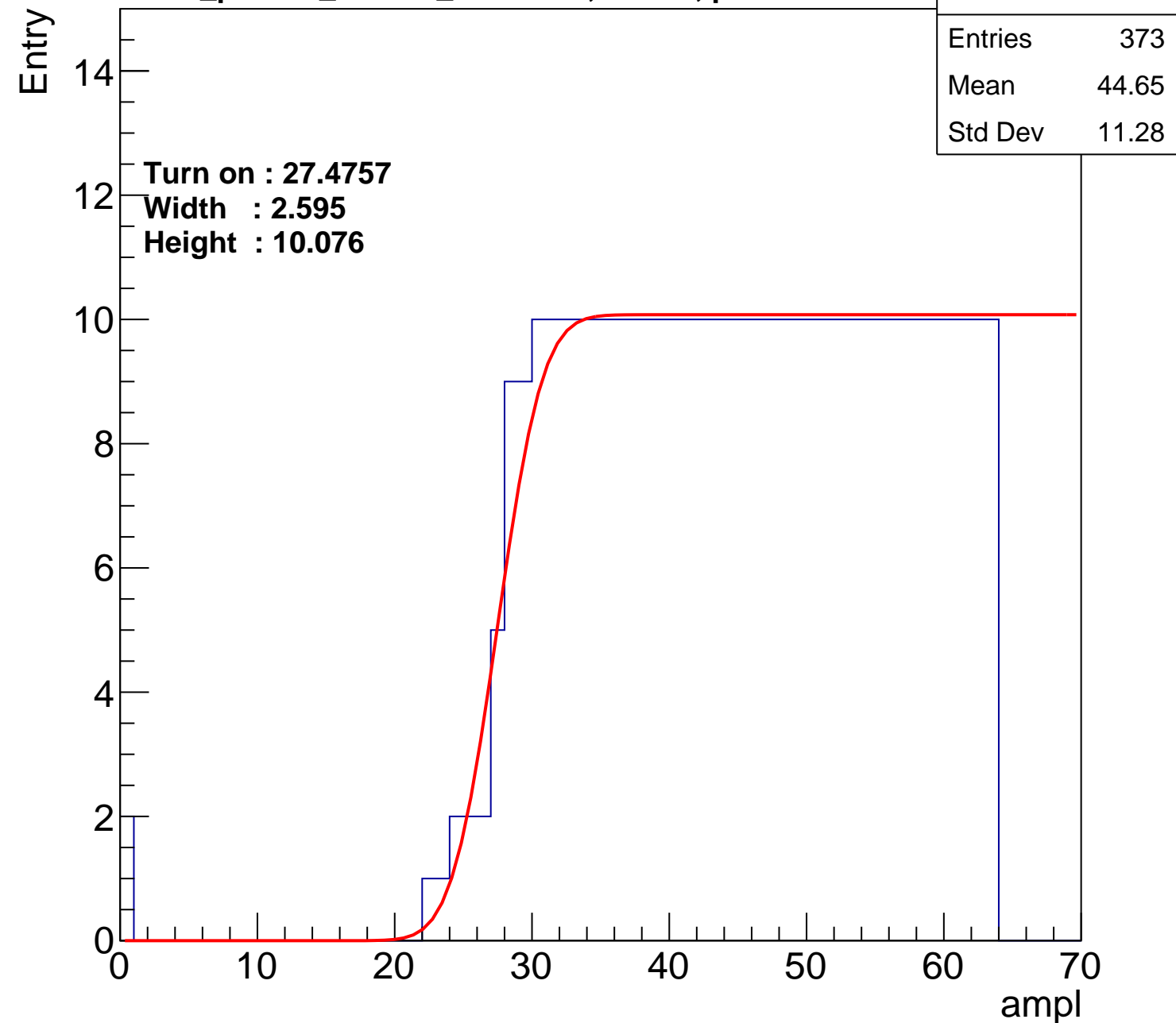
Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U25-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.45
Std Dev	11.88

Turn on : 27.5021

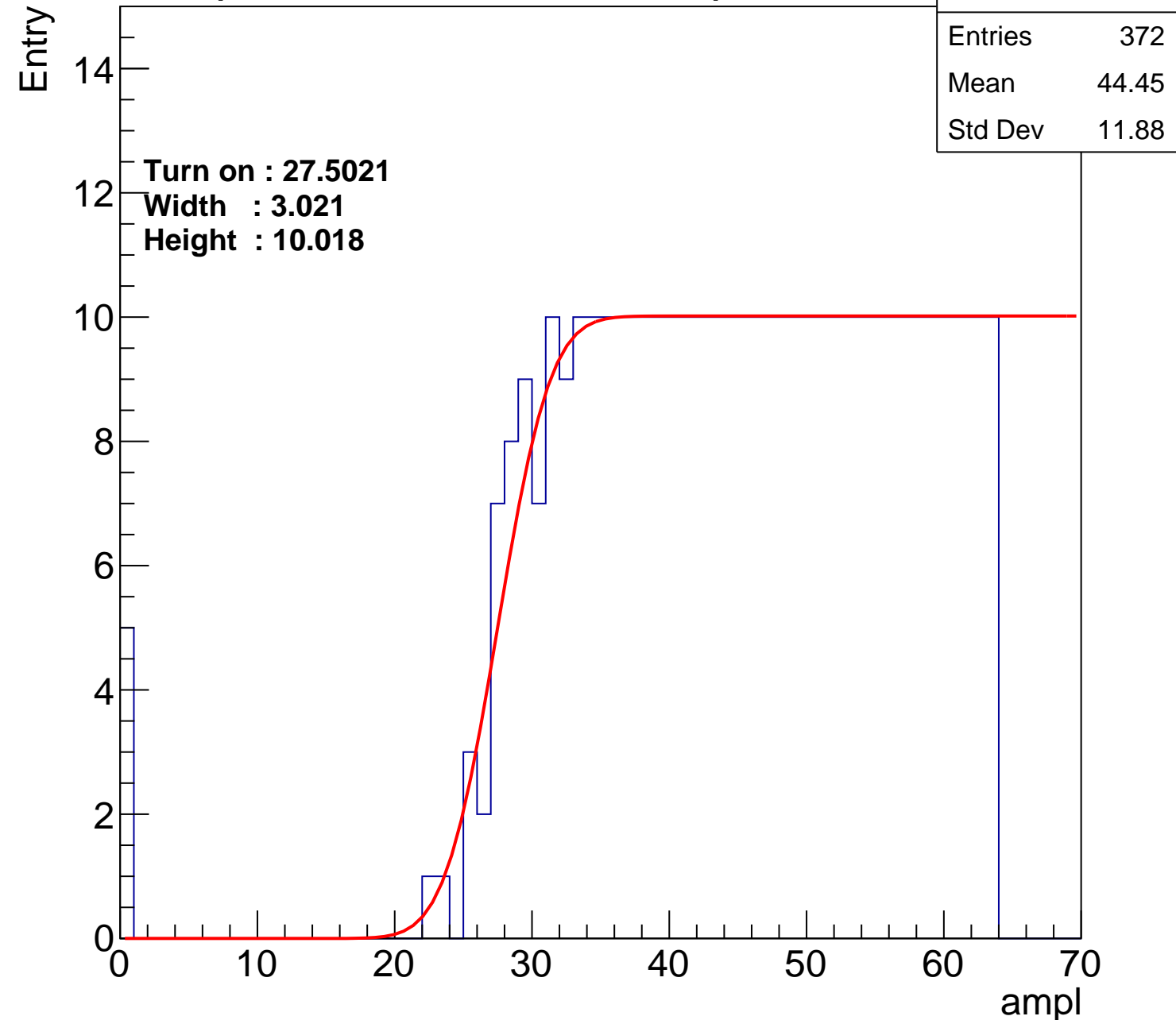
Width : 3.021

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch87

calib_packv5_042523_0143.root, FC#13, port D2

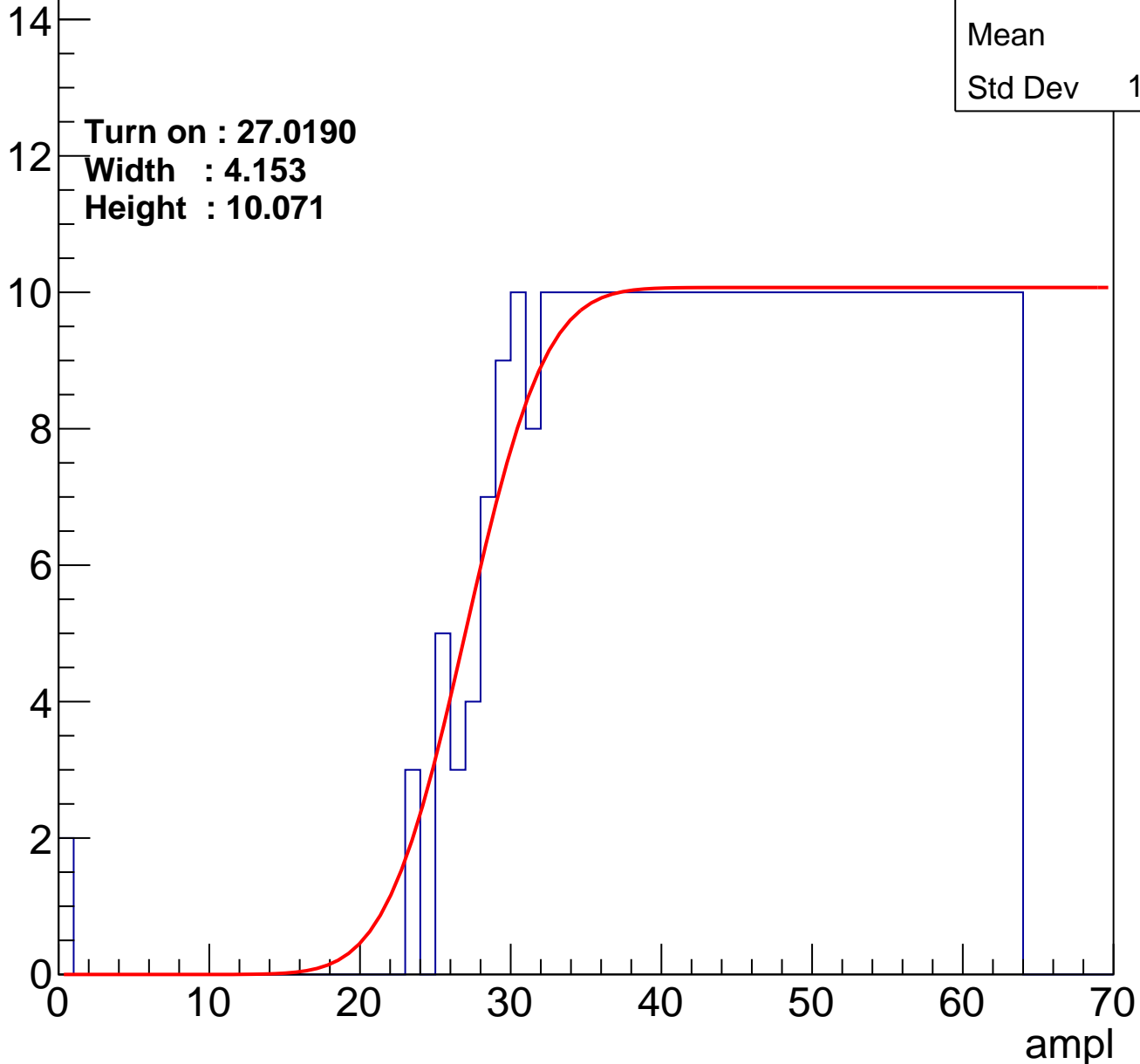
Entries	371
Mean	44.7
Std Dev	11.29

Turn on : 27.0190

Width : 4.153

Height : 10.071

Entry



B1L003S, U25-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.33
Std Dev	11.73

Turn on : 26.8548

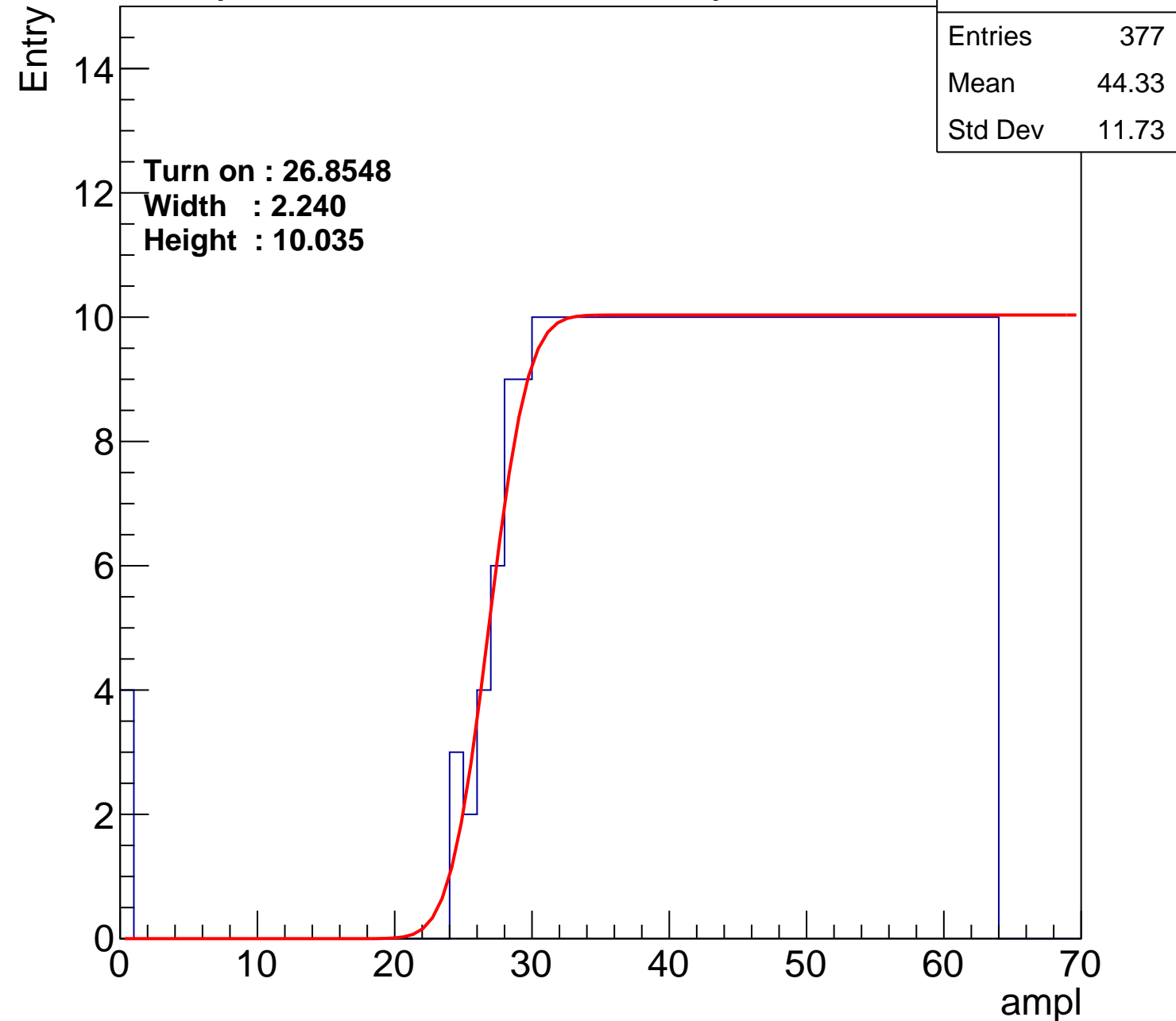
Width : 2.240

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch89

calib_packv5_042523_0143.root, FC#13, port D2

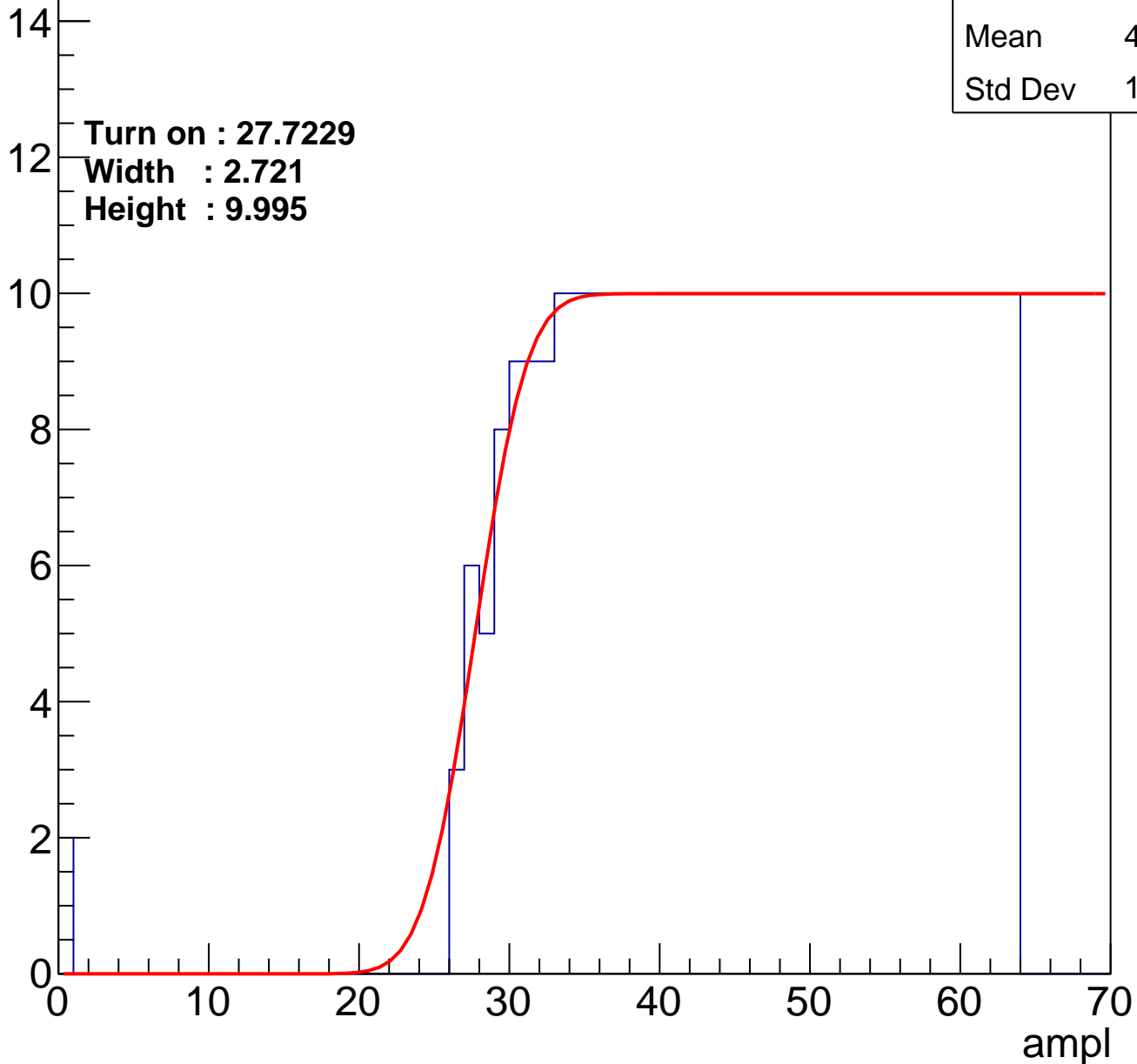
Entries	361
Mean	45.23
Std Dev	10.97

Turn on : 27.7229

Width : 2.721

Height : 9.995

Entry



B1L003S, U25-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44
Std Dev	12.46

Turn on : 27.6905

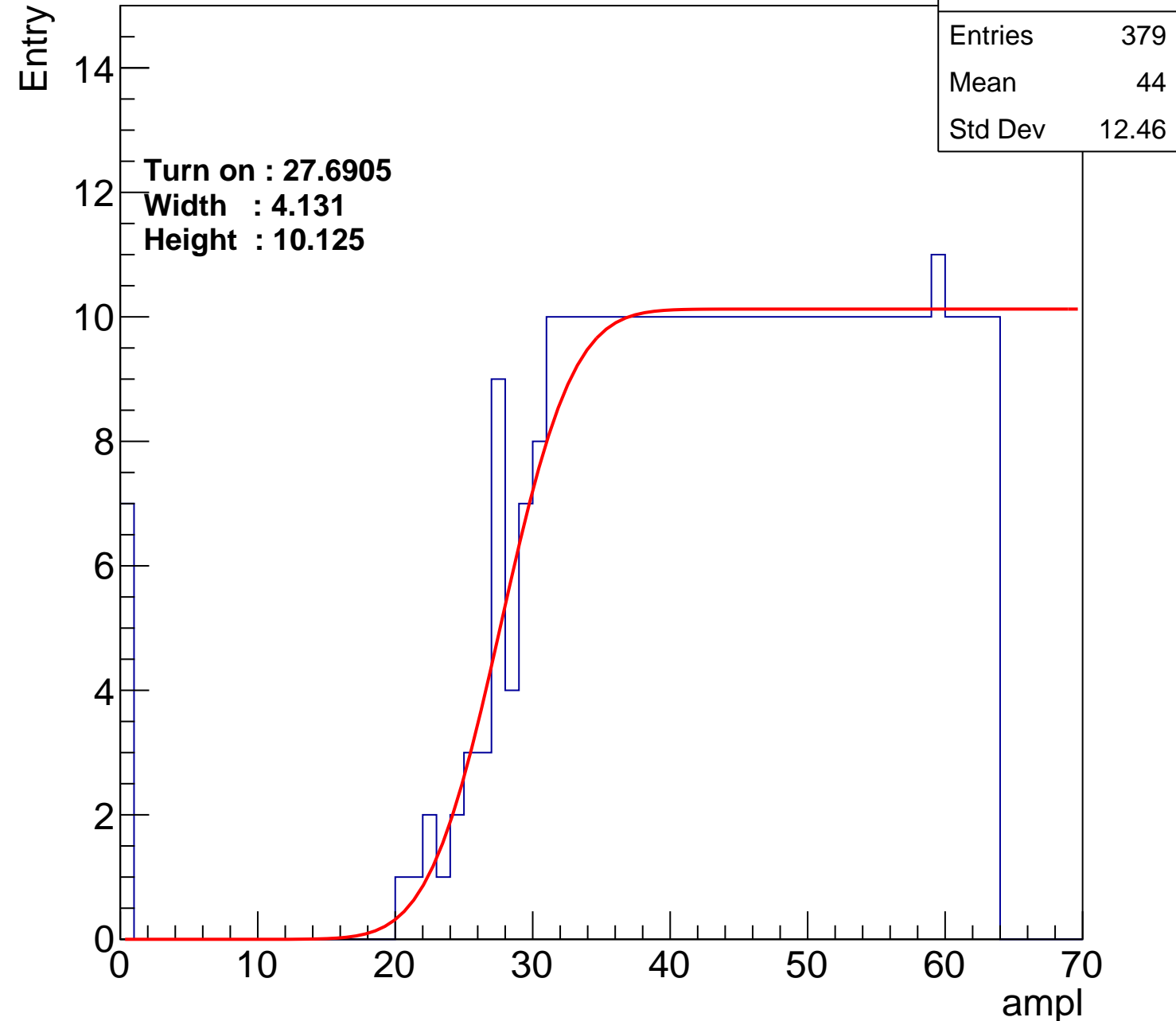
Width : 4.131

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch91

calib_packv5_042523_0143.root, FC#13, port D2

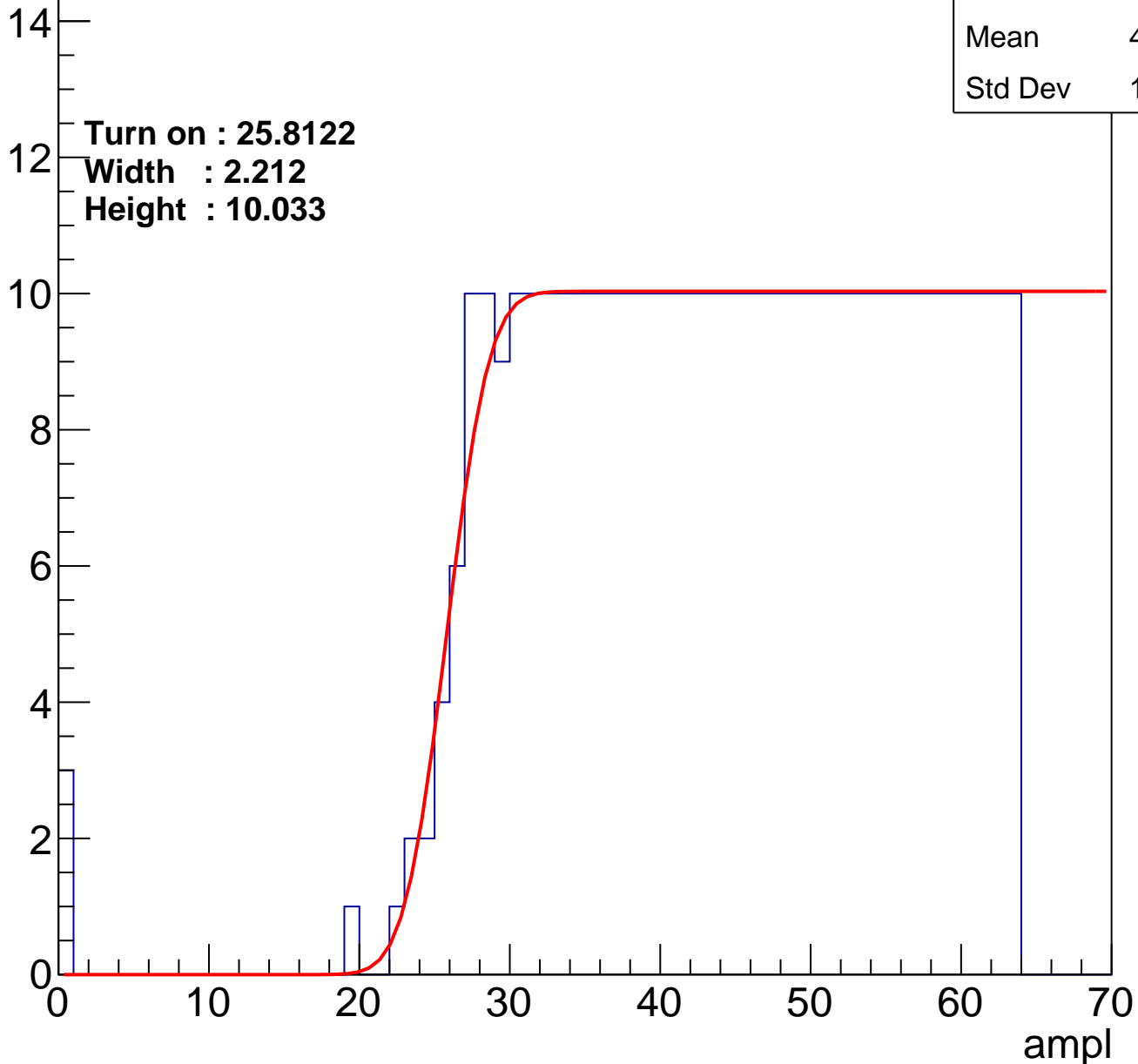
Entries	388
Mean	43.85
Std Dev	11.83

Turn on : 25.8122

Width : 2.212

Height : 10.033

Entry



B1L003S, U25-ch92

calib_packv5_042523_0143.root, FC#13, port D2

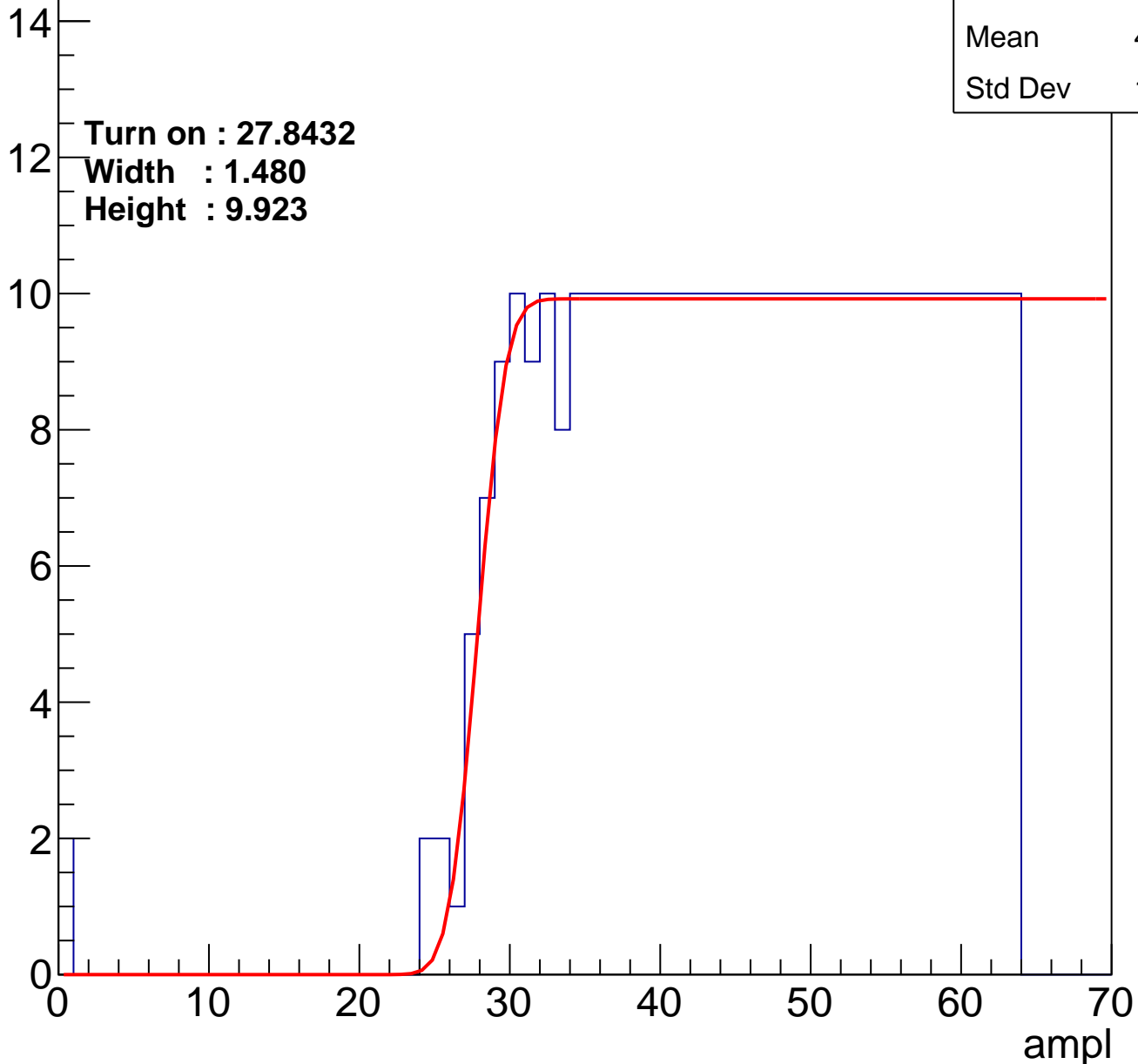
Entry

Entries	365
Mean	45.01
Std Dev	11.11

Turn on : 27.8432

Width : 1.480

Height : 9.923



B1L003S, U25-ch93

calib_packv5_042523_0143.root, FC#13, port D2

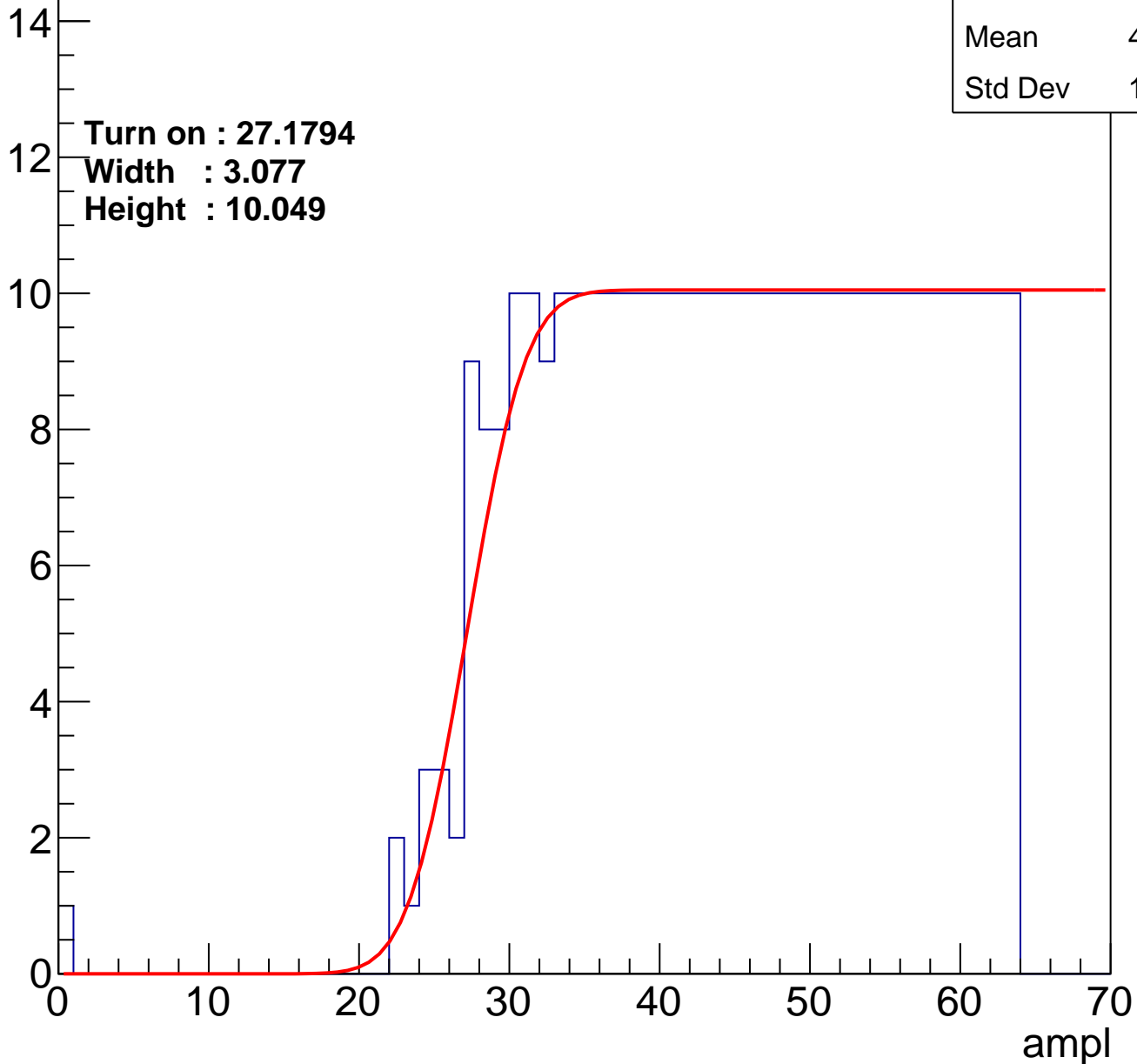
Entry

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 27.1794

Width : 3.077

Height : 10.049



B1L003S, U25-ch94

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.69
Std Dev	11.49

Turn on : 27.4377

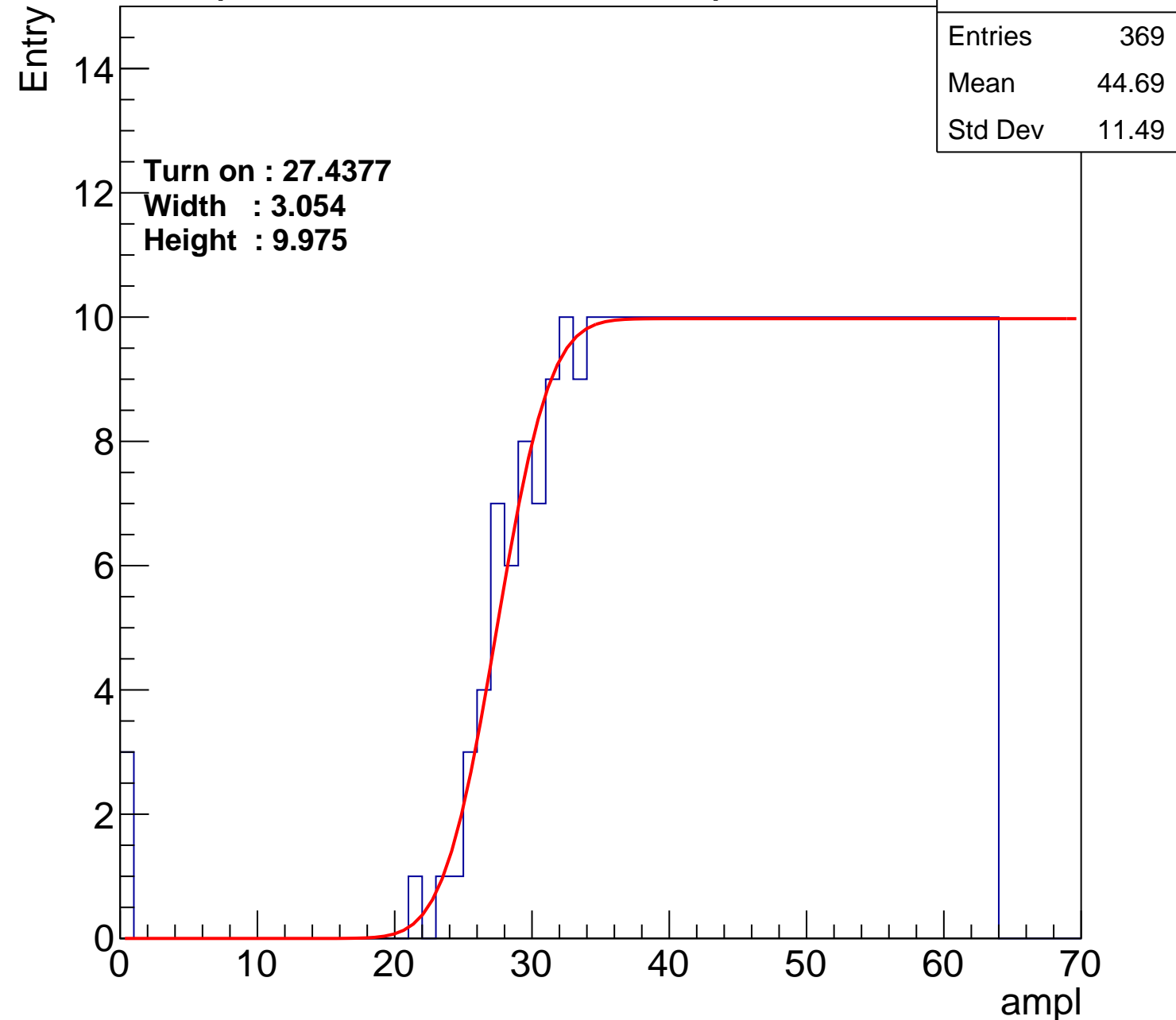
Width : 3.054

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.4763

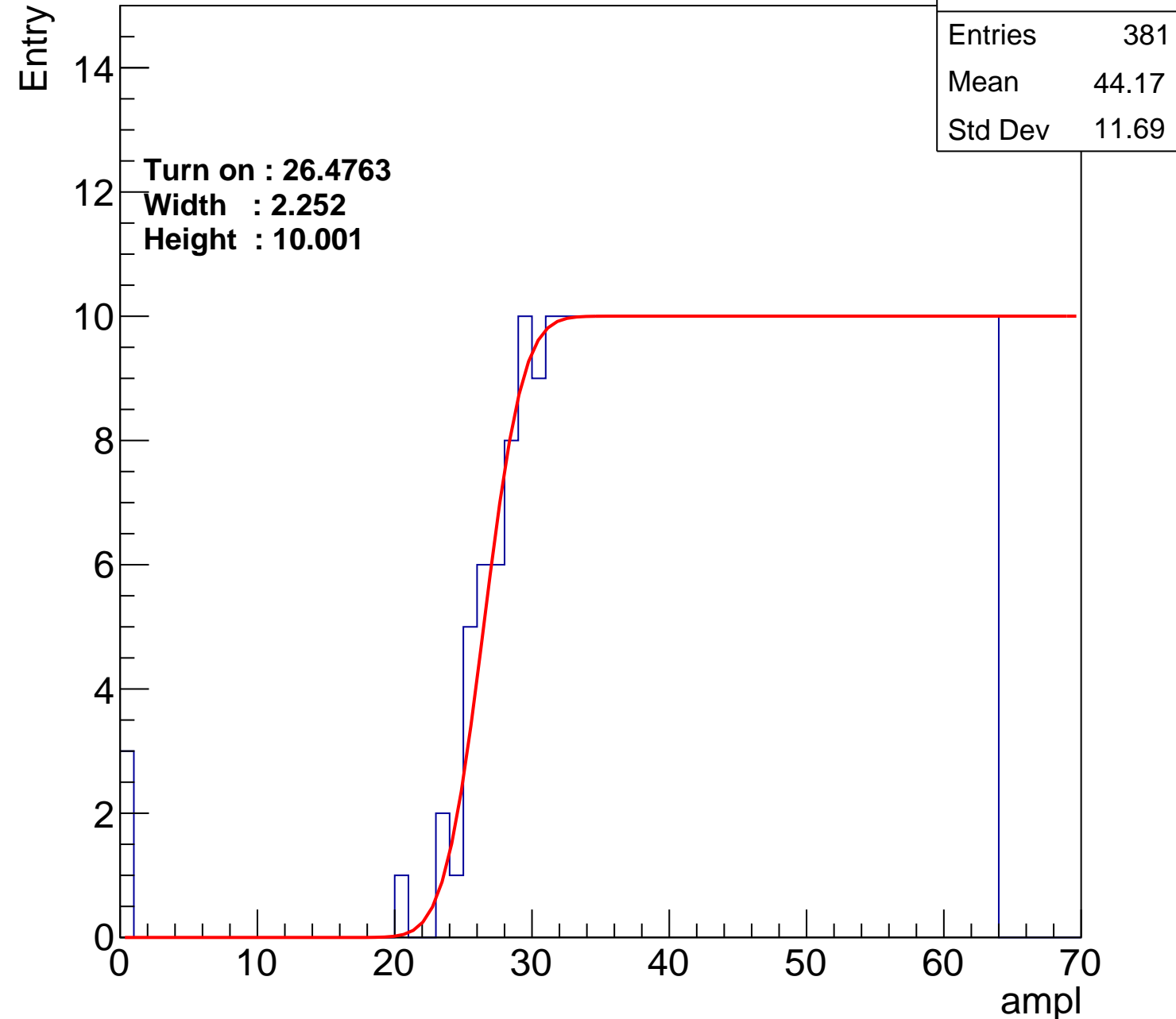
Width : 2.252

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
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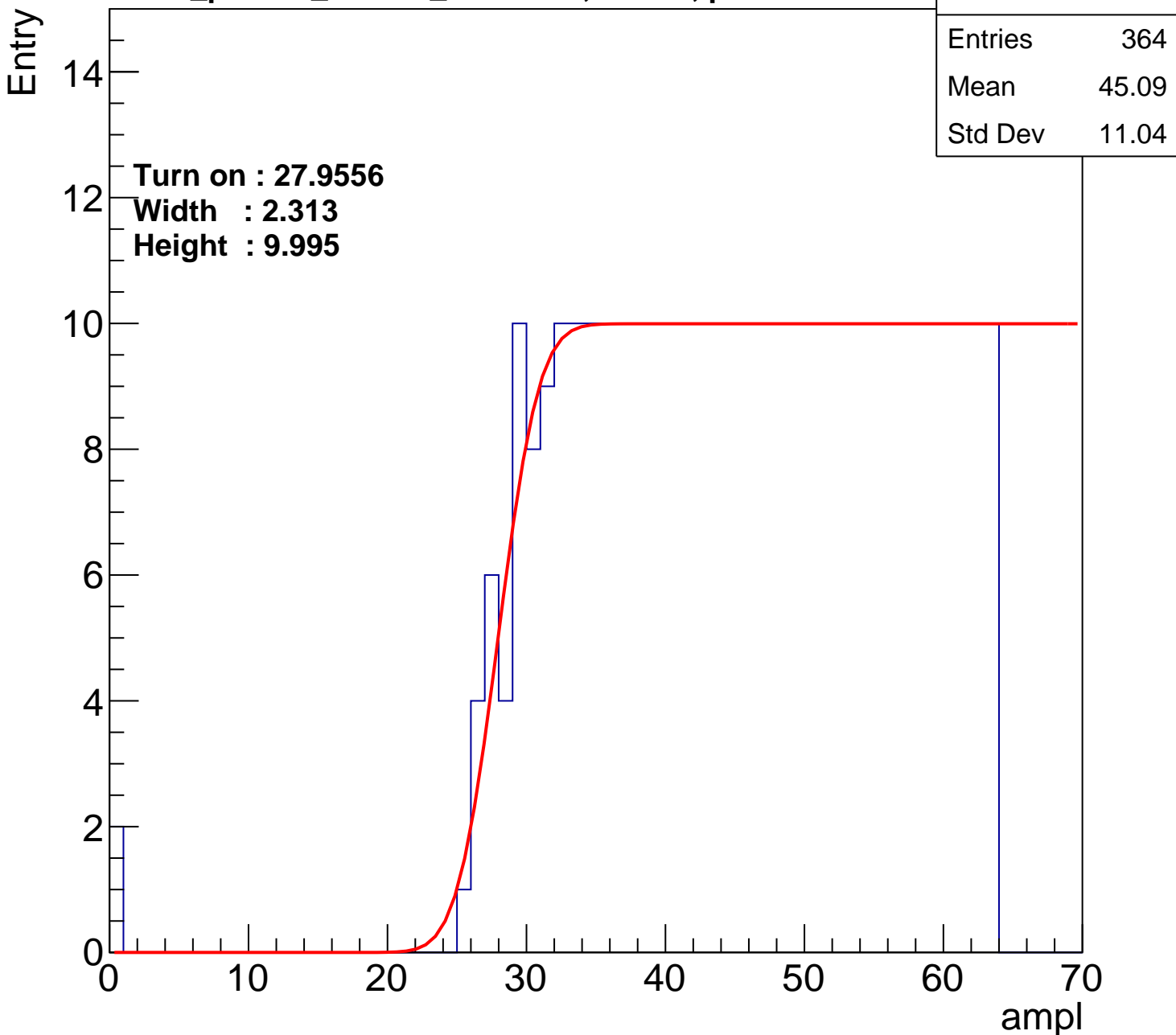
Mean	45.09
------	-------

Std Dev	11.04
---------	-------

Turn on : 27.9556

Width : 2.313

Height : 9.995



B1L003S, U25-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.87
Std Dev	11.32

Turn on : 27.3158

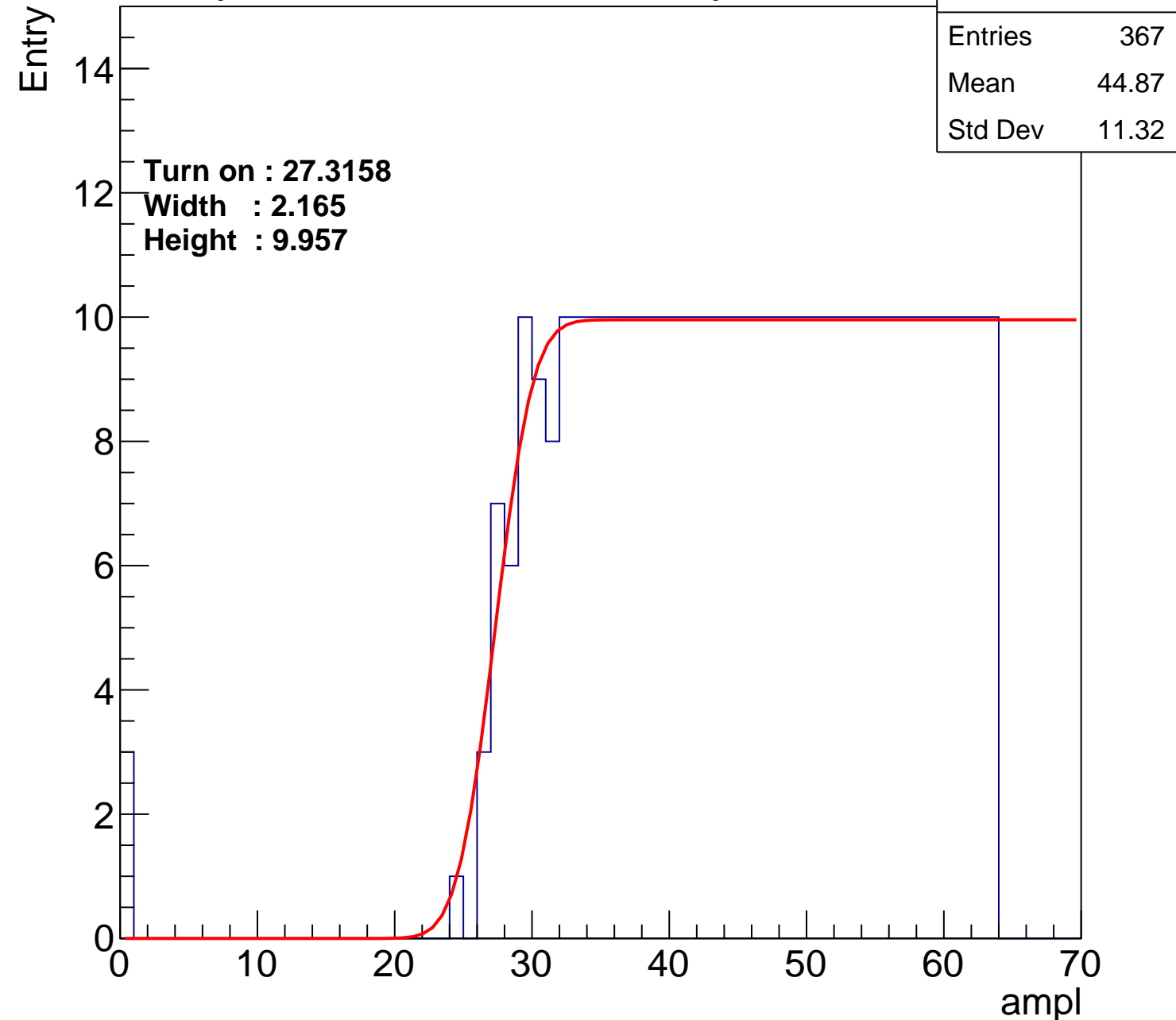
Width : 2.165

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.94
Std Dev	11.52

Turn on : 27.8931

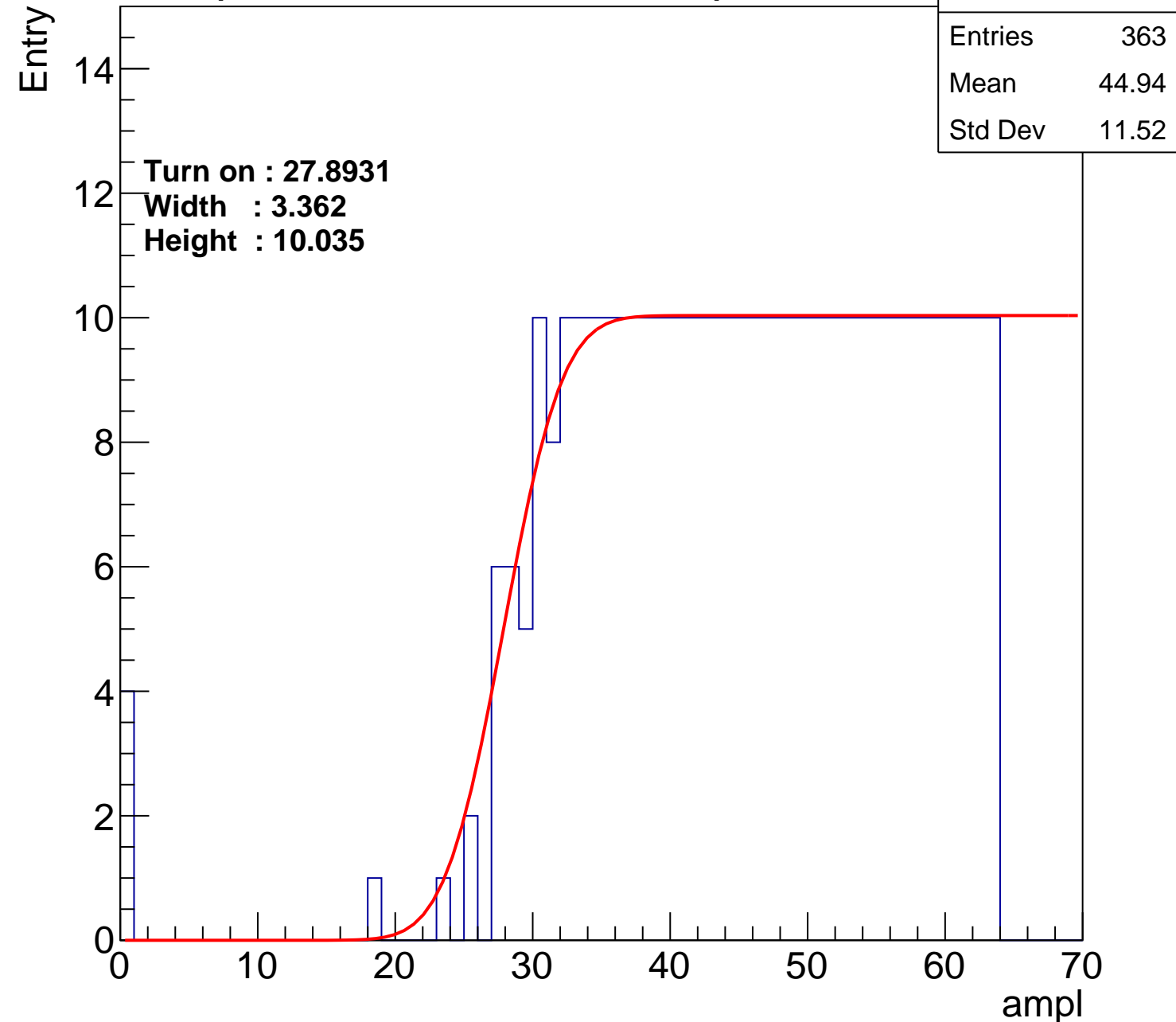
Width : 3.362

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.54
Std Dev	11.52

Turn on : 27.0468

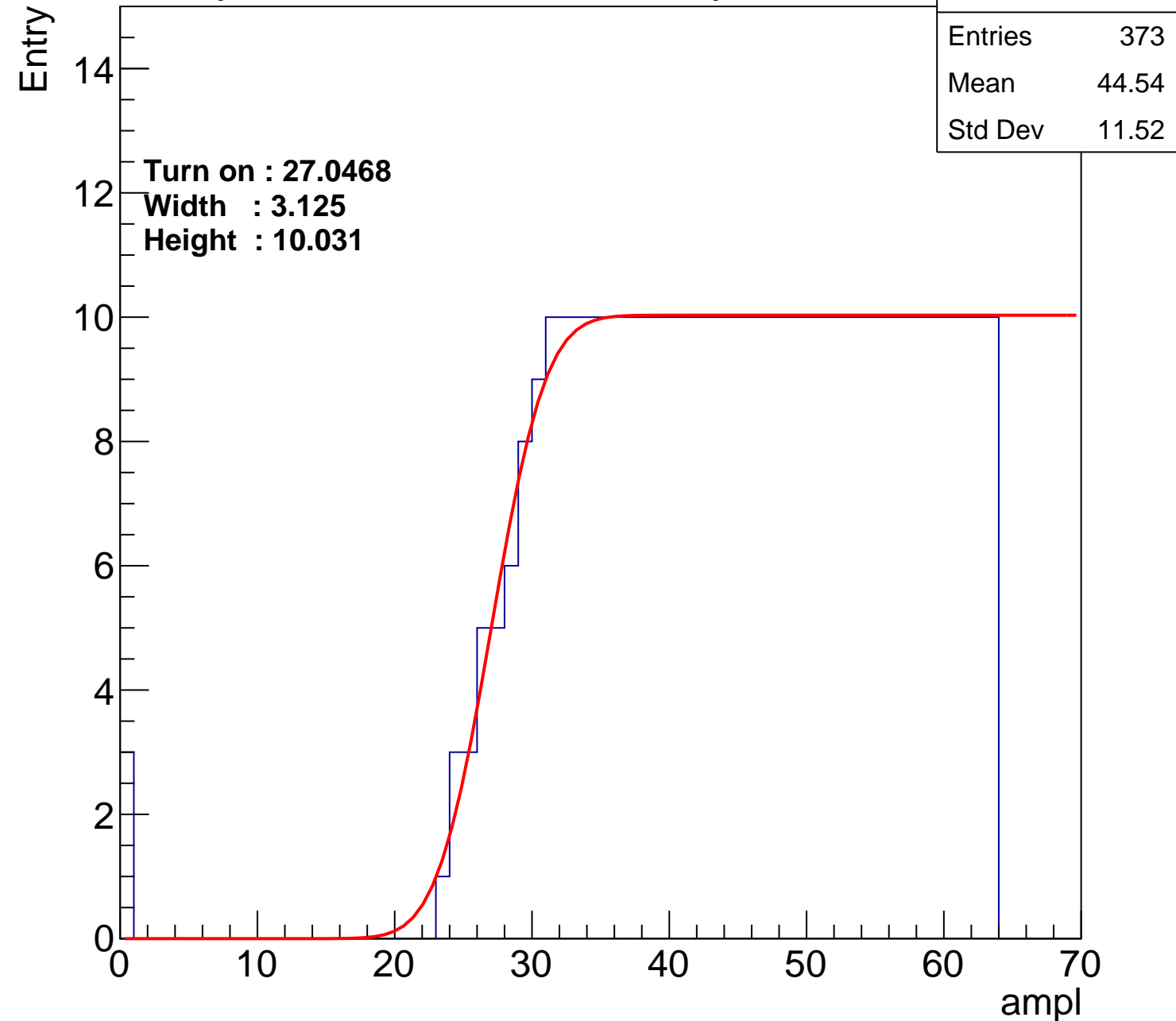
Width : 3.125

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch100

calib_packv5_042523_0143.root, FC#13, port D2

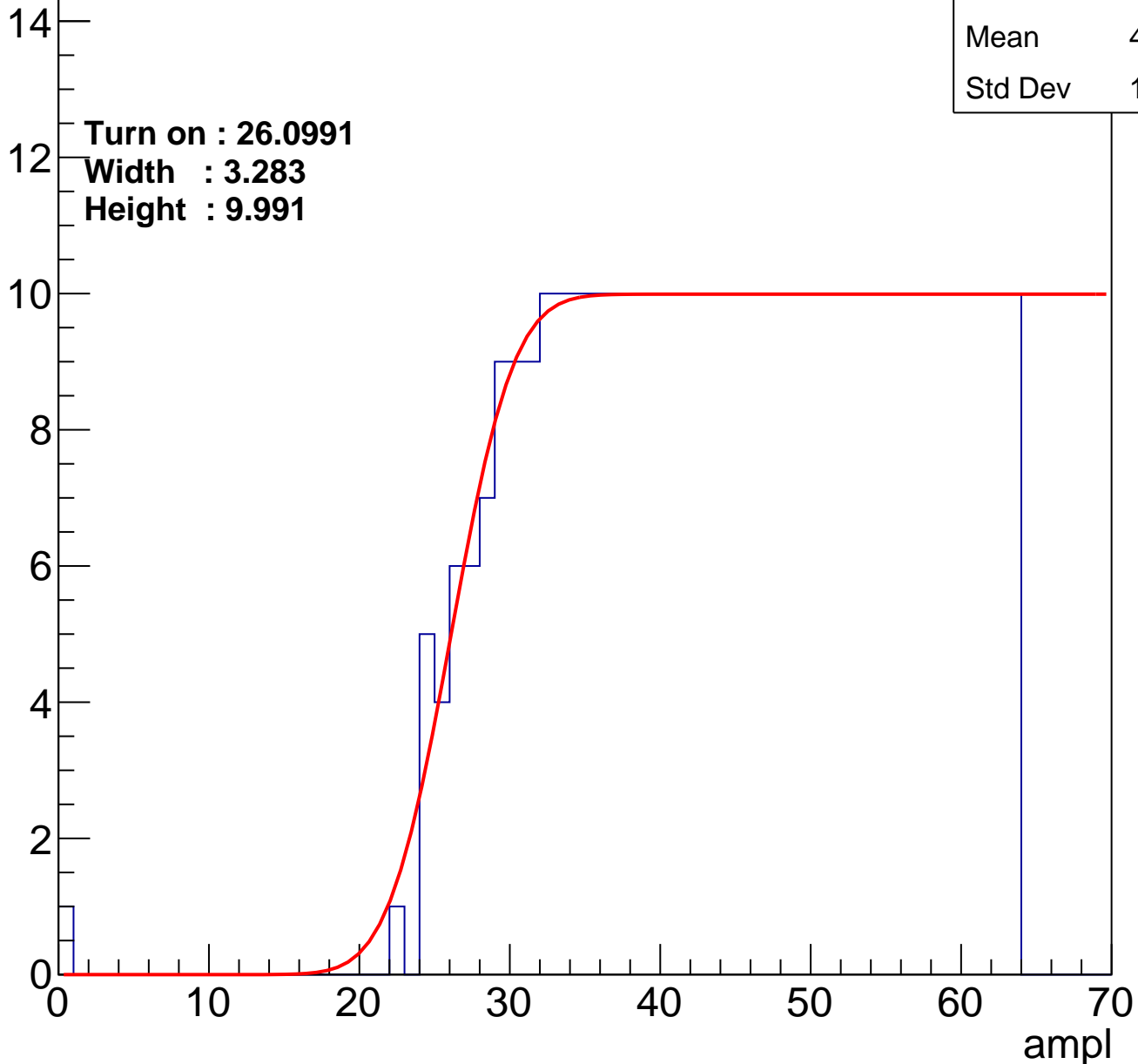
Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.0991

Width : 3.283

Height : 9.991

Entry



B1L003S, U25-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.03
Std Dev	10.95

Turn on : 27.5535

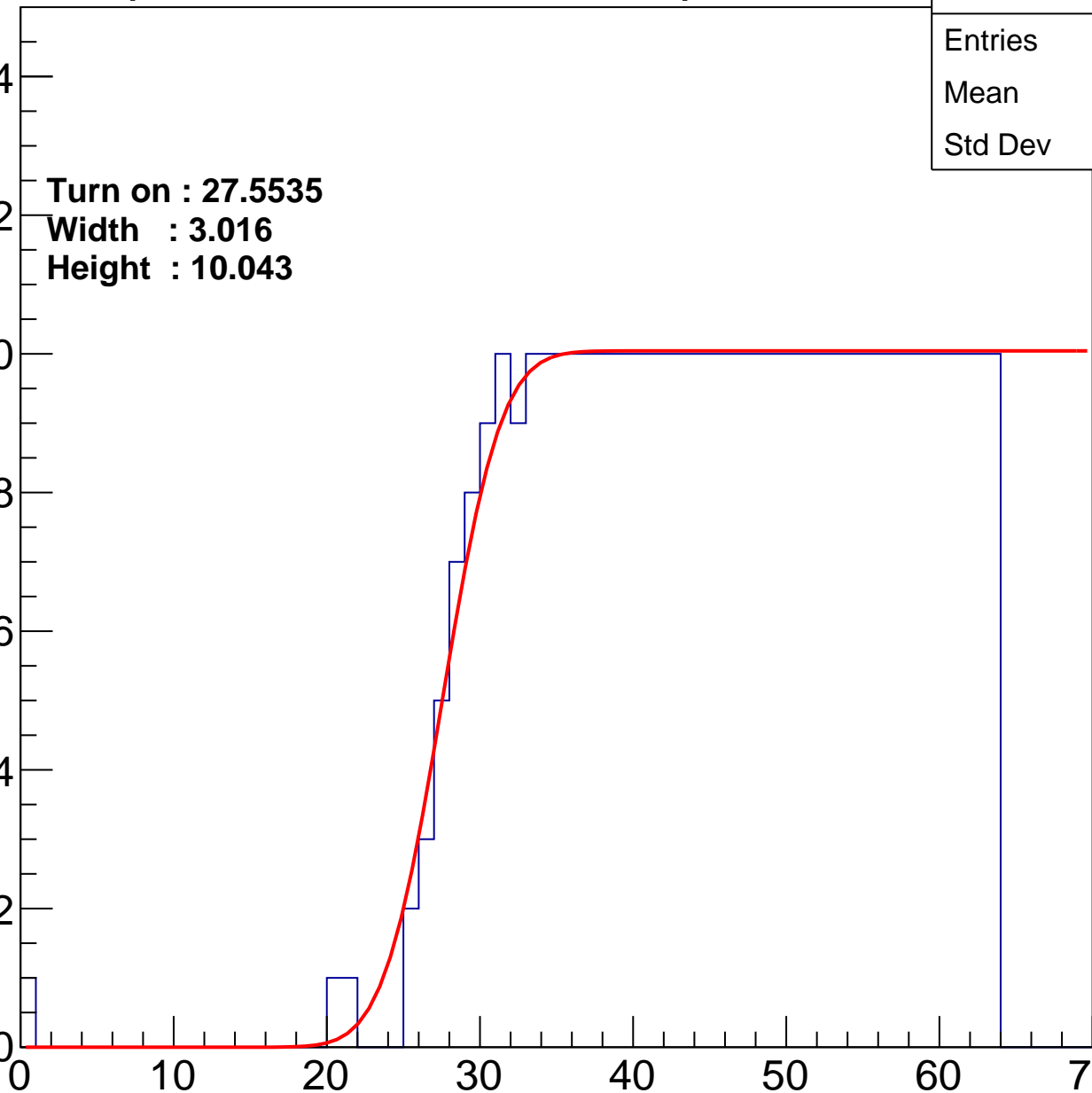
Width : 3.016

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.7
Std Dev	11.1

Turn on : 26.8483

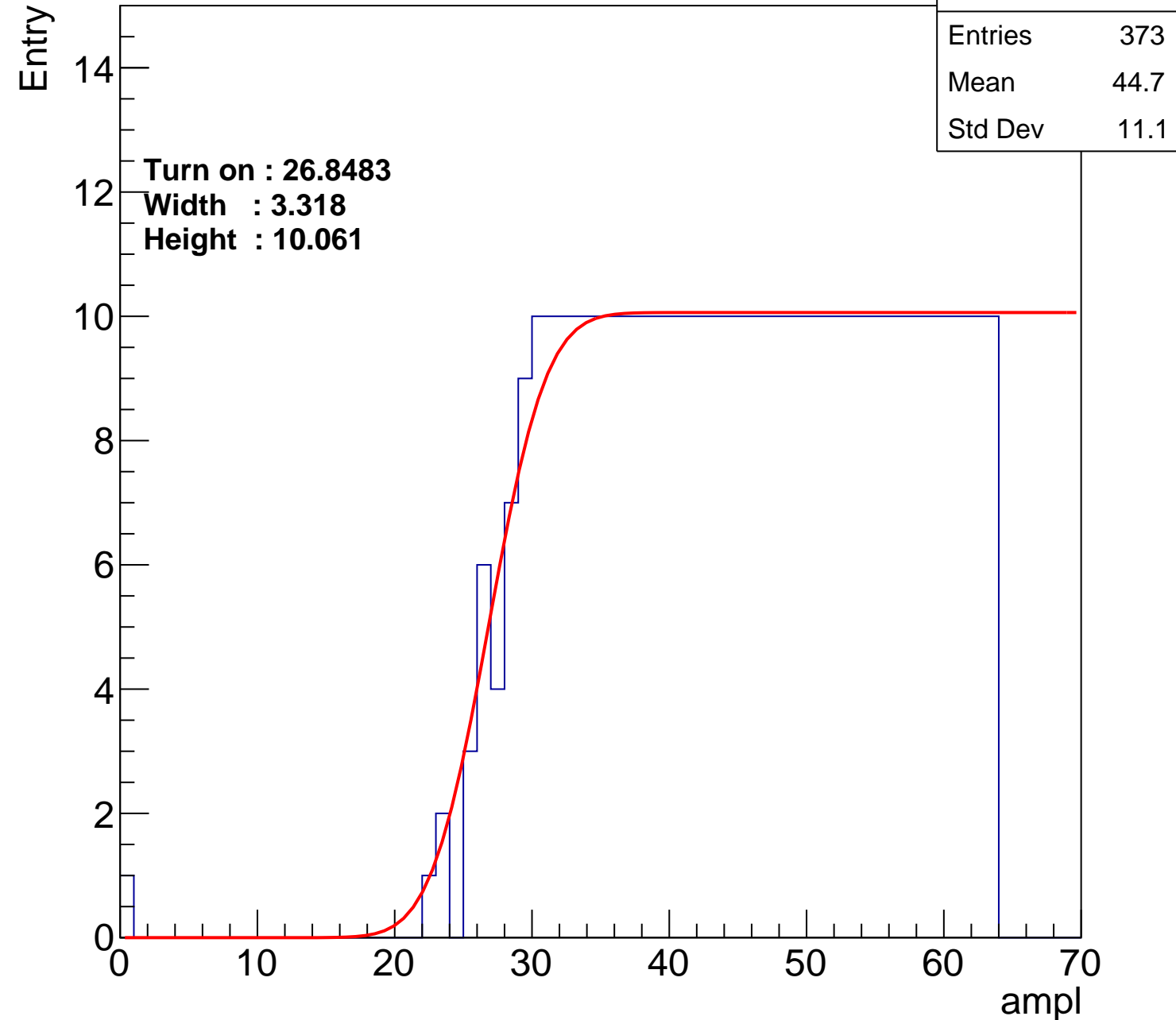
Width : 3.318

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch103

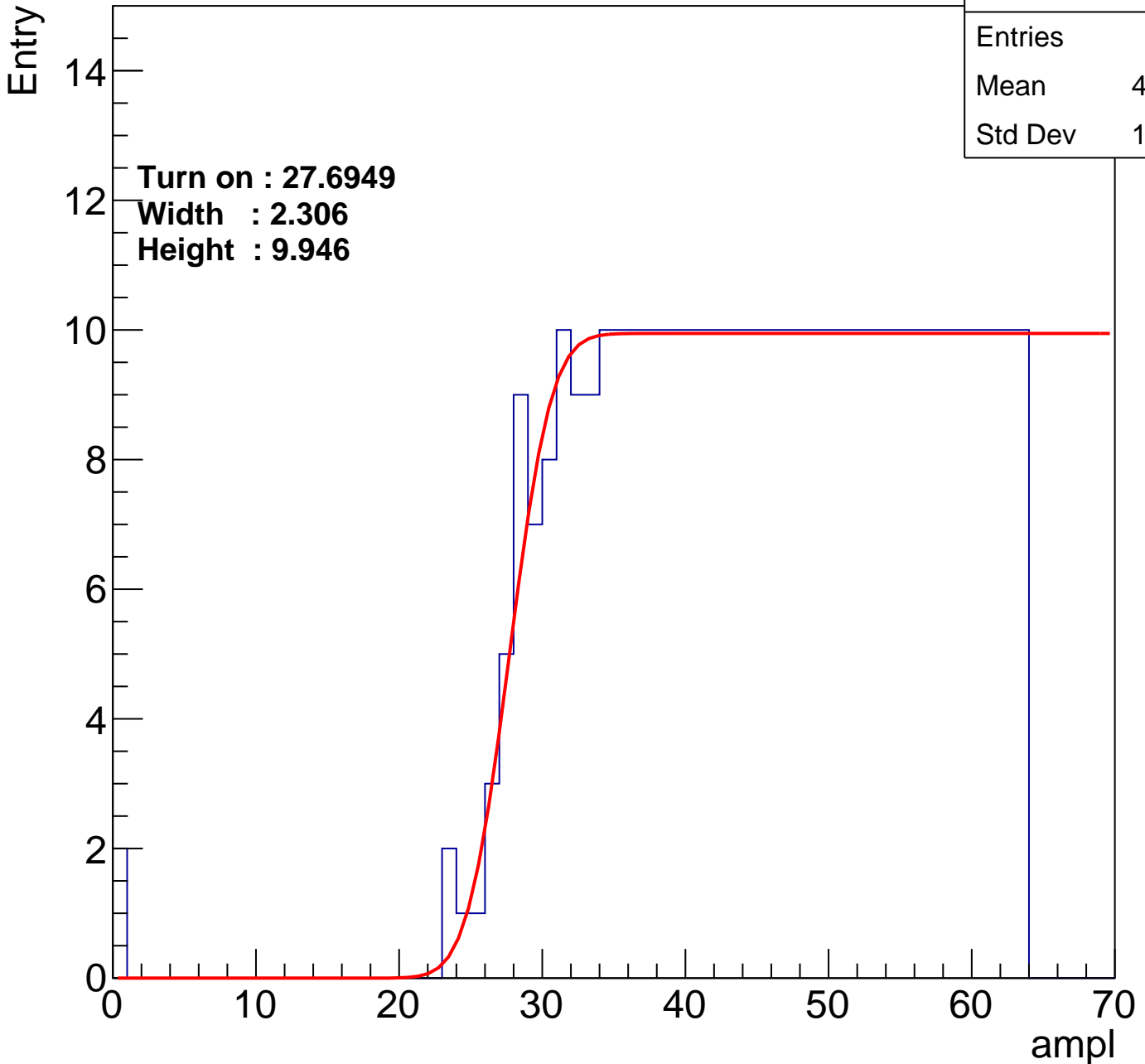
calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.6949

Width : 2.306

Height : 9.946



B1L003S, U25-ch104

calib_packv5_042523_0143.root, FC#13, port D2

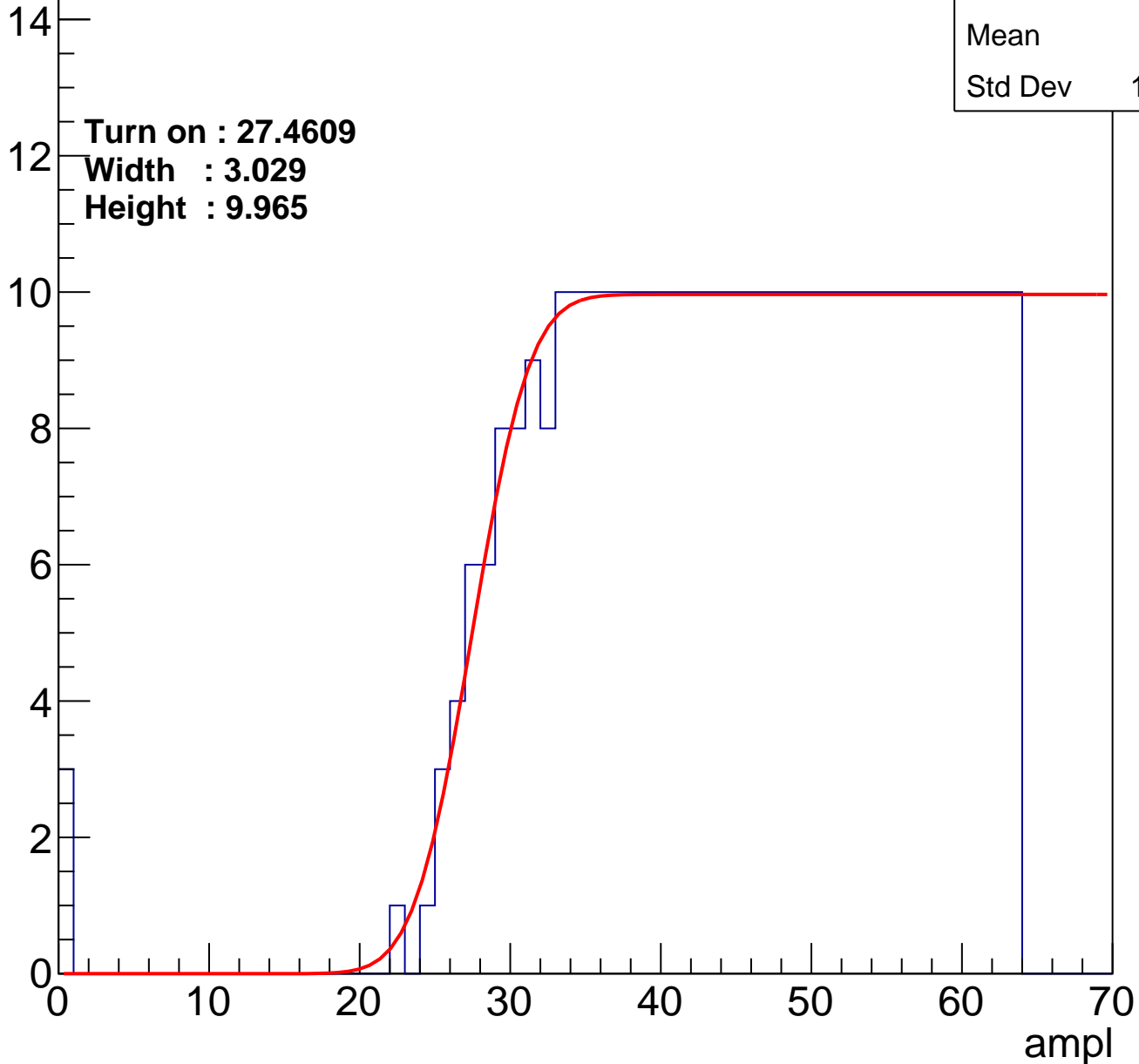
Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 27.4609

Width : 3.029

Height : 9.965

Entry



B1L003S, U25-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.22
Std Dev	11.89

Turn on : 27.1931

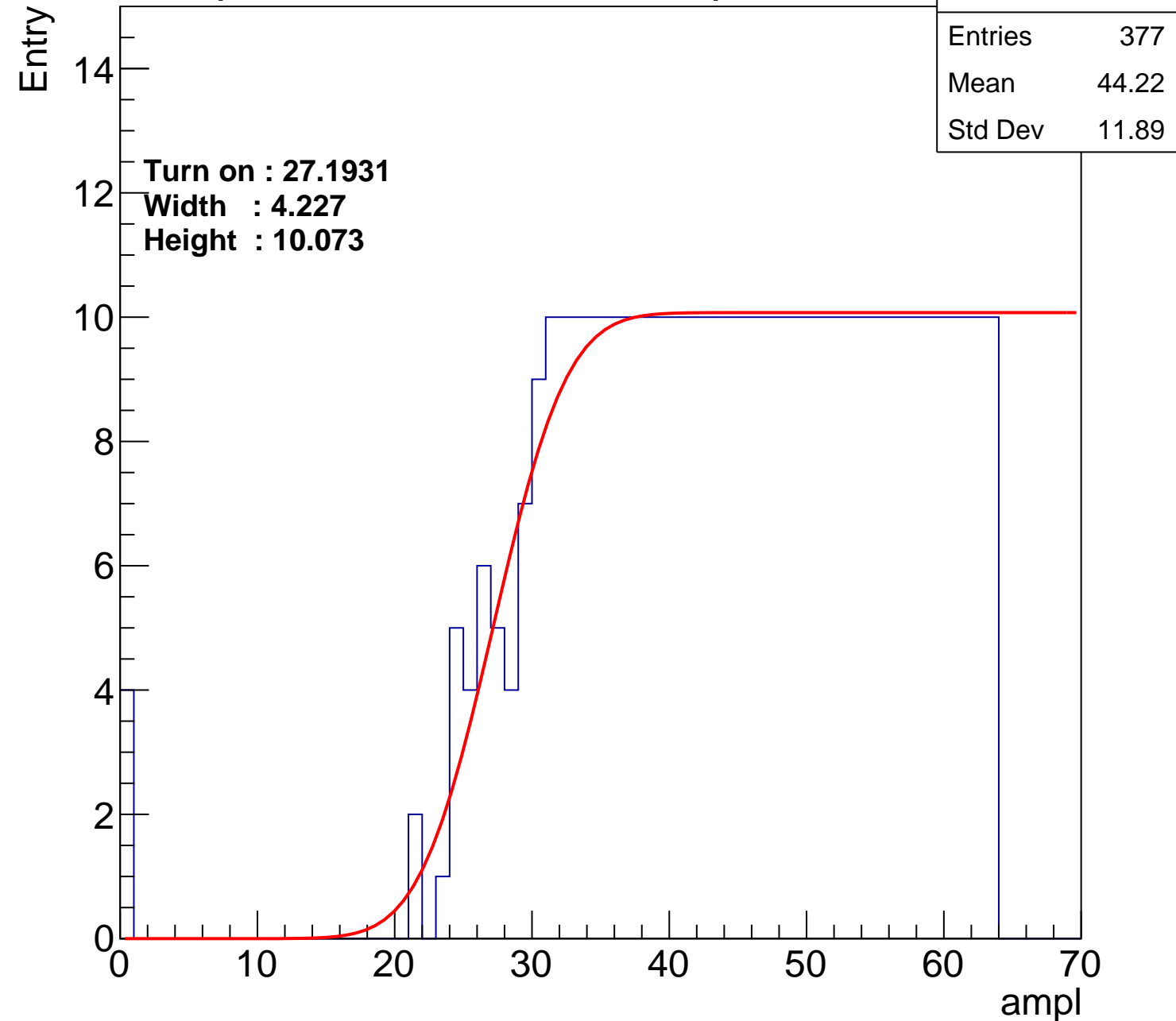
Width : 4.227

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.5324

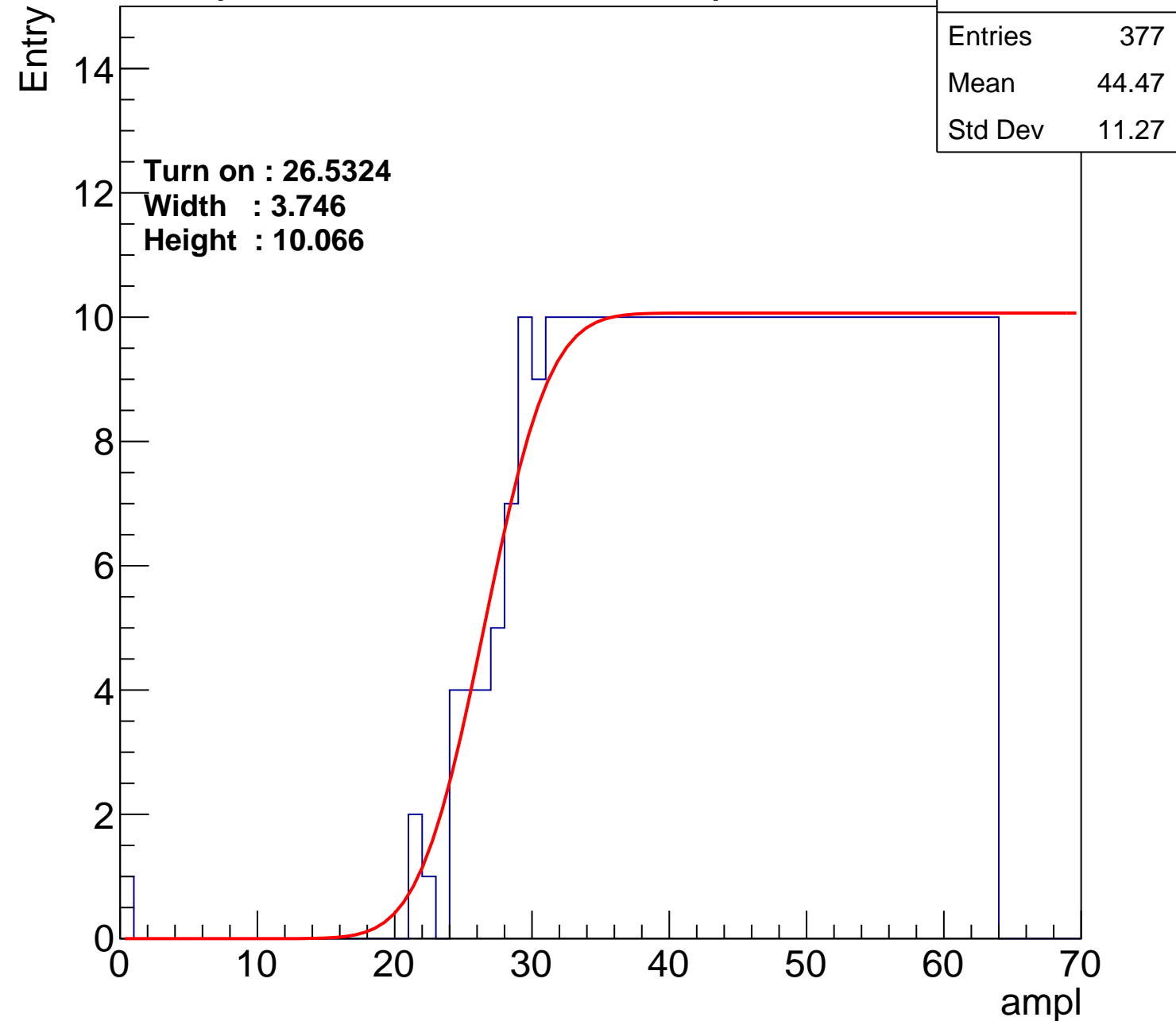
Width : 3.746

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.92
Std Dev	11.54

Turn on : 27.8448

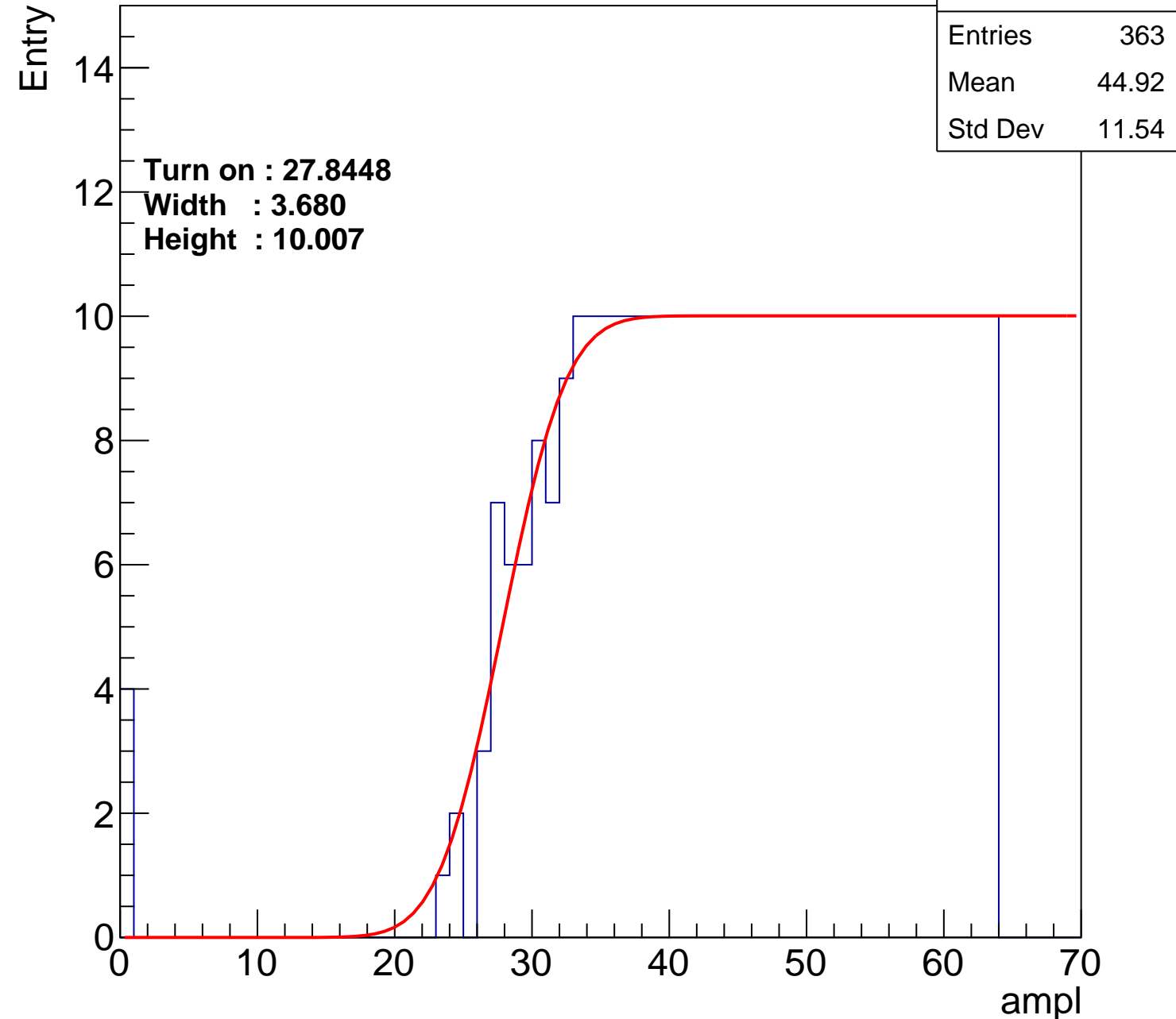
Width : 3.680

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch108

calib_packv5_042523_0143.root, FC#13, port D2

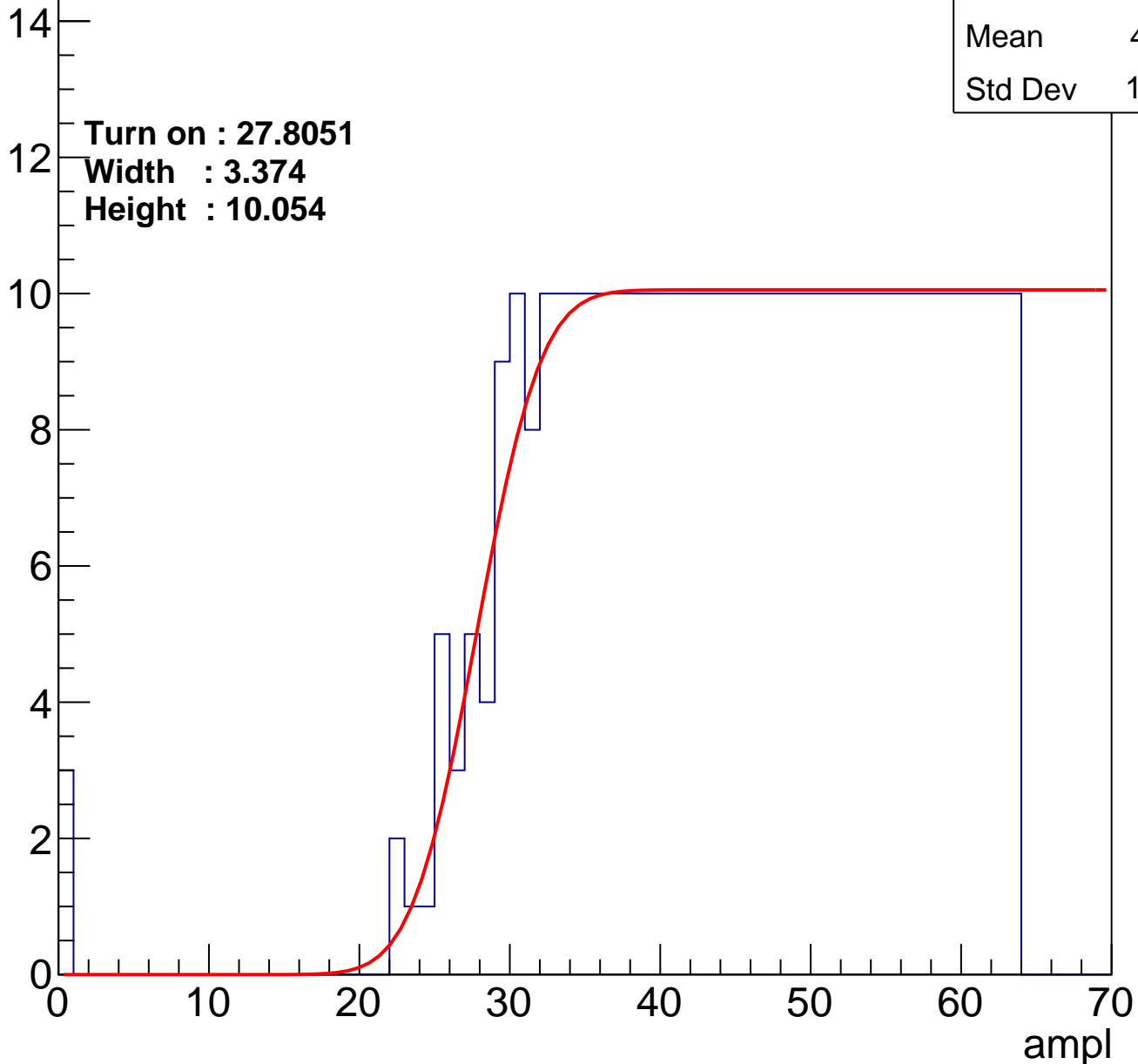
Entries	371
Mean	44.61
Std Dev	11.52

Turn on : 27.8051

Width : 3.374

Height : 10.054

Entry



B1L003S, U25-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.96
Std Dev	11.97

Turn on : 26.1323

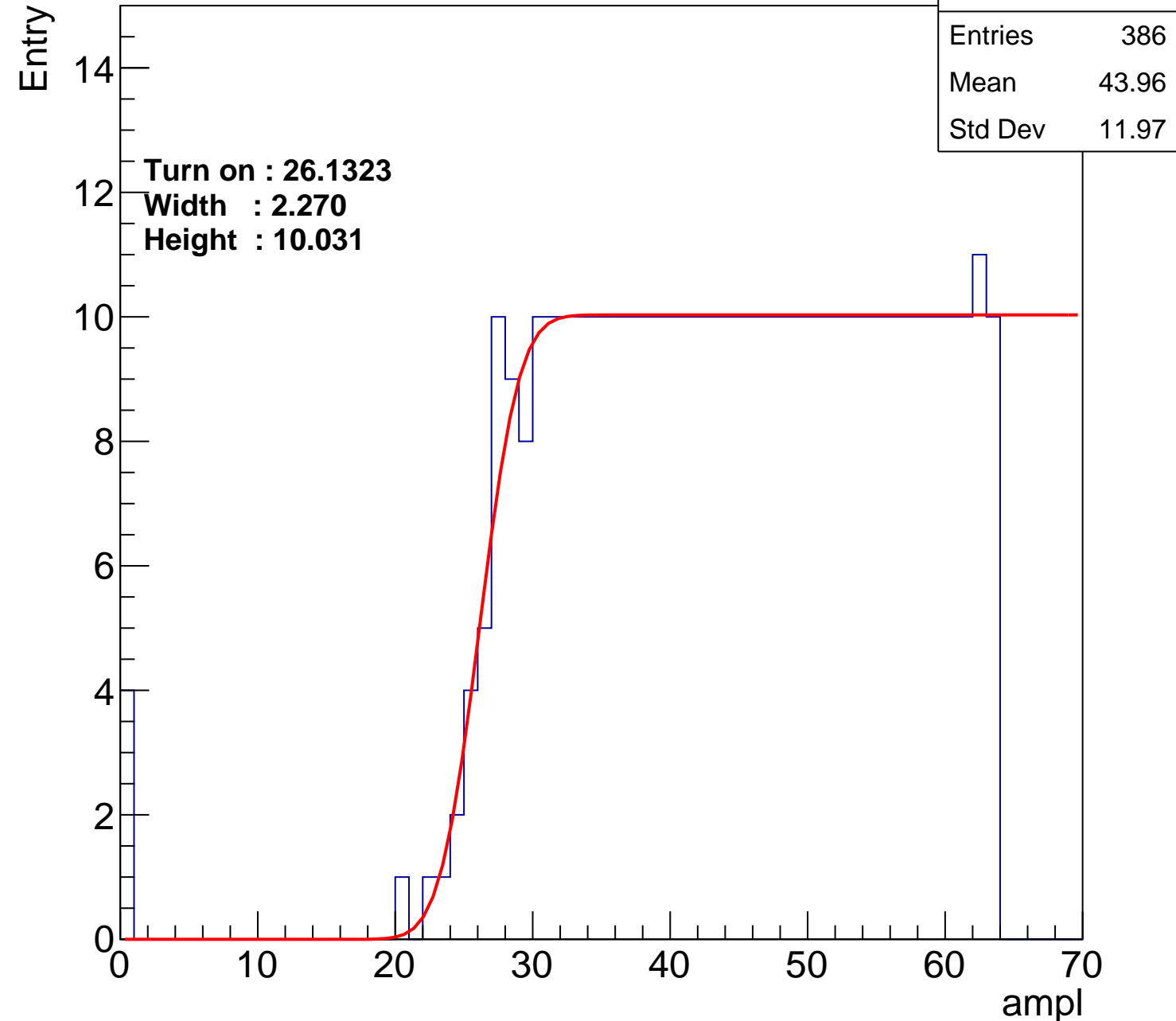
Width : 2.270

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.5608

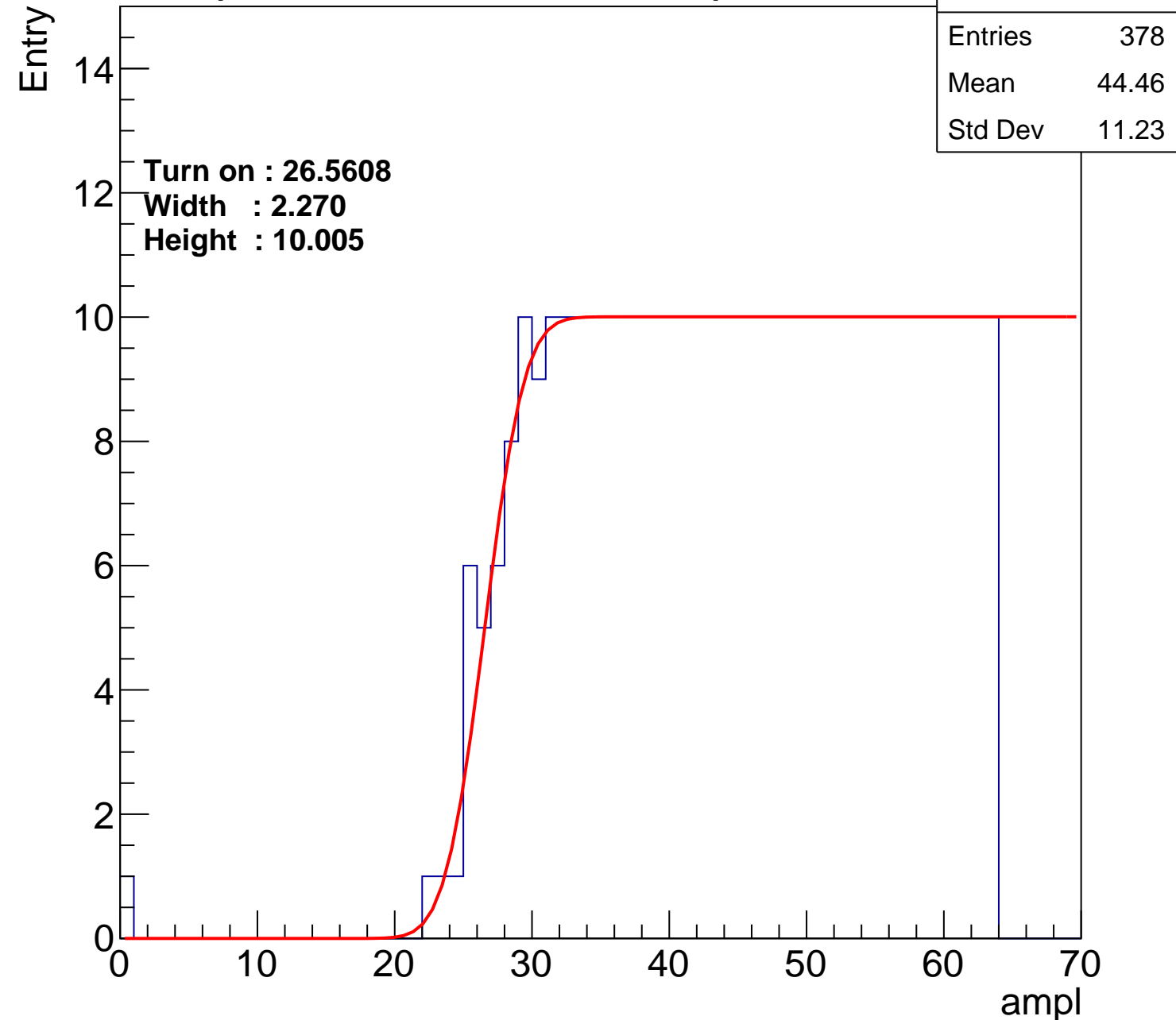
Width : 2.270

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.79
Std Dev	10.56

Turn on : 29.0548

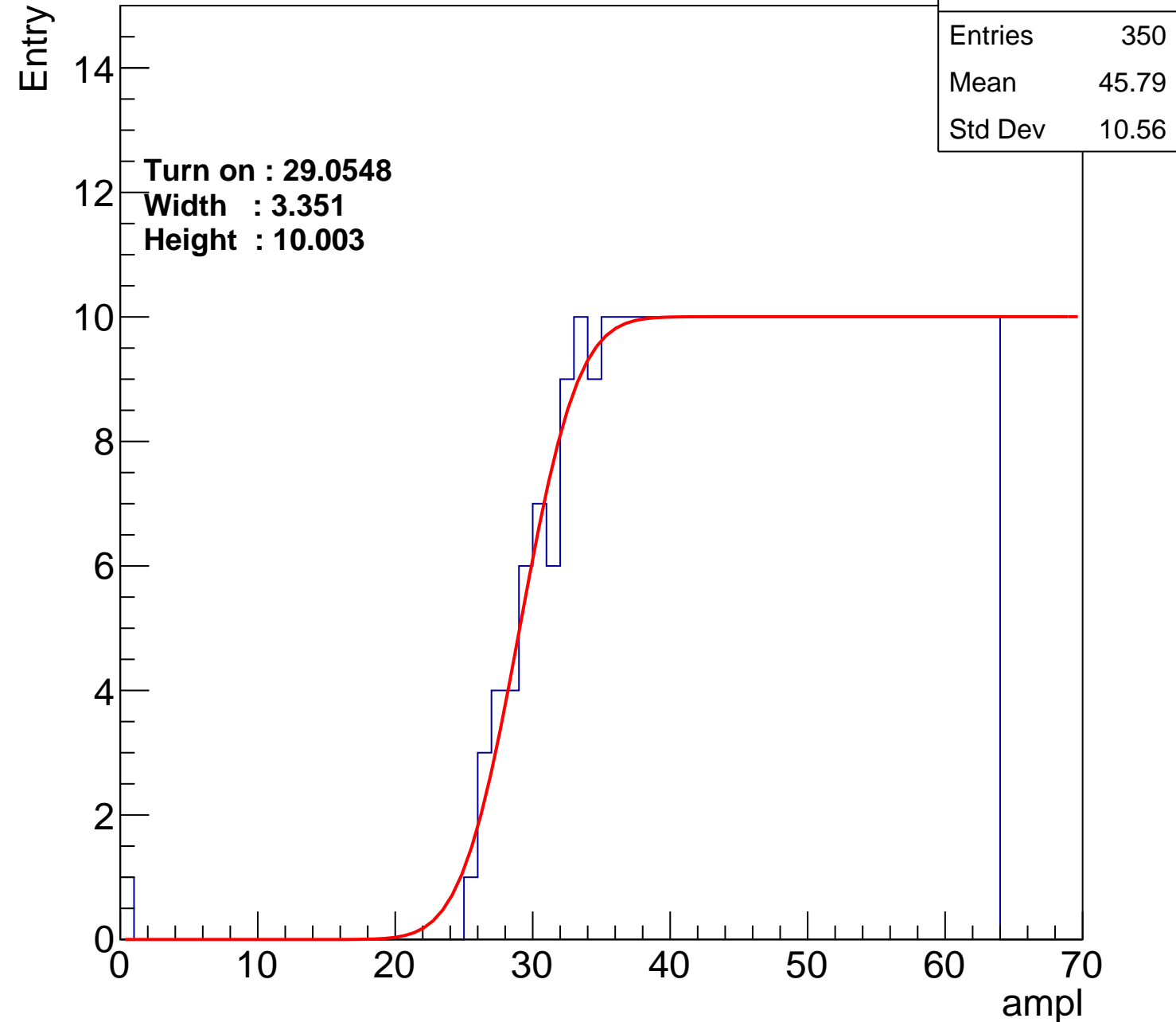
Width : 3.351

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.92
Std Dev	11.98

Turn on : 26.4888

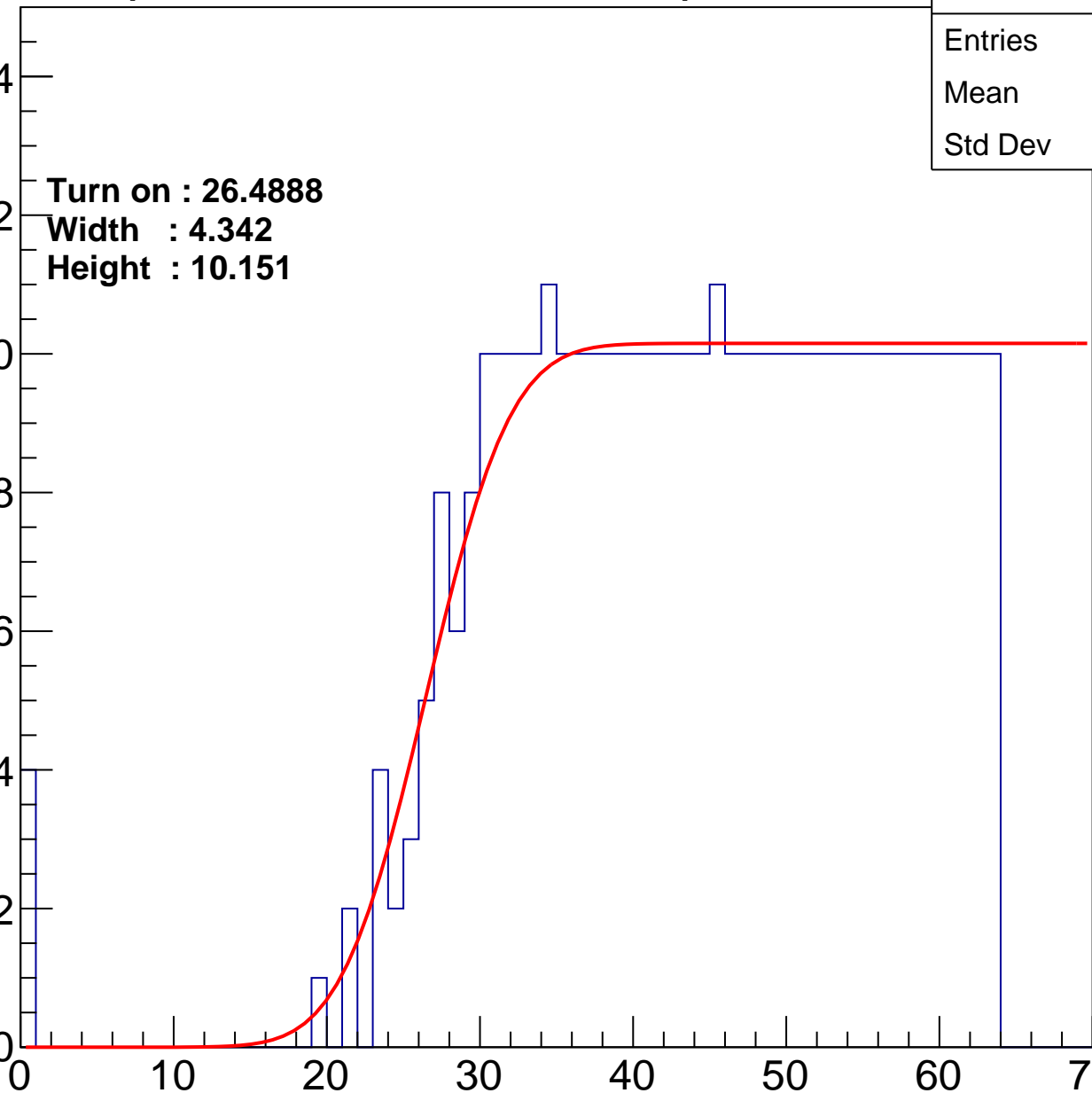
Width : 4.342

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 27.4479

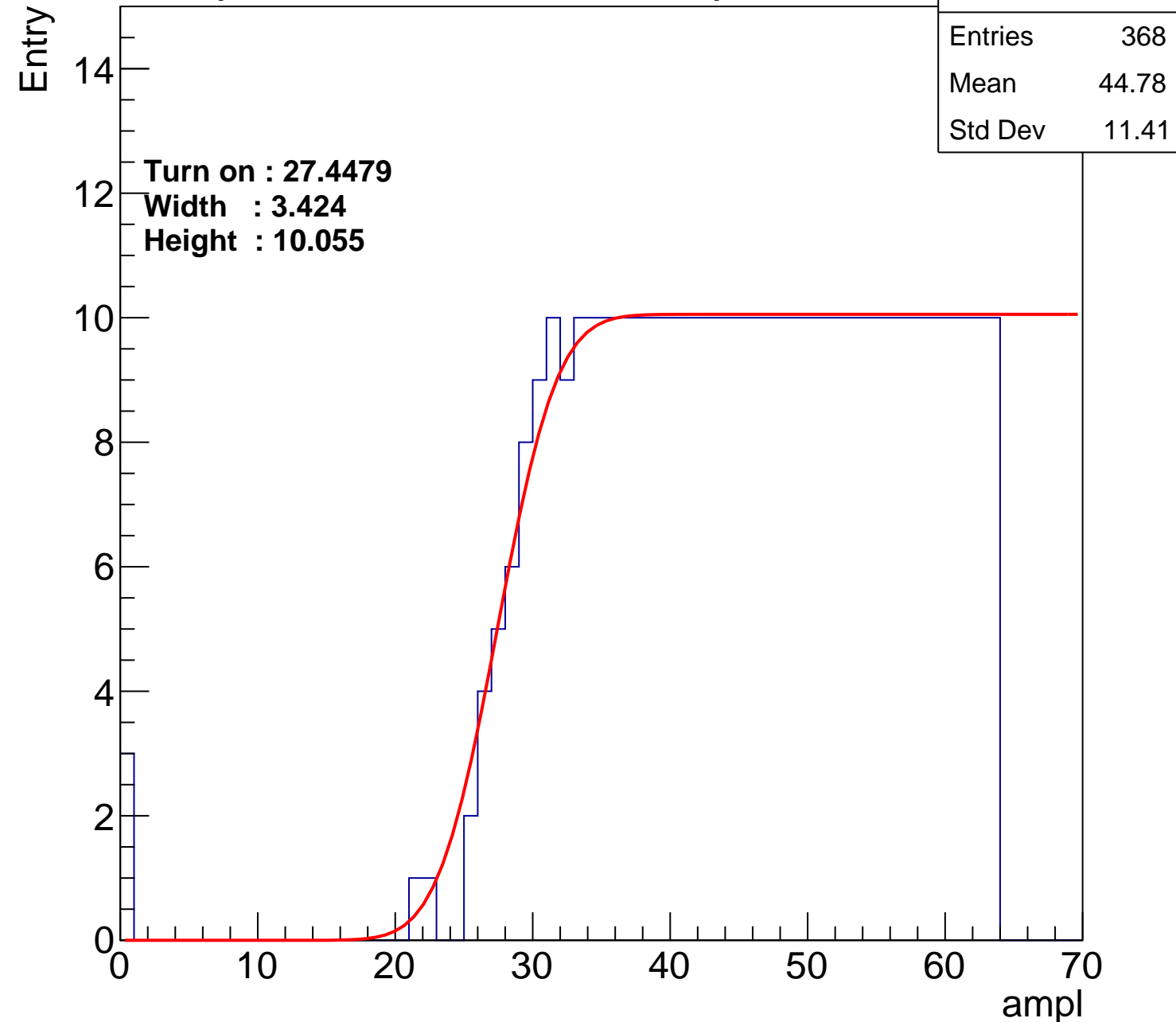
Width : 3.424

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	352
Mean	45.7
Std Dev	10.6

Turn on : 28.8860

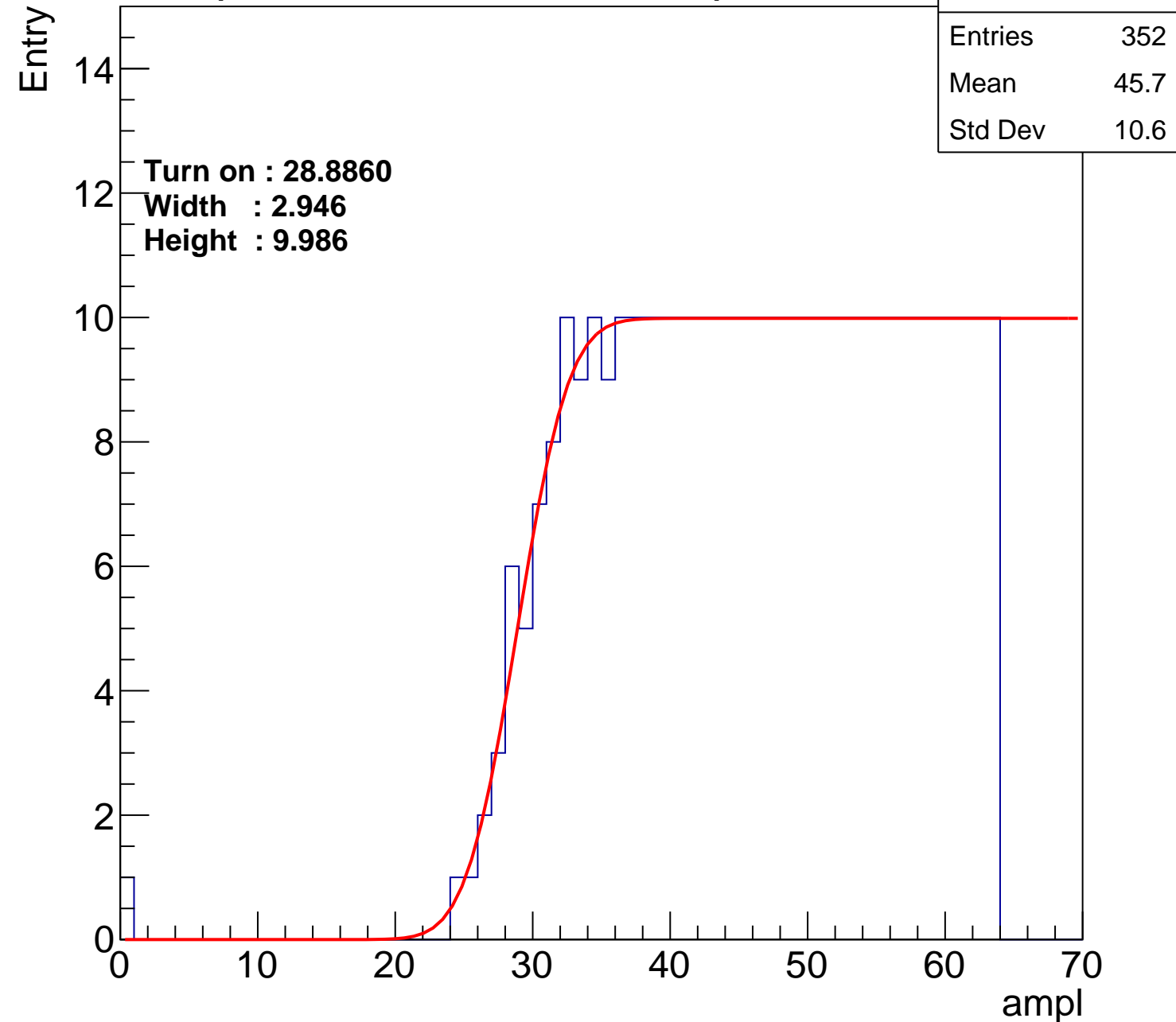
Width : 2.946

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.87
Std Dev	11.23

Turn on : 27.4148

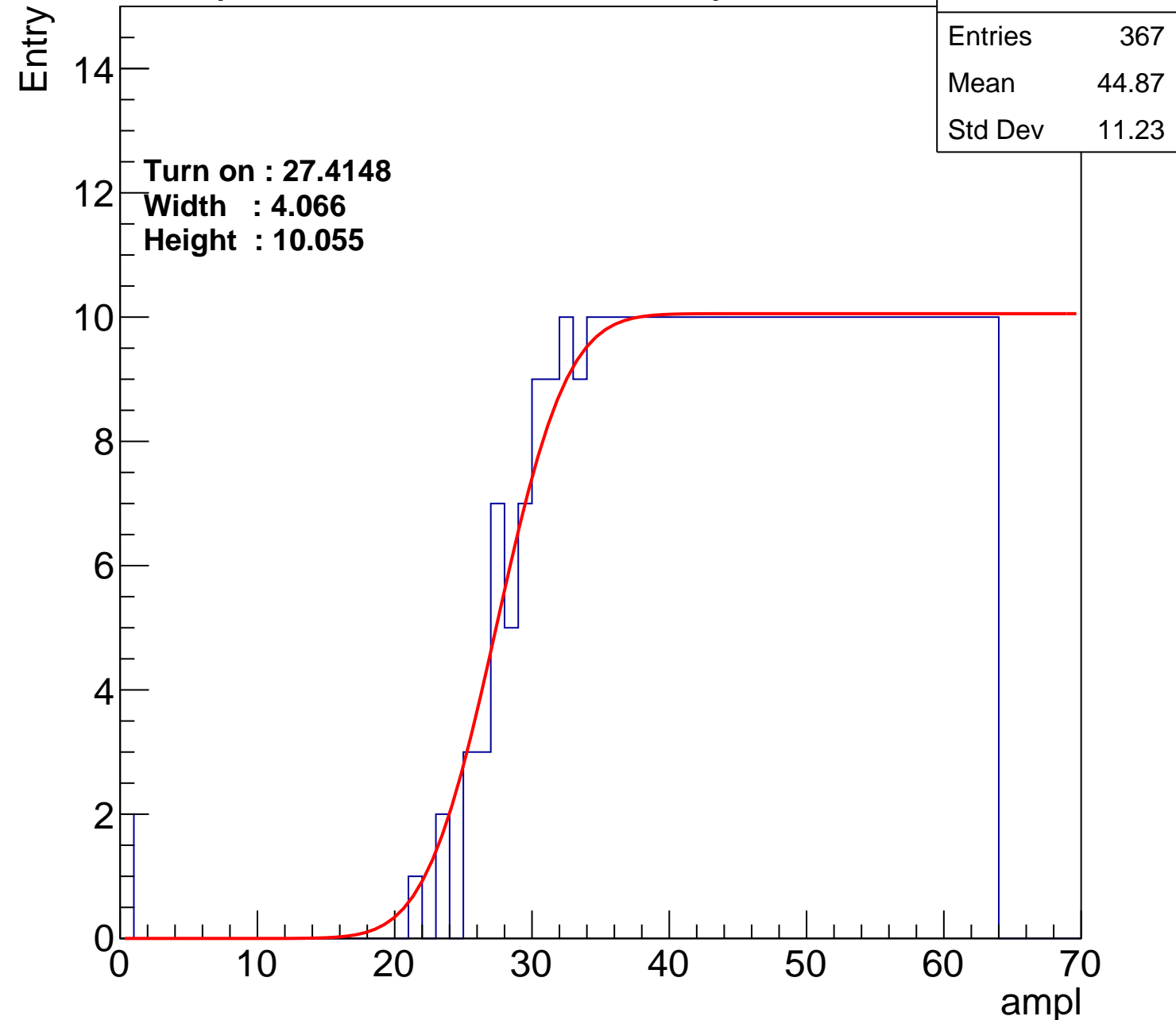
Width : 4.066

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.79
Std Dev	11.59

Turn on : 28.0226

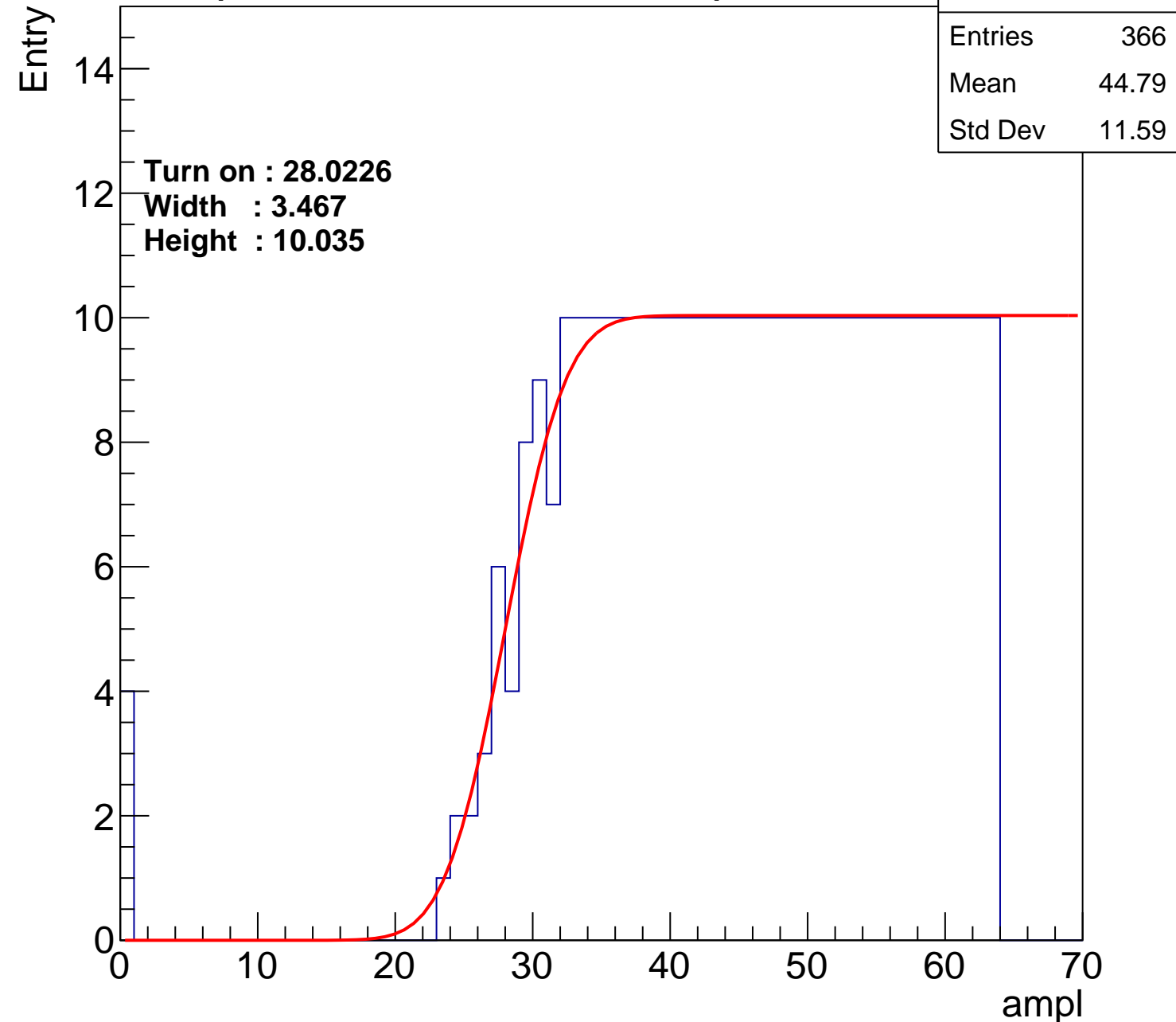
Width : 3.467

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.02
Std Dev	11.18

Turn on : 28.2144

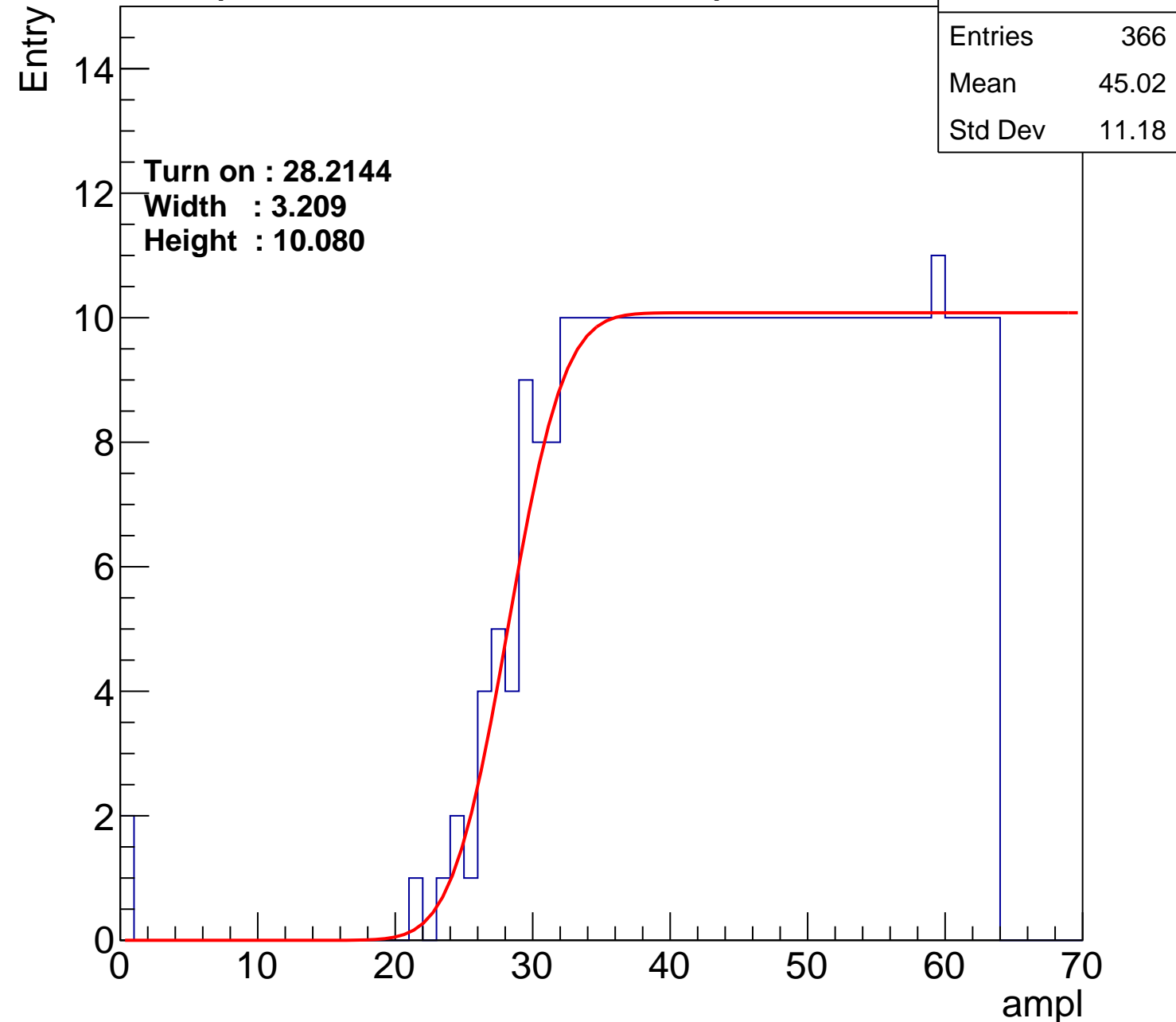
Width : 3.209

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.18
Std Dev	11.26

Turn on : 28.5862

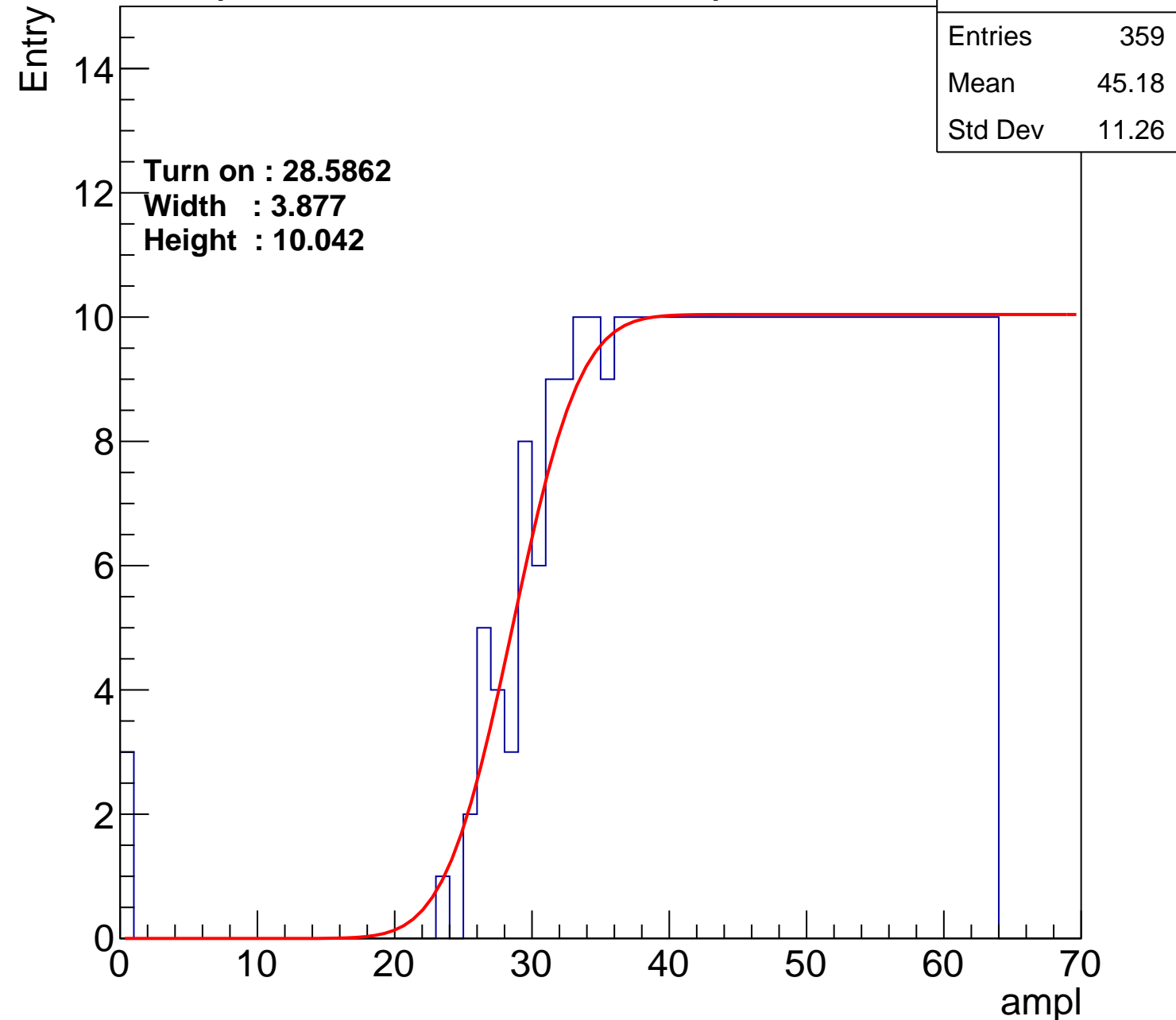
Width : 3.877

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch119

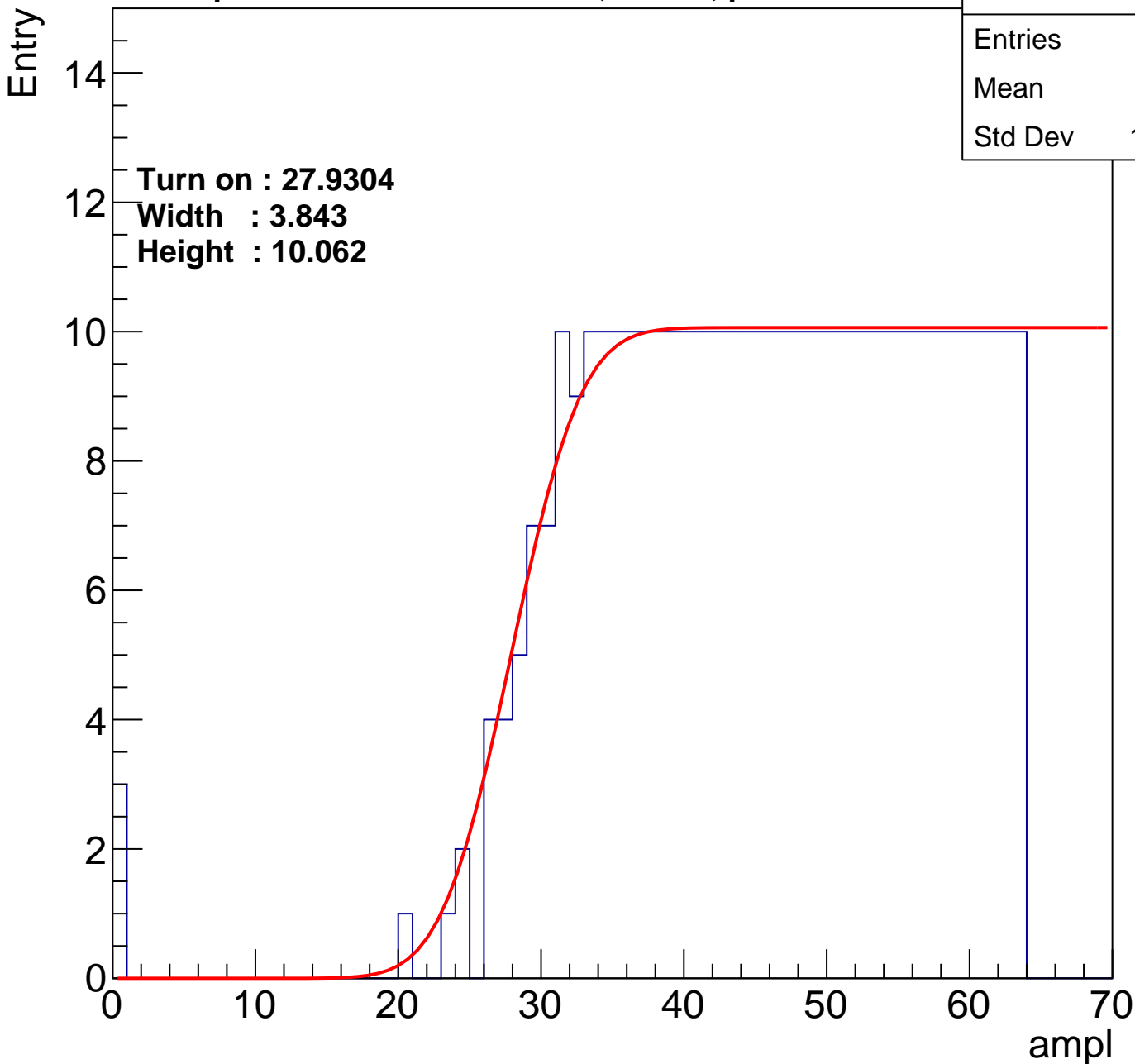
calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45
Std Dev	11.34

Turn on : 27.9304

Width : 3.843

Height : 10.062



B1L003S, U25-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.53
Std Dev	12.3

Turn on : 25.5626

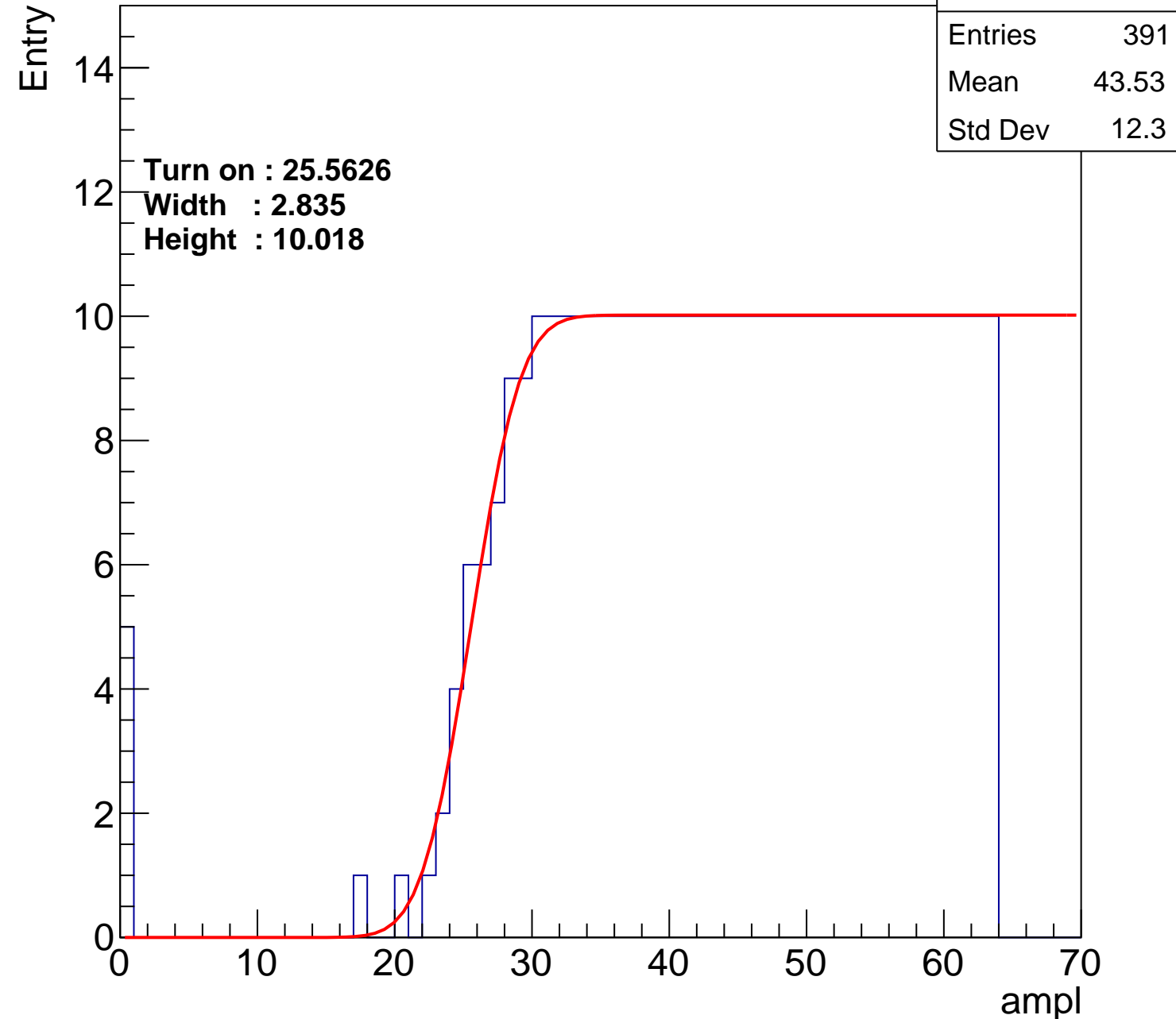
Width : 2.835

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.32
Std Dev	11.55

Turn on : 29.8412

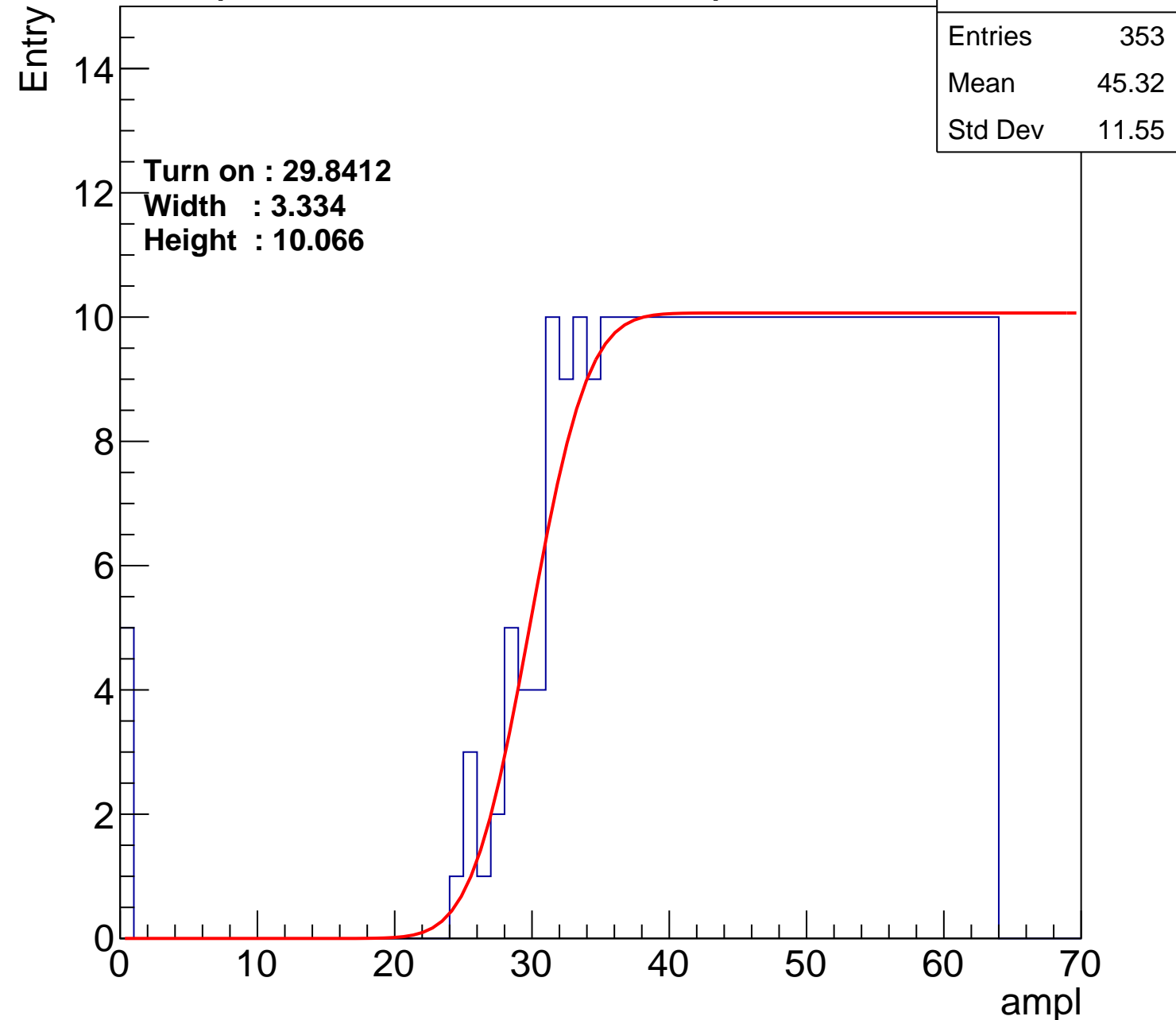
Width : 3.334

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries 369

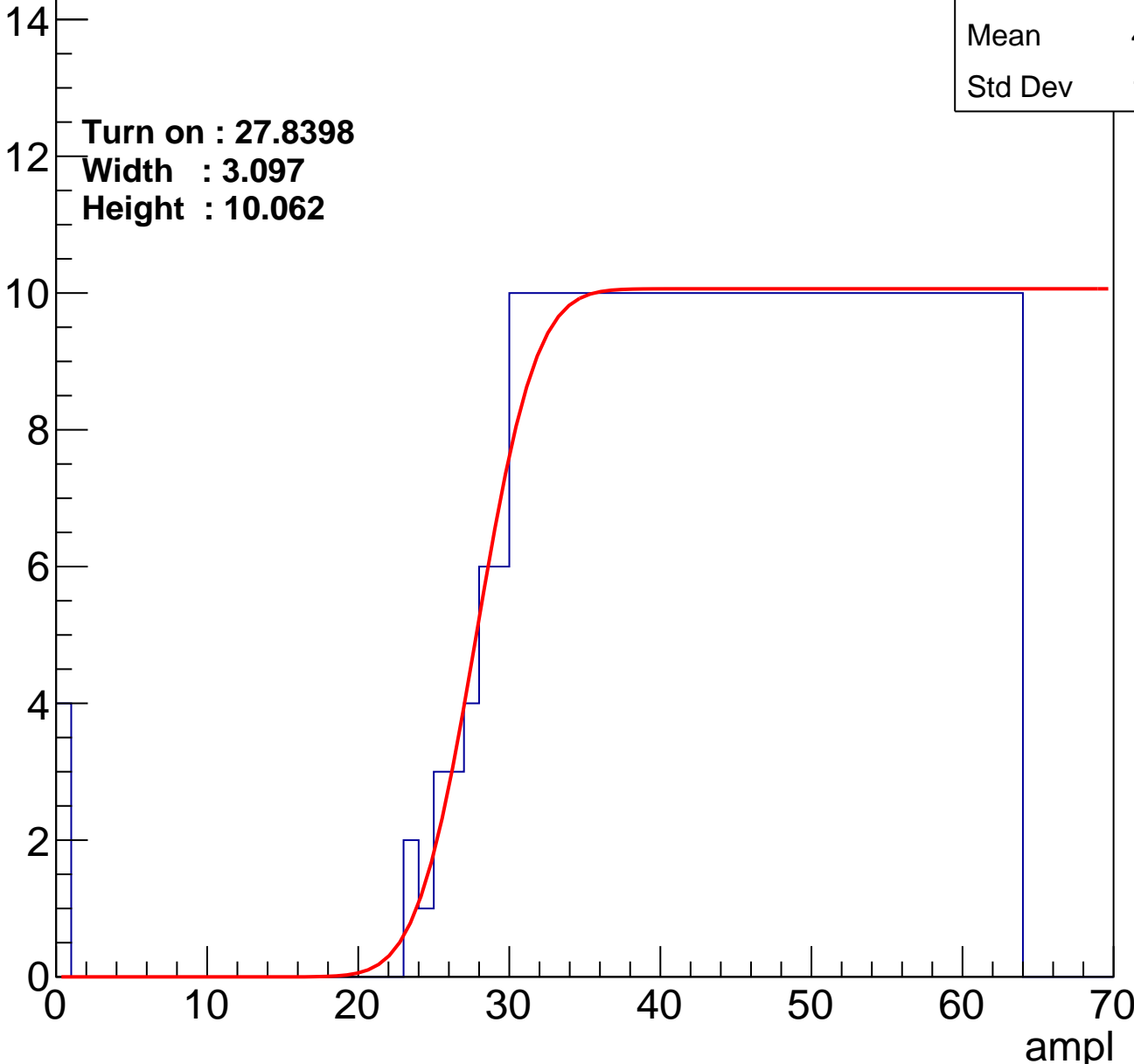
Mean 44.67

Std Dev 11.62

Turn on : 27.8398

Width : 3.097

Height : 10.062



B1L003S, U25-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	343
Mean	46.17
Std Dev	10.32

Turn on : 30.1173

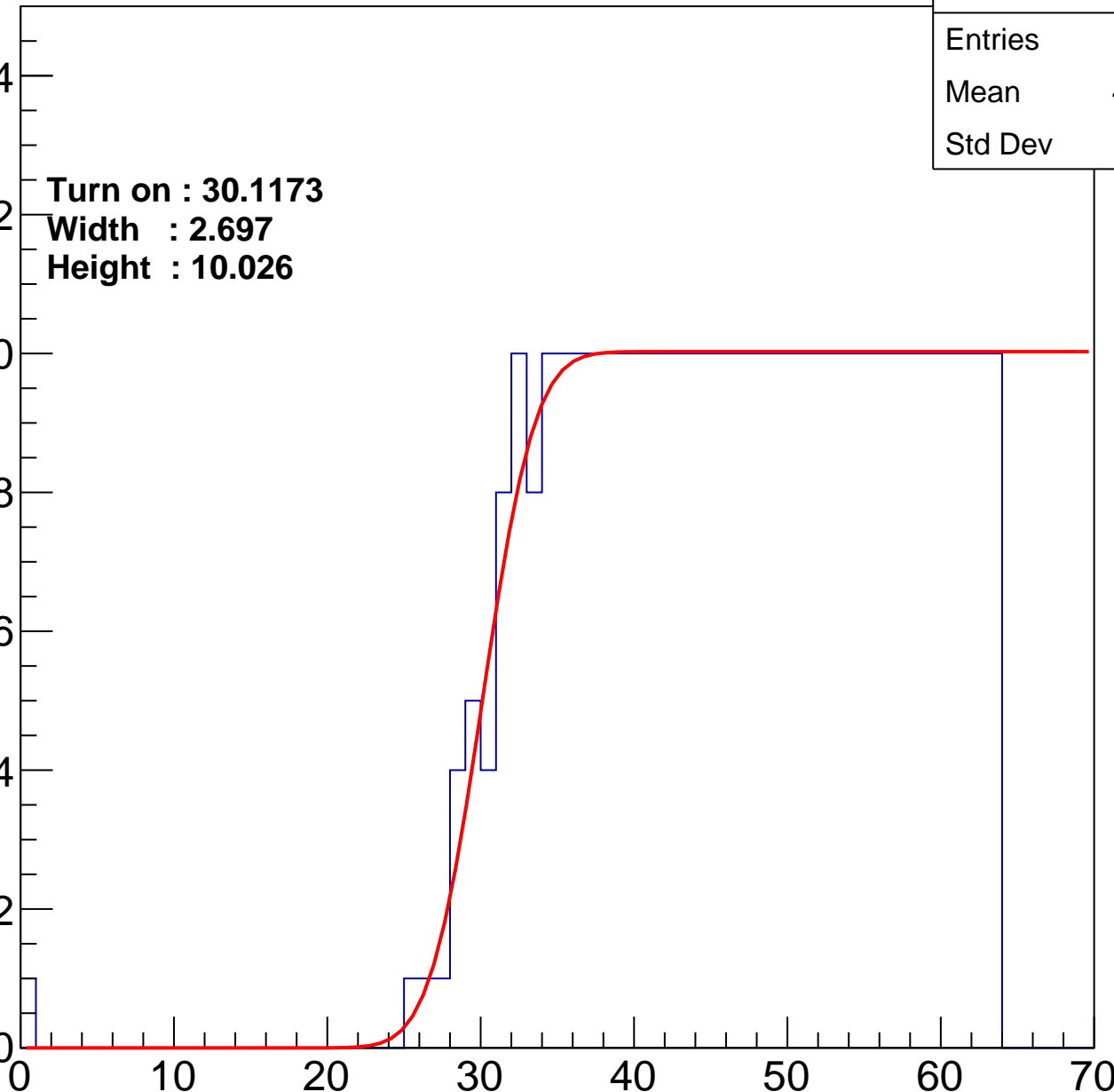
Width : 2.697

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.45
Std Dev	11.7

Turn on : 26.7526

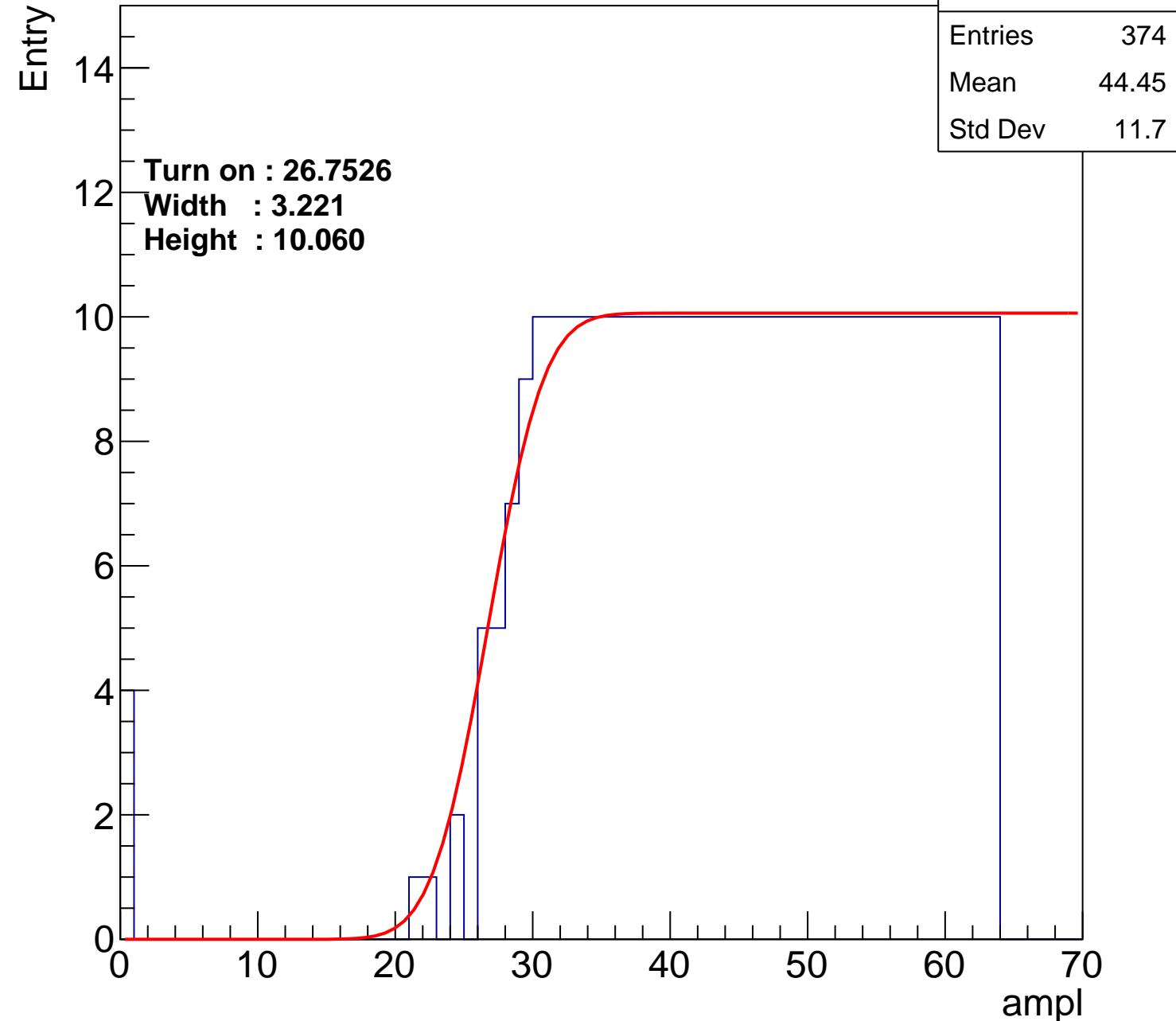
Width : 3.221

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch125

calib_packv5_042523_0143.root, FC#13, port D2

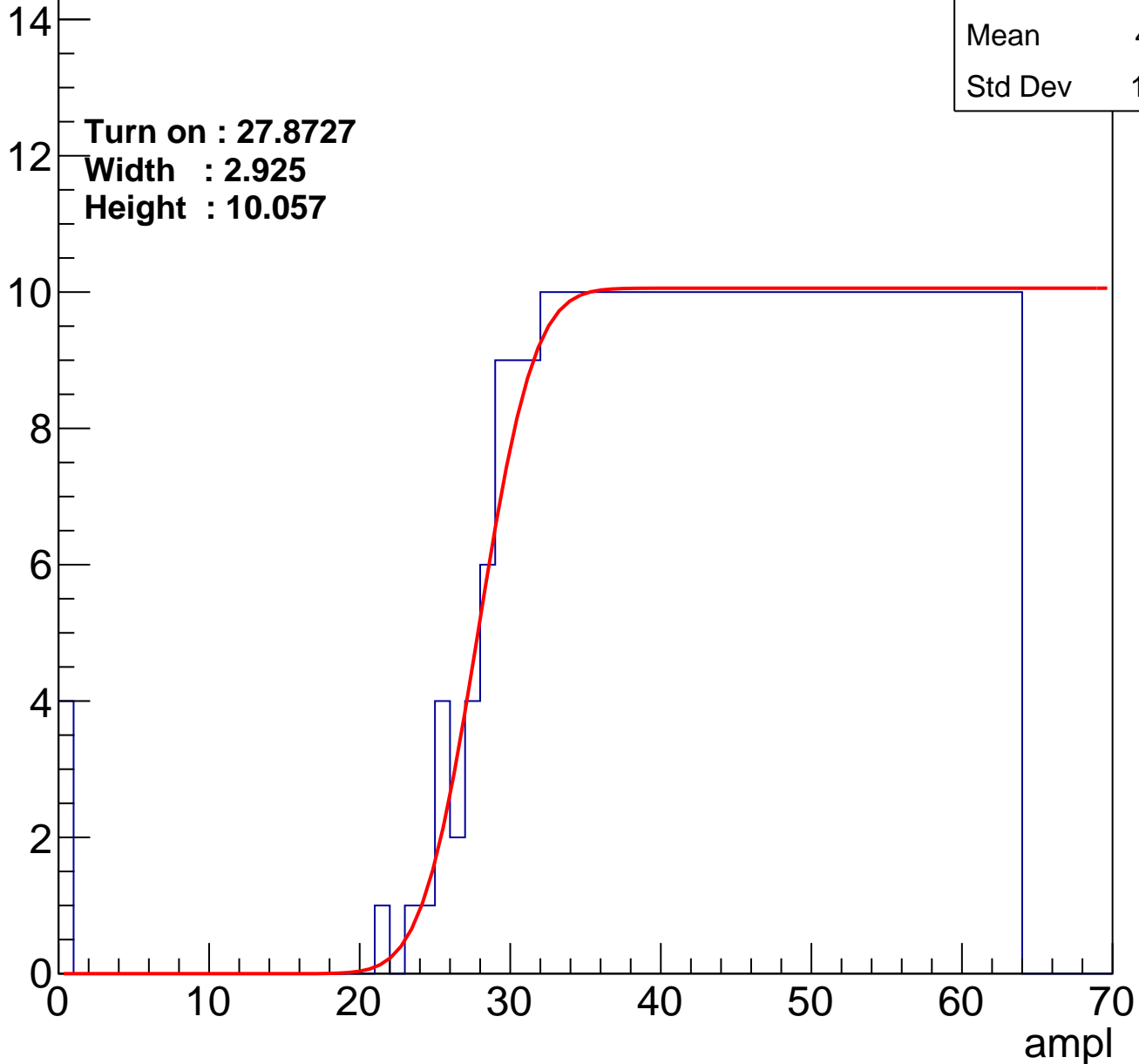
Entries	370
Mean	44.61
Std Dev	11.66

Turn on : 27.8727

Width : 2.925

Height : 10.057

Entry



B1L003S, U25-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.34
Std Dev	11.8

Turn on : 27.1904

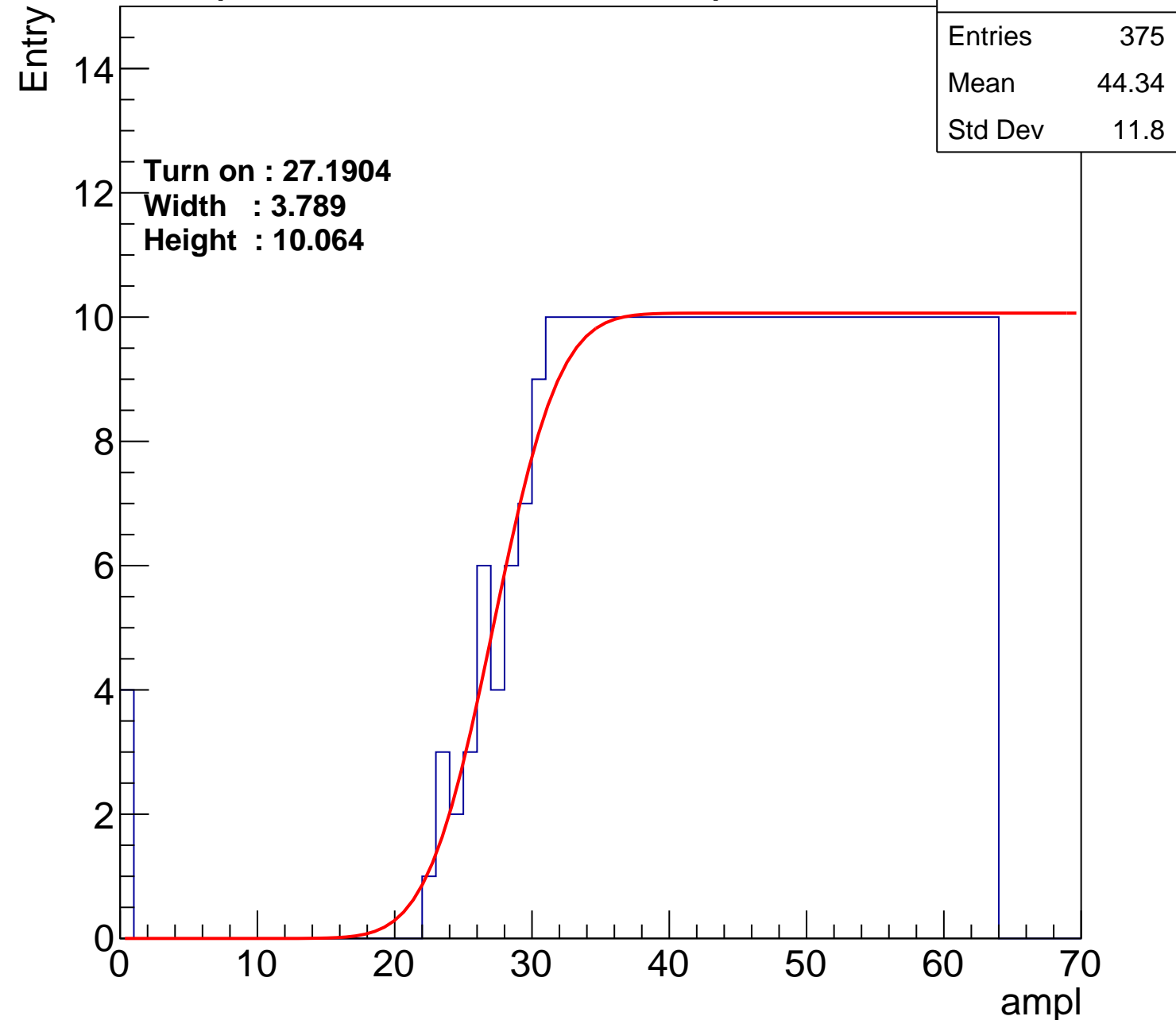
Width : 3.789

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U25-ch127

calib_packv5_042523_0143.root, FC#13, port D2

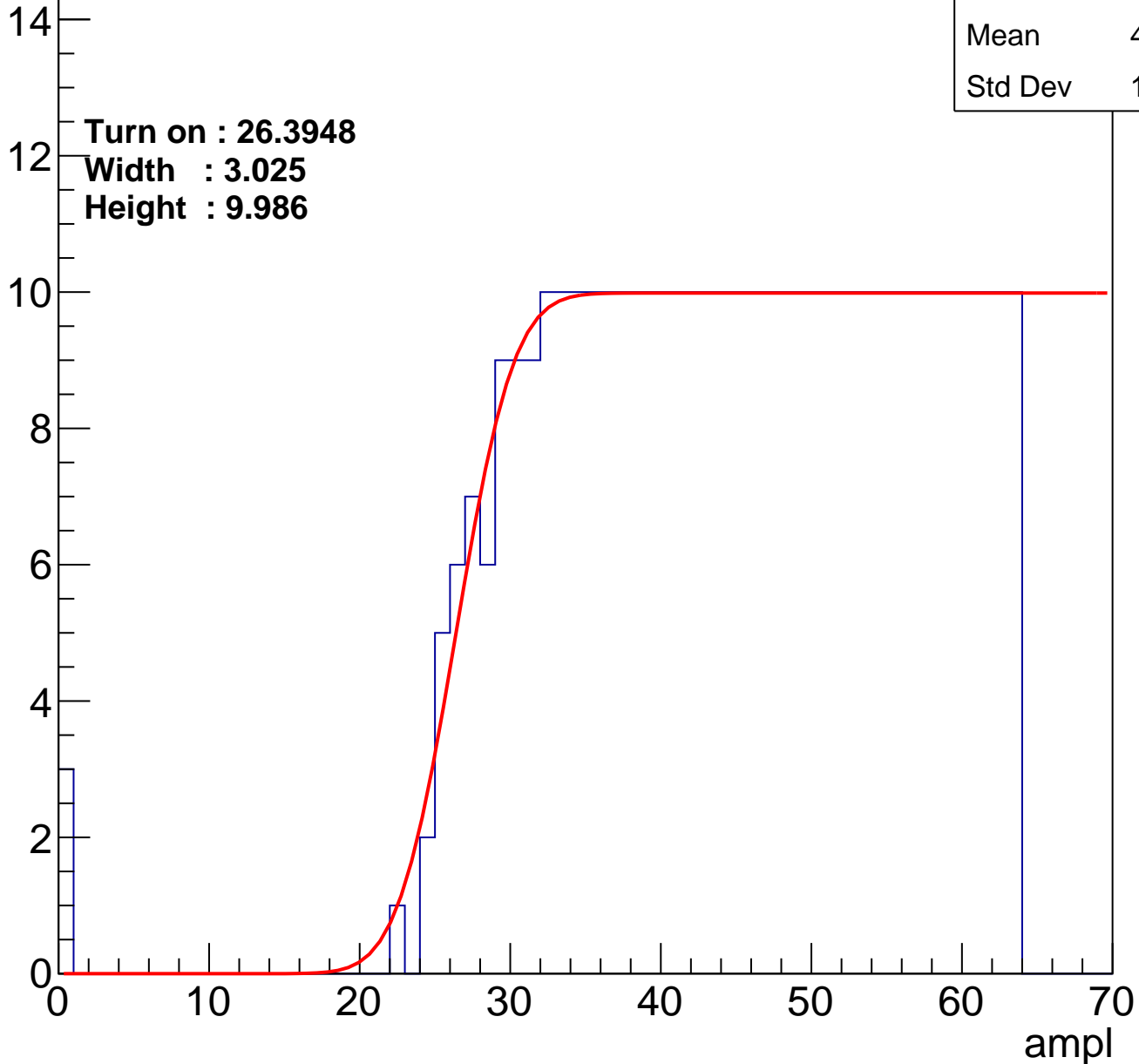
Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.3948

Width : 3.025

Height : 9.986

Entry



B1L003S, U25-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.3948

Width : 3.025

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl

