



# B1L100S, U26-ch0

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	43.74
Std Dev	12.34

Turn on : 26.5885

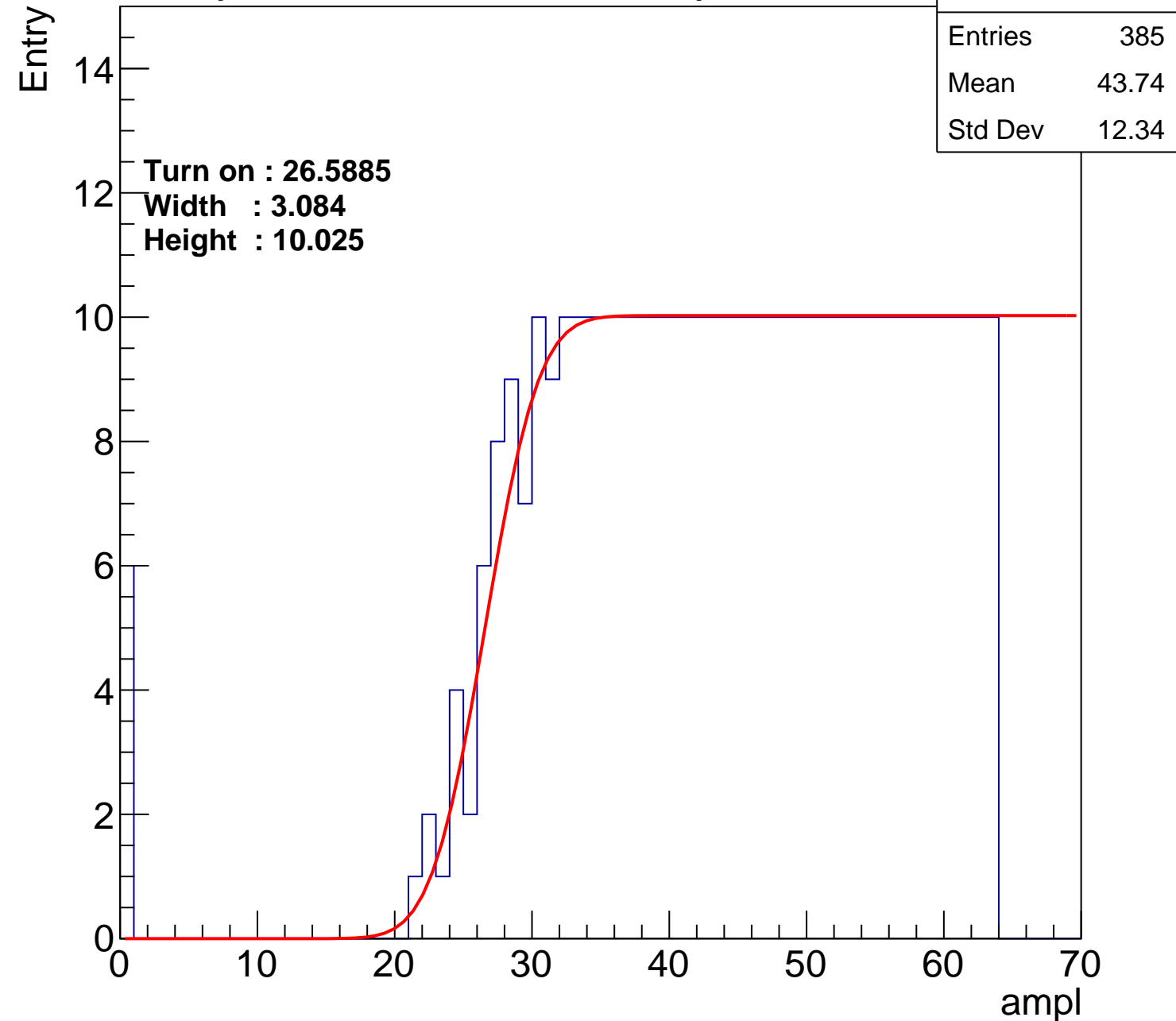
Width : 3.084

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch1

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.74
Std Dev	11.47

Turn on : 27.6688

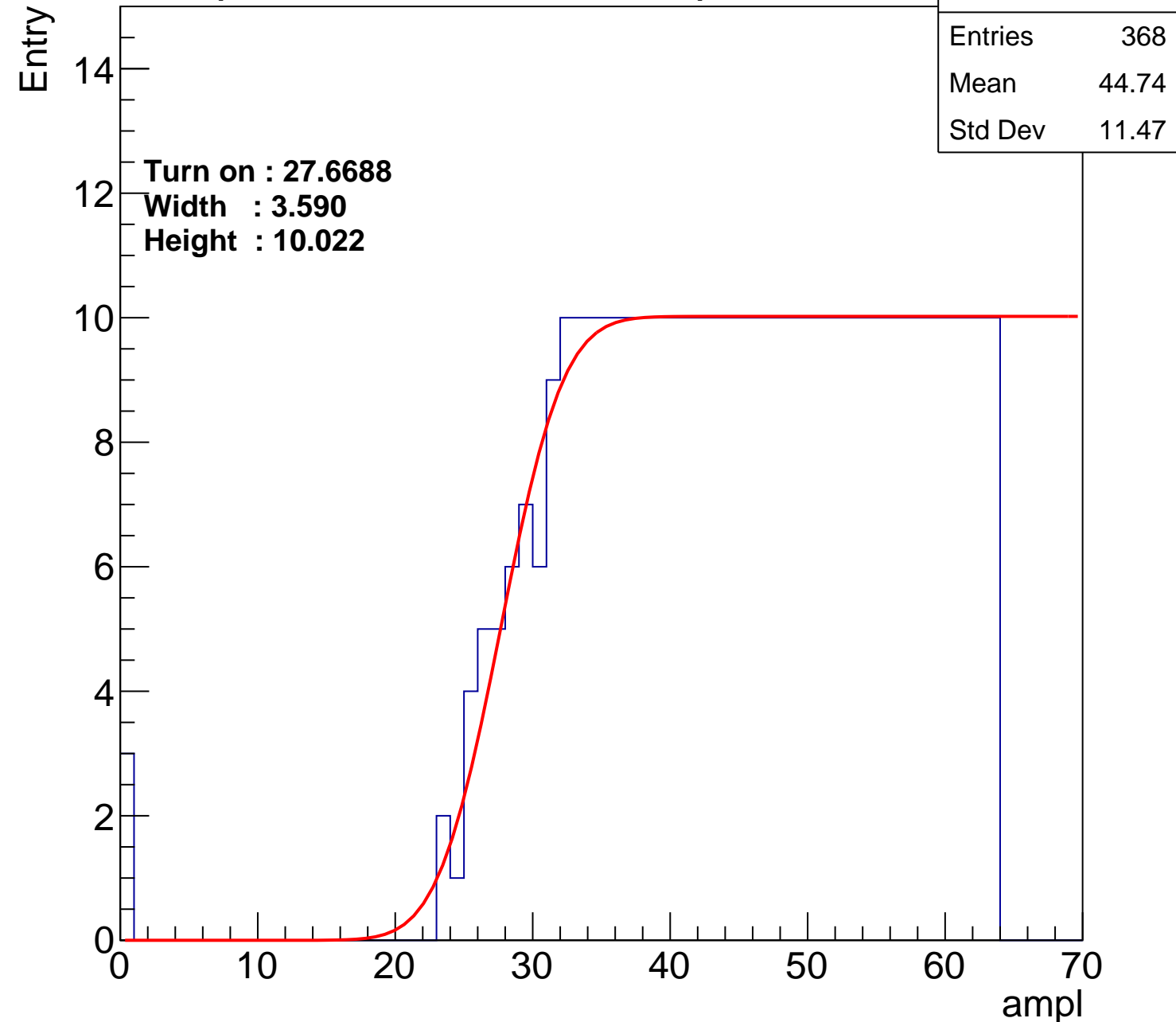
Width : 3.590

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch2

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 27.5434

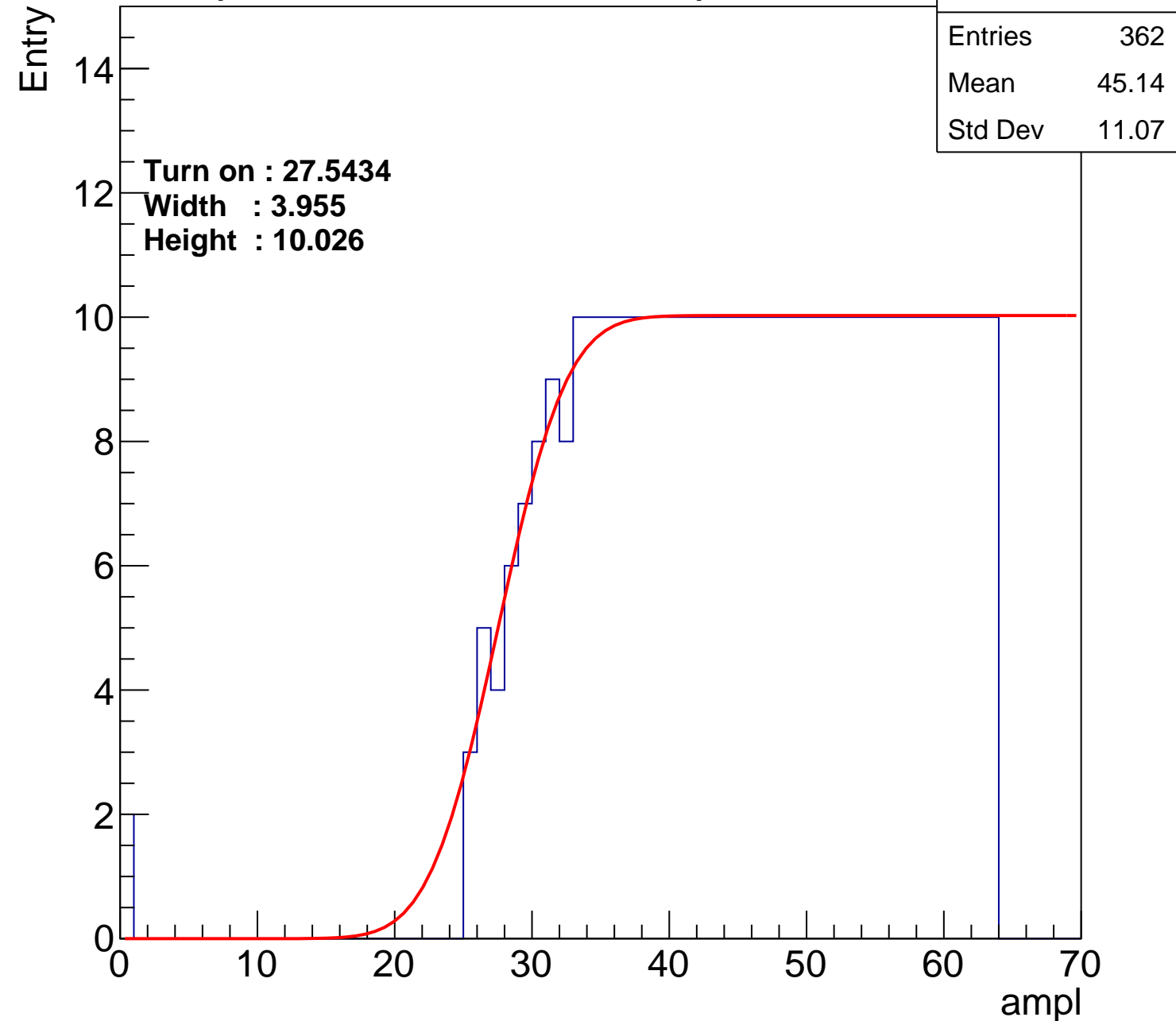
Width : 3.955

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch3

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	365
Mean	45.01
Std Dev	11.12

**Turn on : 28.2839**

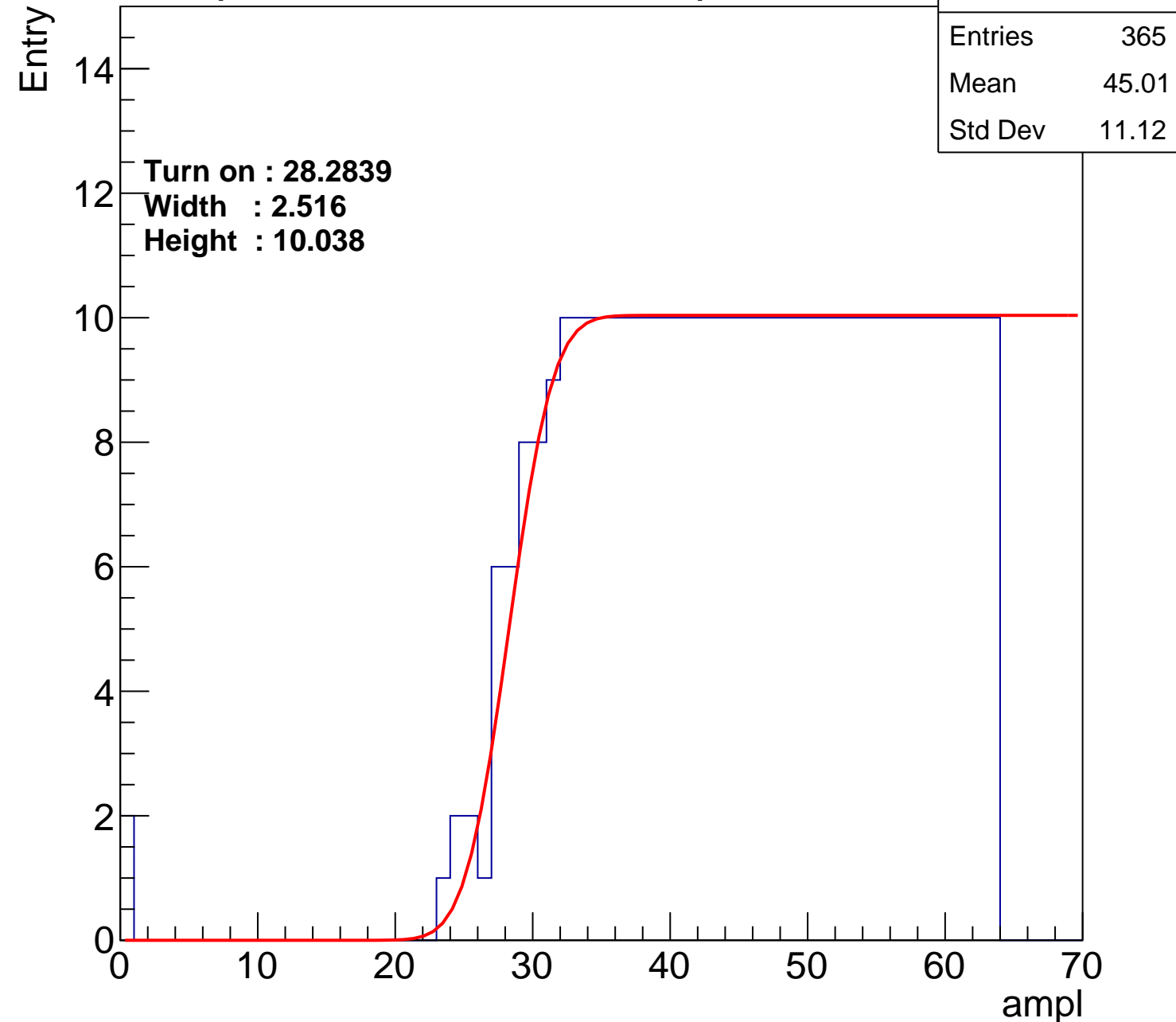
**Width : 2.516**

**Height : 10.038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch4

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 25.4931

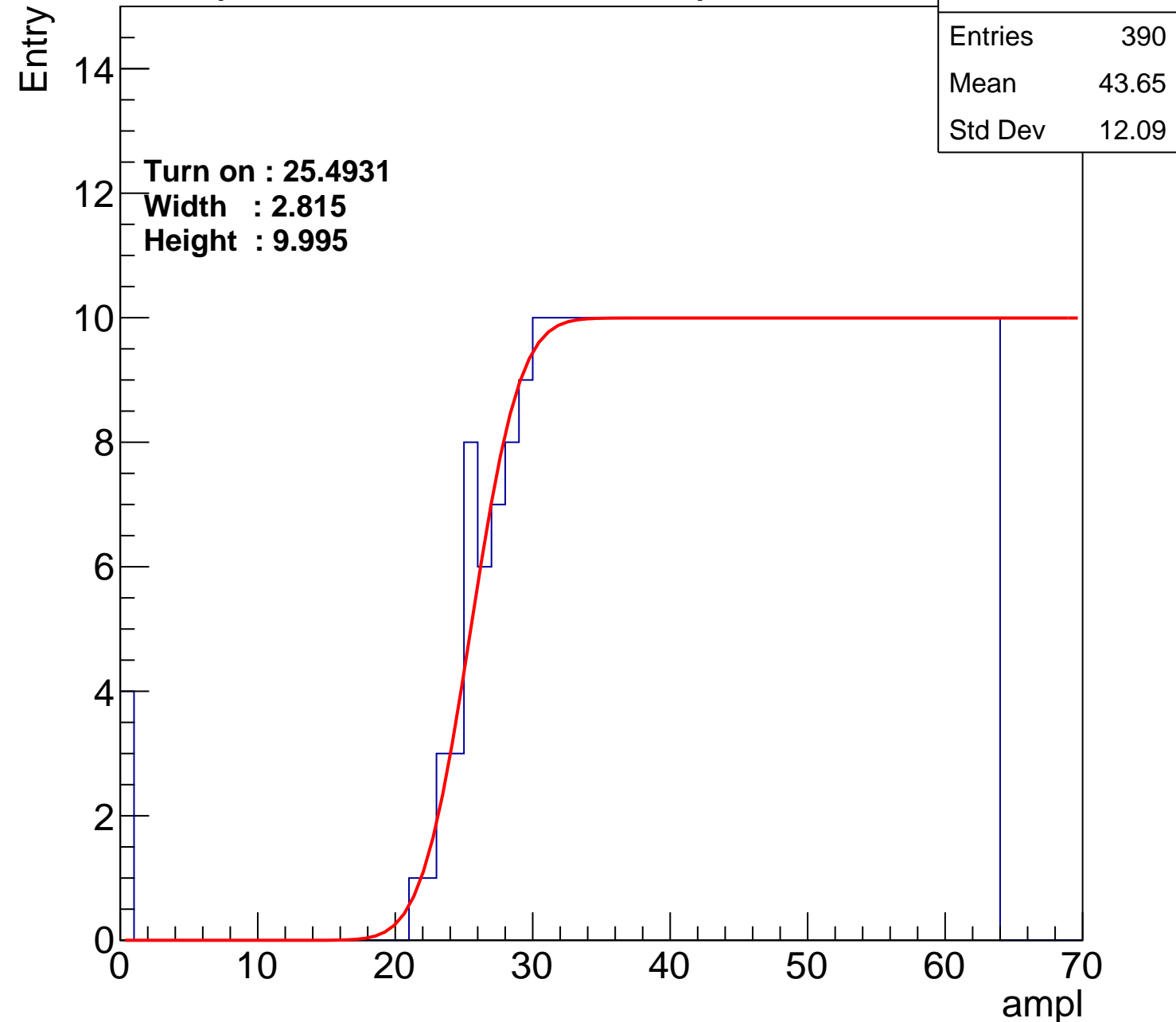
Width : 2.815

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch5

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.1
Std Dev	11.13

Turn on : 28.2837

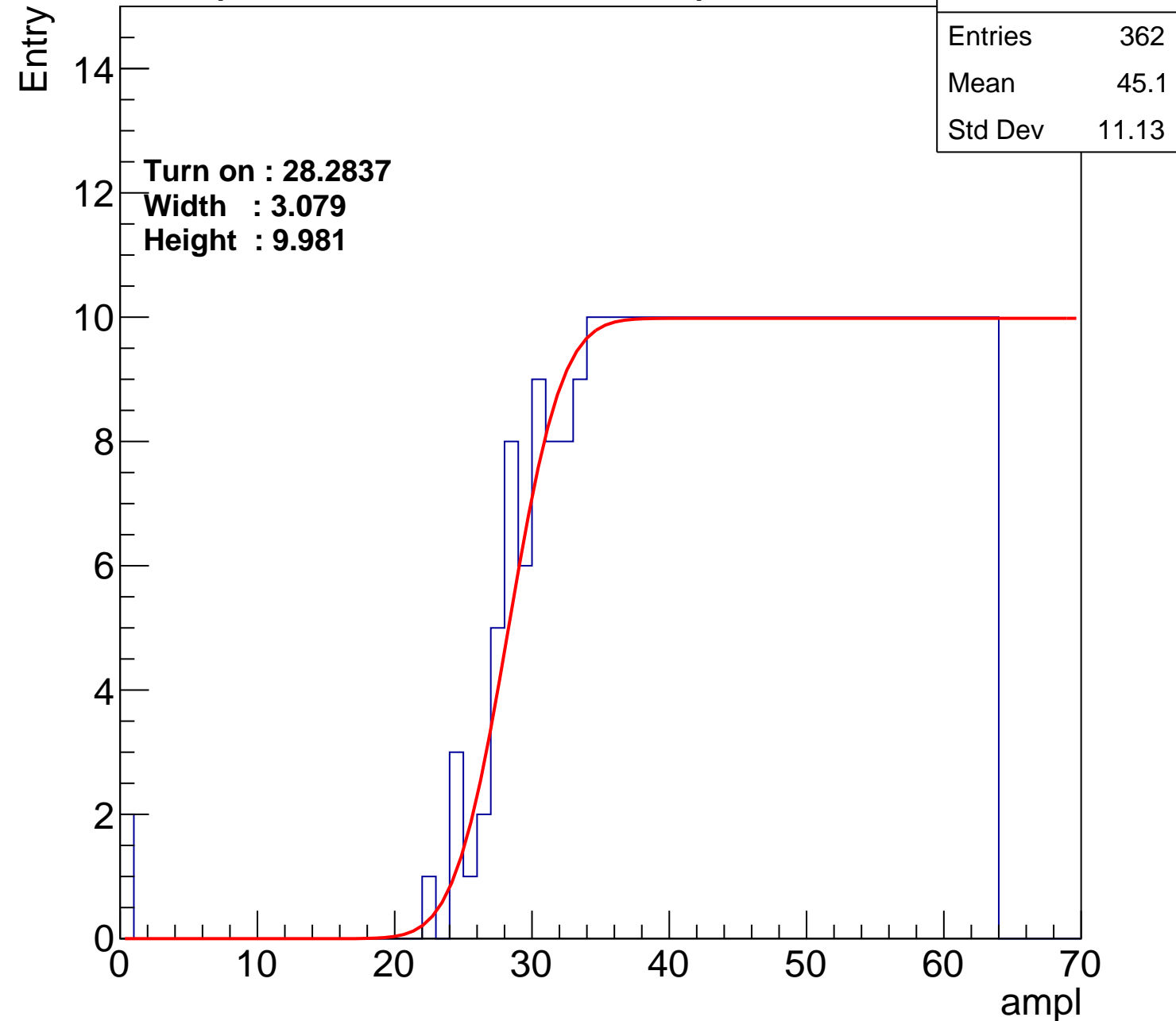
Width : 3.079

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch6

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	359
Mean	45.06
Std Dev	11.62

Turn on : 28.5694

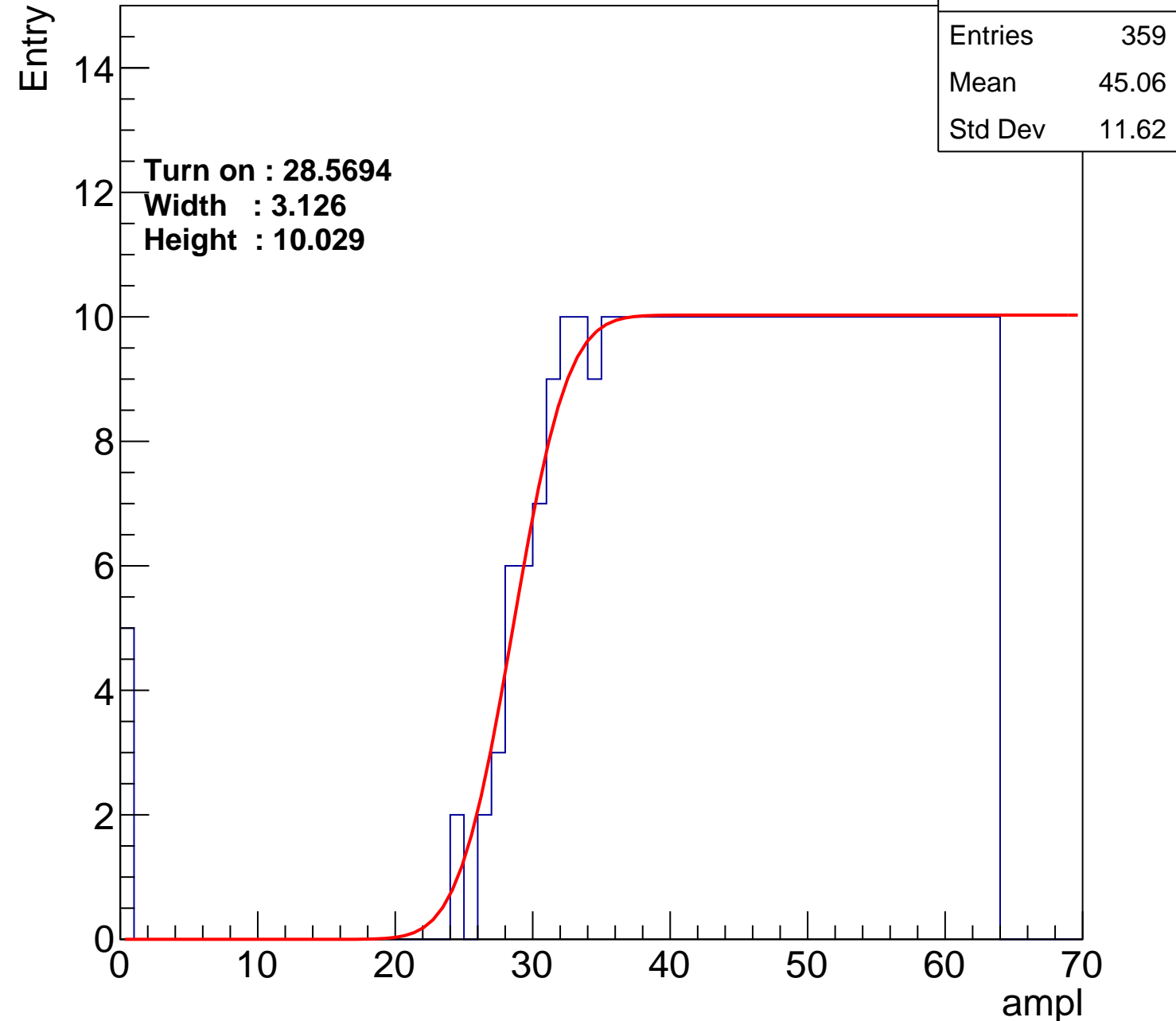
Width : 3.126

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch7

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.27
Std Dev	11.98

Turn on : 27.0445

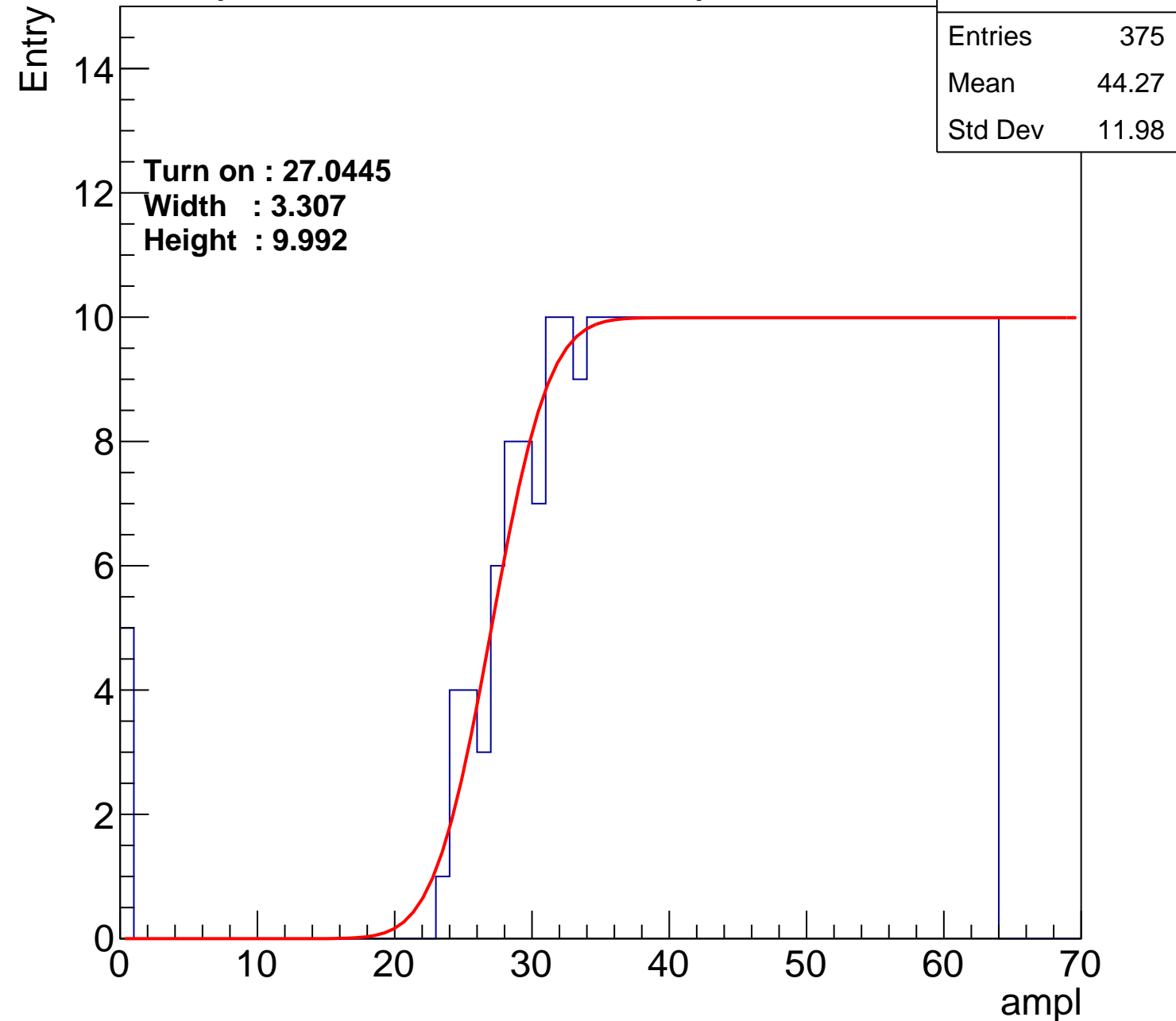
Width : 3.307

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch8

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 26.0450

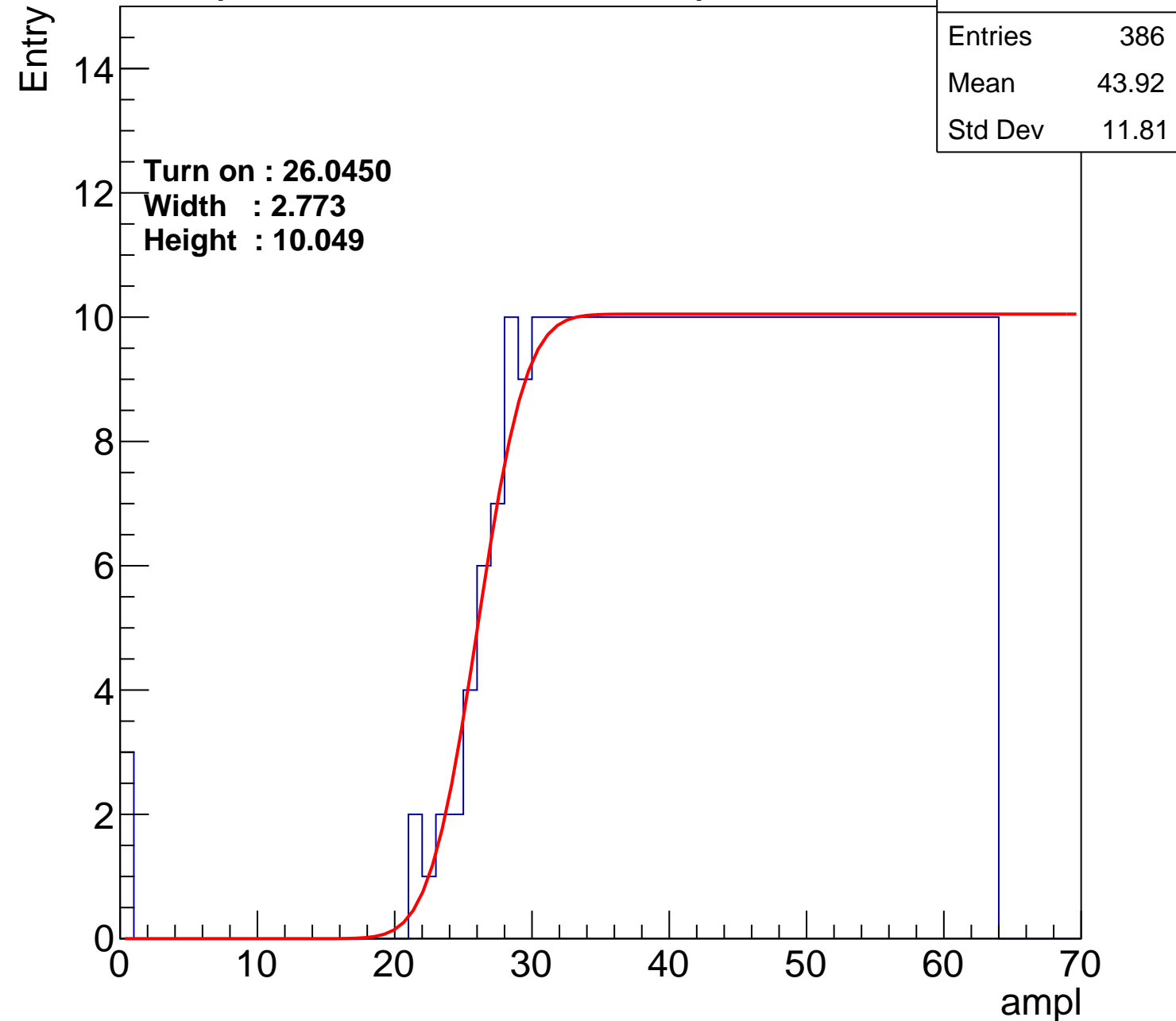
Width : 2.773

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch9

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	366
Mean	44.83
Std Dev	11.44

Turn on : 27.7152

Width : 3.919

Height : 10.029

Entry

14

12

10

8

6

4

2

0

0

10

20

30

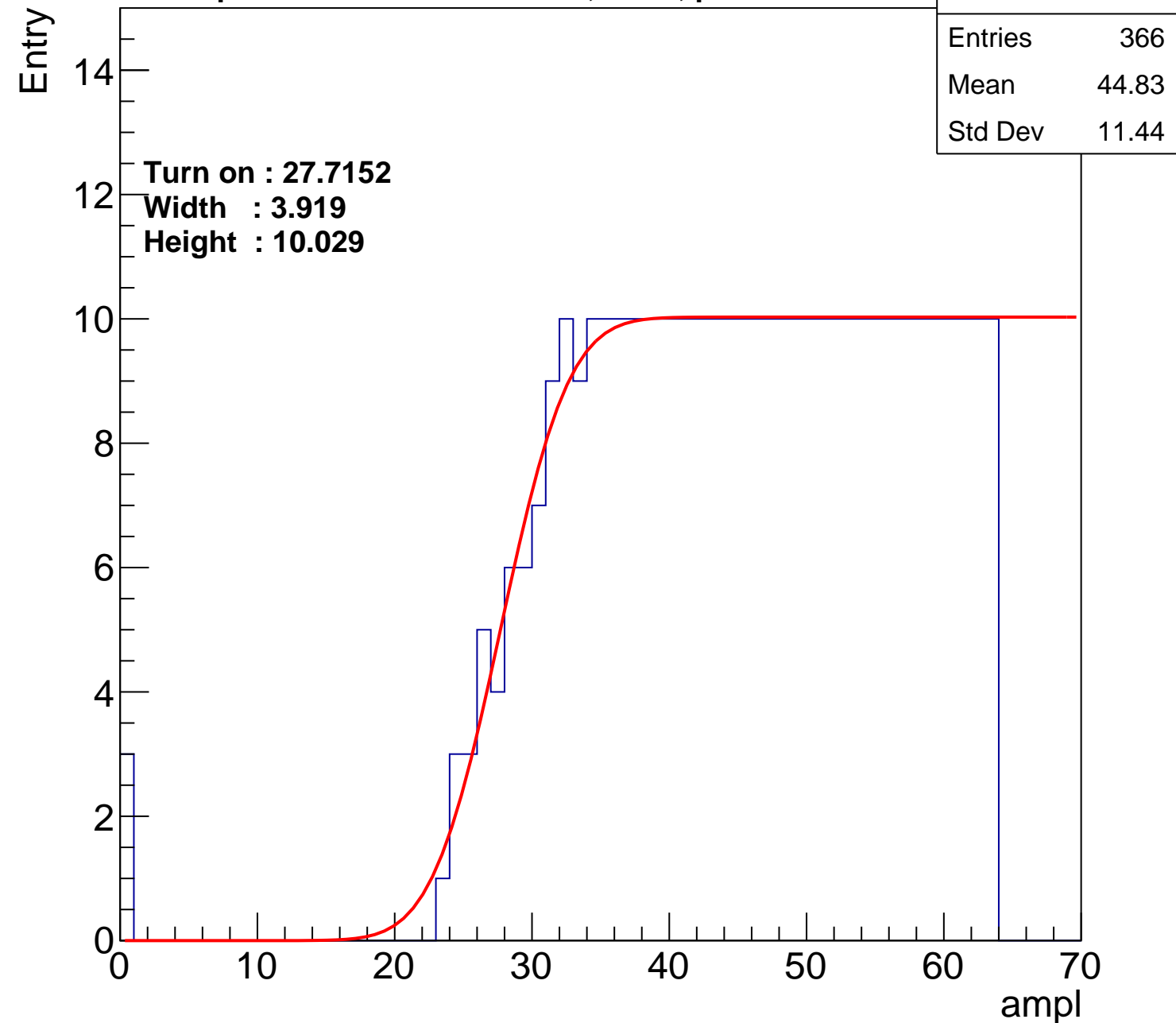
40

50

60

70

ampl



# B1L100S, U26-ch10

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.75
Std Dev	11.41

Turn on : 27.5106

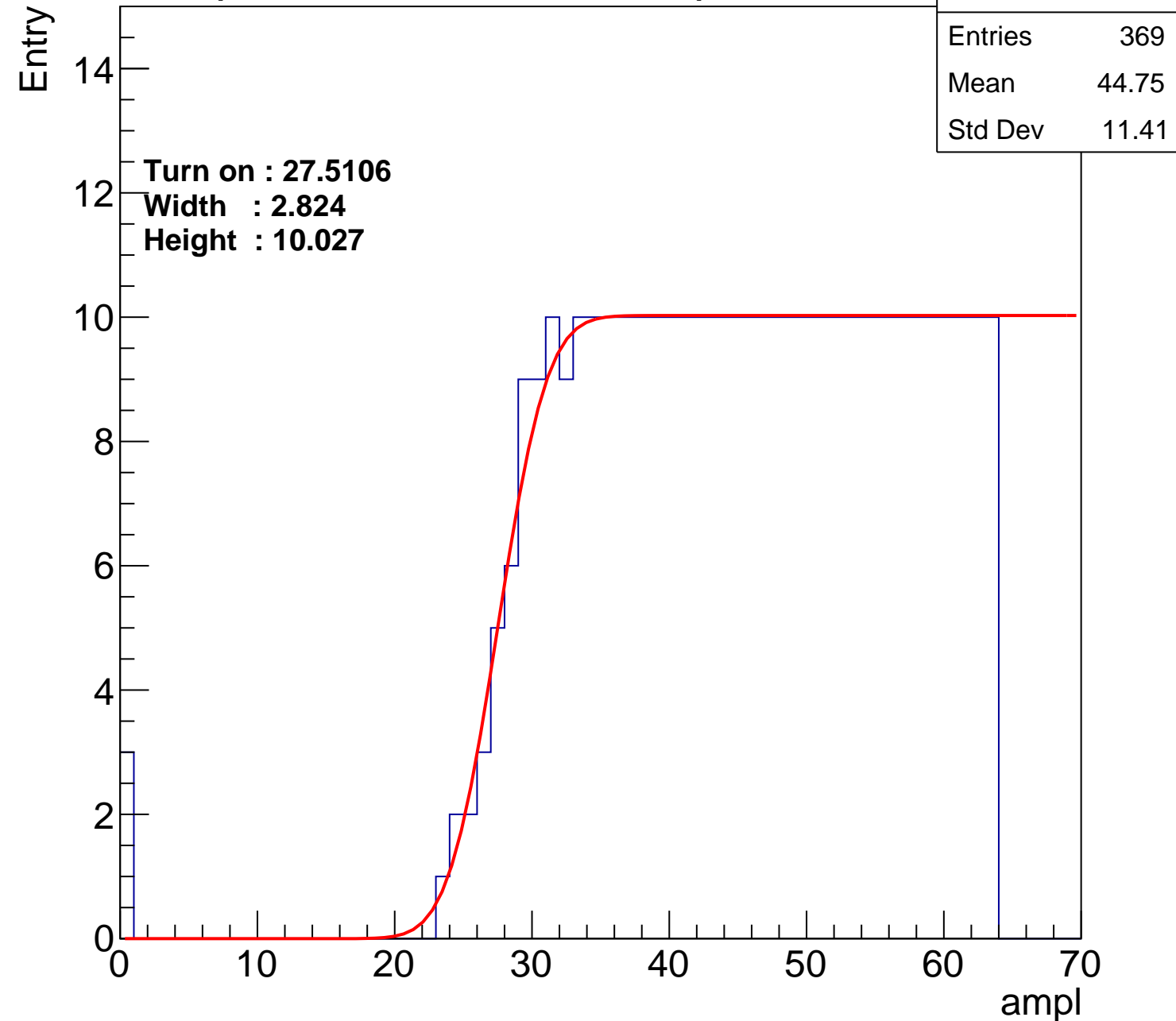
Width : 2.824

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch11

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	357
Mean	45.43
Std Dev	10.77

Turn on : 28.5910

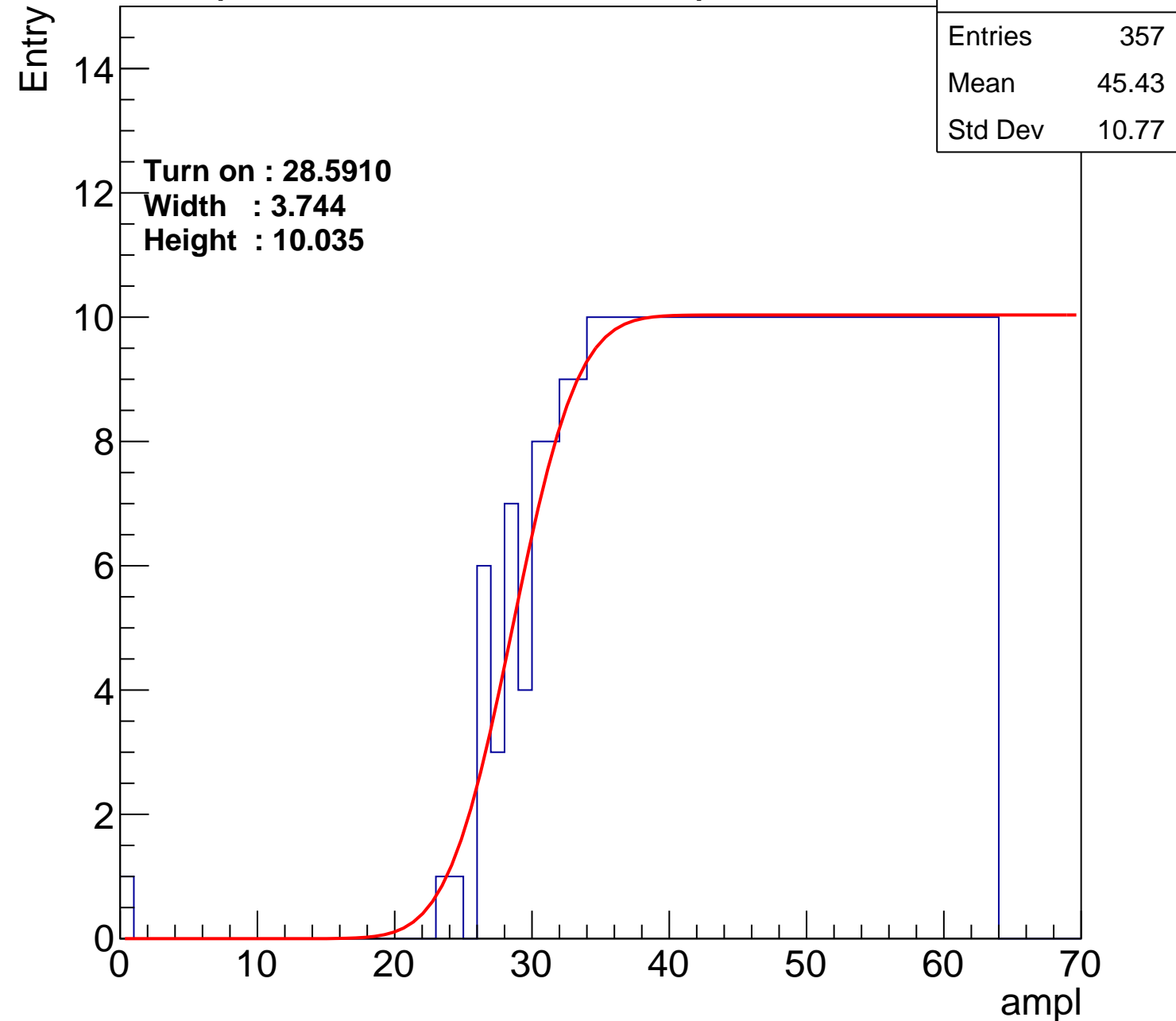
Width : 3.744

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch12

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	358
Mean	45.36
Std Dev	10.92

Turn on : 27.7706

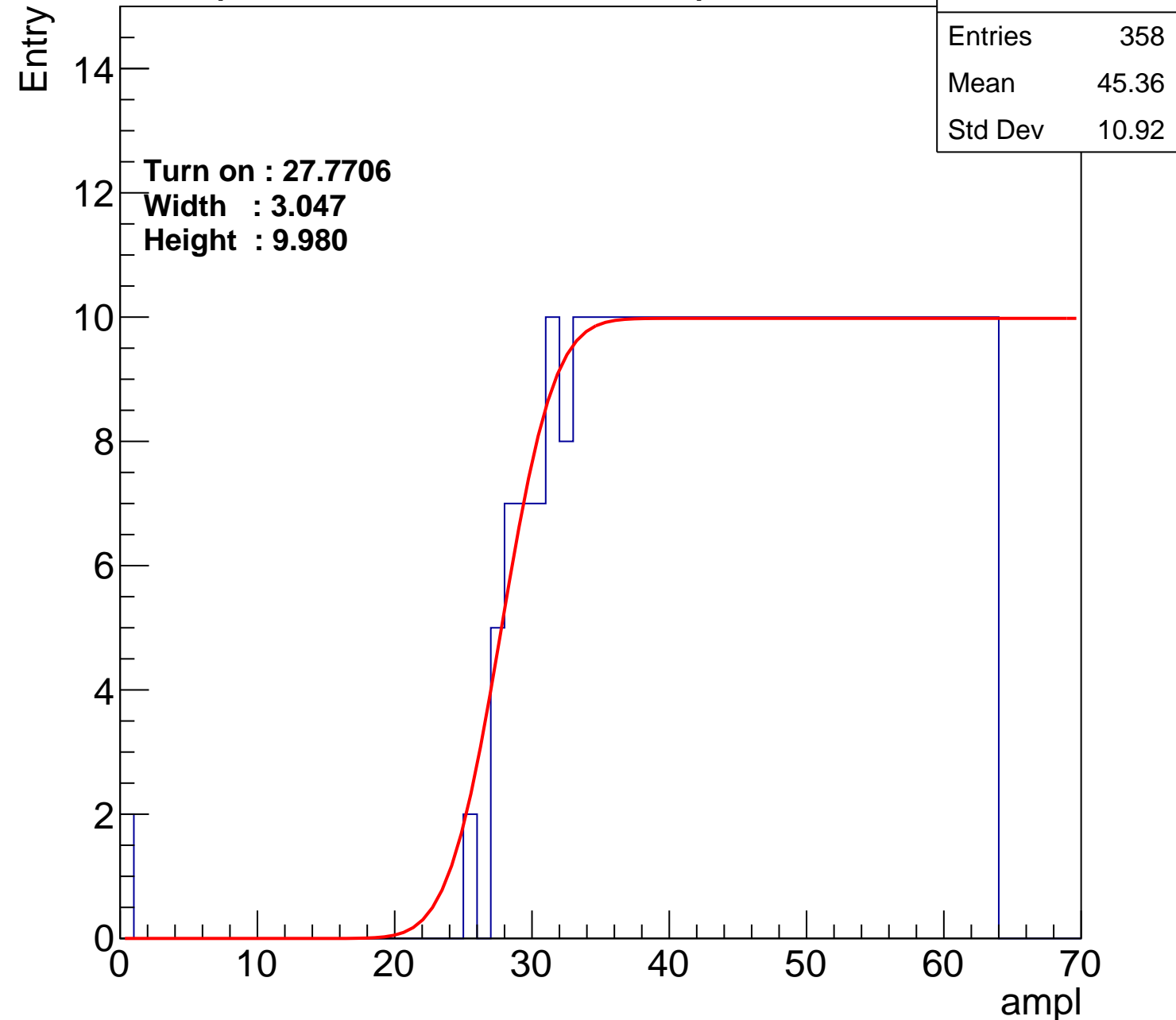
Width : 3.047

Height : 9.980

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch13

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	361
Mean	45.18
Std Dev	11.06

Turn on : 28.5456

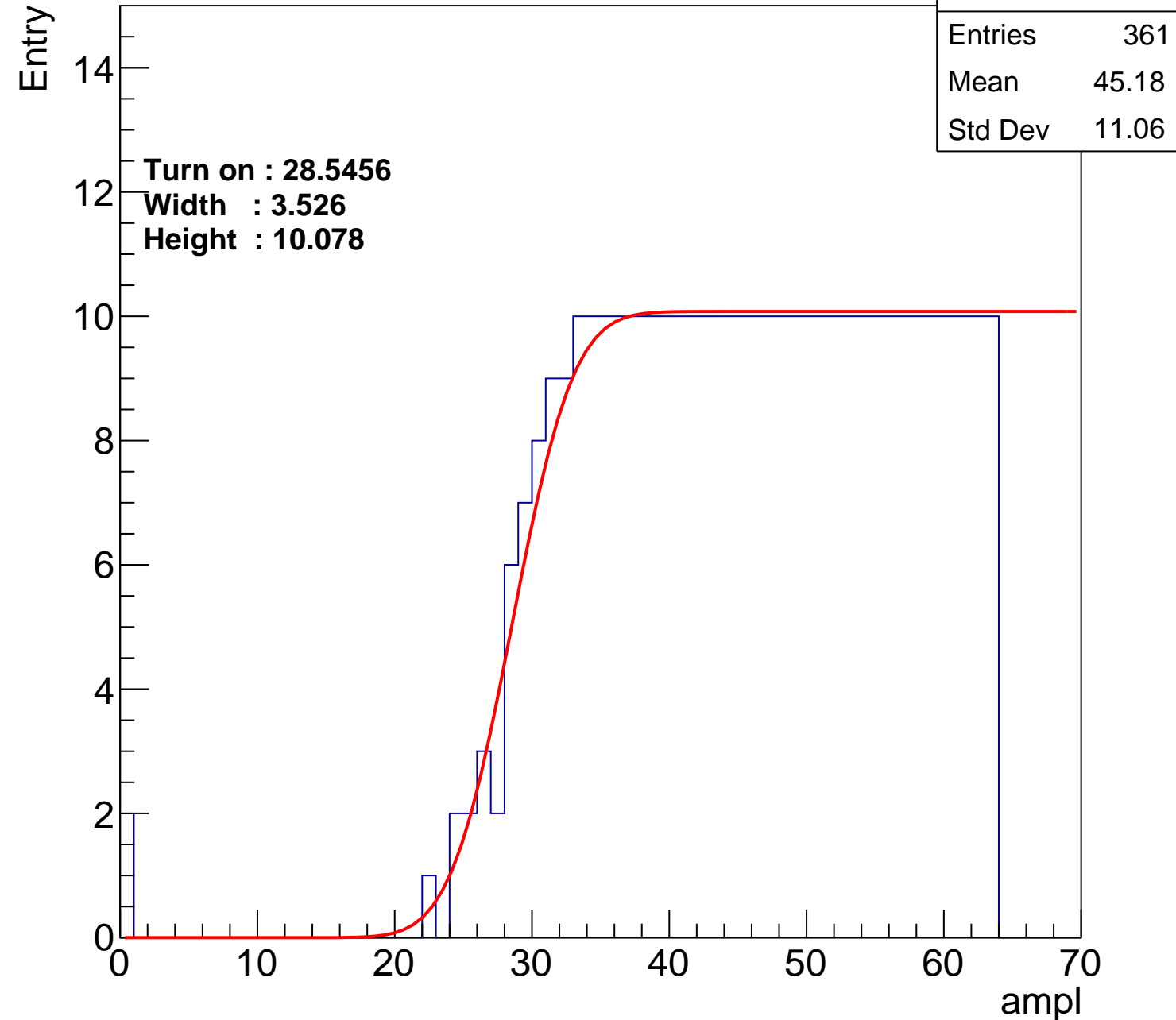
Width : 3.526

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch14

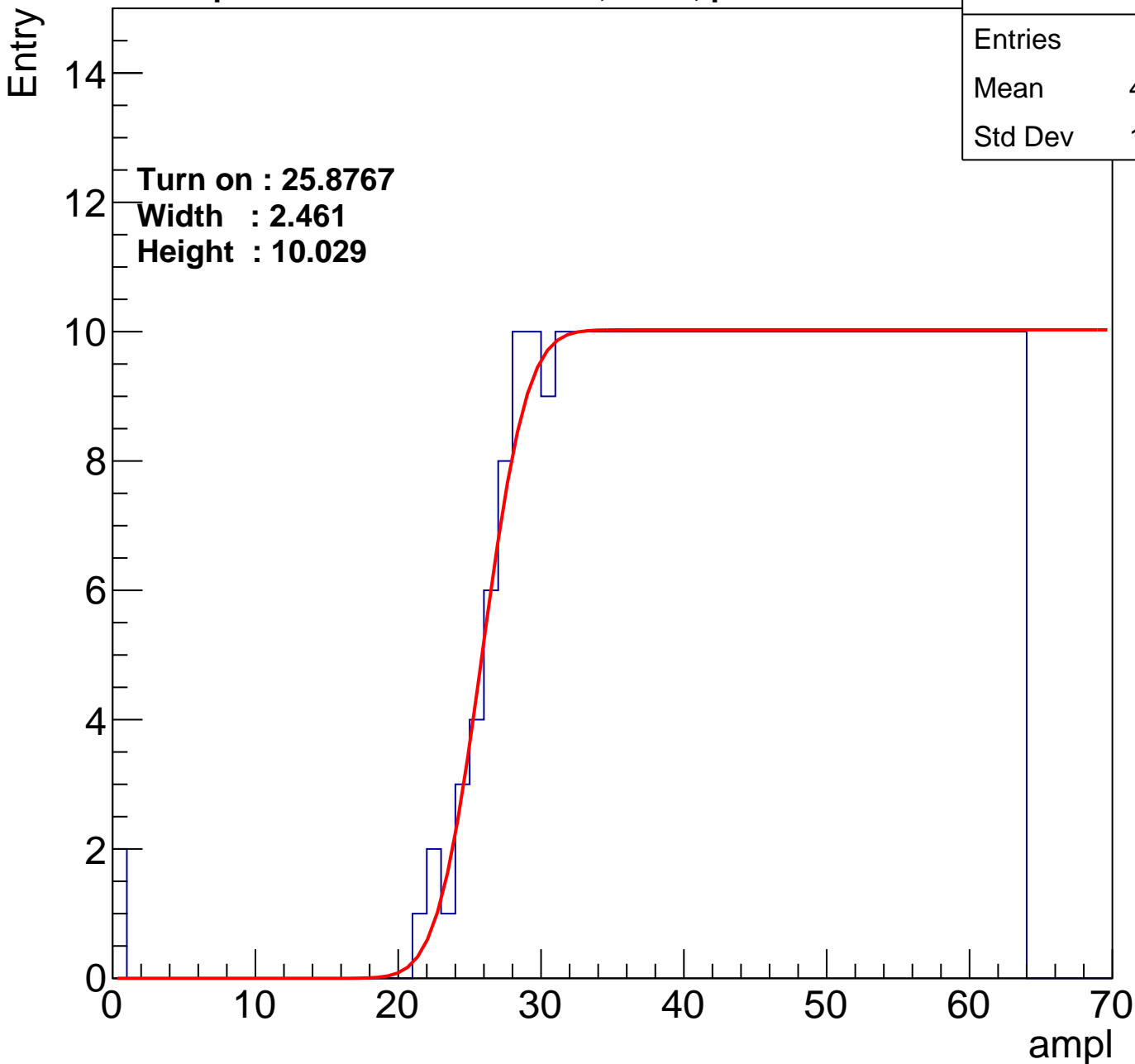
calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.8767

Width : 2.461

Height : 10.029





# B1L100S, U26-ch15

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.2696

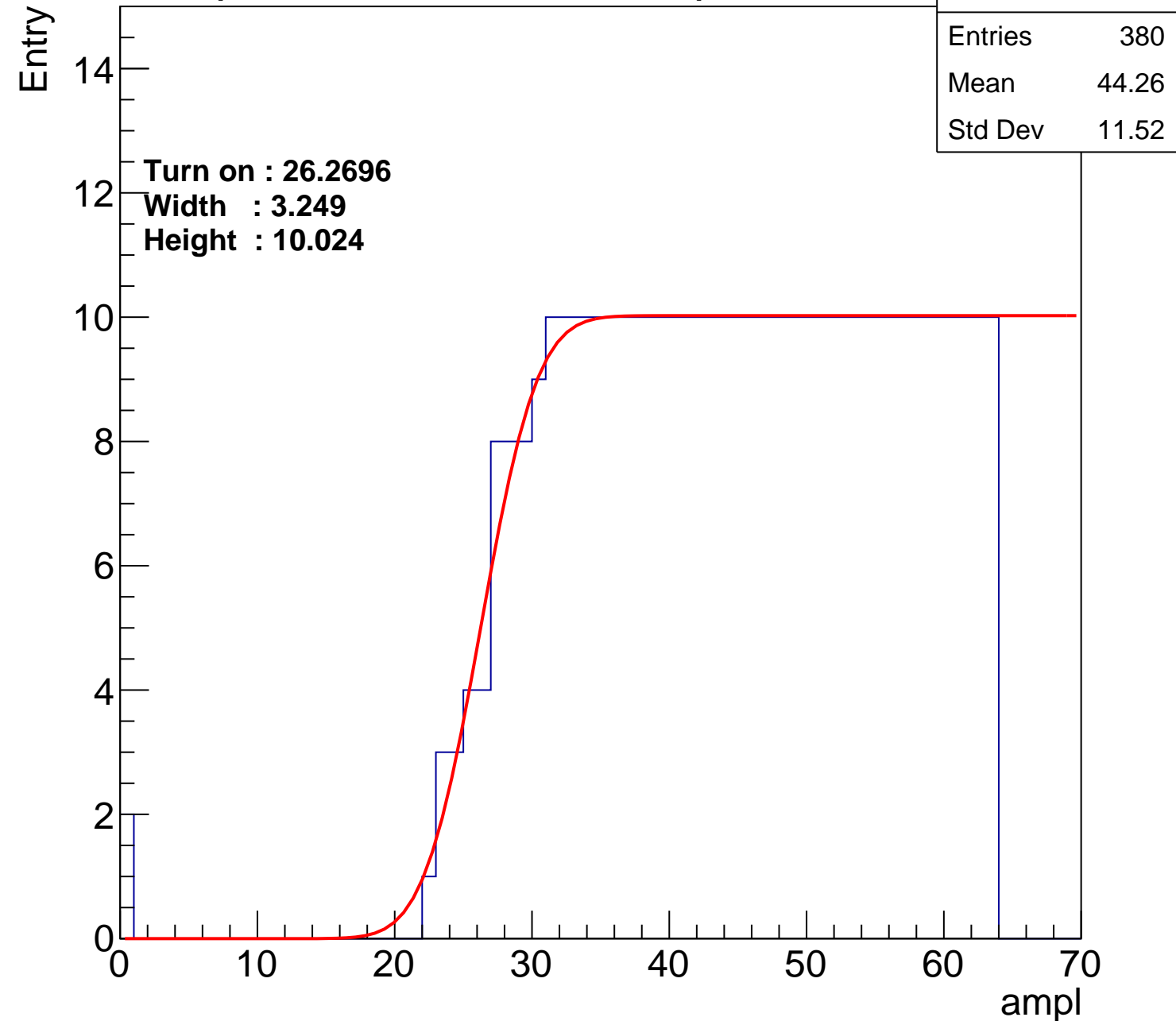
Width : 3.249

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch16

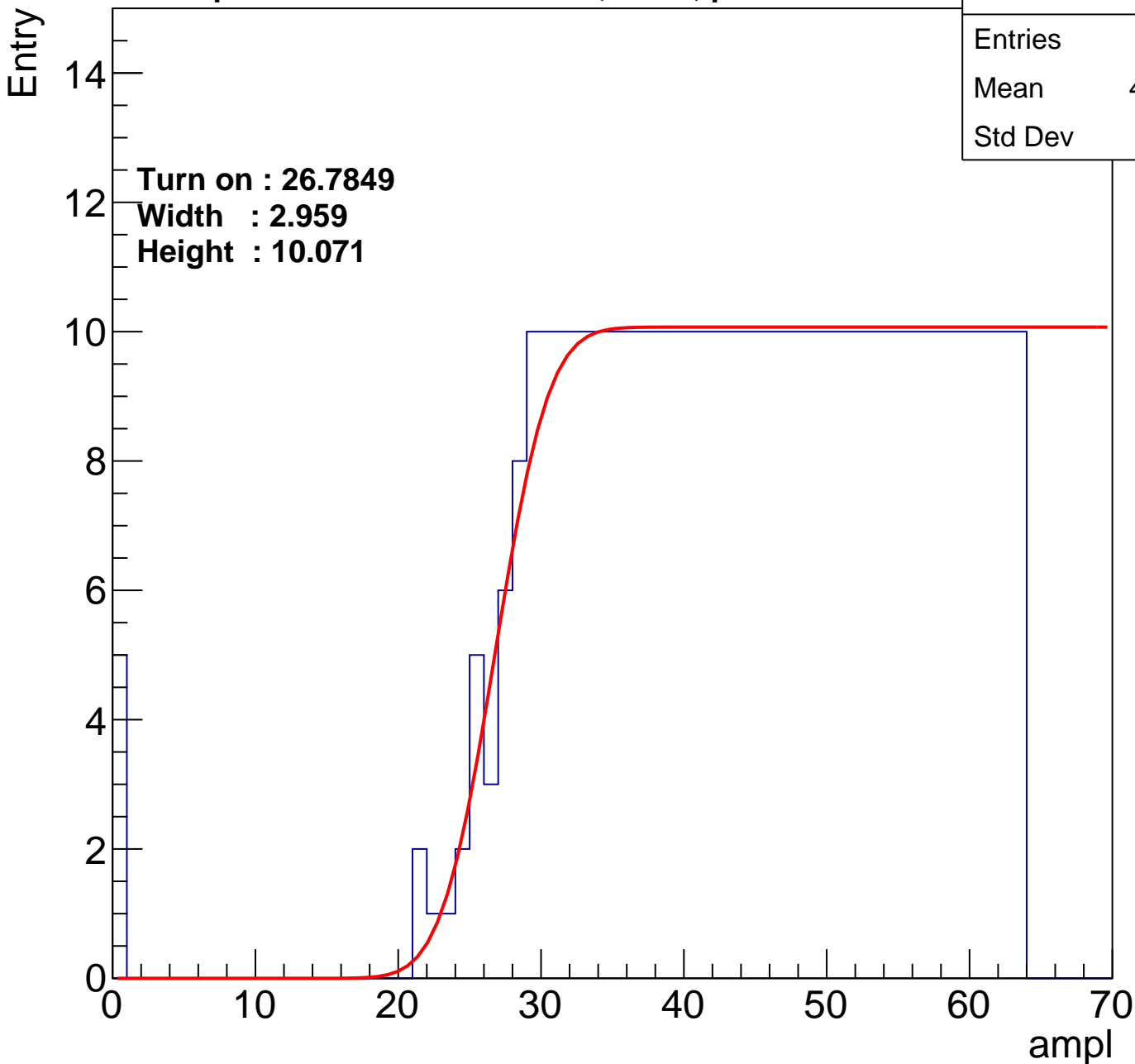
calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	43.93
Std Dev	12.1

Turn on : 26.7849

Width : 2.959

Height : 10.071



# B1L100S, U26-ch17

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	357
Mean	45.47
Std Dev	10.71

Turn on : 29.2085

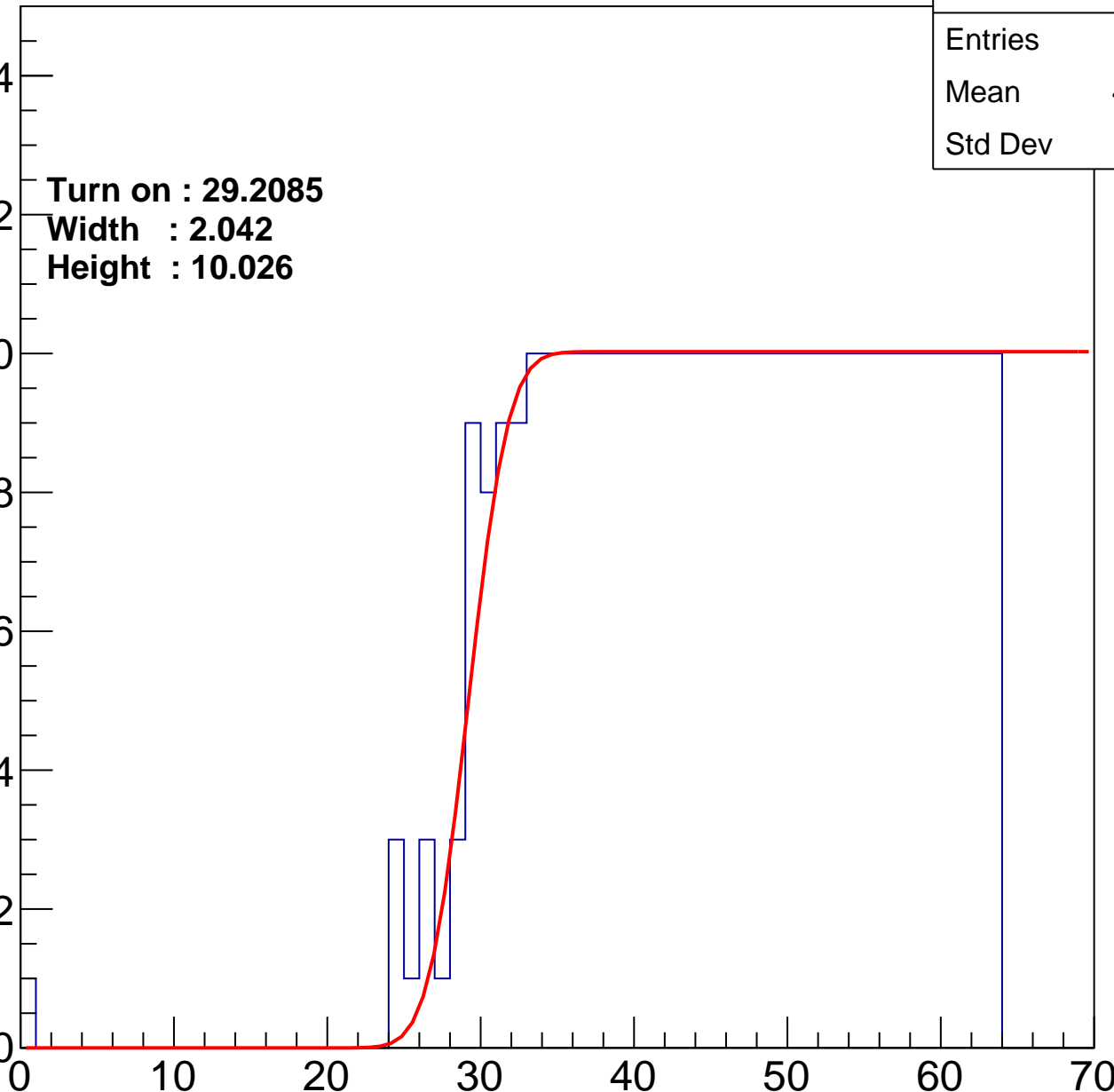
Width : 2.042

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch18

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	345
Mean	45.97
Std Dev	10.67

Turn on : 30.3170

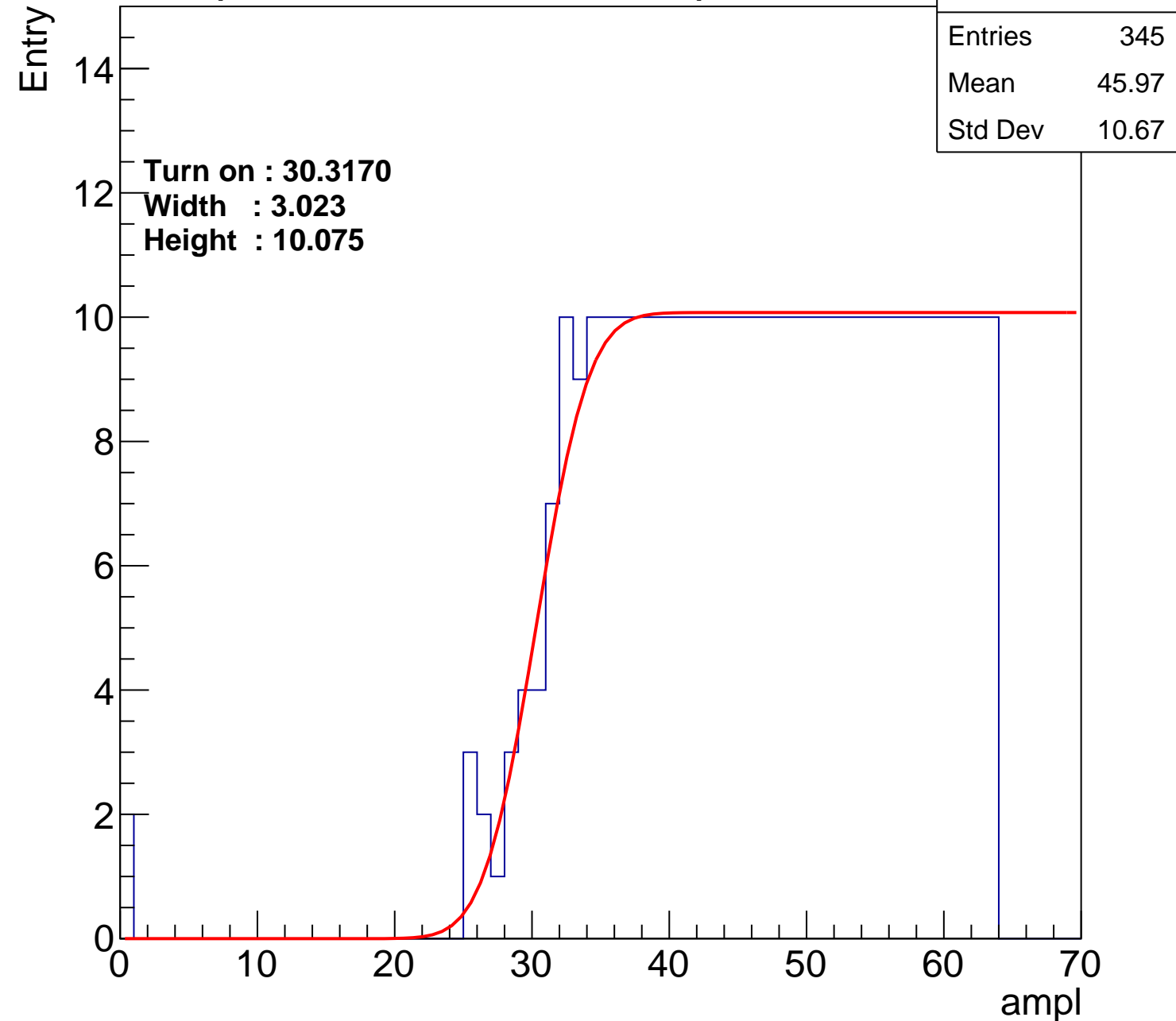
Width : 3.023

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch19

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	360
Mean	45.25
Std Dev	11

**Turn on : 28.5343**

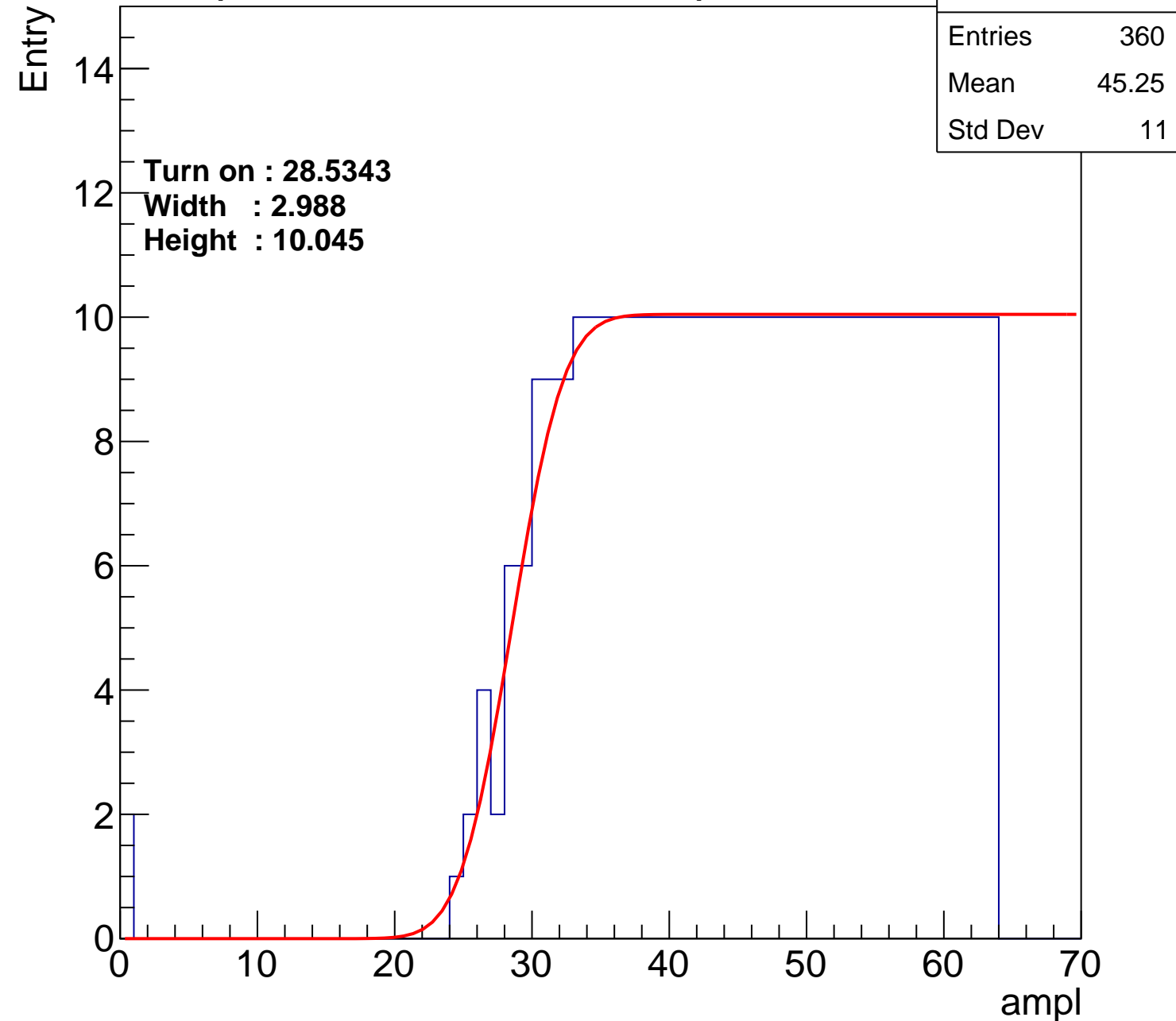
**Width : 2.988**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch20

calib\_packv5\_042523\_0143.root, FC#4, port A2

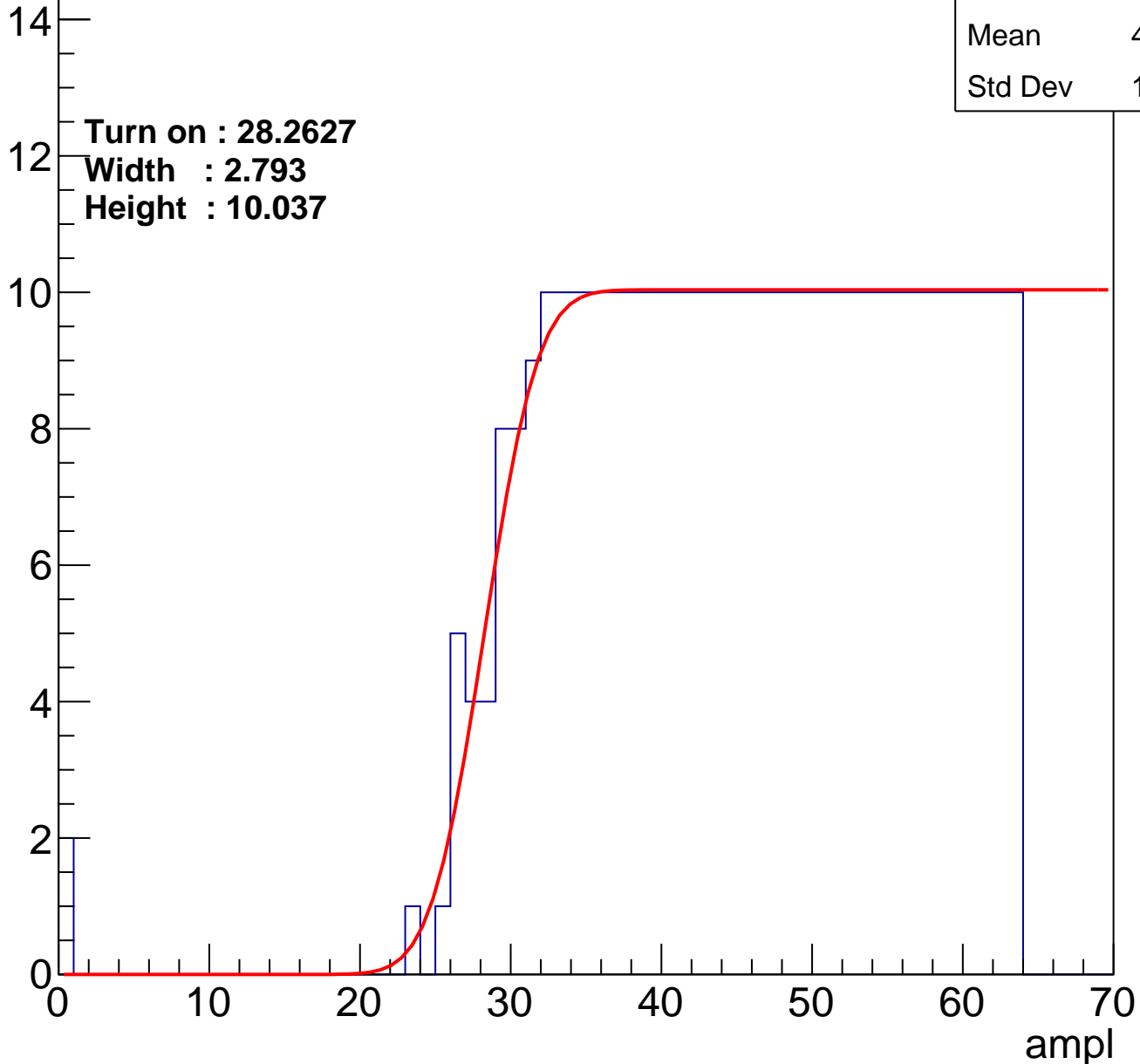
Entries	362
Mean	45.16
Std Dev	11.03

Turn on : 28.2627

Width : 2.793

Height : 10.037

Entry



# B1L100S, U26-ch21

calib\_packv5\_042523\_0143.root, FC#4, port A2

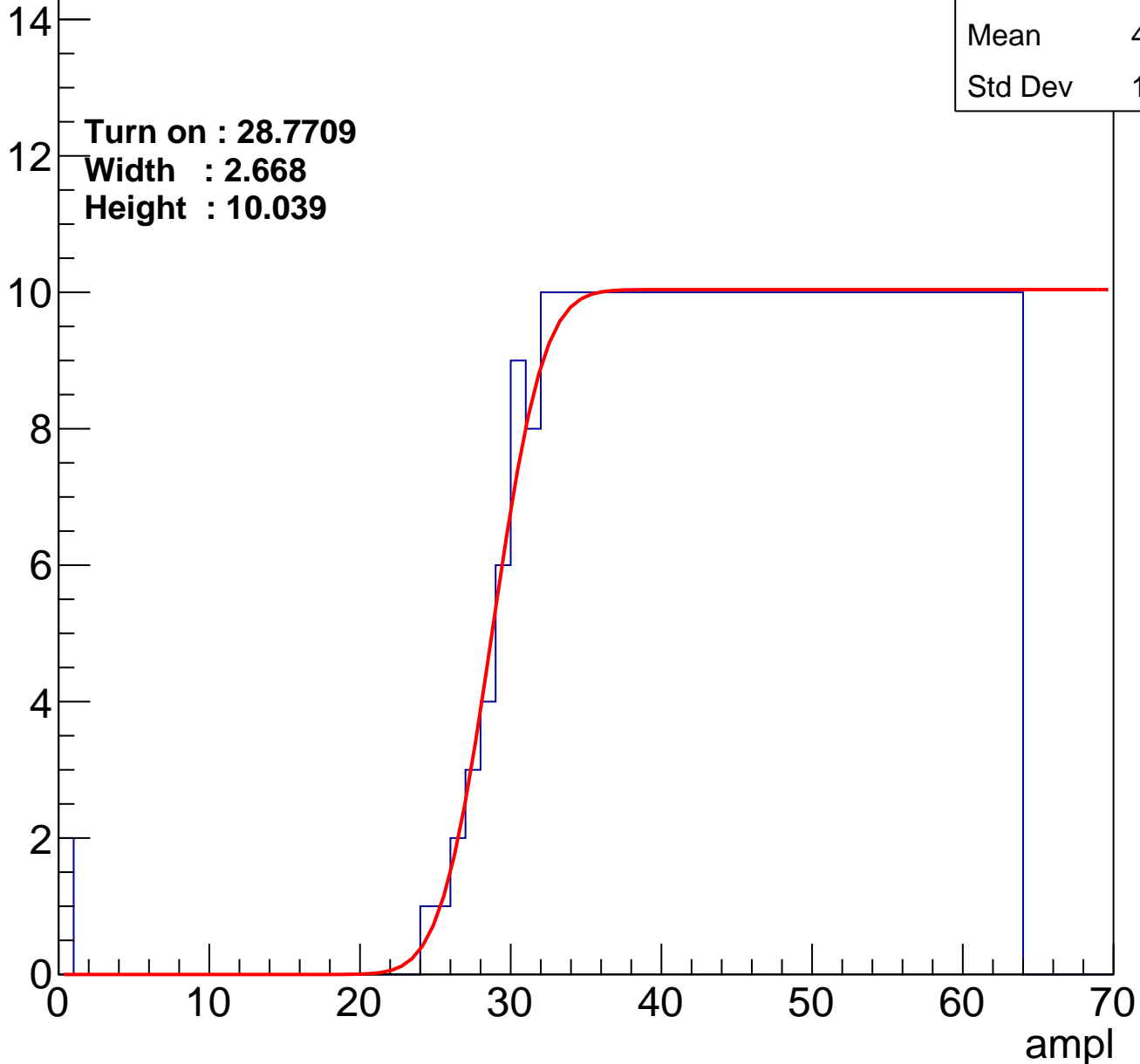
Entries	356
Mean	45.47
Std Dev	10.87

**Turn on : 28.7709**

**Width : 2.668**

**Height : 10.039**

Entry



# B1L100S, U26-ch22

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 26.6725

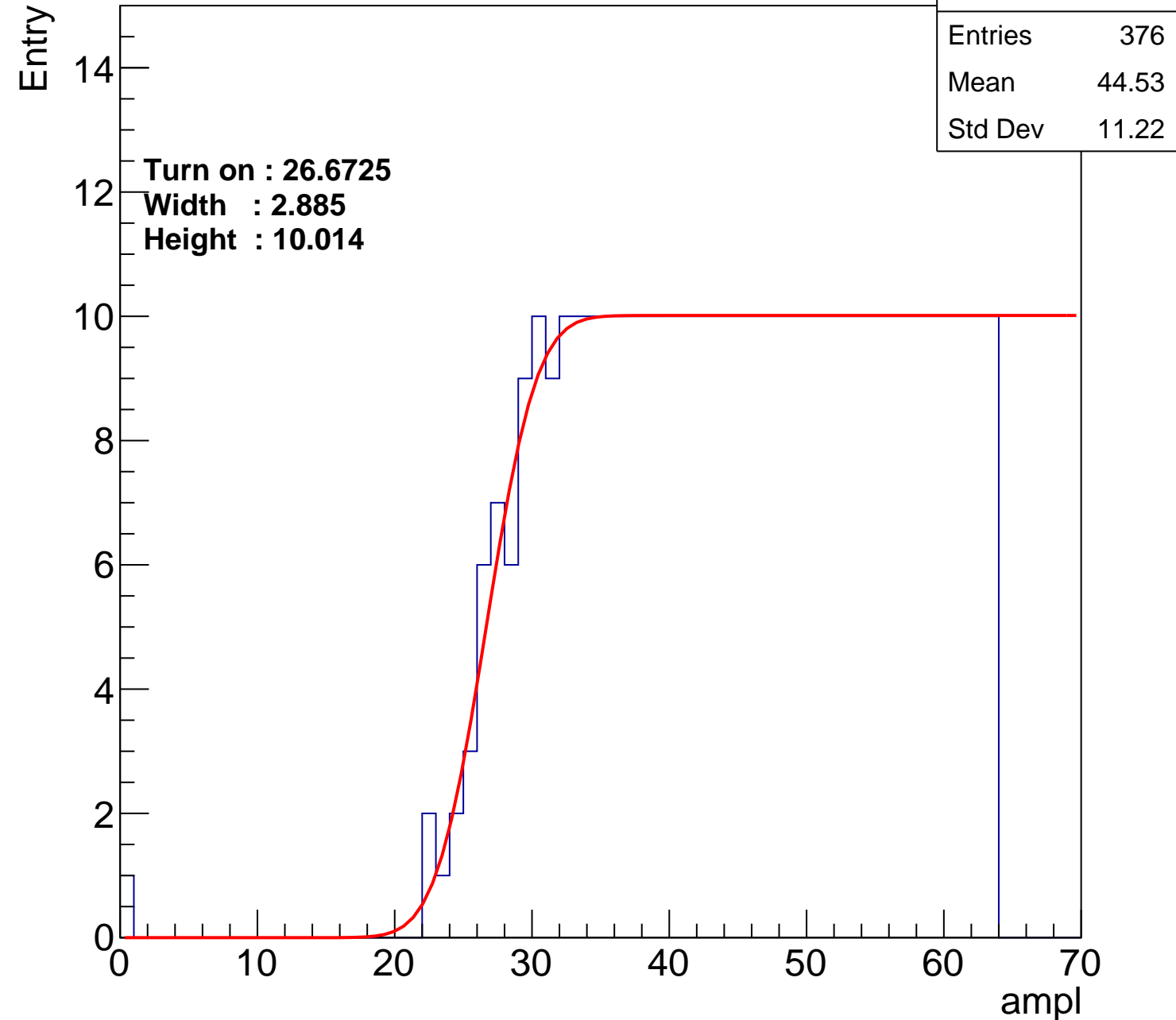
Width : 2.885

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch23

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	360
Mean	45.19
Std Dev	11.19

Turn on : 28.5467

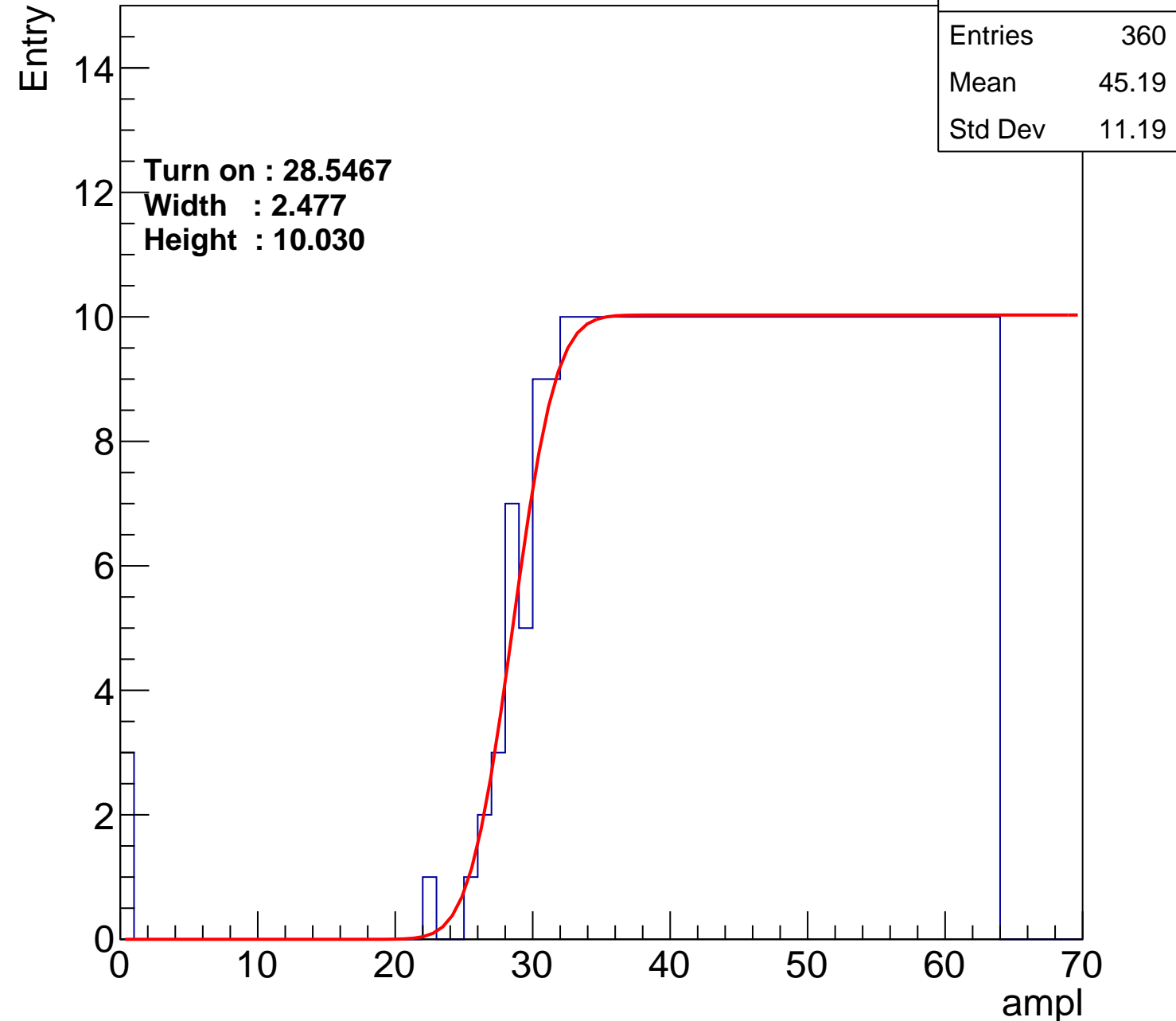
Width : 2.477

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch24

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.5836

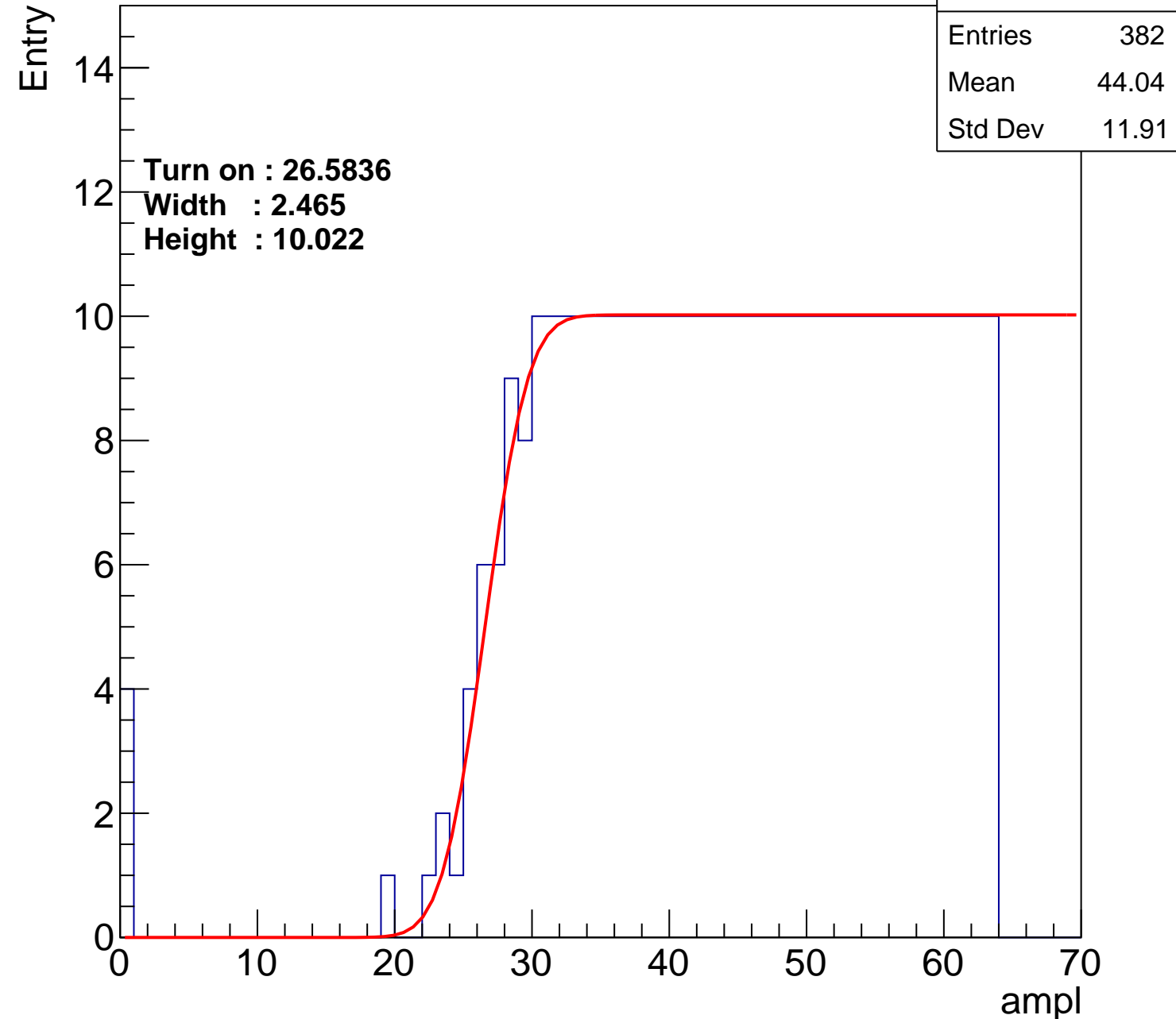
Width : 2.465

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch25

calib\_packv5\_042523\_0143.root, FC#4, port A2

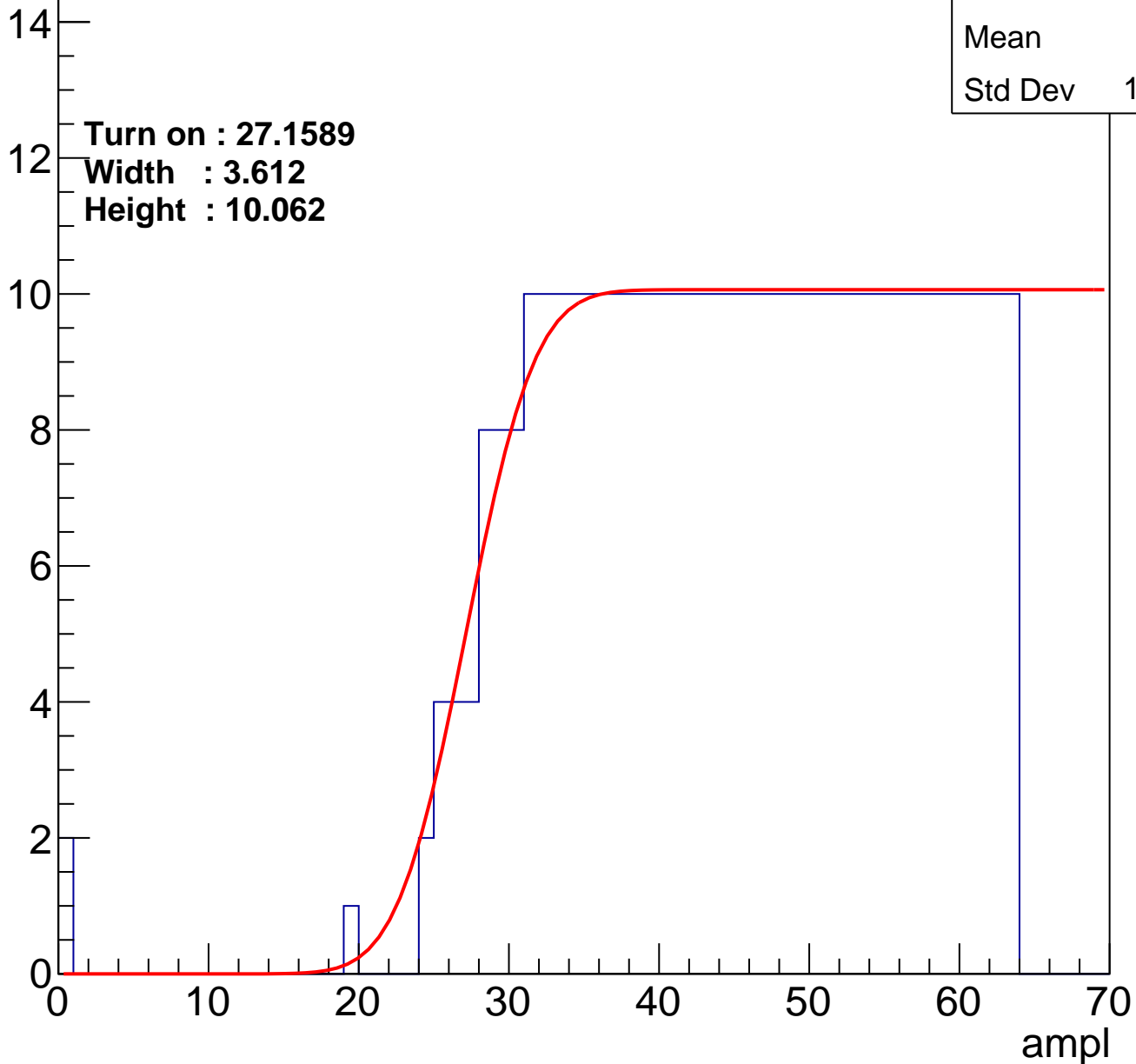
Entries	371
Mean	44.7
Std Dev	11.29

Turn on : 27.1589

Width : 3.612

Height : 10.062

Entry



# B1L100S, U26-ch26

calib\_packv5\_042523\_0143.root, FC#4, port A2

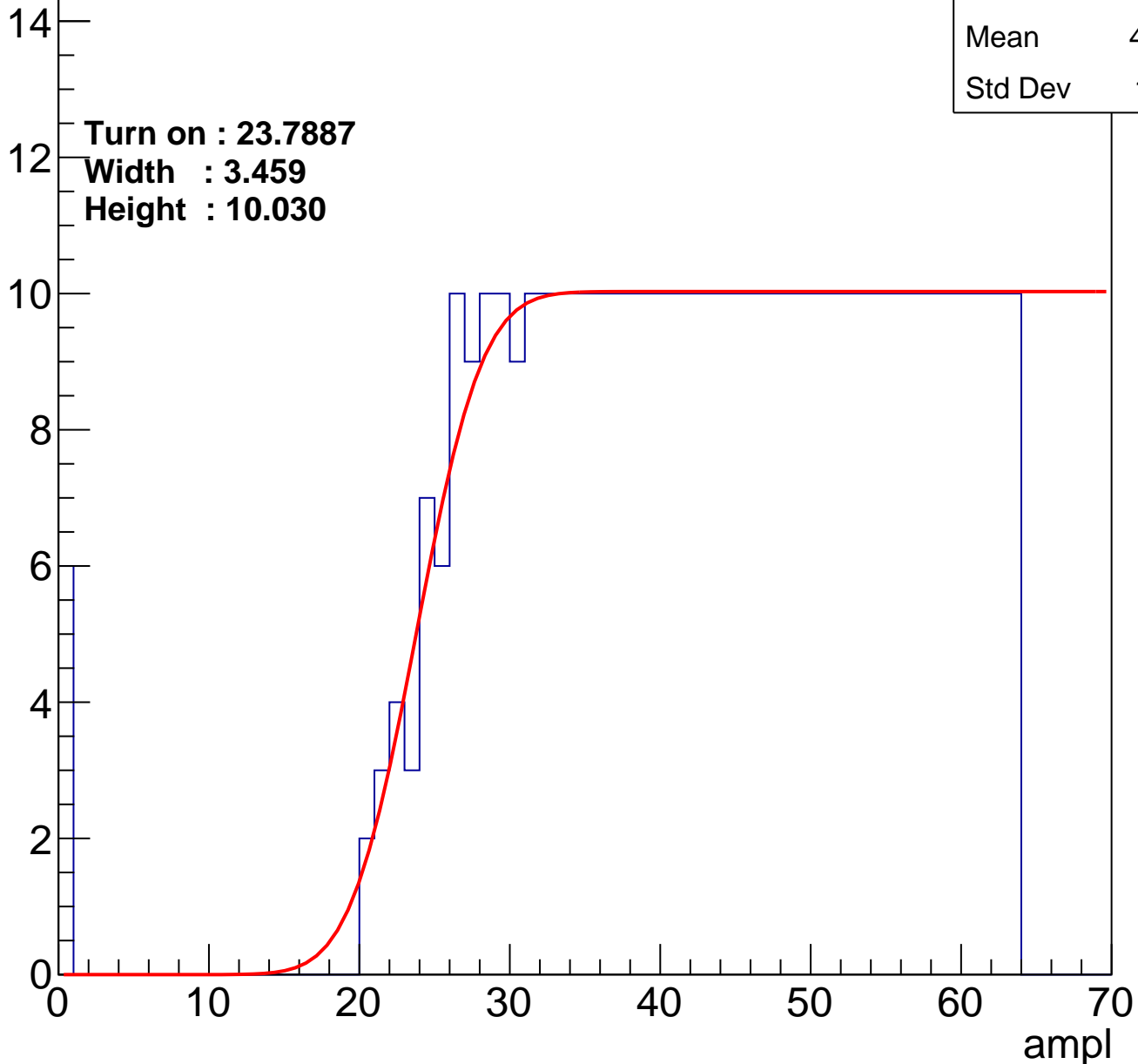
Entries	409
Mean	42.62
Std Dev	12.81

Turn on : 23.7887

Width : 3.459

Height : 10.030

Entry



# B1L100S, U26-ch27

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	356
Mean	45.52
Std Dev	10.68

Turn on : 27.8980

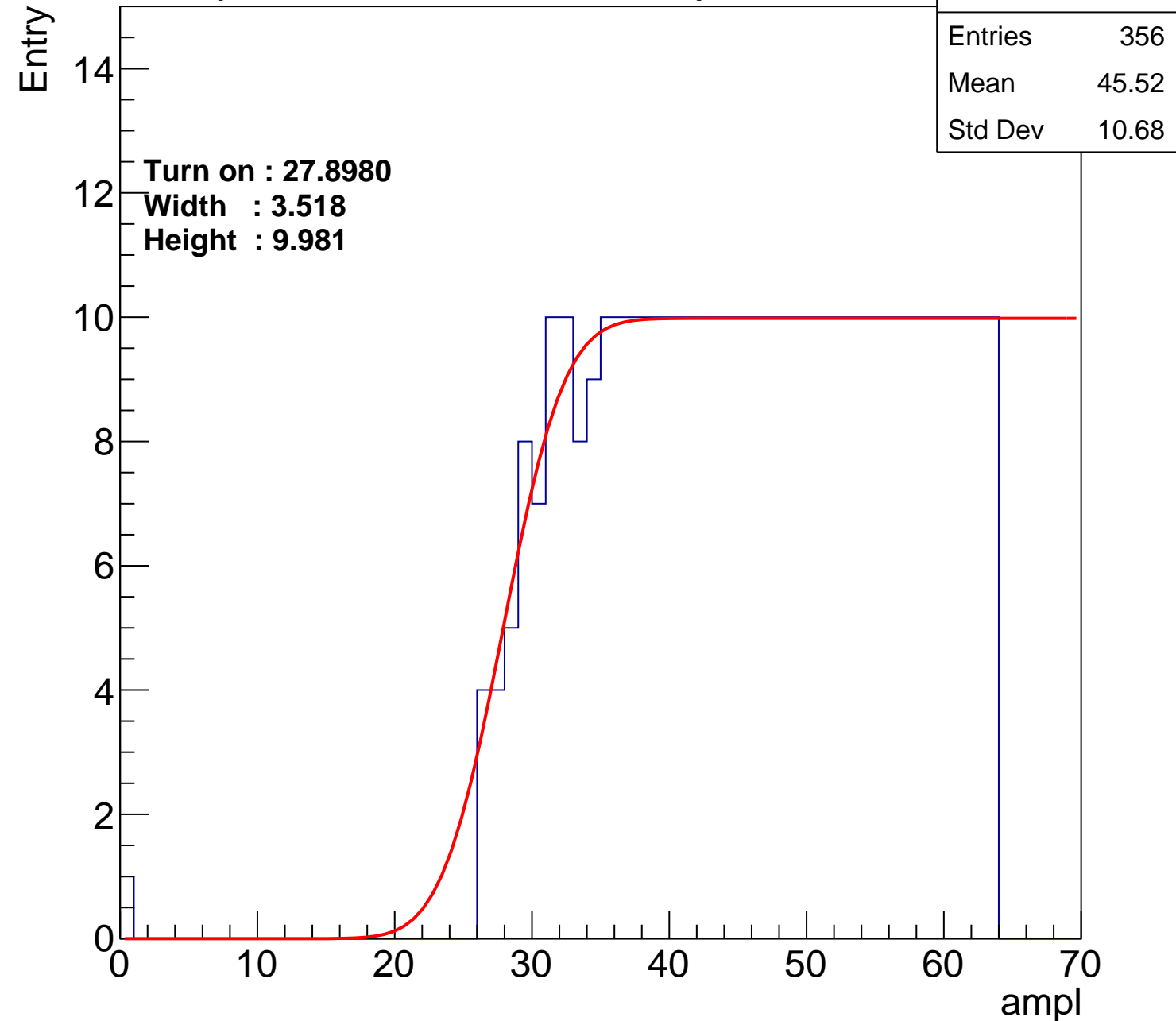
Width : 3.518

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch28

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	356
Mean	45.44
Std Dev	10.91

Turn on : 29.1651

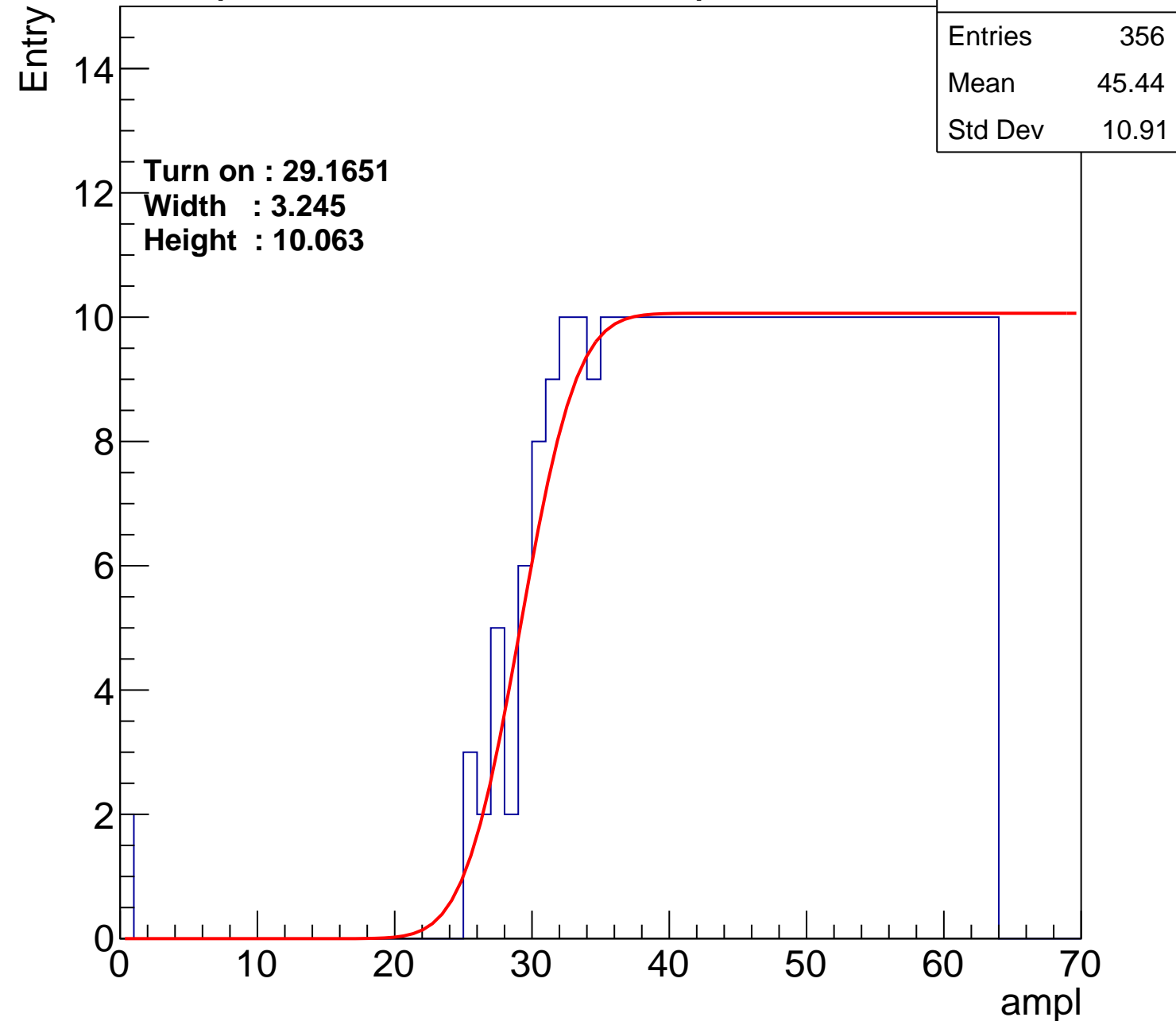
Width : 3.245

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch29

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.38
Std Dev	11.5

Turn on : 27.1970

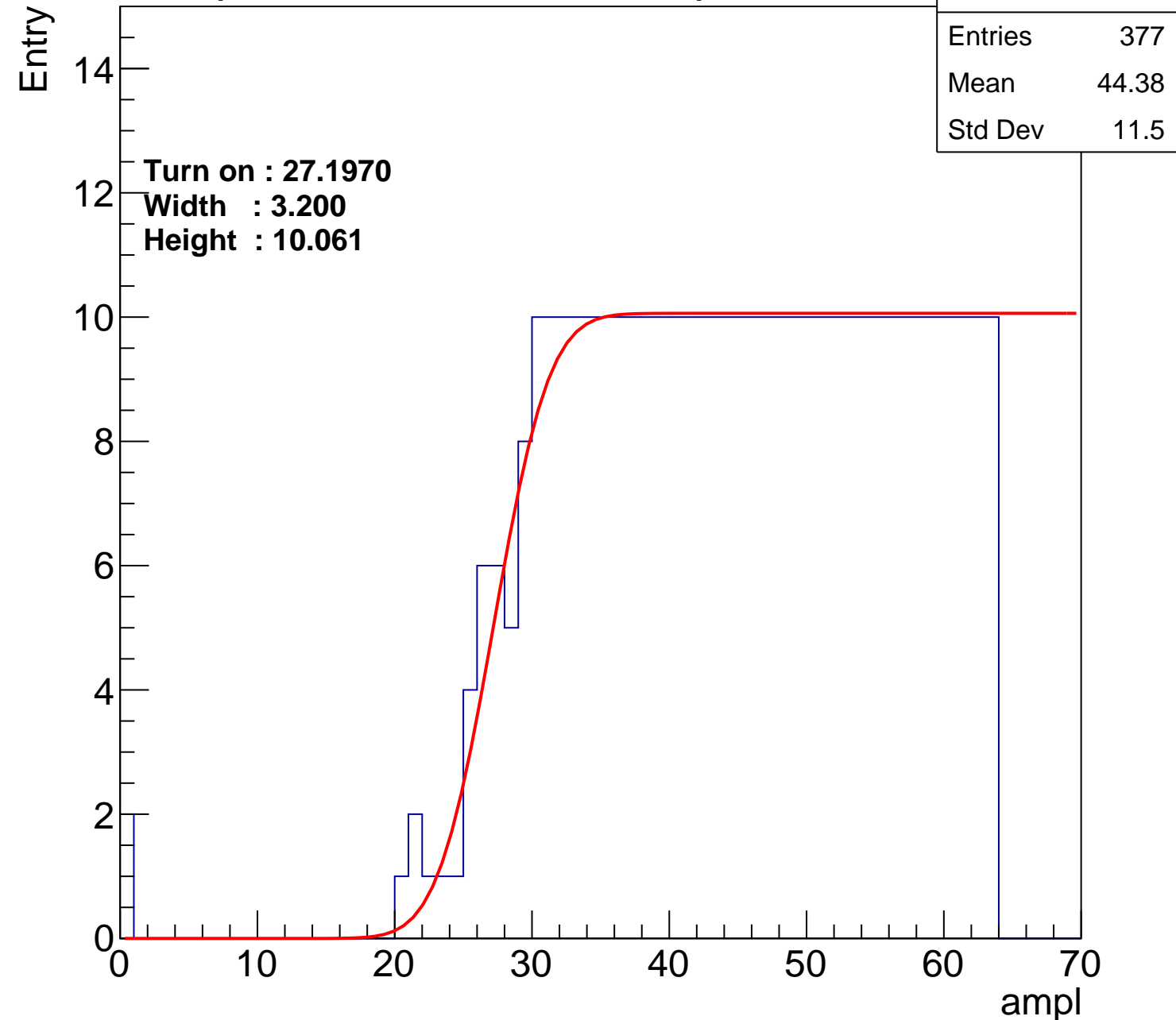
Width : 3.200

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch30

calib\_packv5\_042523\_0143.root, FC#4, port A2

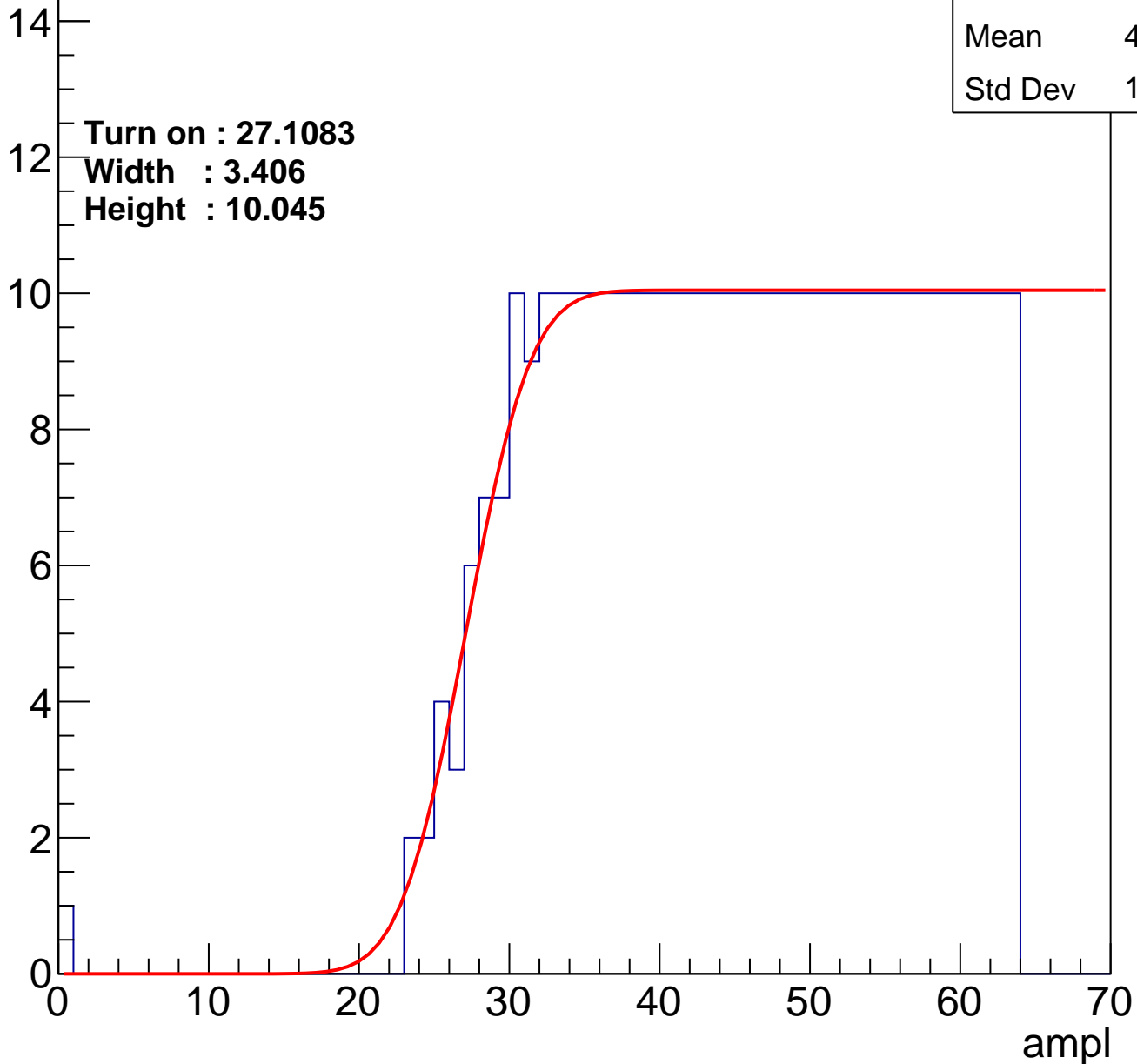
Entries	371
Mean	44.78
Std Dev	11.09

Turn on : 27.1083

Width : 3.406

Height : 10.045

Entry





# B1L100S, U26-ch31

calib\_packv5\_042523\_0143.root, FC#4, port A2

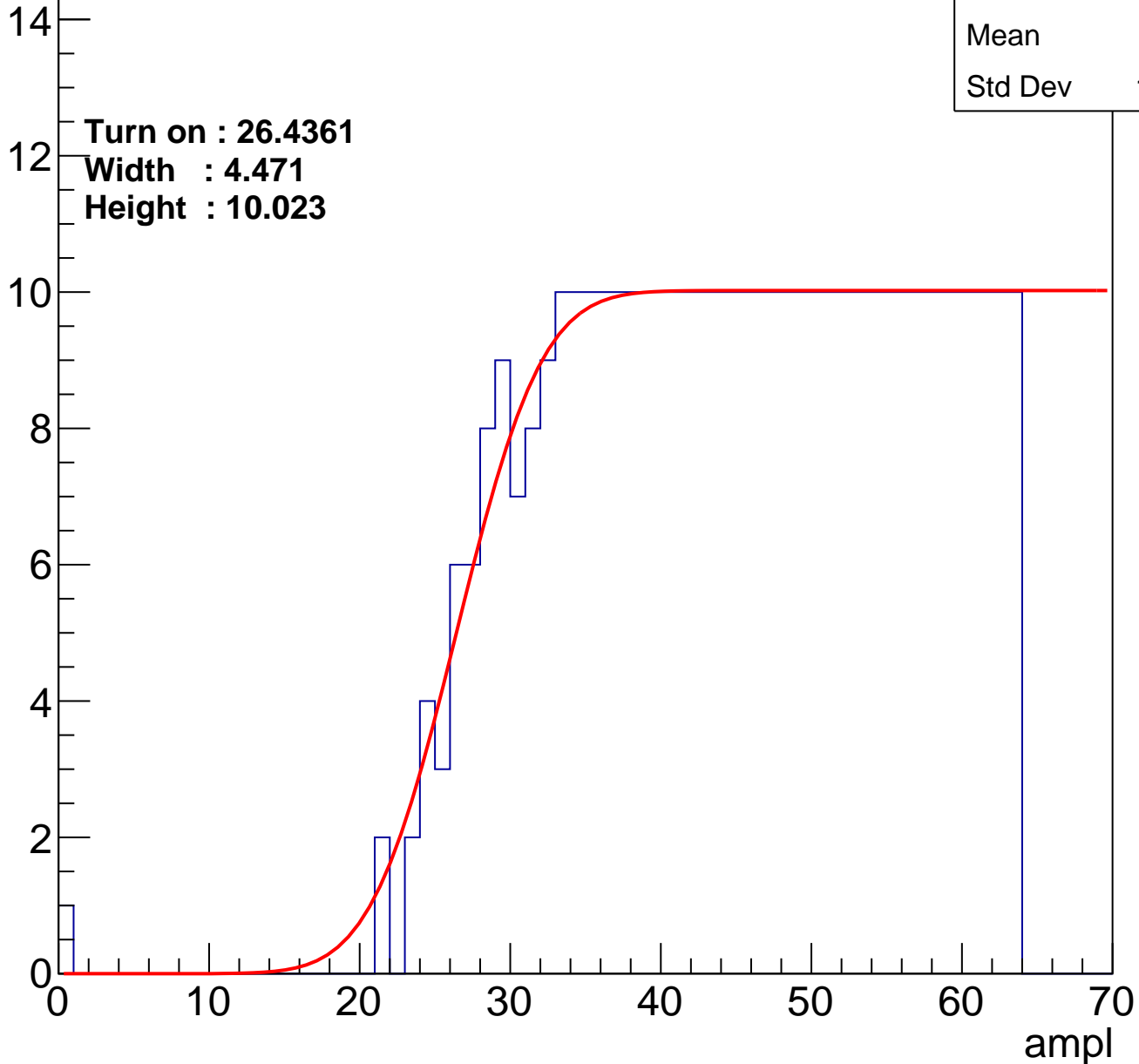
Entries	375
Mean	44.5
Std Dev	11.31

Turn on : 26.4361

Width : 4.471

Height : 10.023

Entry



# B1L100S, U26-ch32

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	361
Mean	44.94
Std Dev	11.7

**Turn on : 28.2973**

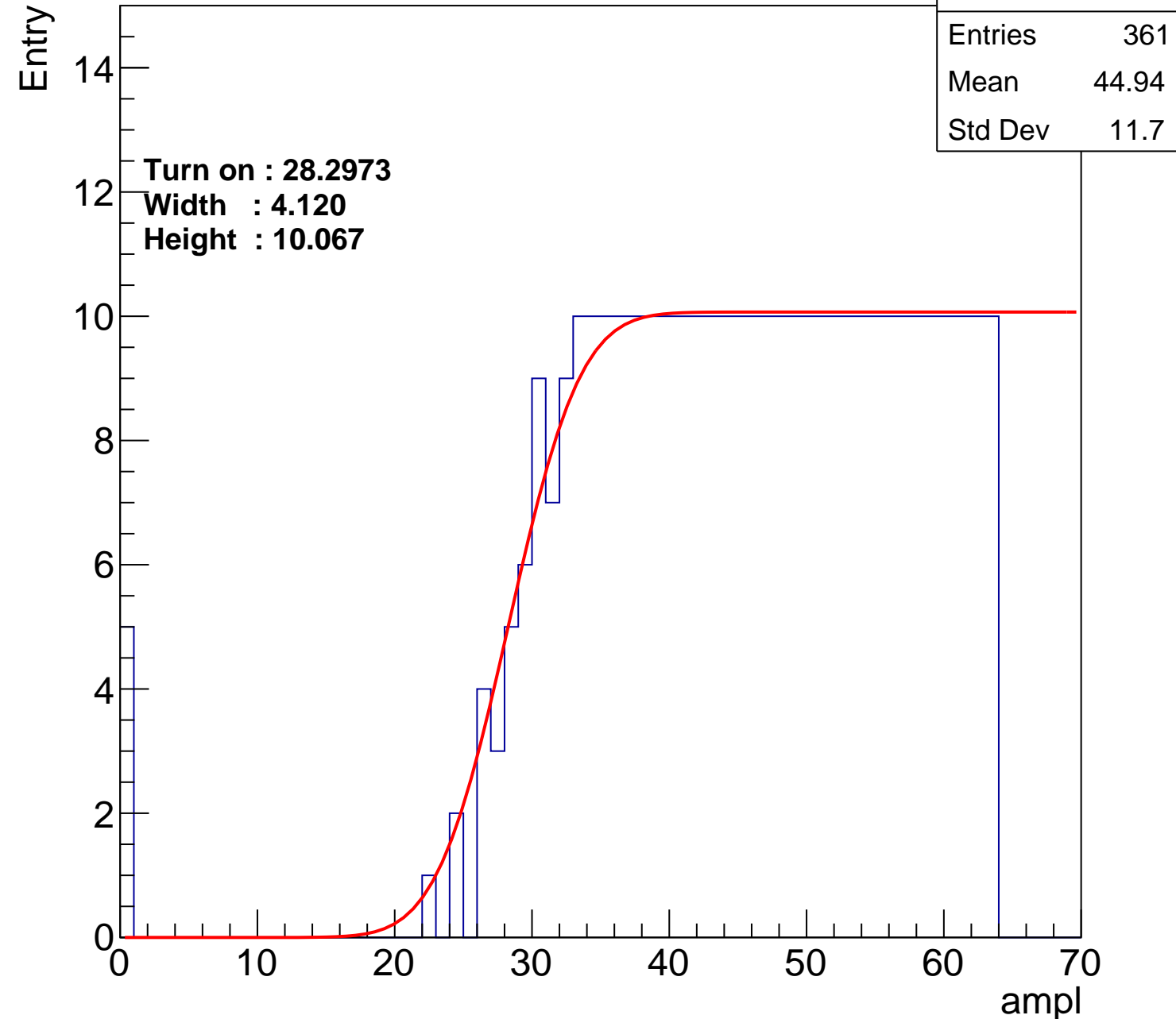
**Width : 4.120**

**Height : 10.067**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch33

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.78
Std Dev	11.26

Turn on : 27.4247

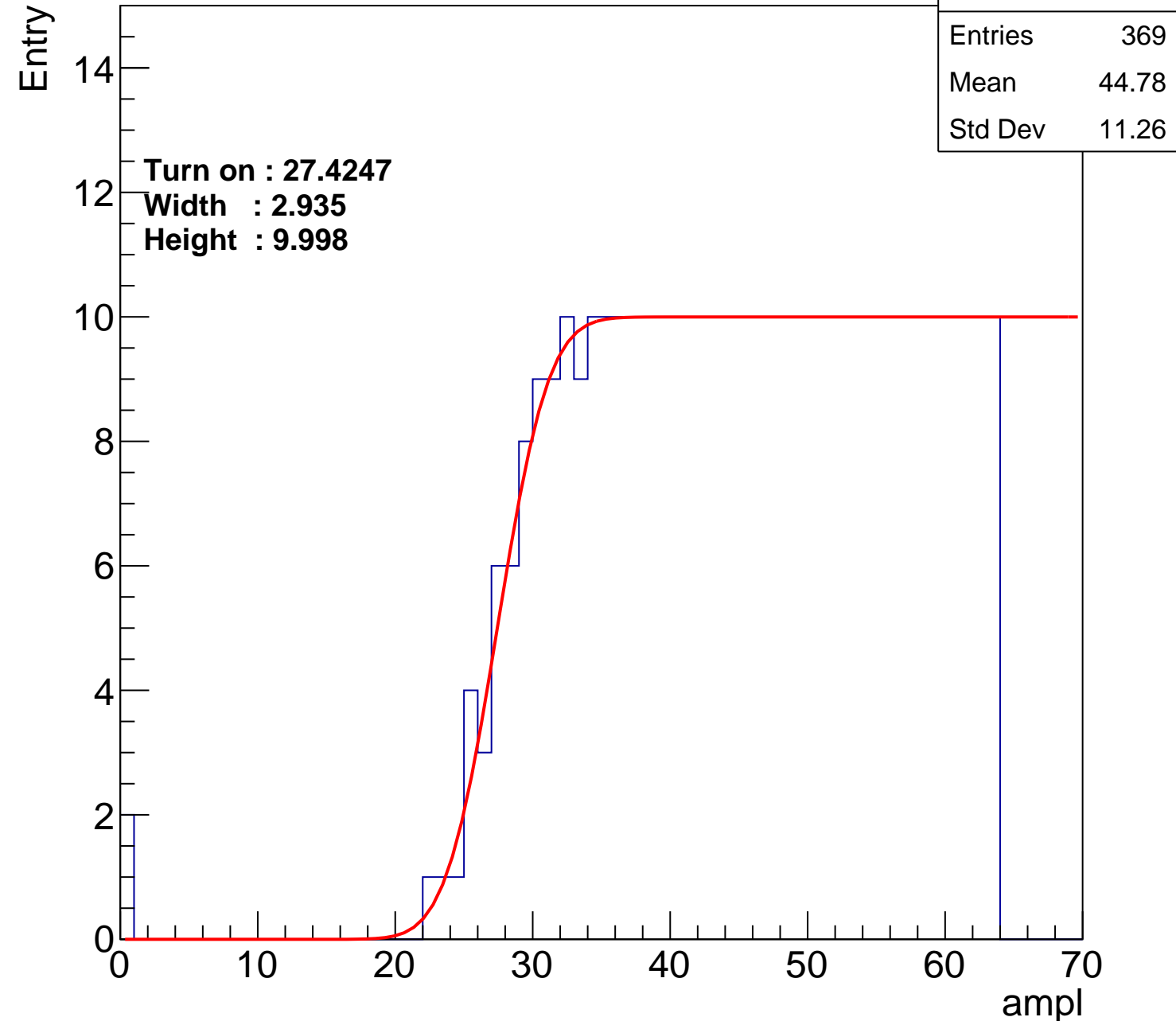
Width : 2.935

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch34

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	362
Mean	45.13
Std Dev	11.09

Turn on : 28.1144

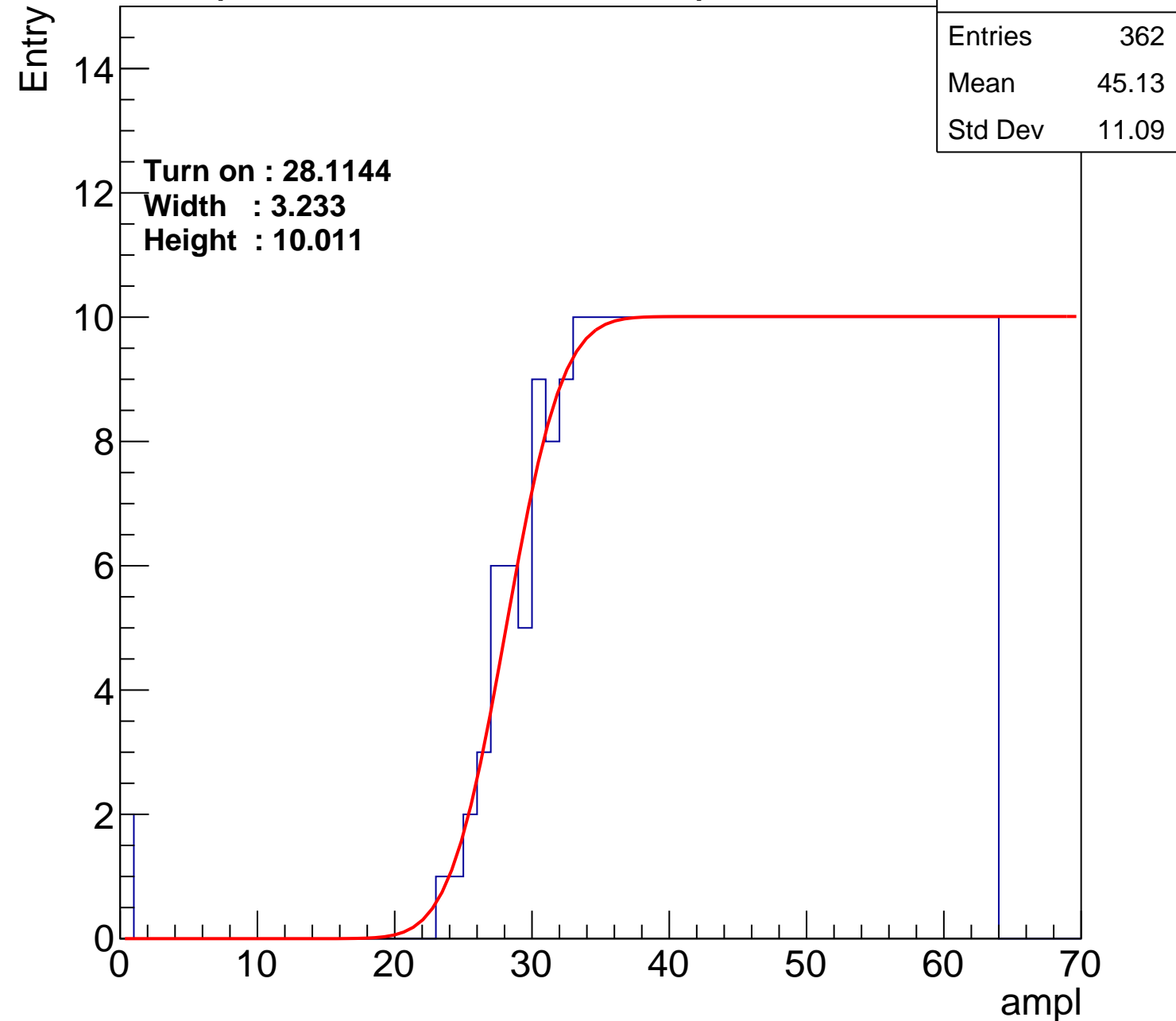
Width : 3.233

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	373
Mean	44.73
Std Dev	11.05

Mean	44.73
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Std Dev	11.05
---------	-------

**Width : 2.778**

**Height : 10.015**



# B1L100S, U26-ch36

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	383
Mean	44.06
Std Dev	11.75

Turn on : 26.4914

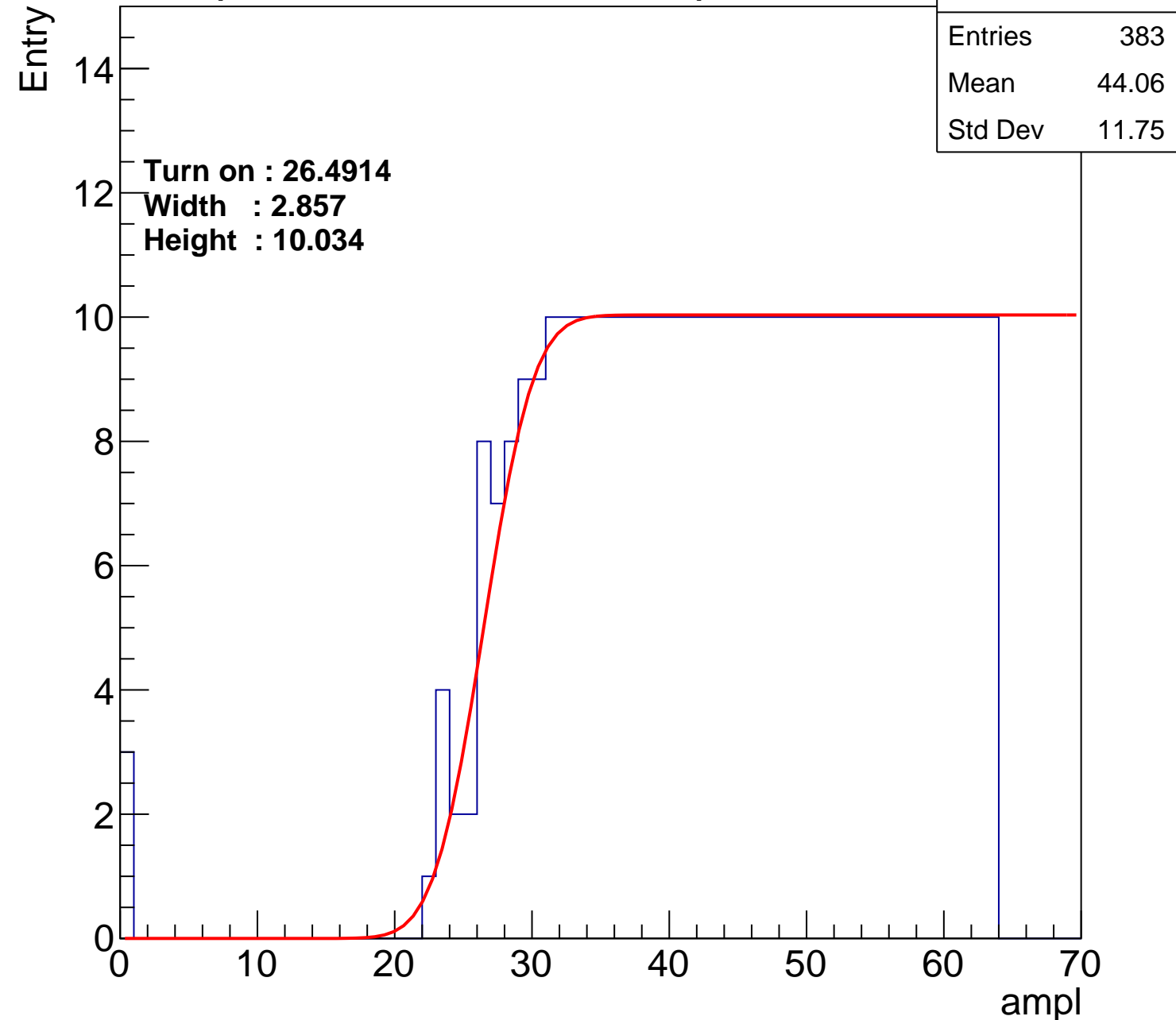
Width : 2.857

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch37

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 25.9530

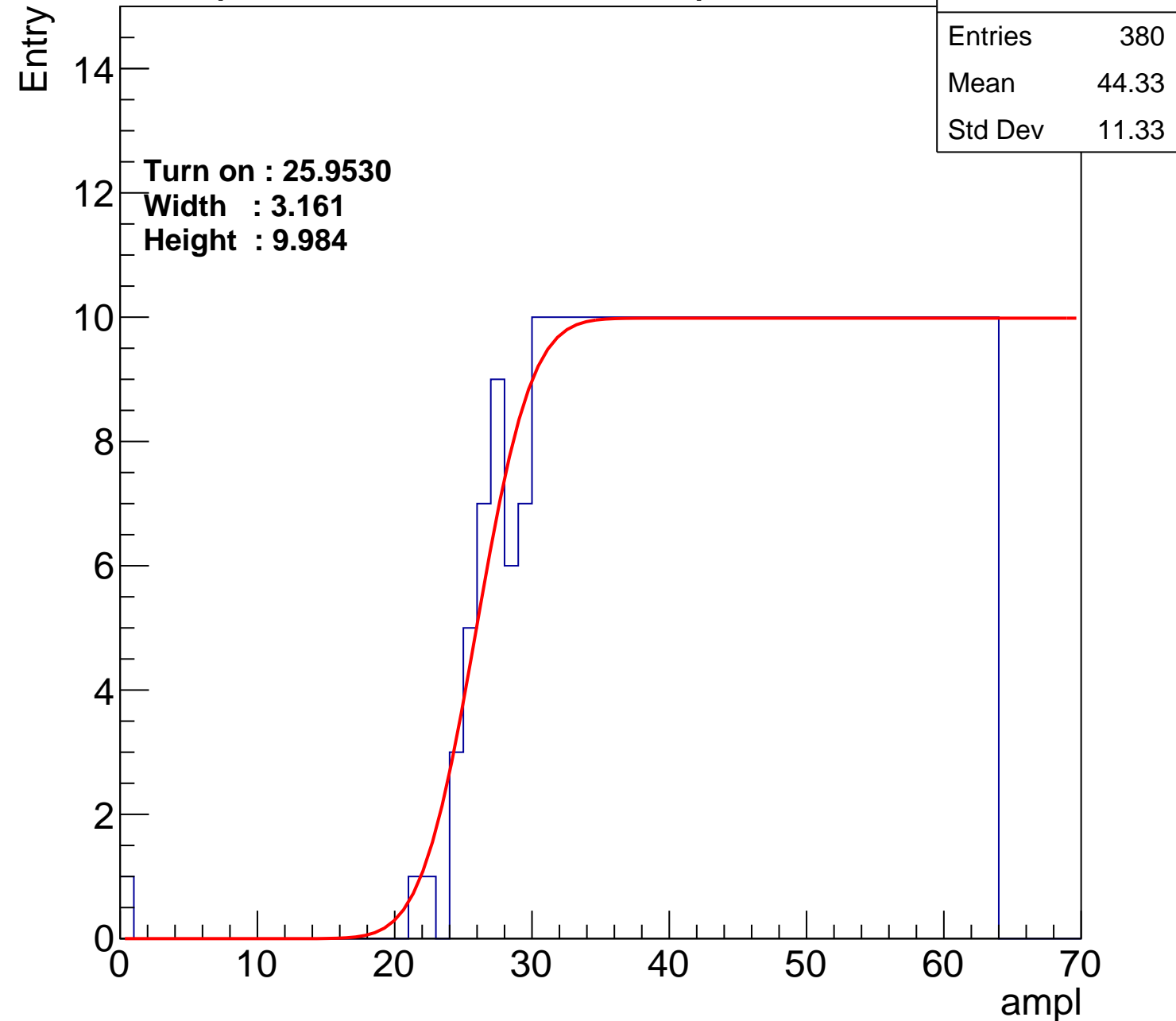
Width : 3.161

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch38

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.5091

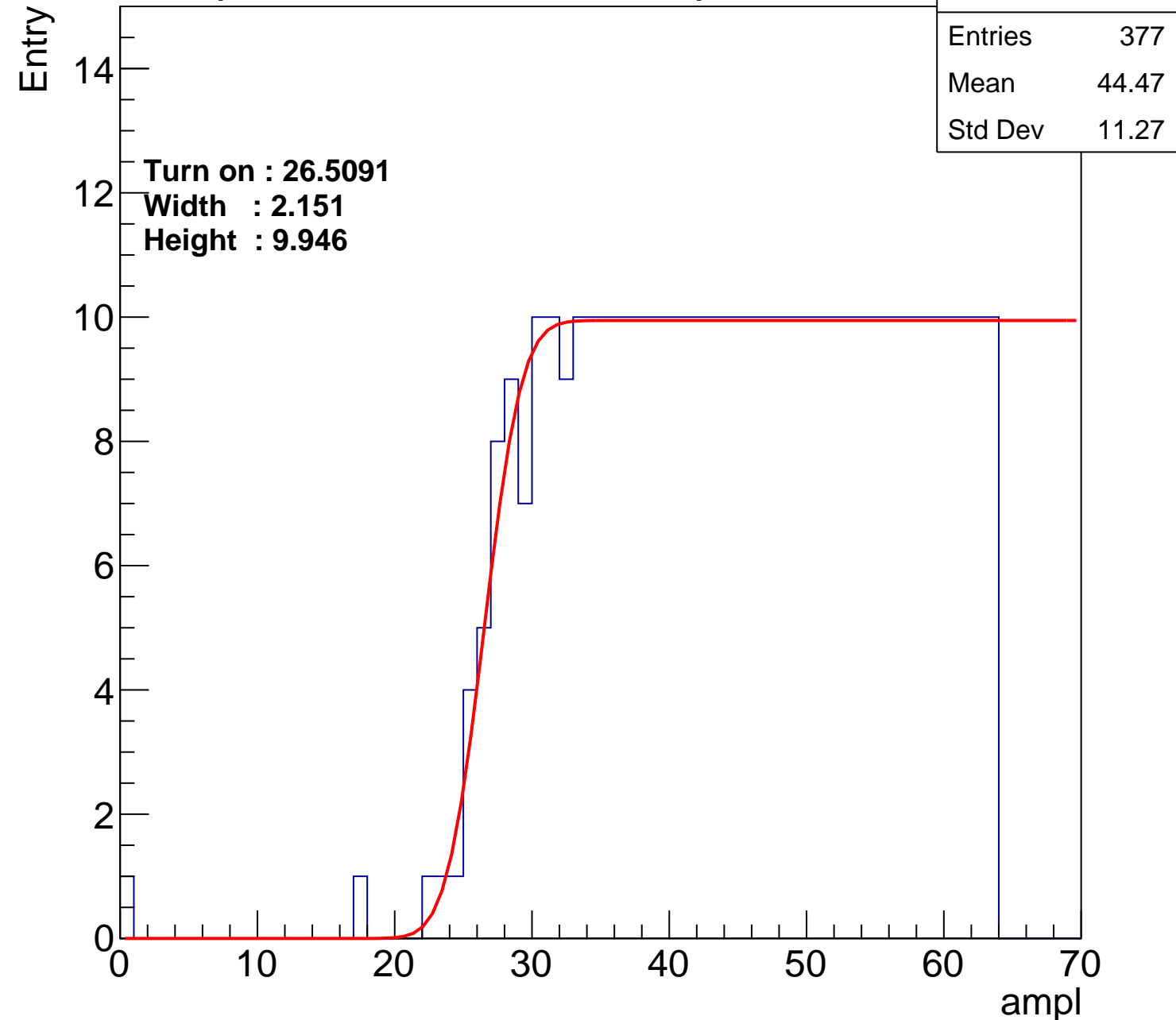
Width : 2.151

Height : 9.946

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch39

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	45.13
Std Dev	10.89

Turn on : 27.8596

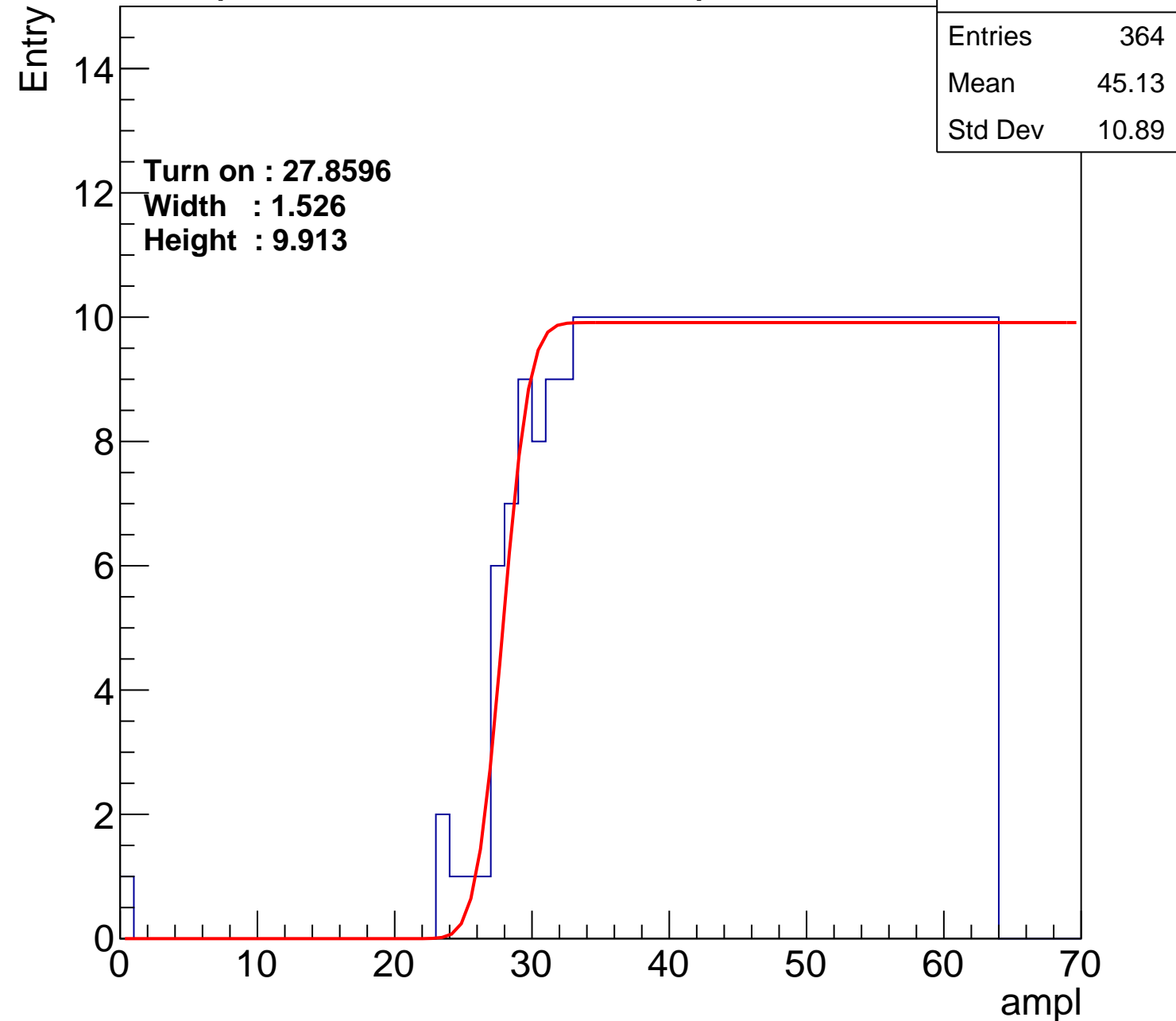
Width : 1.526

Height : 9.913

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch40

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	366
Mean	44.89
Std Dev	11.27

Turn on : 28.1932

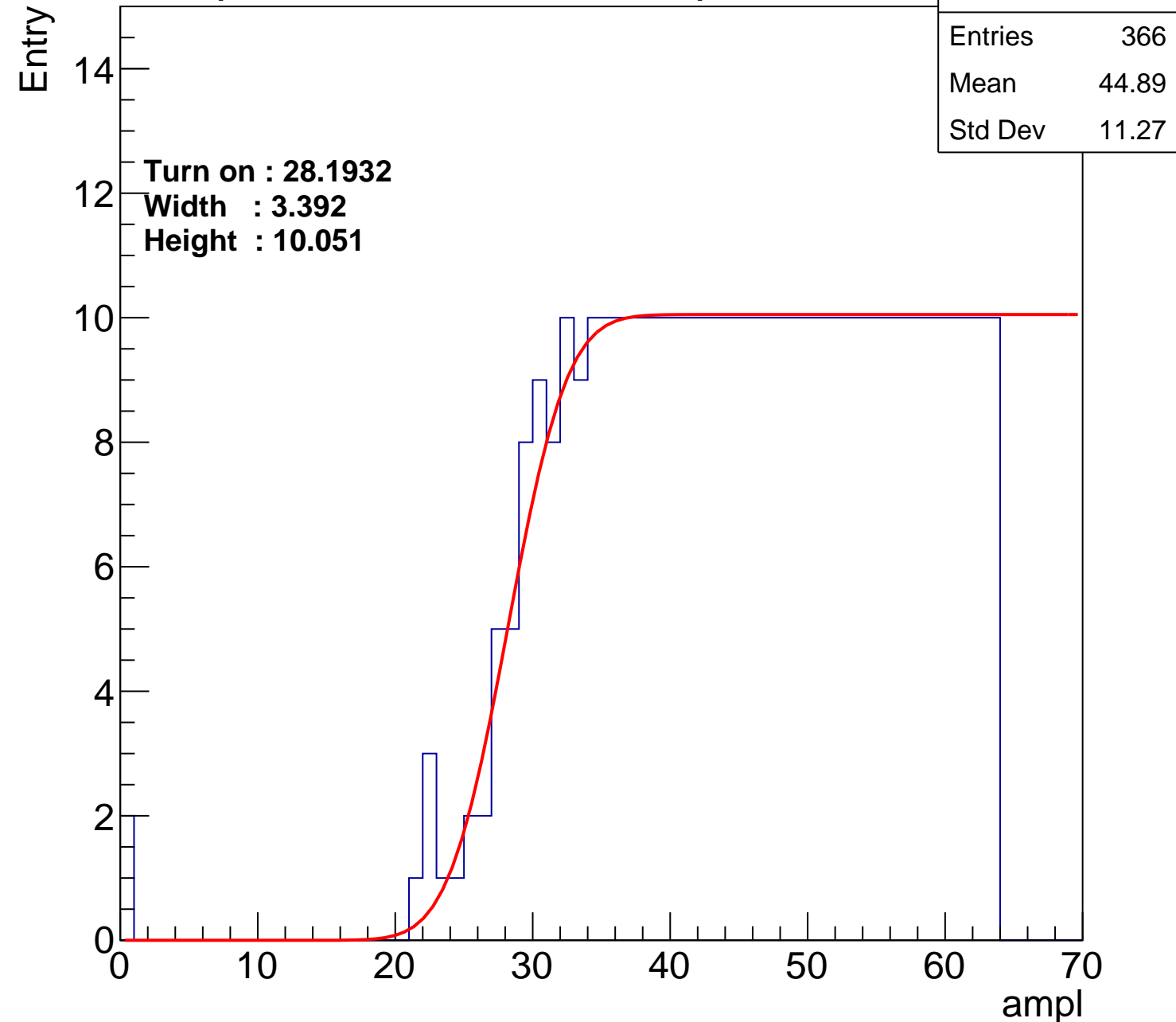
Width : 3.392

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch41

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.6
Std Dev	11.49

Turn on : 26.9574

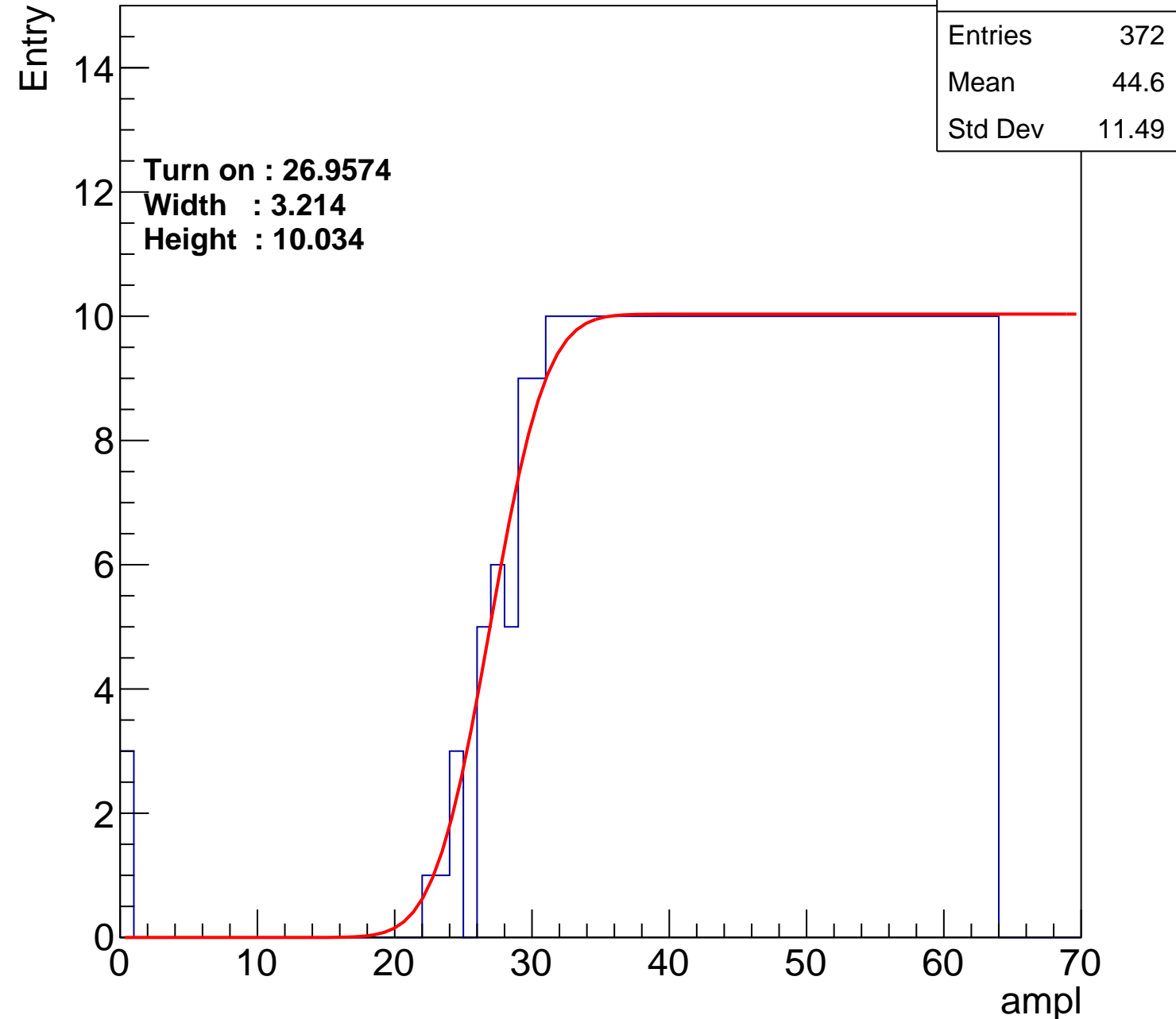
Width : 3.214

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch42

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	357
Mean	45.26
Std Dev	11.25

Turn on : 28.8012

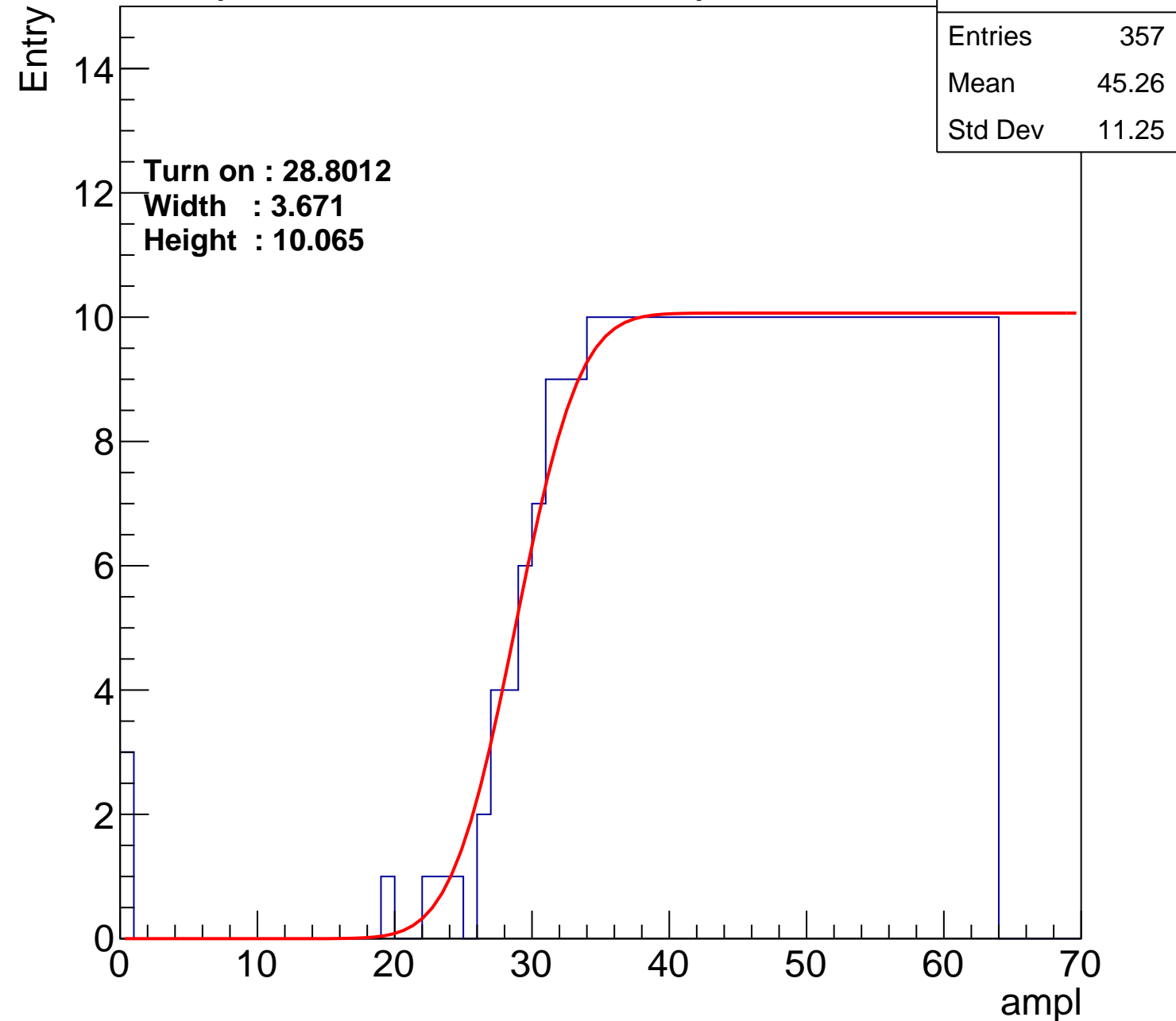
Width : 3.671

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch43

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	360
Mean	45.19
Std Dev	11.18

**Turn on : 28.1609**

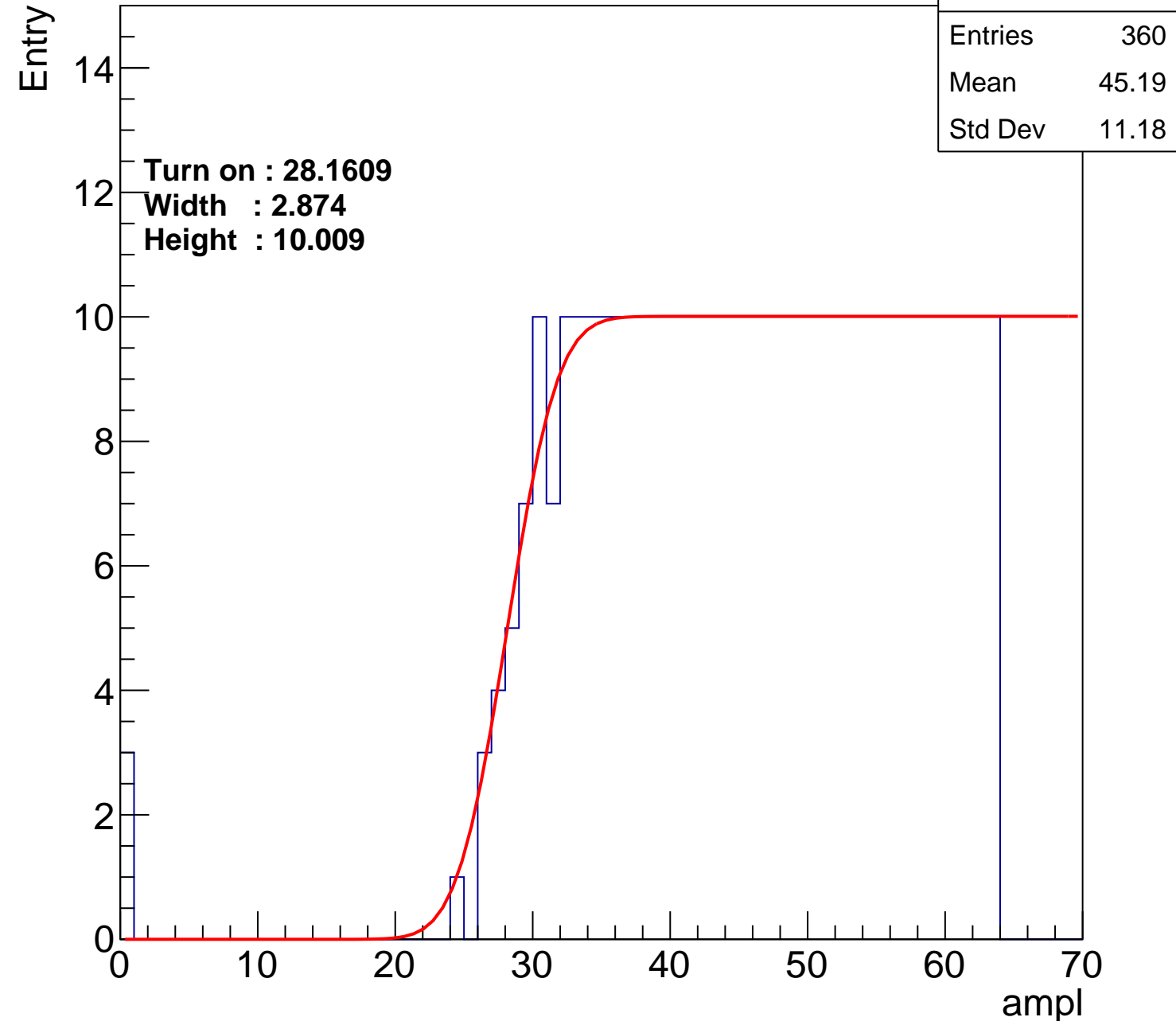
**Width : 2.874**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch44

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	358
Mean	45.38
Std Dev	10.9

Turn on : 28.3453

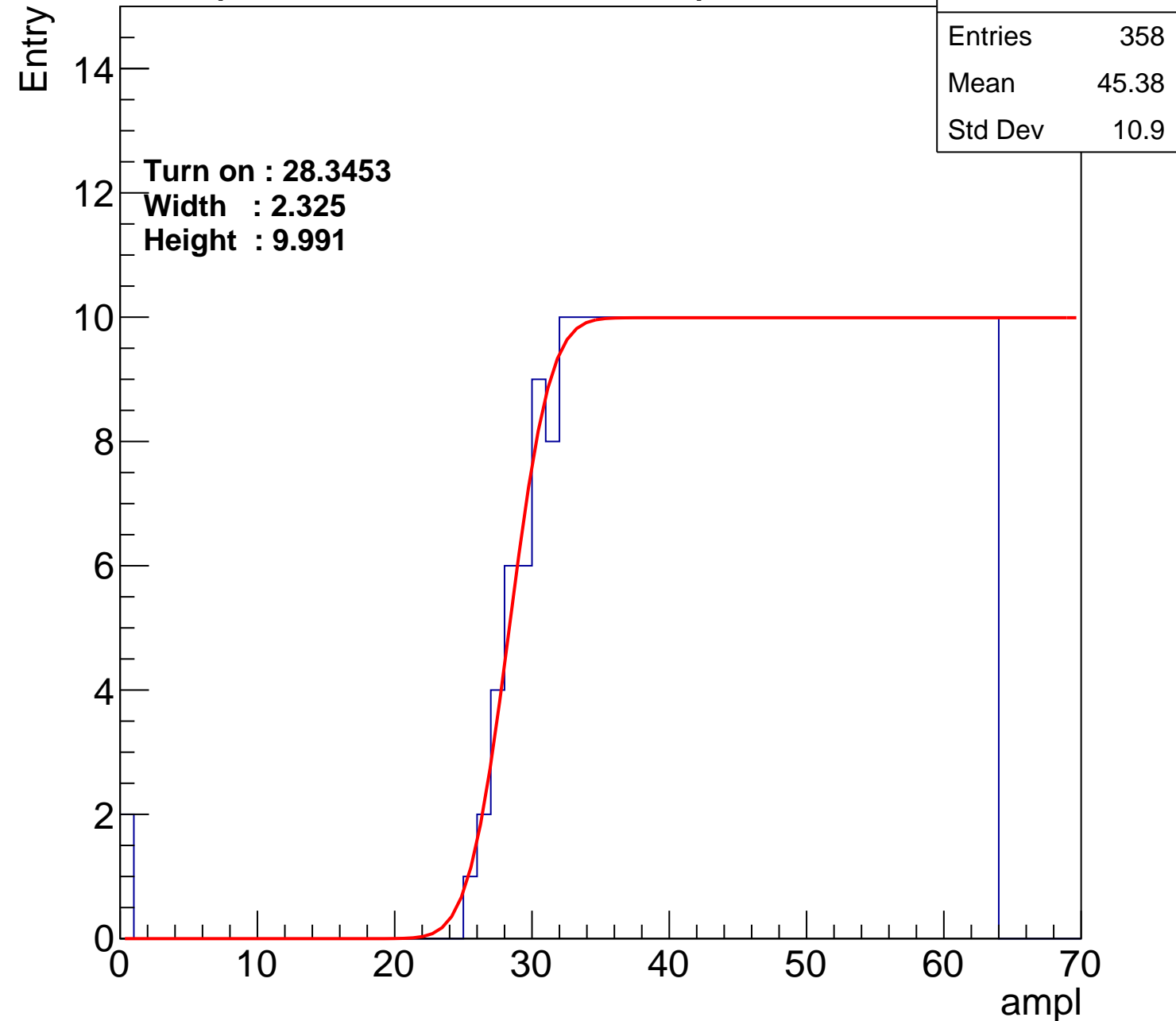
Width : 2.325

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch45

calib\_packv5\_042523\_0143.root, FC#4, port A2

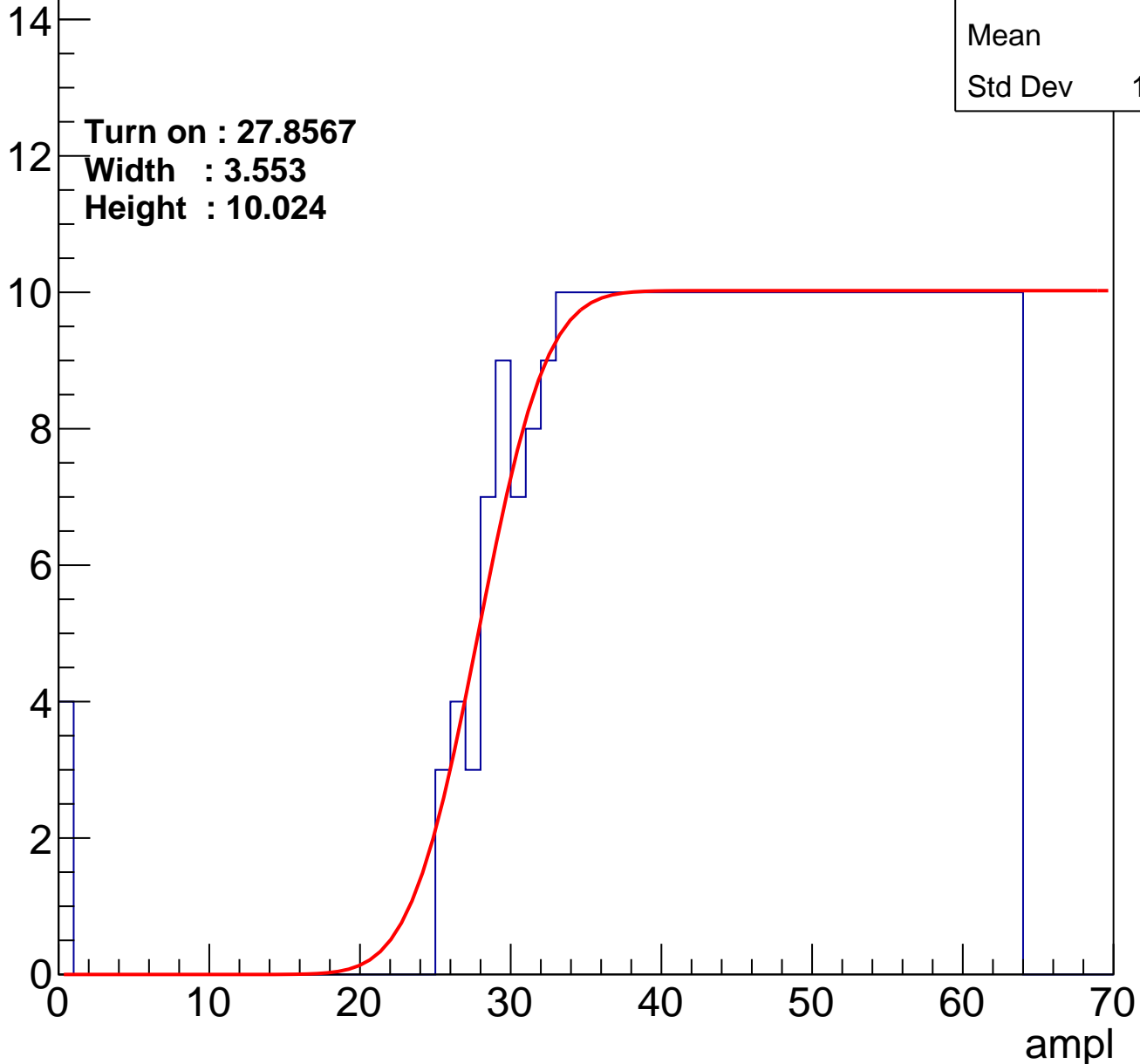
Entries	364
Mean	44.9
Std Dev	11.52

Turn on : 27.8567

Width : 3.553

Height : 10.024

Entry



# B1L100S, U26-ch46

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	359
Mean	45.34
Std Dev	10.81

Turn on : 28.0133

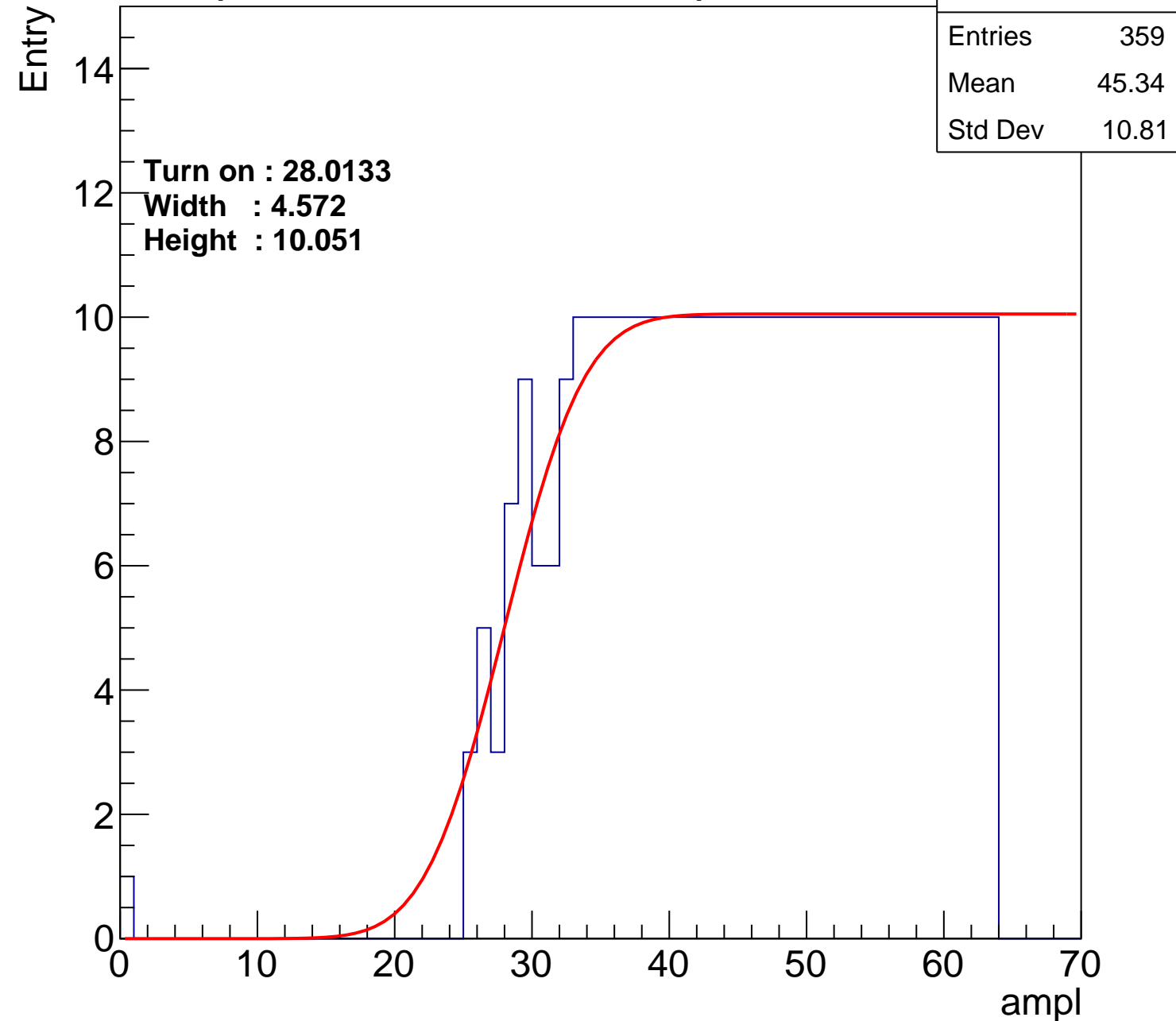
Width : 4.572

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch47

calib\_packv5\_042523\_0143.root, FC#4, port A2

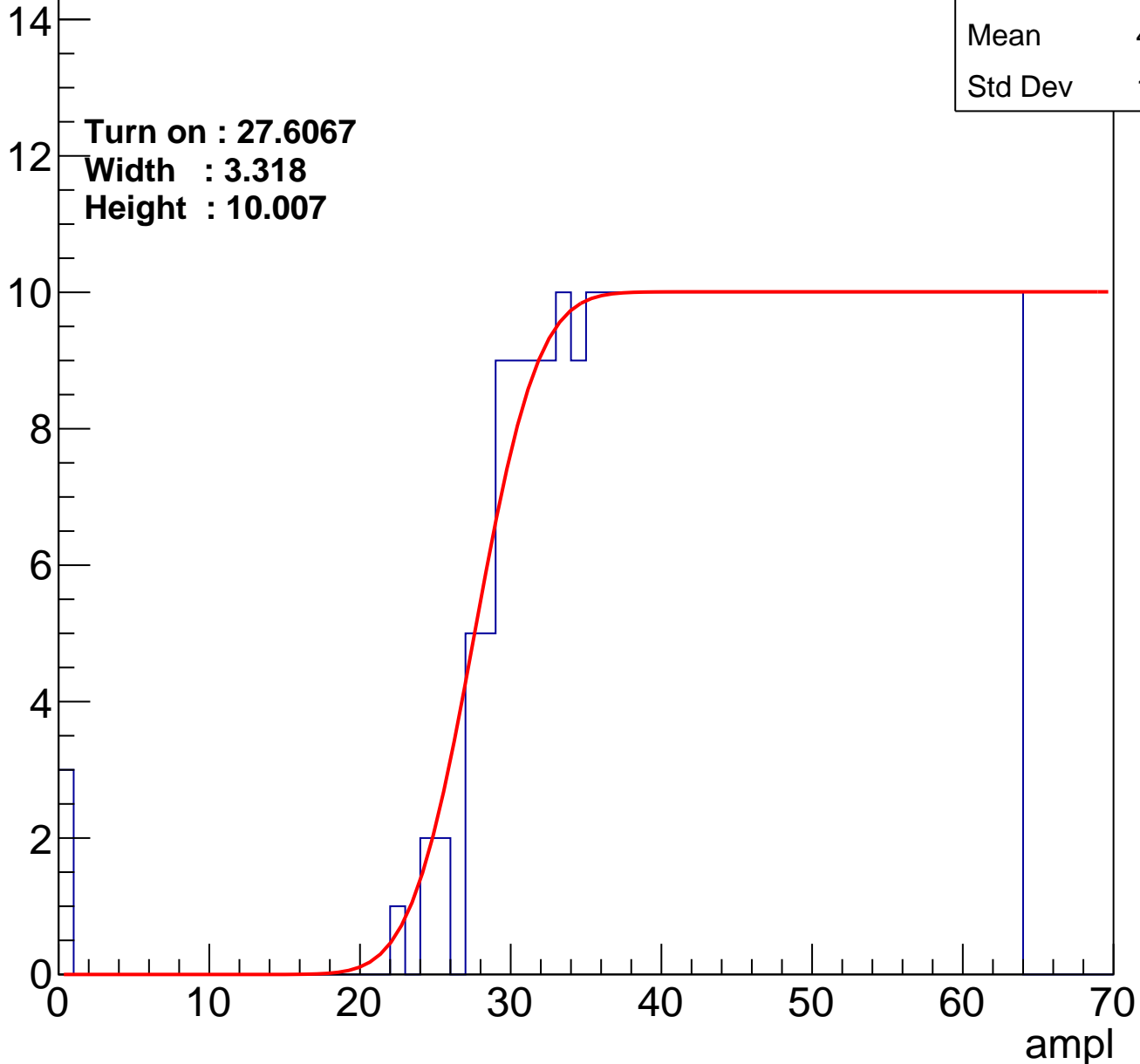
Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 27.6067

Width : 3.318

Height : 10.007

Entry



# B1L100S, U26-ch48

calib\_packv5\_042523\_0143.root, FC#4, port A2

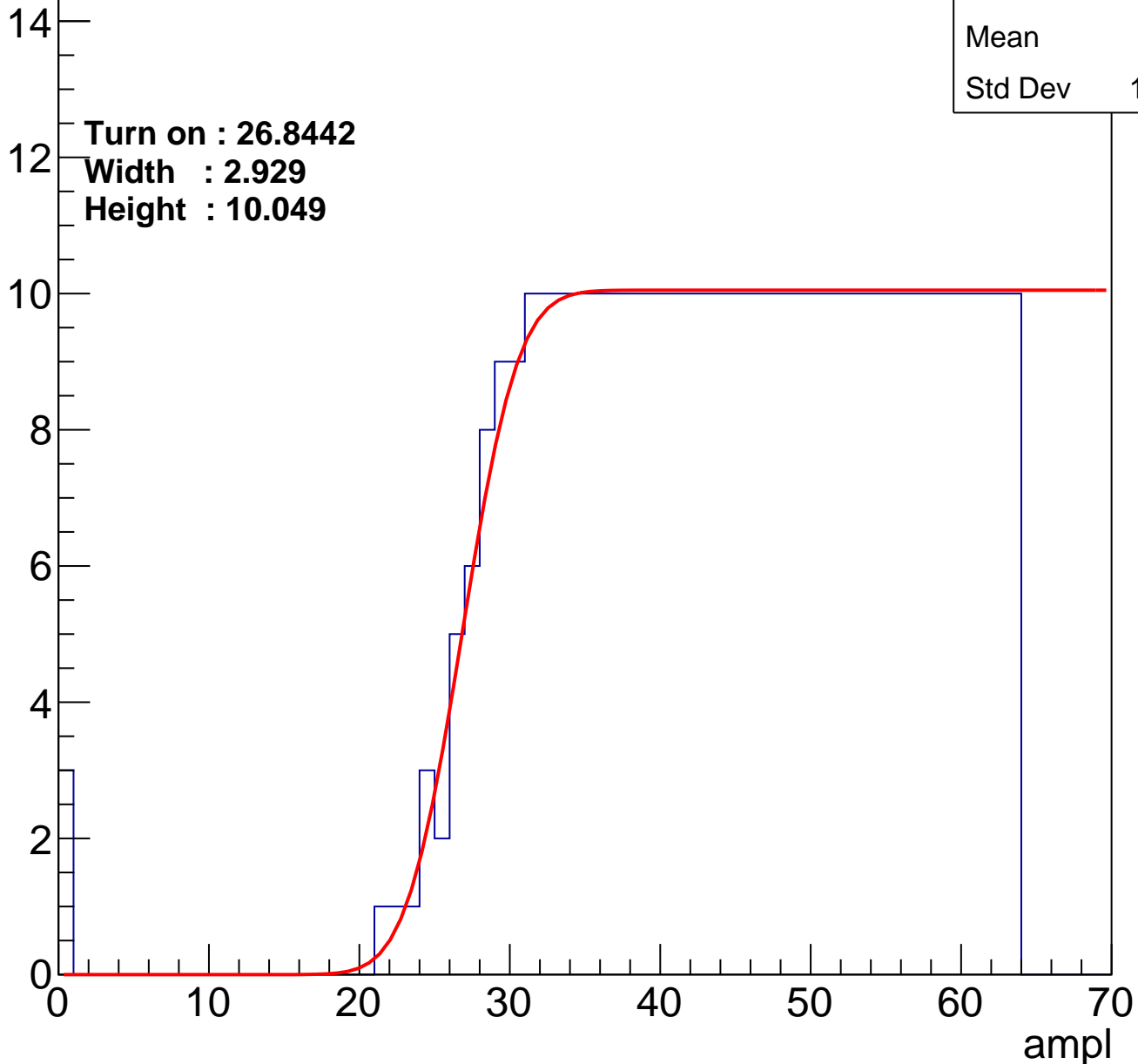
Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.8442

Width : 2.929

Height : 10.049

Entry



# B1L100S, U26-ch49

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	368
Mean	44.75
Std Dev	11.53

Turn on : 27.4967

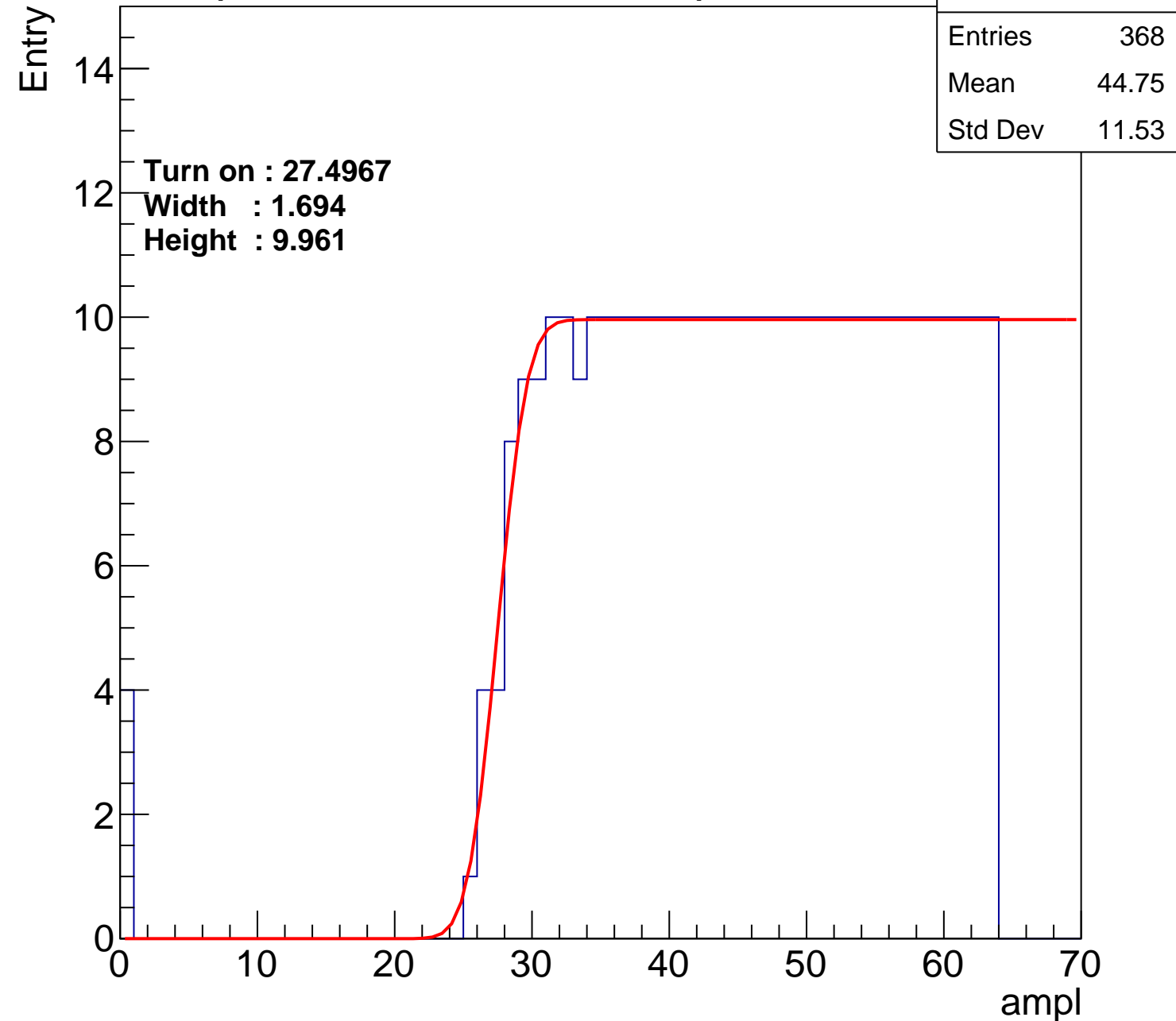
Width : 1.694

Height : 9.961

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch50

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	350
Mean	45.71
Std Dev	10.79

Turn on : 29.4490

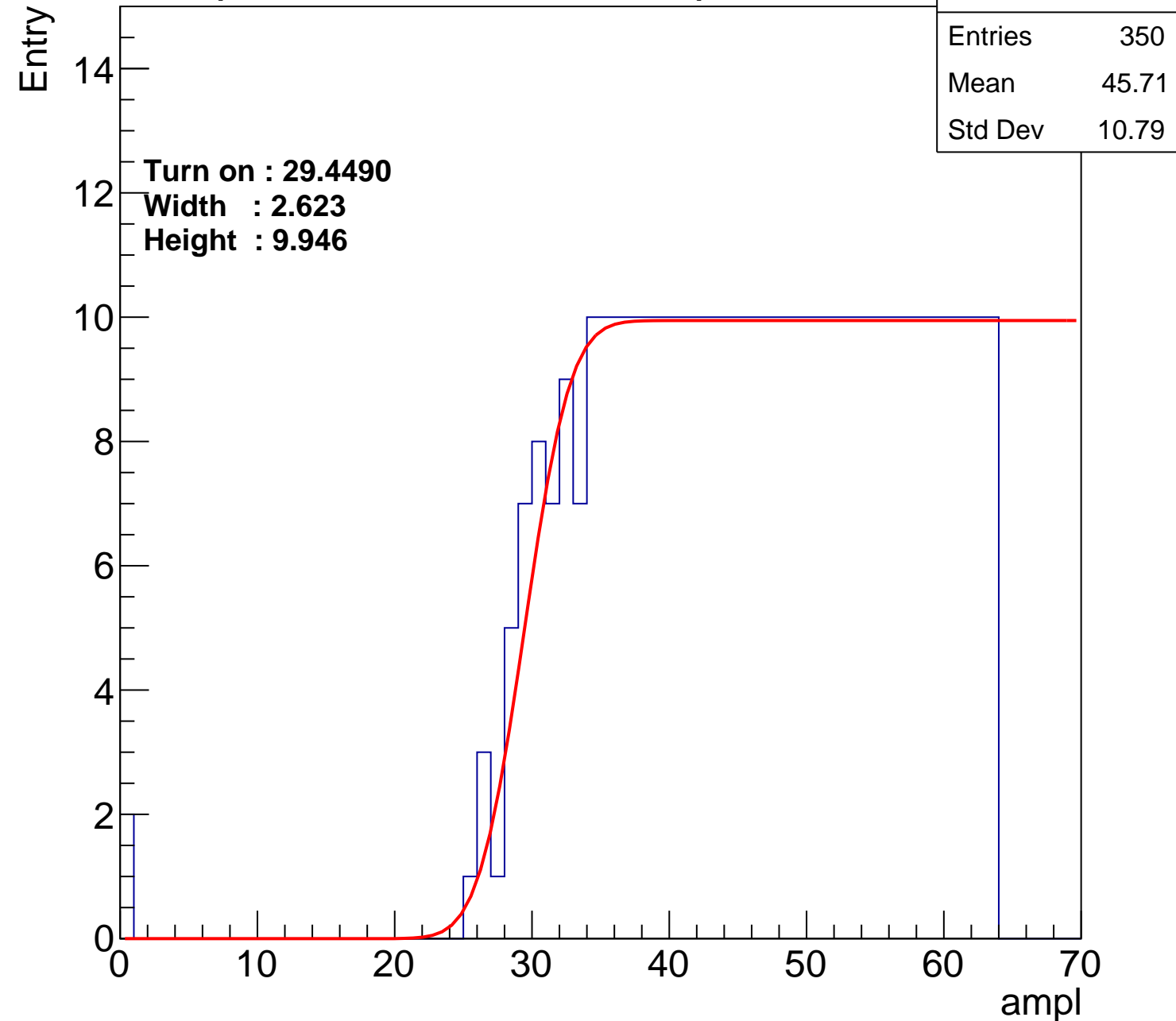
Width : 2.623

Height : 9.946

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch51

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	357
Mean	45.29
Std Dev	11.2

**Turn on : 28.9687**

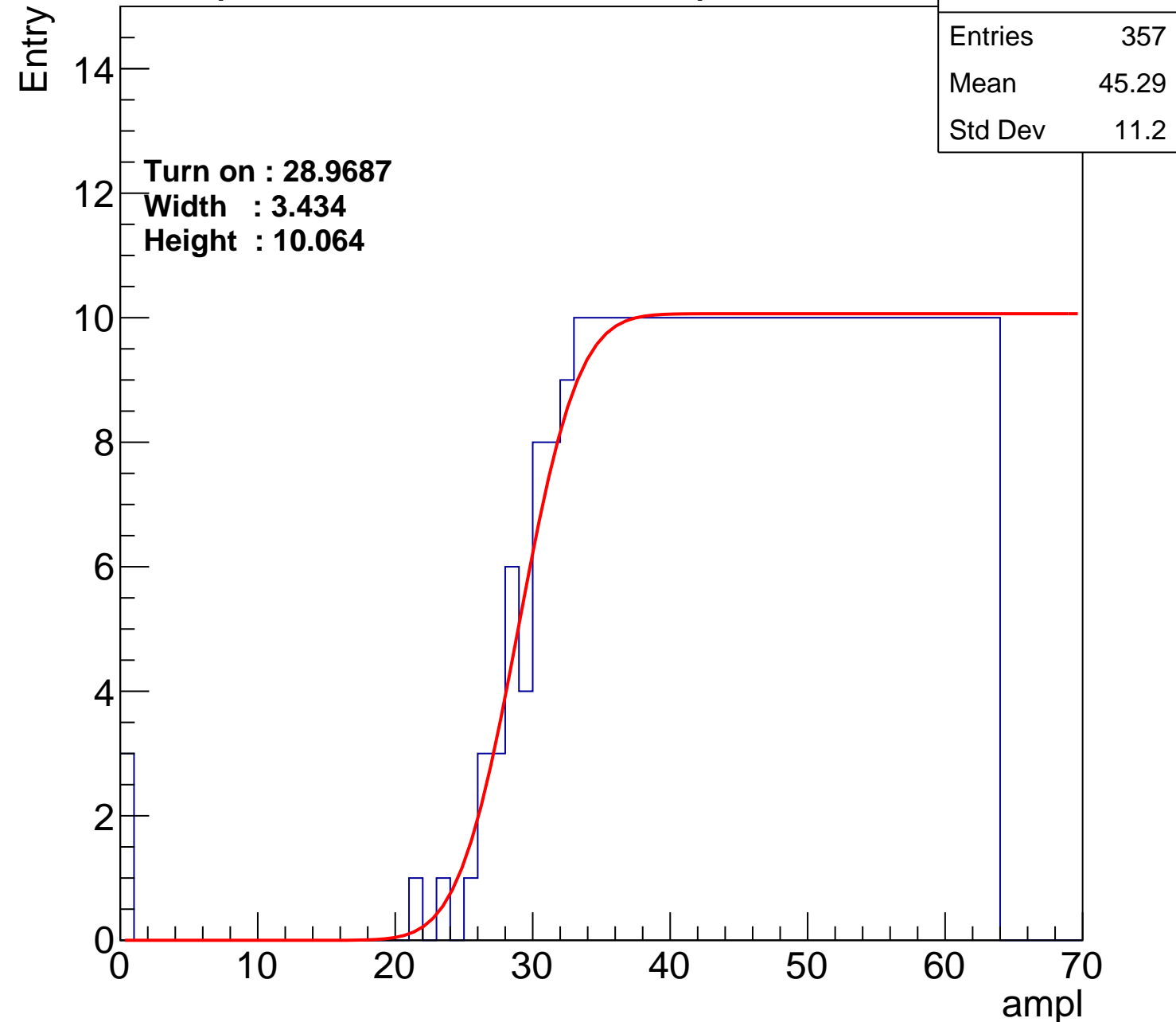
**Width : 3.434**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch52

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.24
Std Dev	11.54

Turn on : 25.8659

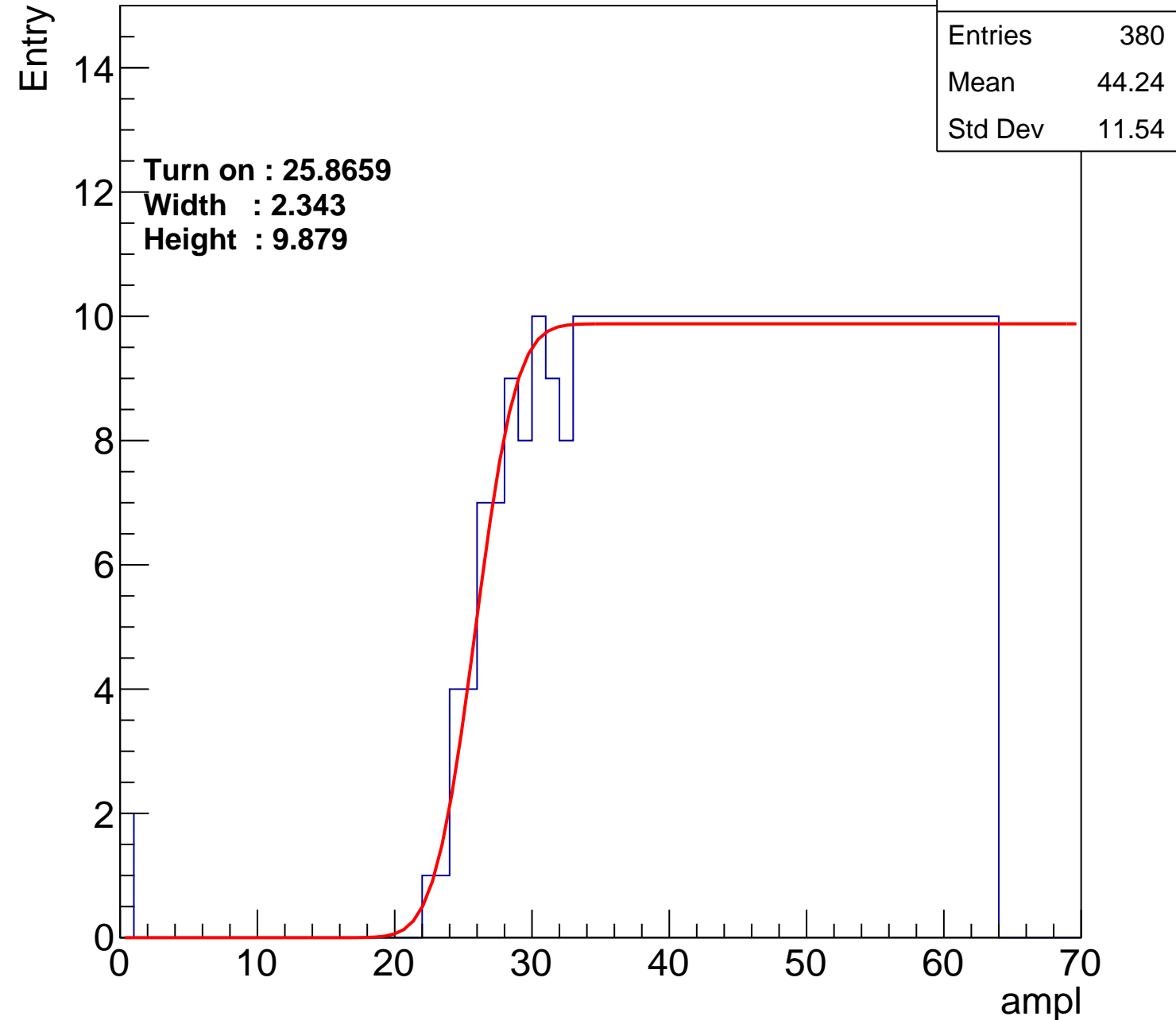
Width : 2.343

Height : 9.879

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch53

calib\_packv5\_042523\_0143.root, FC#4, port A2

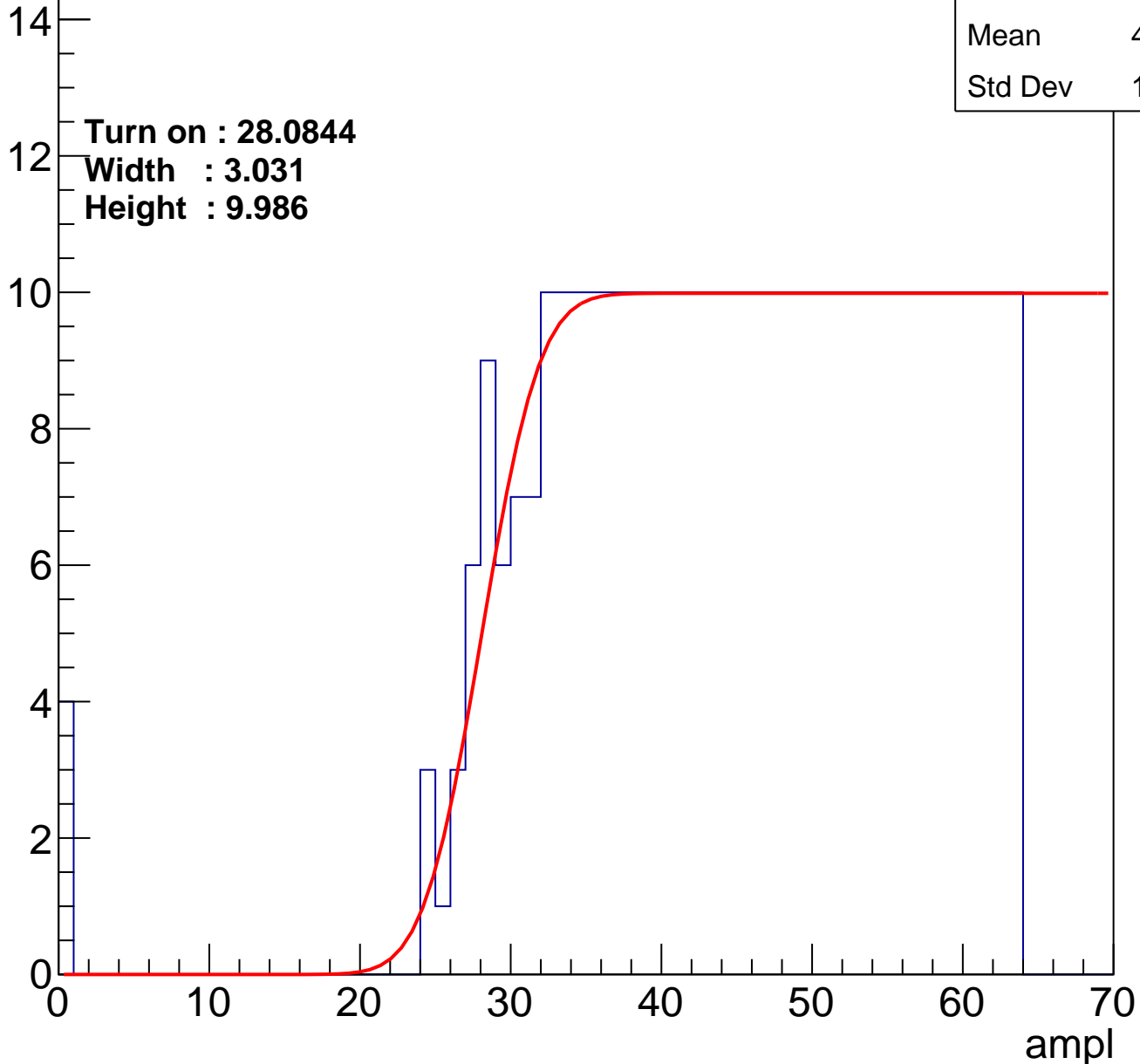
Entries	366
Mean	44.78
Std Dev	11.59

Turn on : 28.0844

Width : 3.031

Height : 9.986

Entry



# B1L100S, U26-ch54

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.37
Std Dev	11.57

Turn on : 26.5091

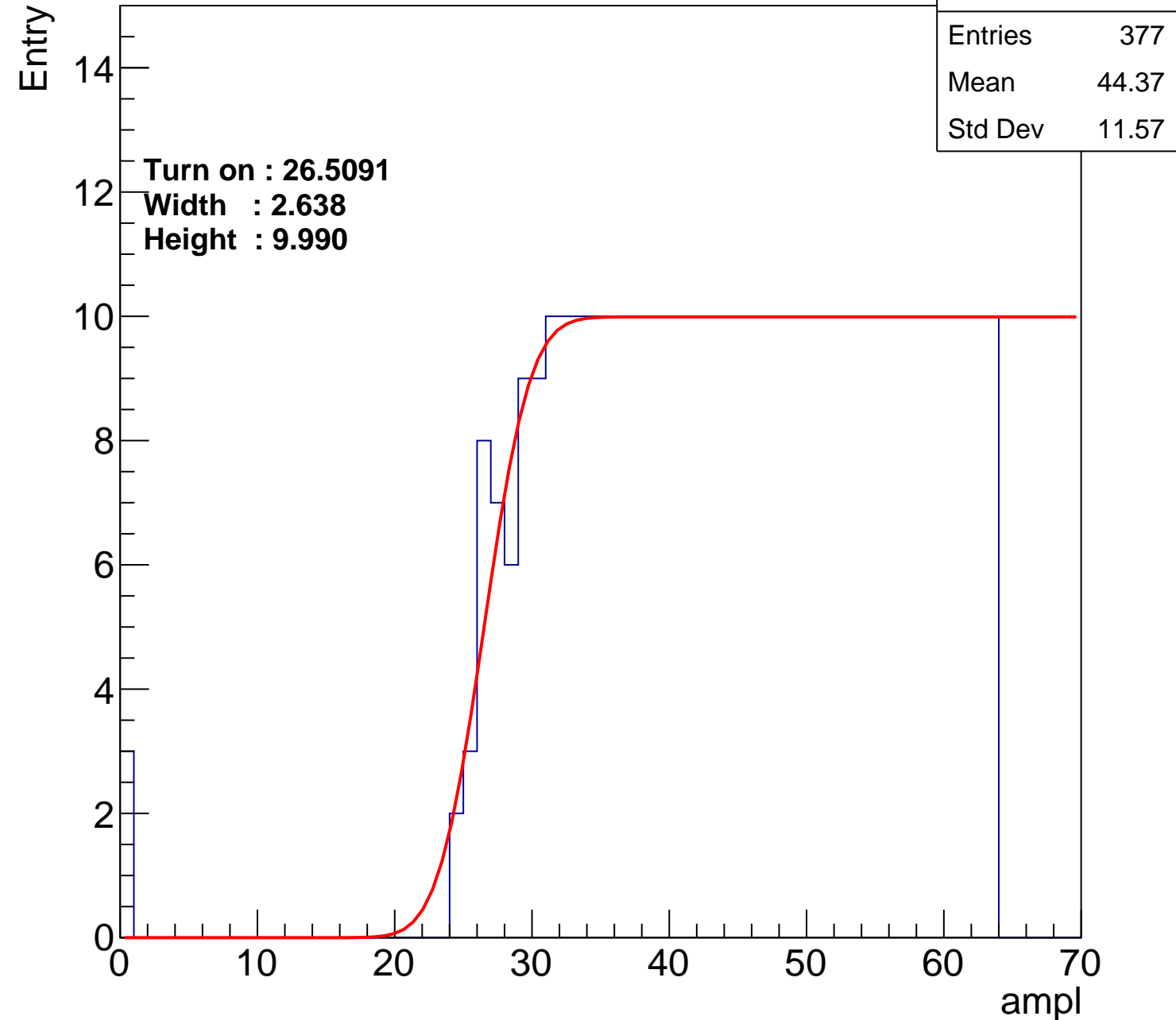
Width : 2.638

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch55

calib\_packv5\_042523\_0143.root, FC#4, port A2

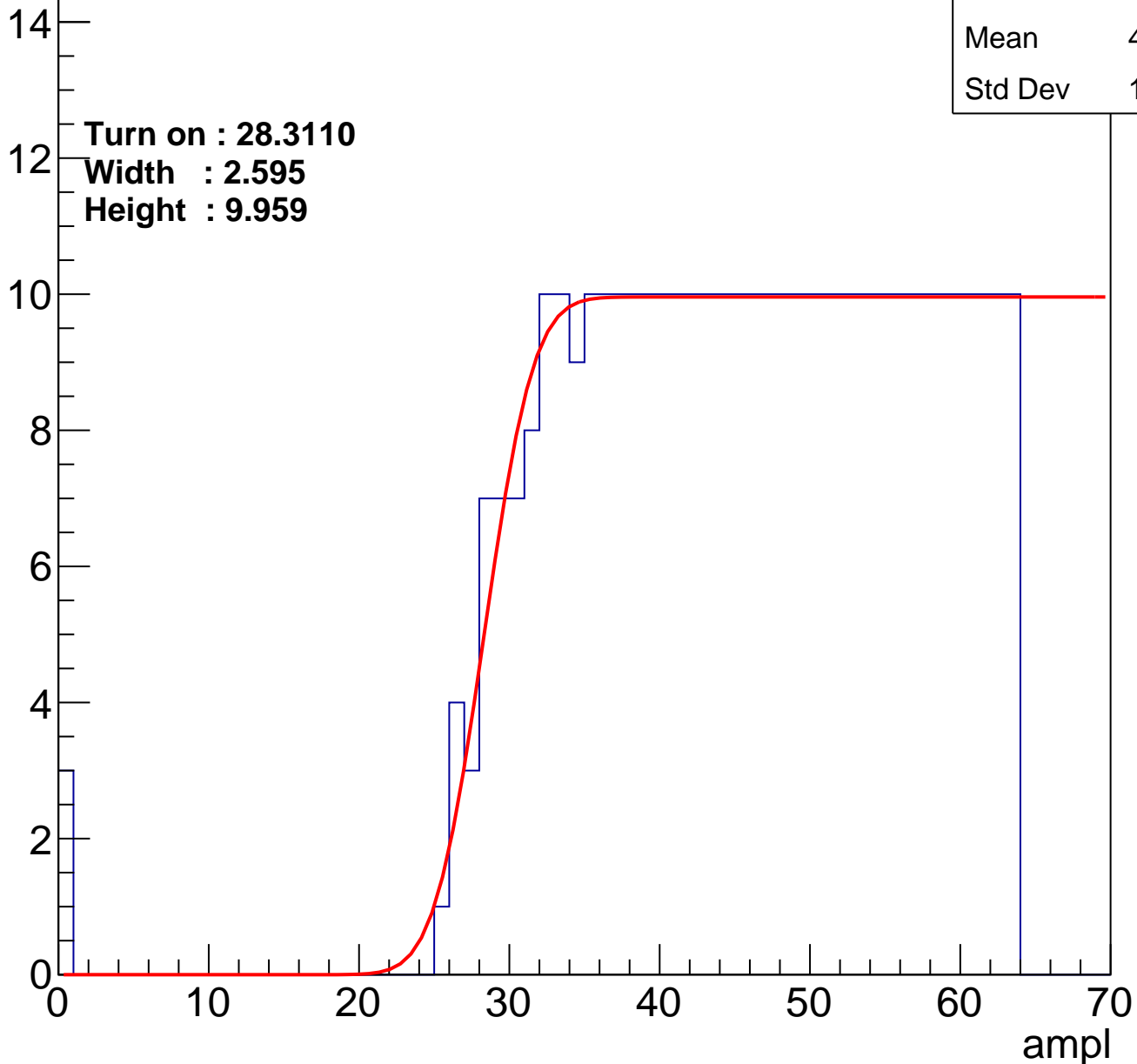
Entries	359
Mean	45.22
Std Dev	11.19

Turn on : 28.3110

Width : 2.595

Height : 9.959

Entry



# B1L100S, U26-ch56

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	359
Mean	45.22
Std Dev	11.19

Turn on : 28.3394

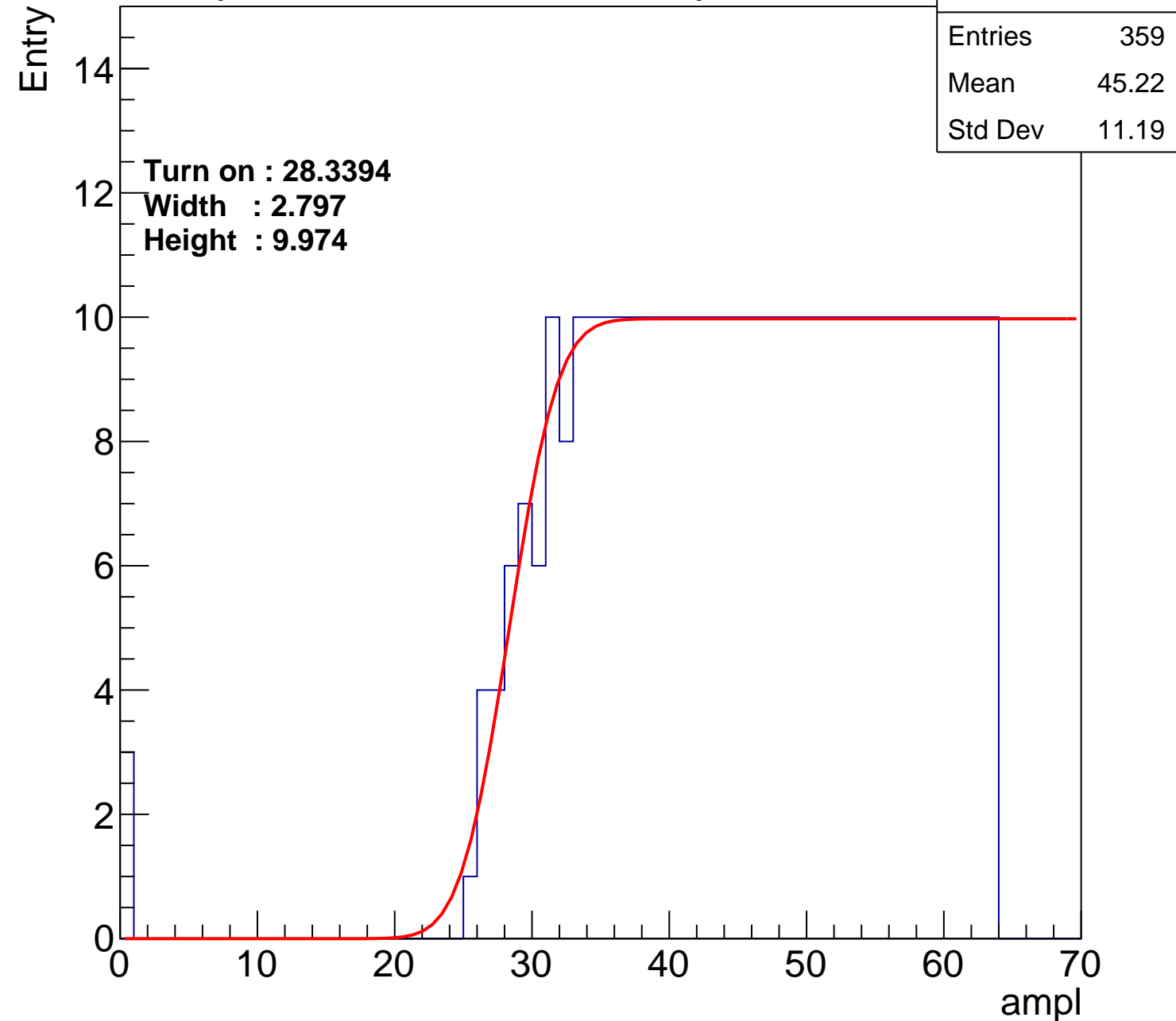
Width : 2.797

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch57

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	359
Mean	45.18
Std Dev	11.26

Turn on : 29.3341

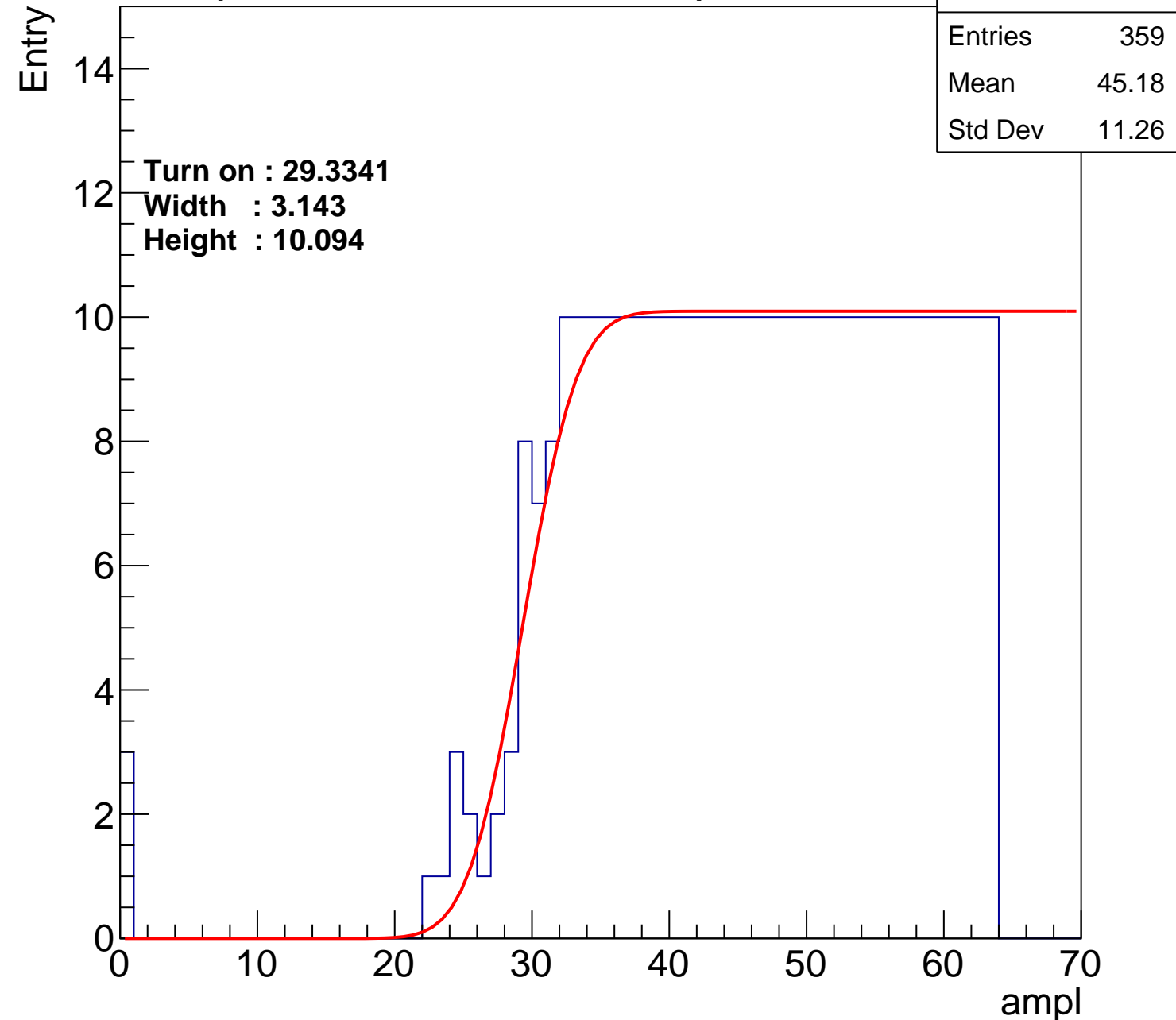
Width : 3.143

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch58

calib\_packv5\_042523\_0143.root, FC#4, port A2

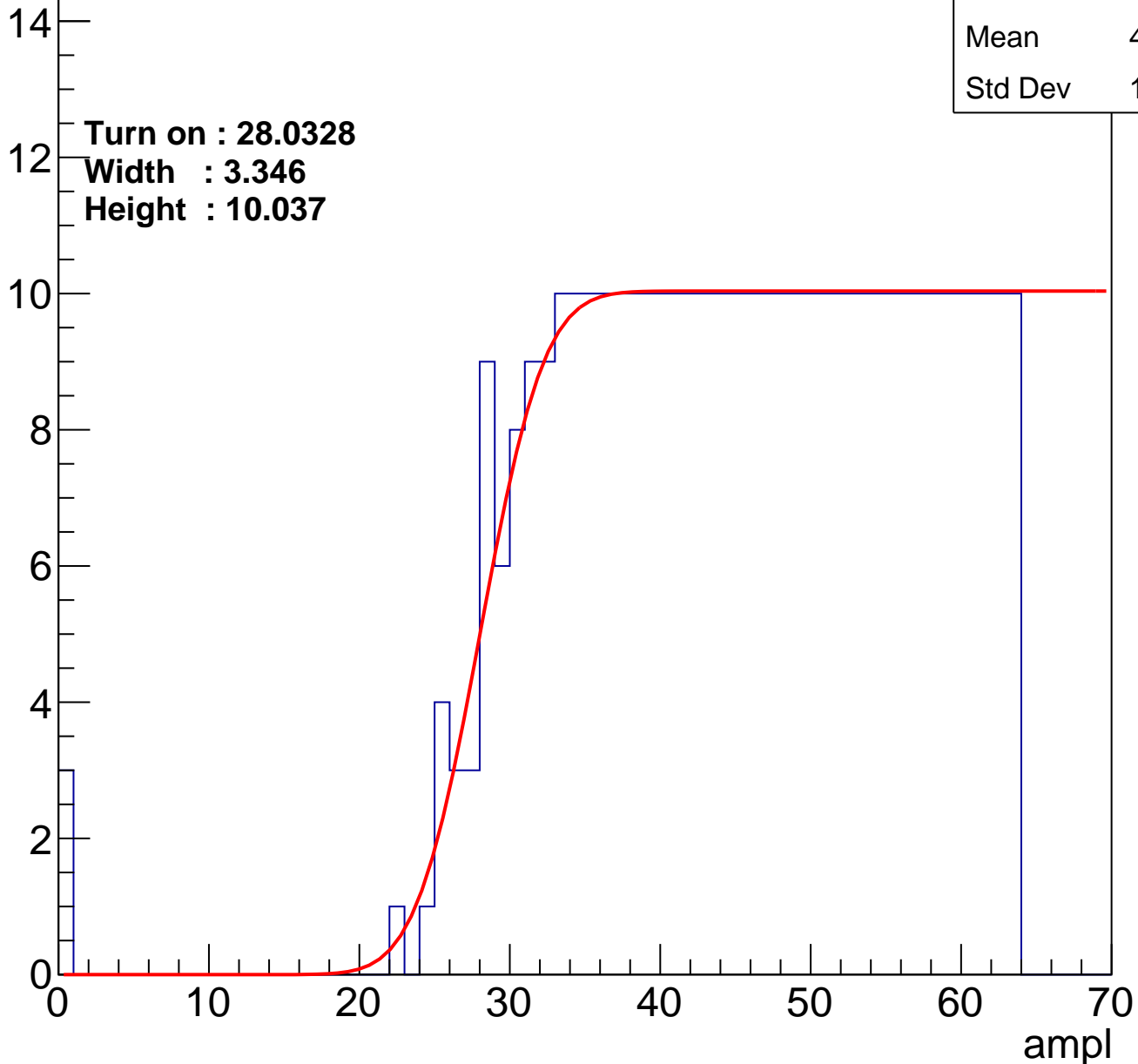
Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 28.0328

Width : 3.346

Height : 10.037

Entry



# B1L100S, U26-ch59

calib\_packv5\_042523\_0143.root, FC#4, port A2

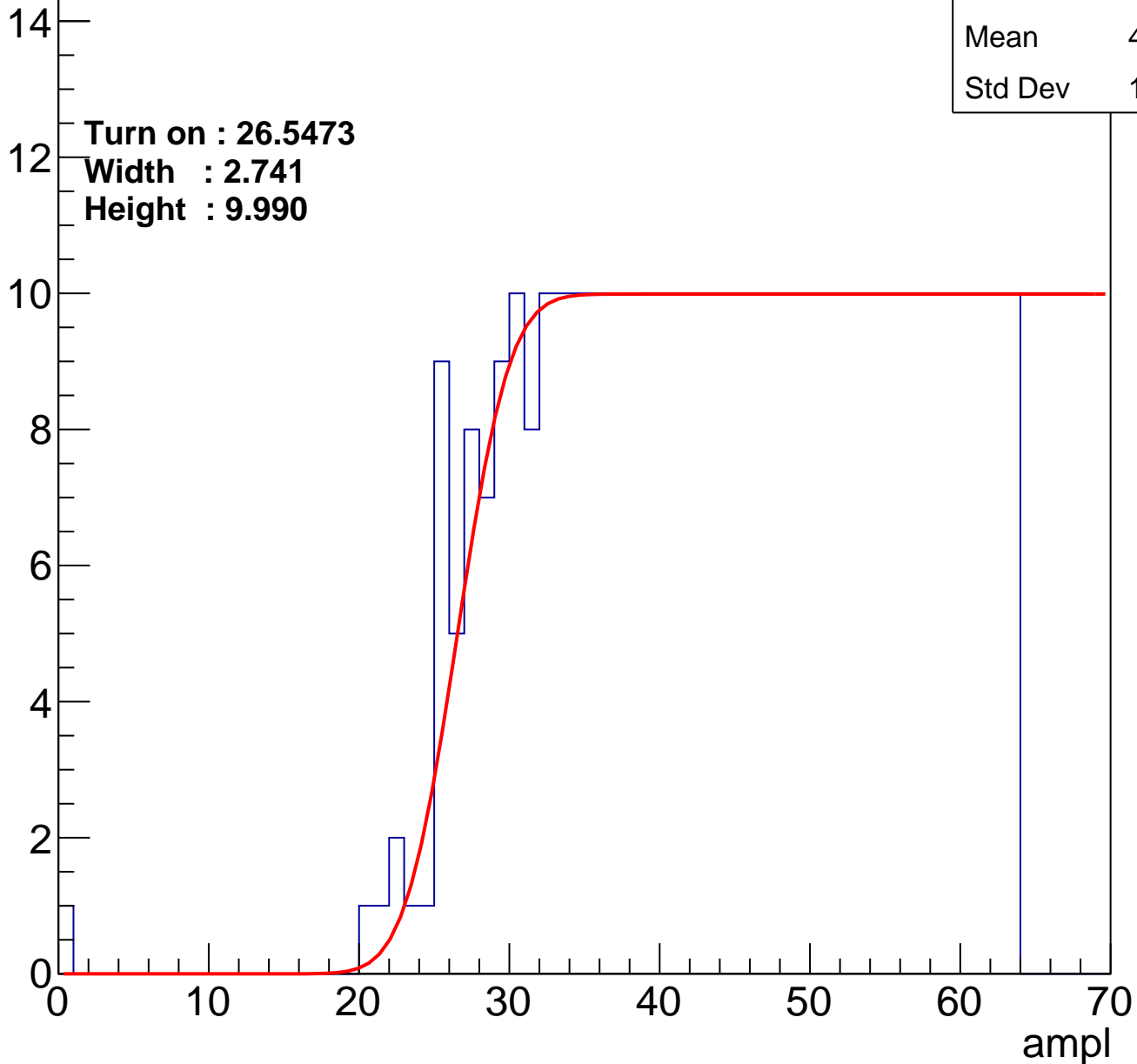
Entries	383
Mean	44.15
Std Dev	11.47

Turn on : 26.5473

Width : 2.741

Height : 9.990

Entry



# B1L100S, U26-ch60

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.69
Std Dev	11.36

Turn on : 27.4468

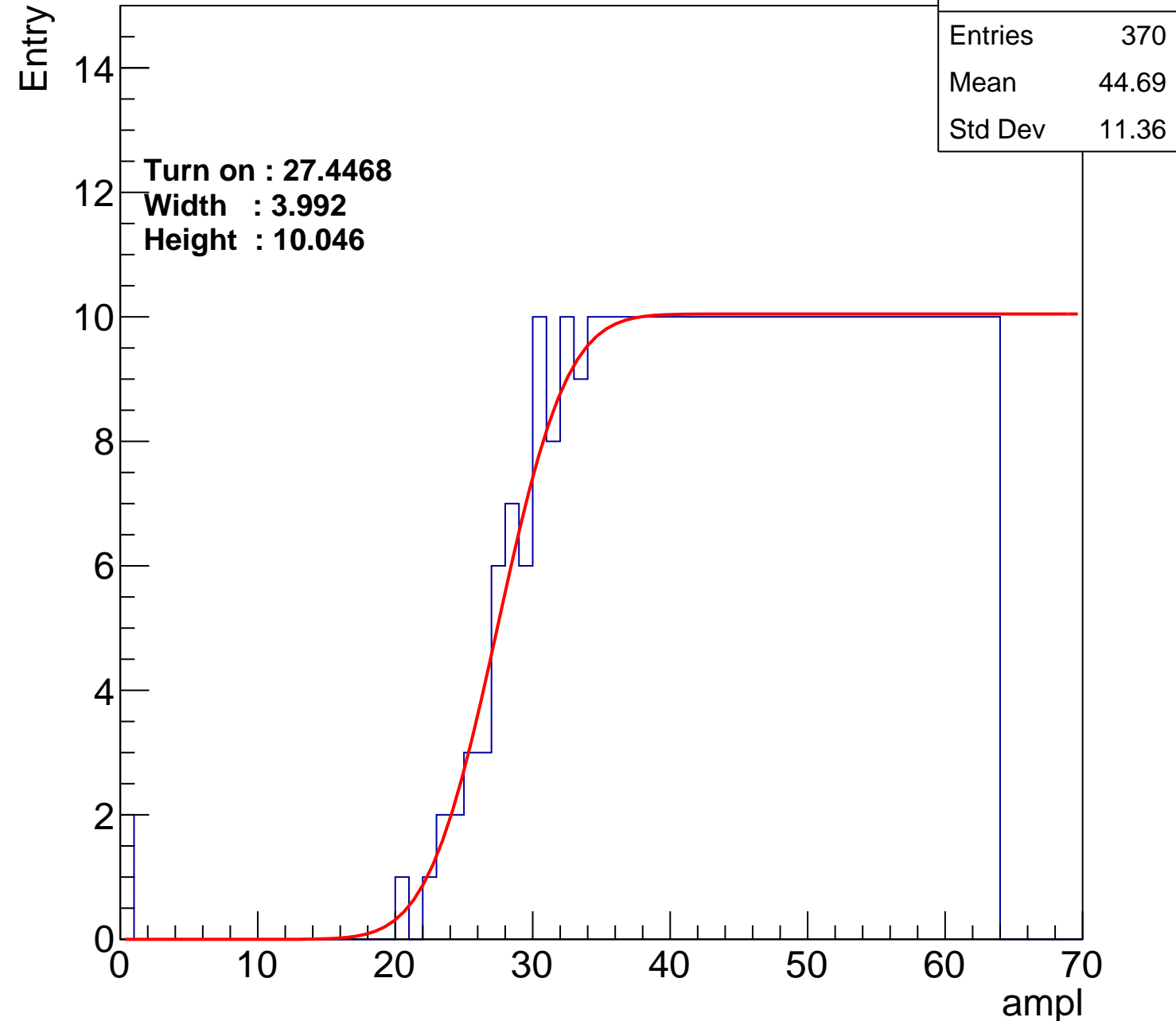
Width : 3.992

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch61

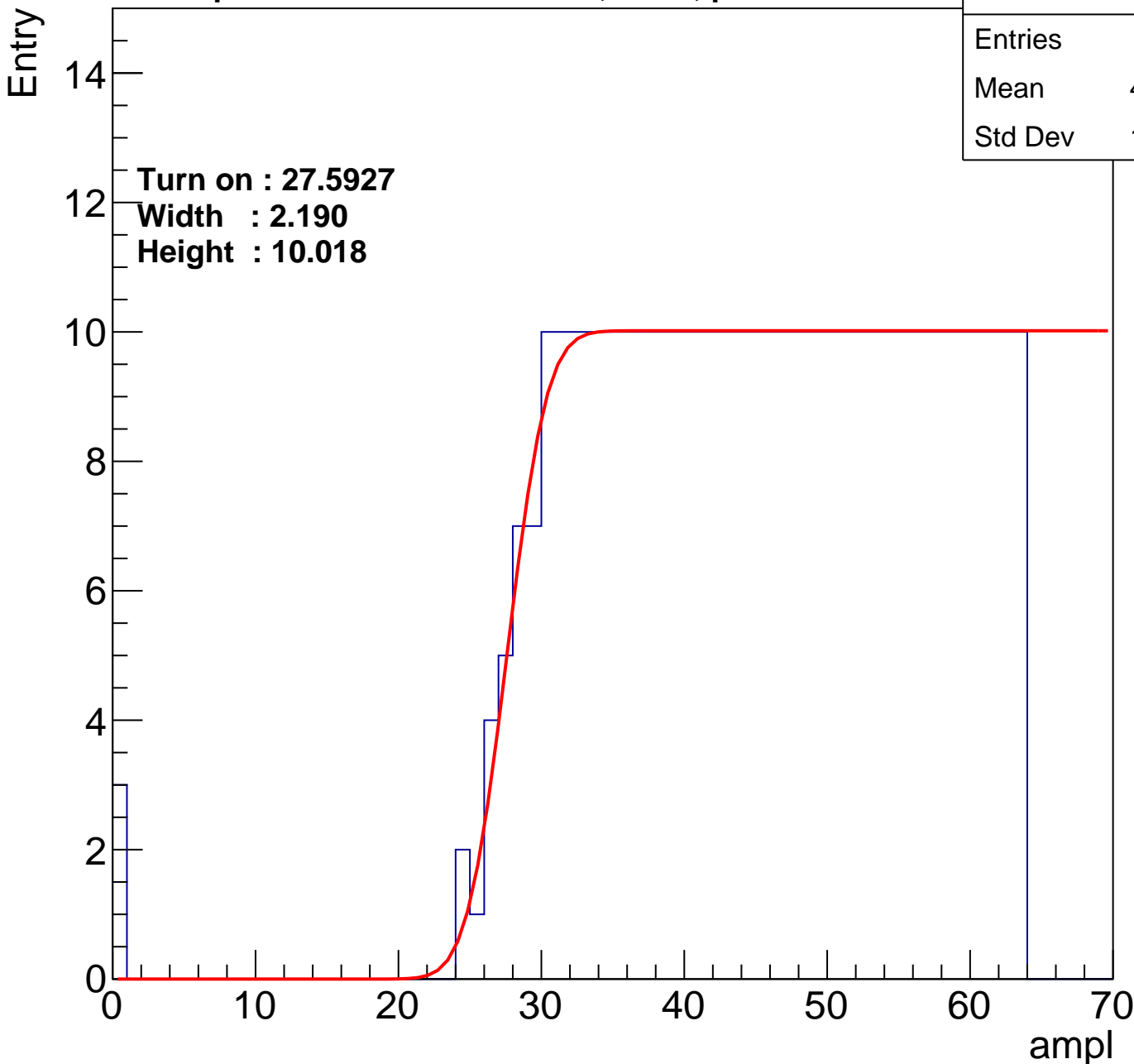
**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	369
Mean	44.77
Std Dev	11.37

**Turn on : 27.5927**

**Width : 2.190**

**Height : 10.018**



# B1L100S, U26-ch62

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.6397

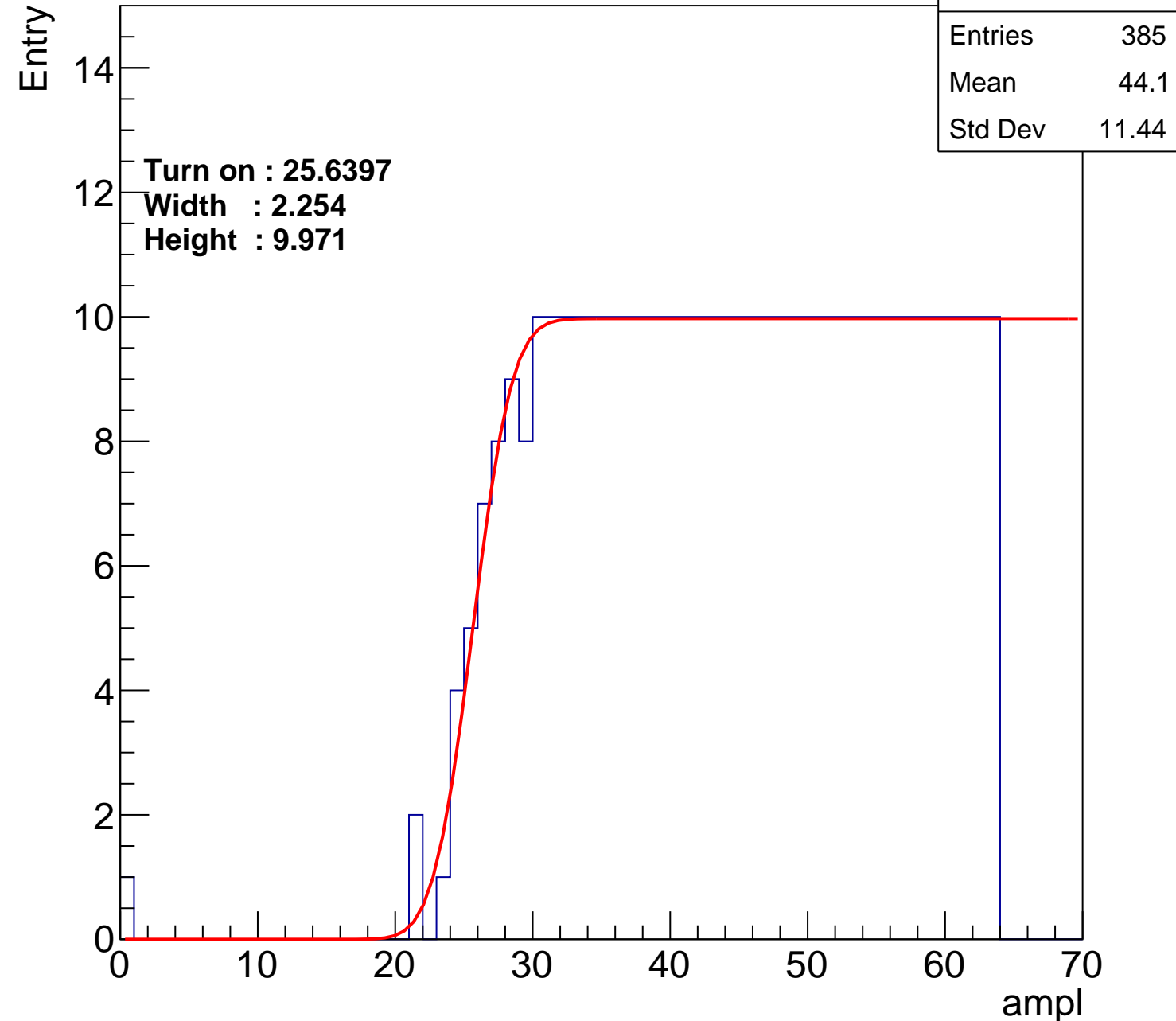
Width : 2.254

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch63

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	366
Mean	44.96
Std Dev	11.05

Turn on : 27.8324

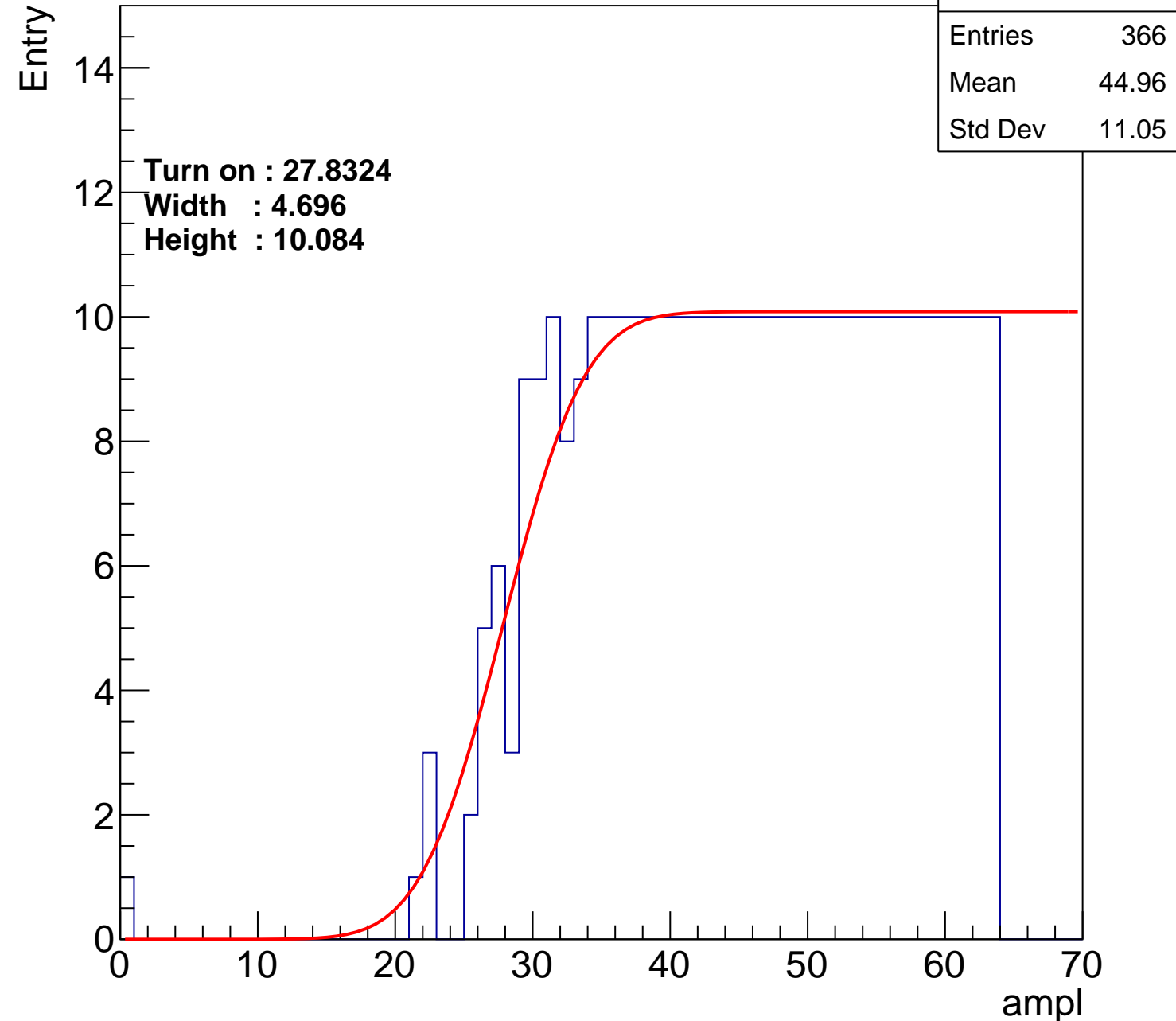
Width : 4.696

Height : 10.084

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch64

calib\_packv5\_042523\_0143.root, FC#4, port A2

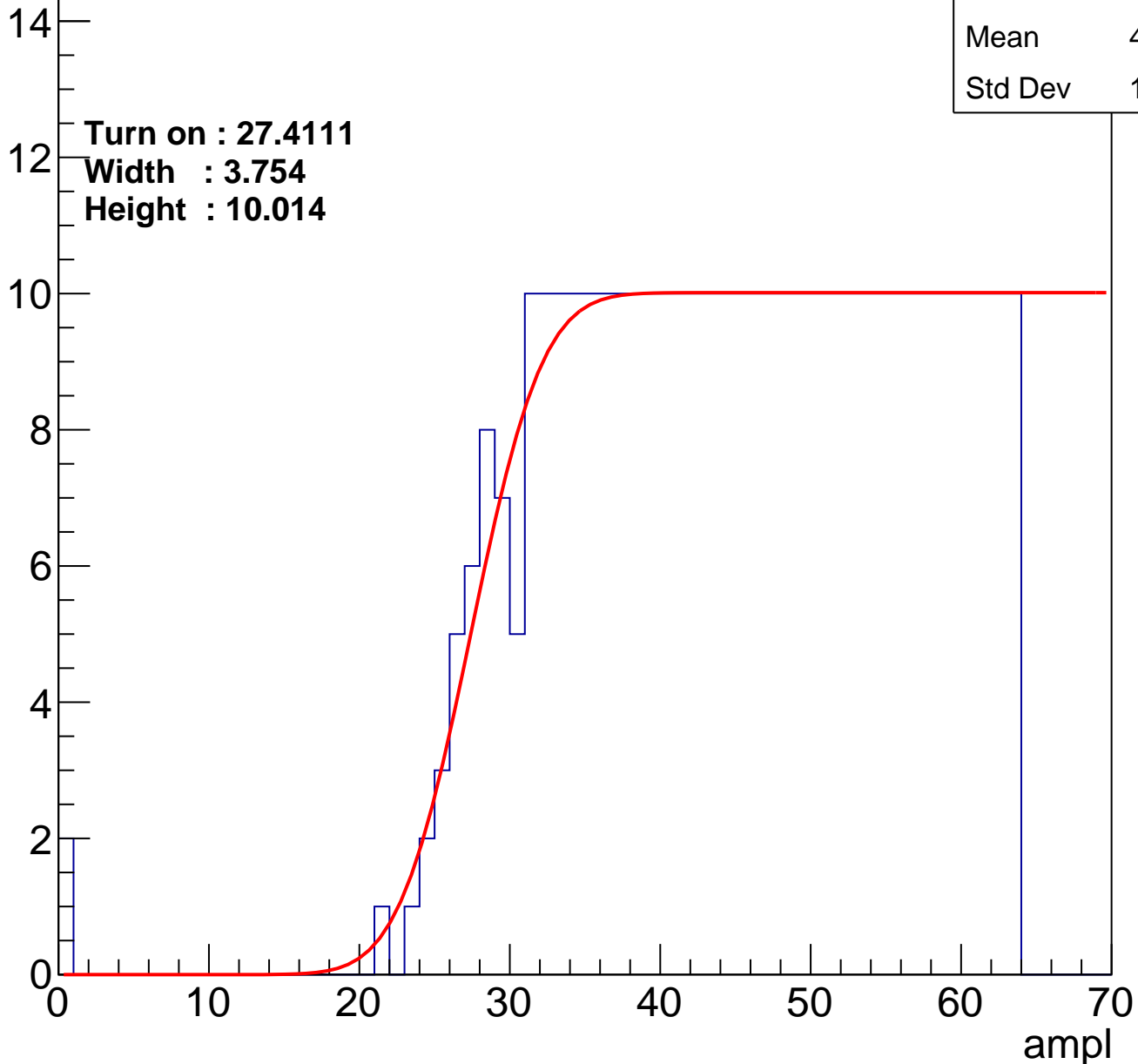
Entries	370
Mean	44.72
Std Dev	11.32

Turn on : 27.4111

Width : 3.754

Height : 10.014

Entry



# B1L100S, U26-ch65

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	358
Mean	45.34
Std Dev	10.97

**Turn on : 28.7395**

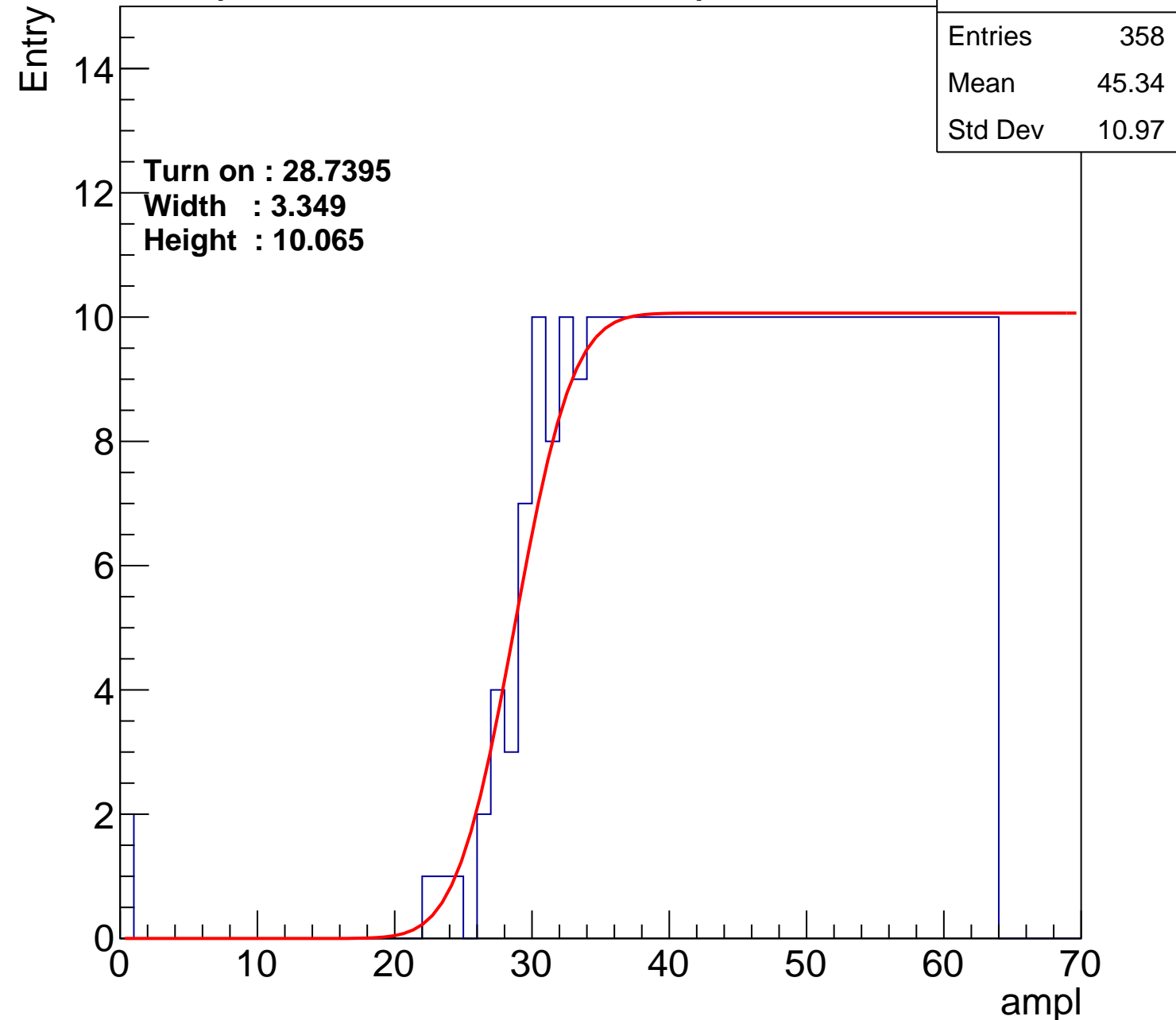
**Width : 3.349**

**Height : 10.065**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch66

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	356
Mean	45.44
Std Dev	10.81

Turn on : 28.5724

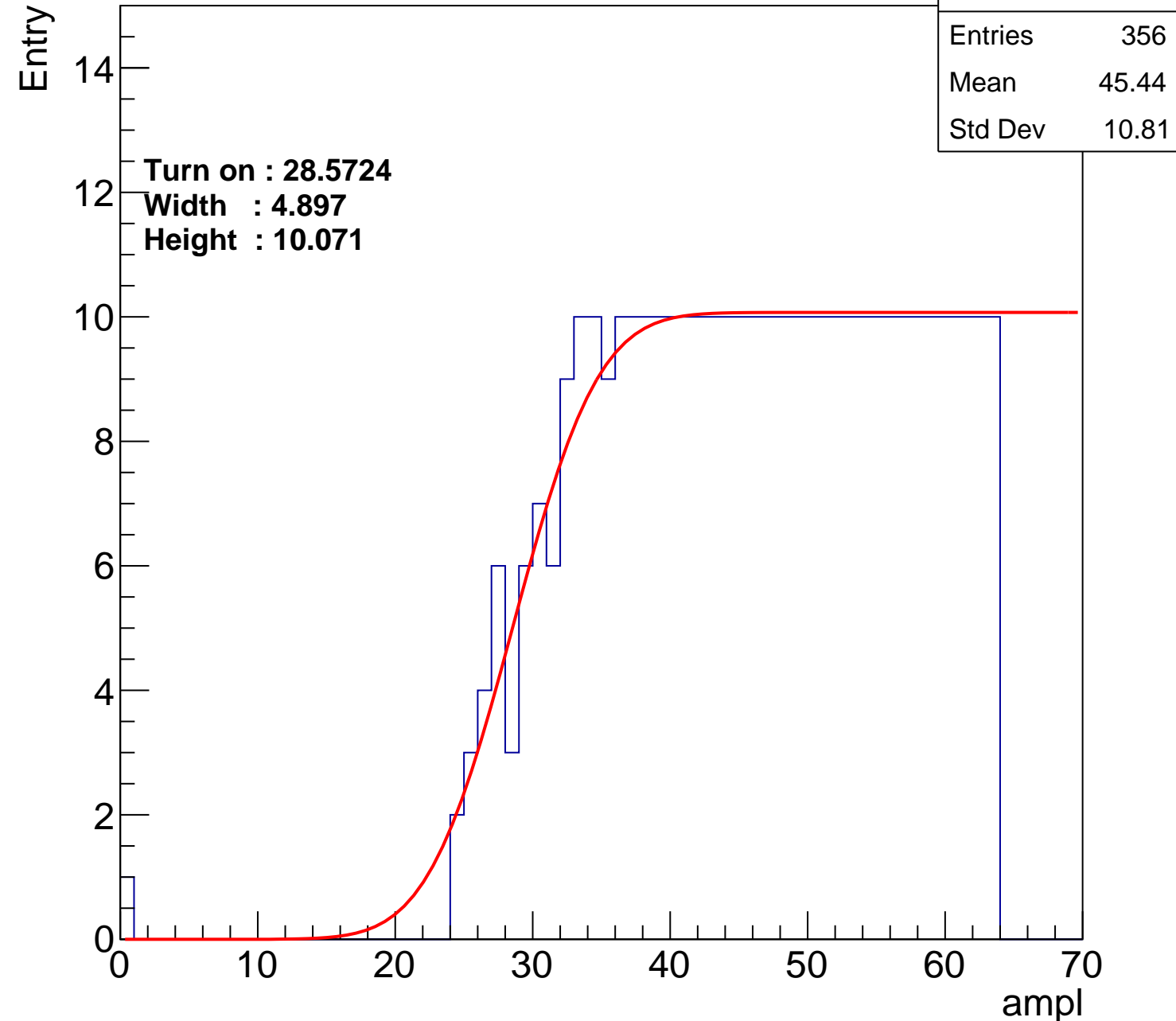
Width : 4.897

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch67

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.7022

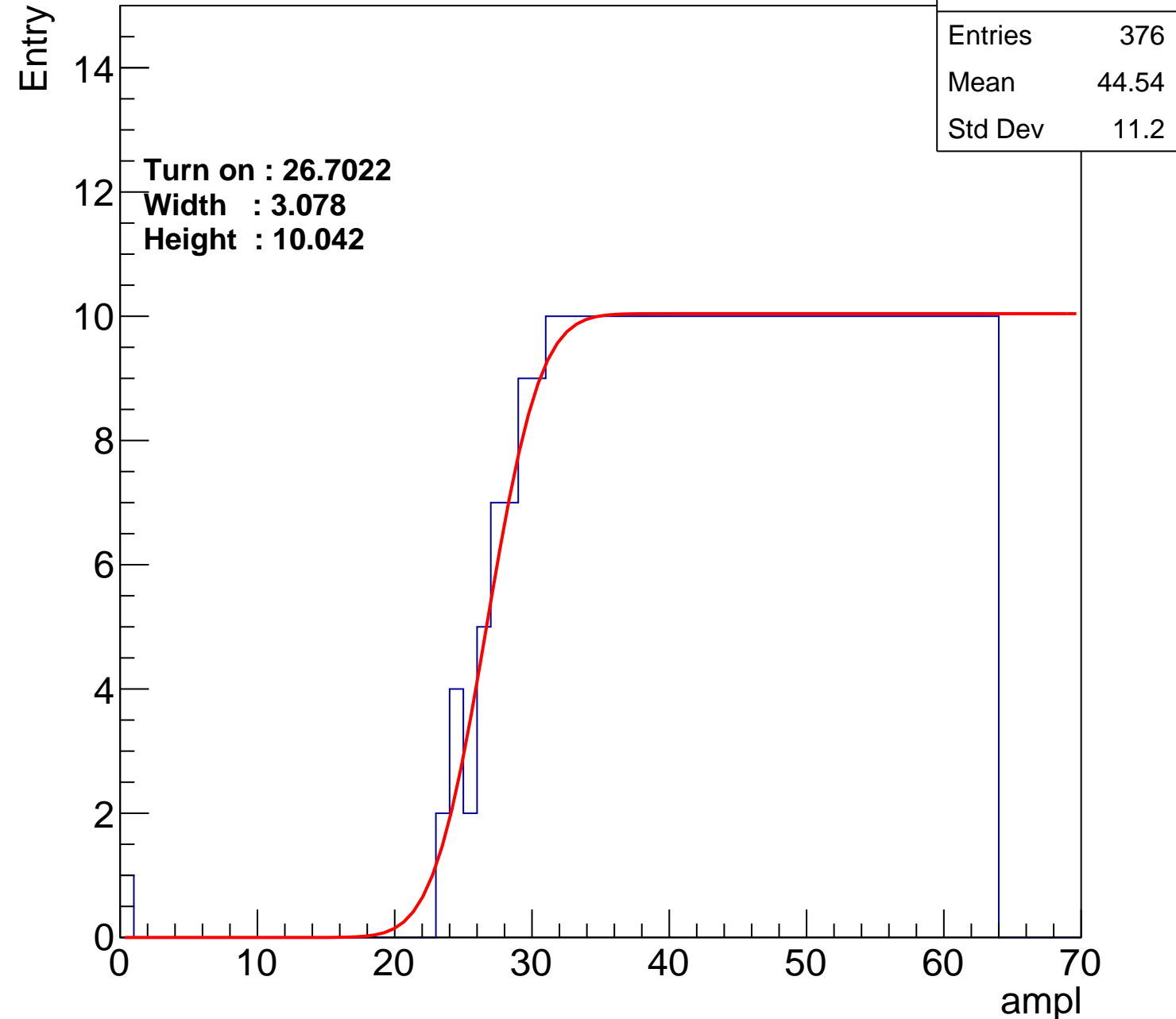
Width : 3.078

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch68

calib\_packv5\_042523\_0143.root, FC#4, port A2

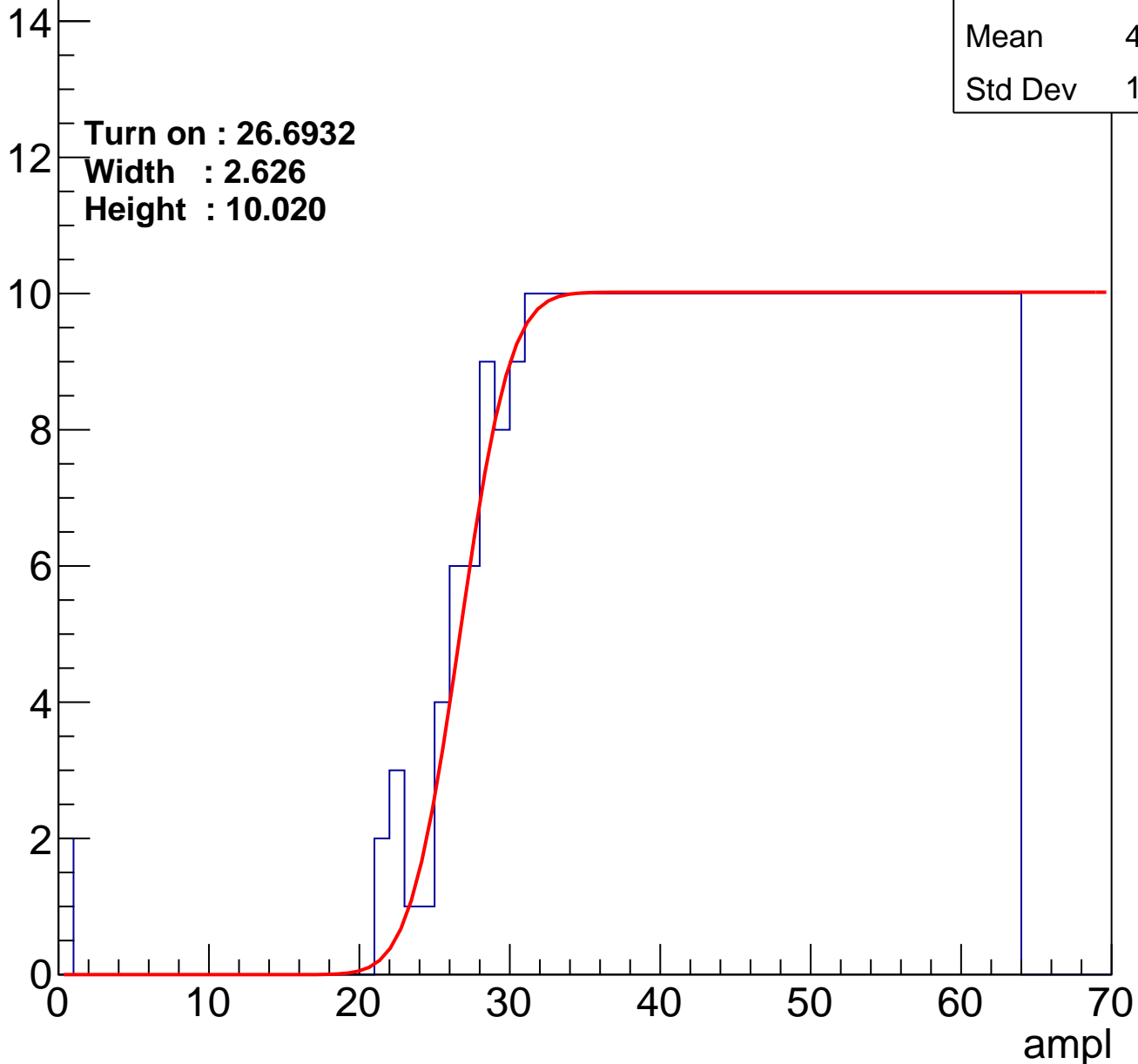
Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.6932

Width : 2.626

Height : 10.020

Entry



# B1L100S, U26-ch69

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	382
Mean	44.19
Std Dev	11.45

Turn on : 26.2172

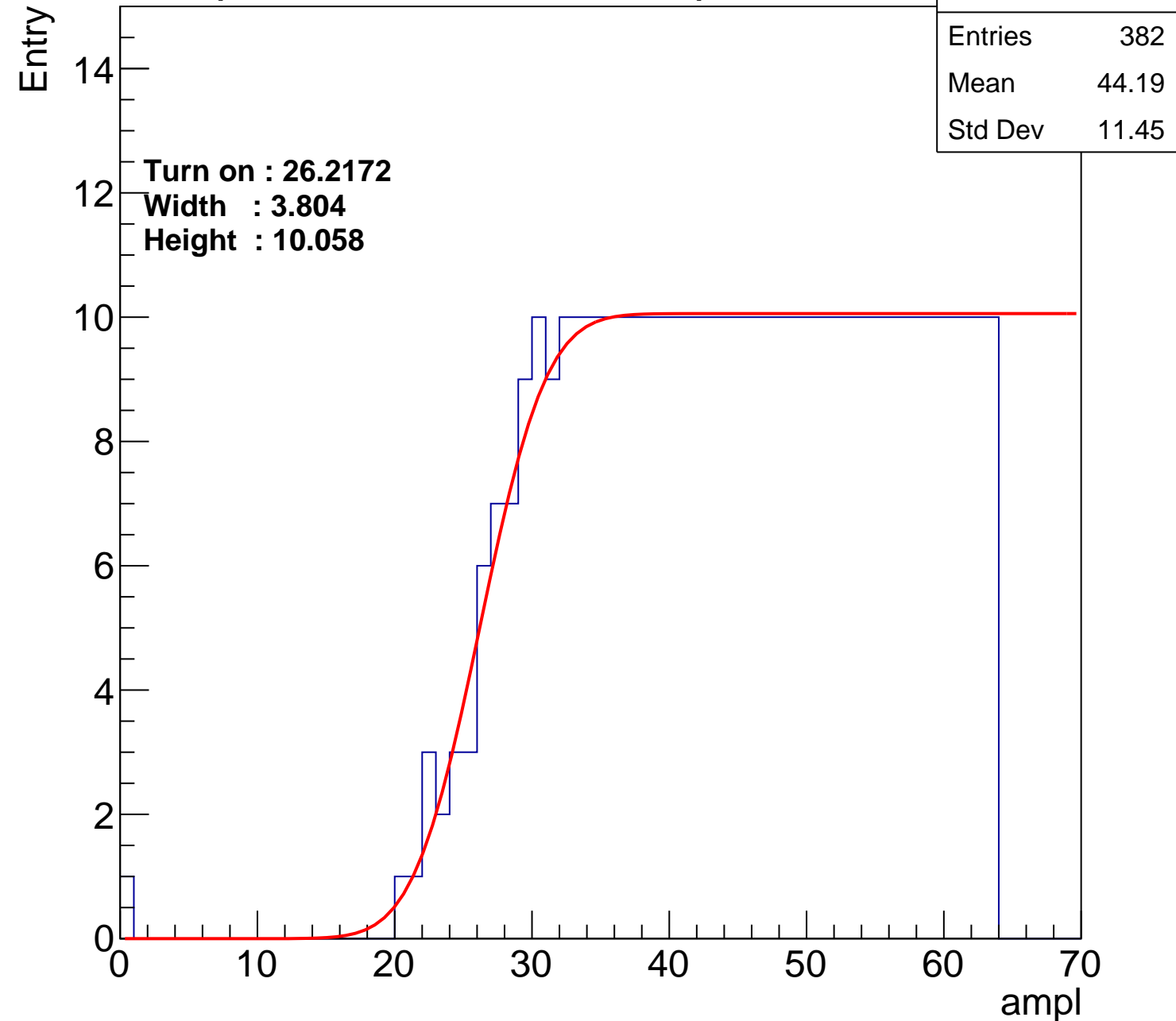
Width : 3.804

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch70

calib\_packv5\_042523\_0143.root, FC#4, port A2

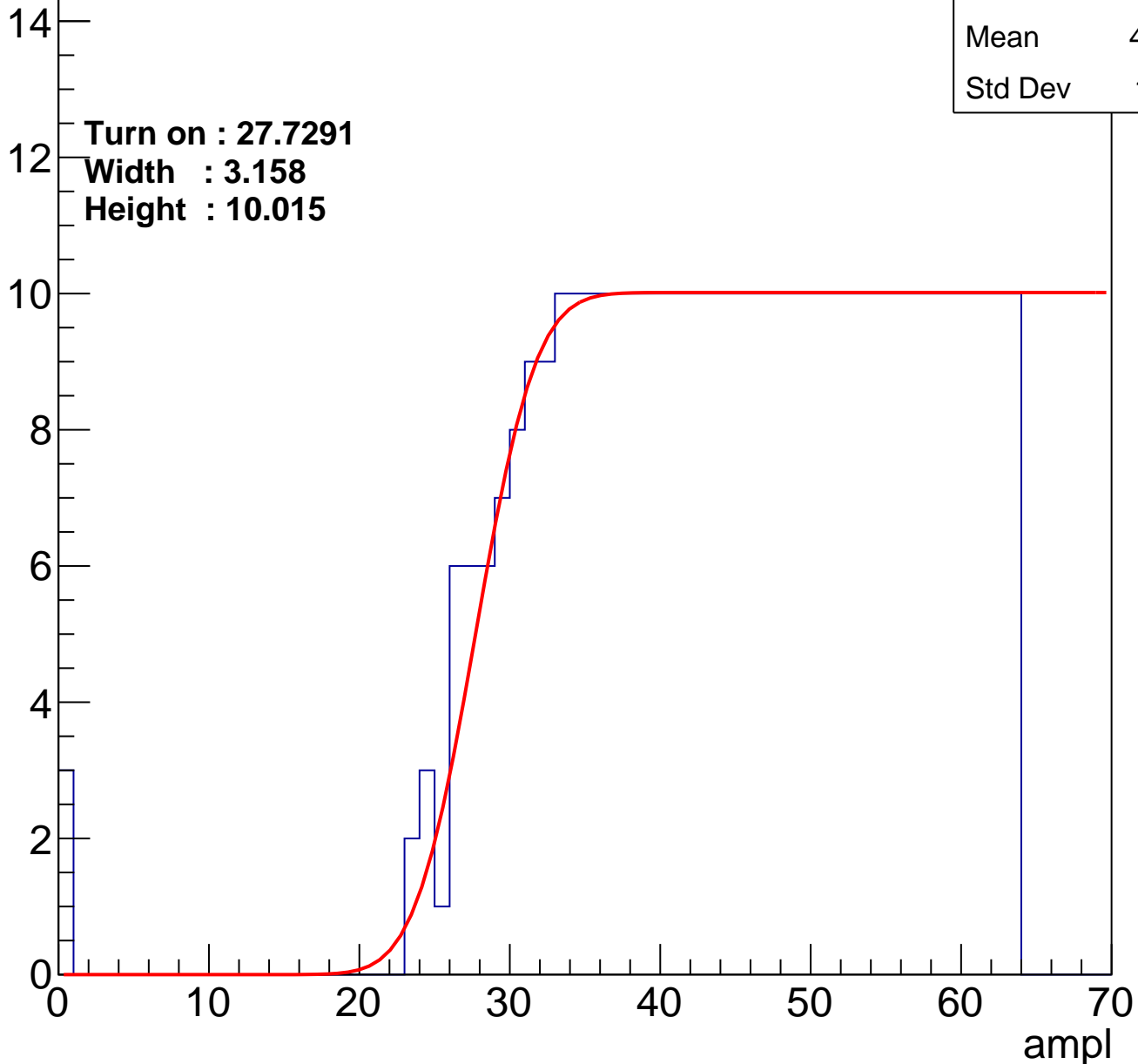
Entries	370
Mean	44.65
Std Dev	11.51

Turn on : 27.7291

Width : 3.158

Height : 10.015

Entry





# B1L100S, U26-ch71

calib\_packv5\_042523\_0143.root, FC#4, port A2

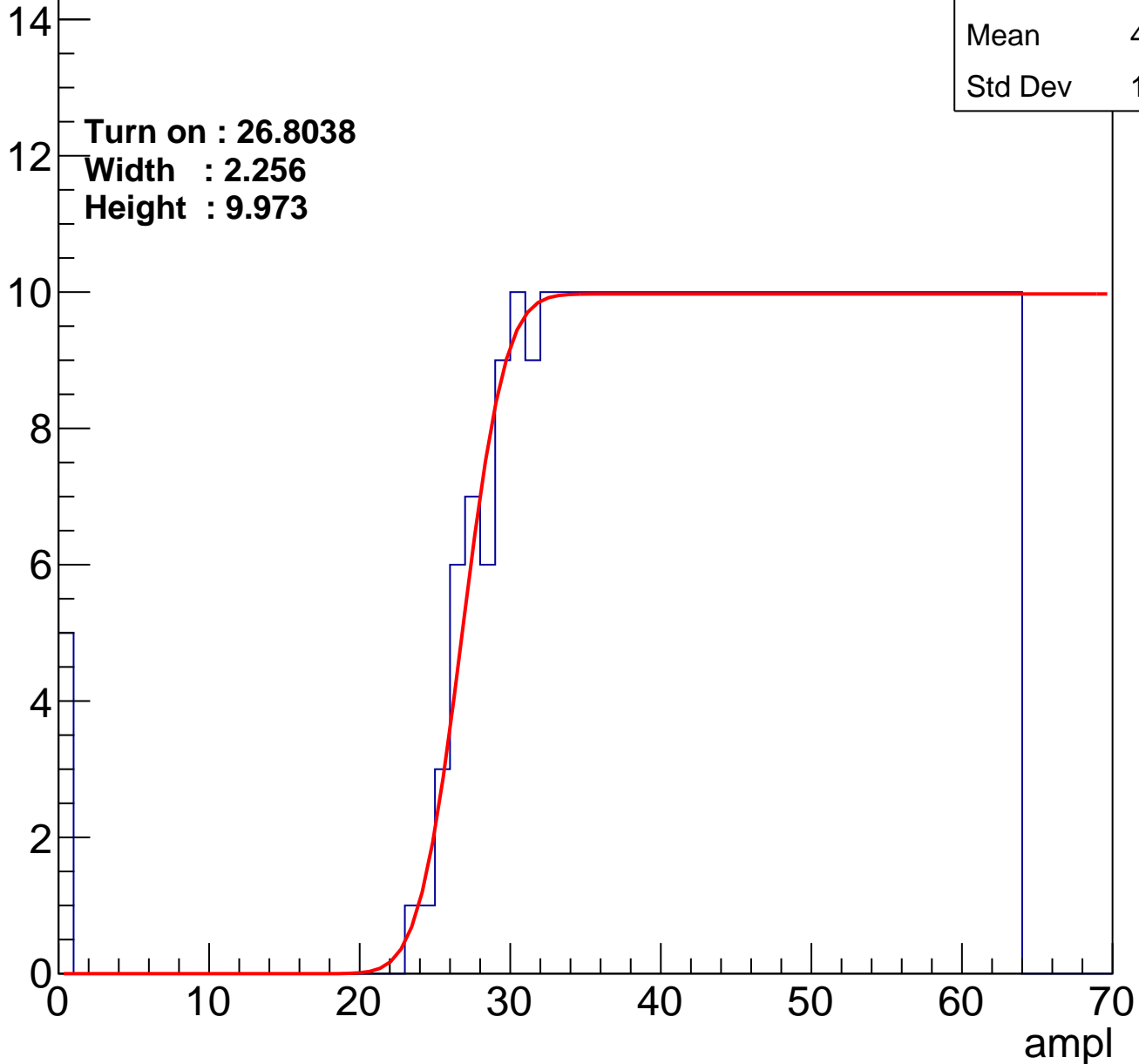
Entries	377
Mean	44.23
Std Dev	11.95

Turn on : 26.8038

Width : 2.256

Height : 9.973

Entry



# B1L100S, U26-ch72

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	45.07
Std Dev	10.99

Turn on : 27.8846

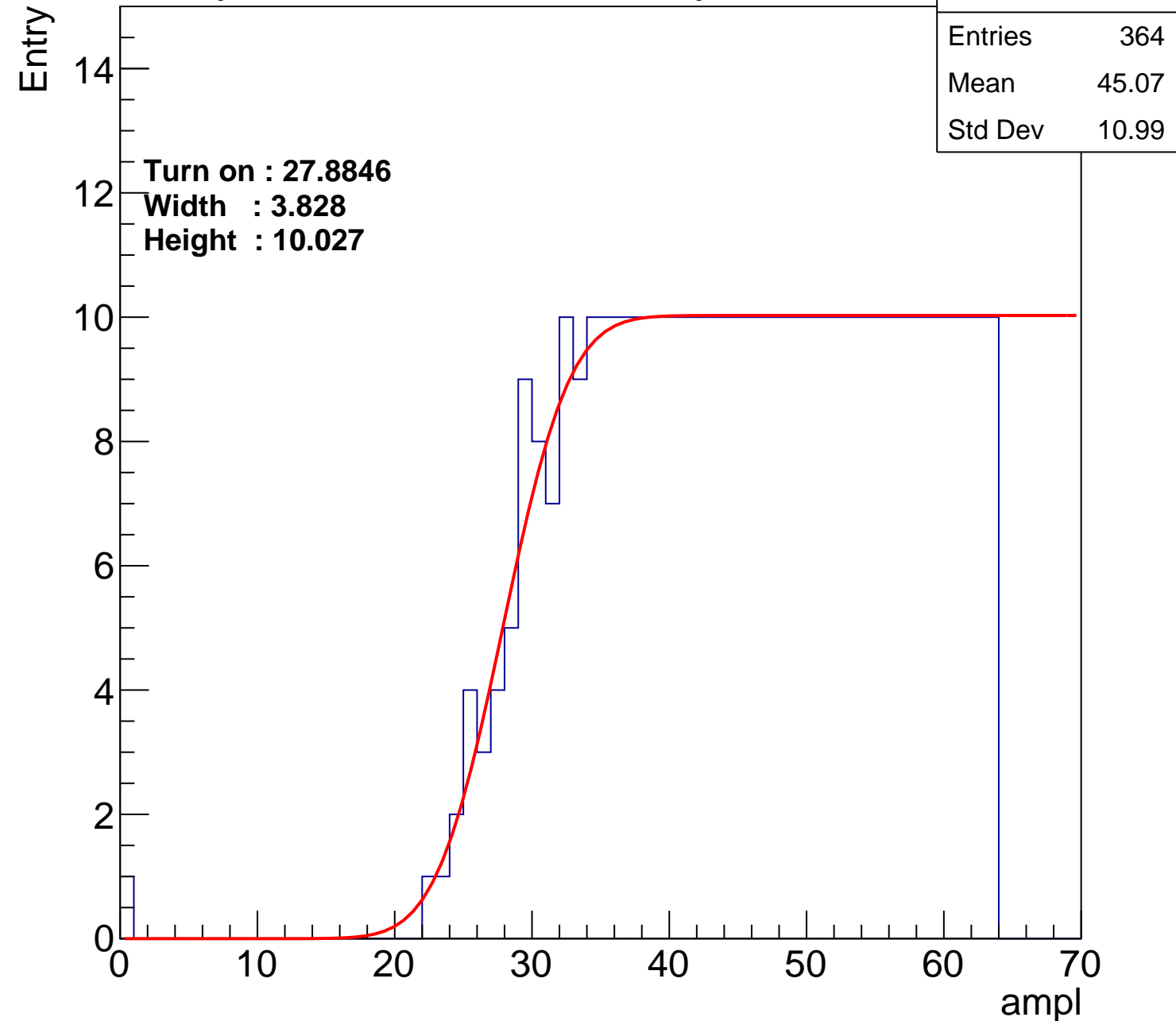
Width : 3.828

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch73

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.84
Std Dev	11.09

Turn on : 27.8740

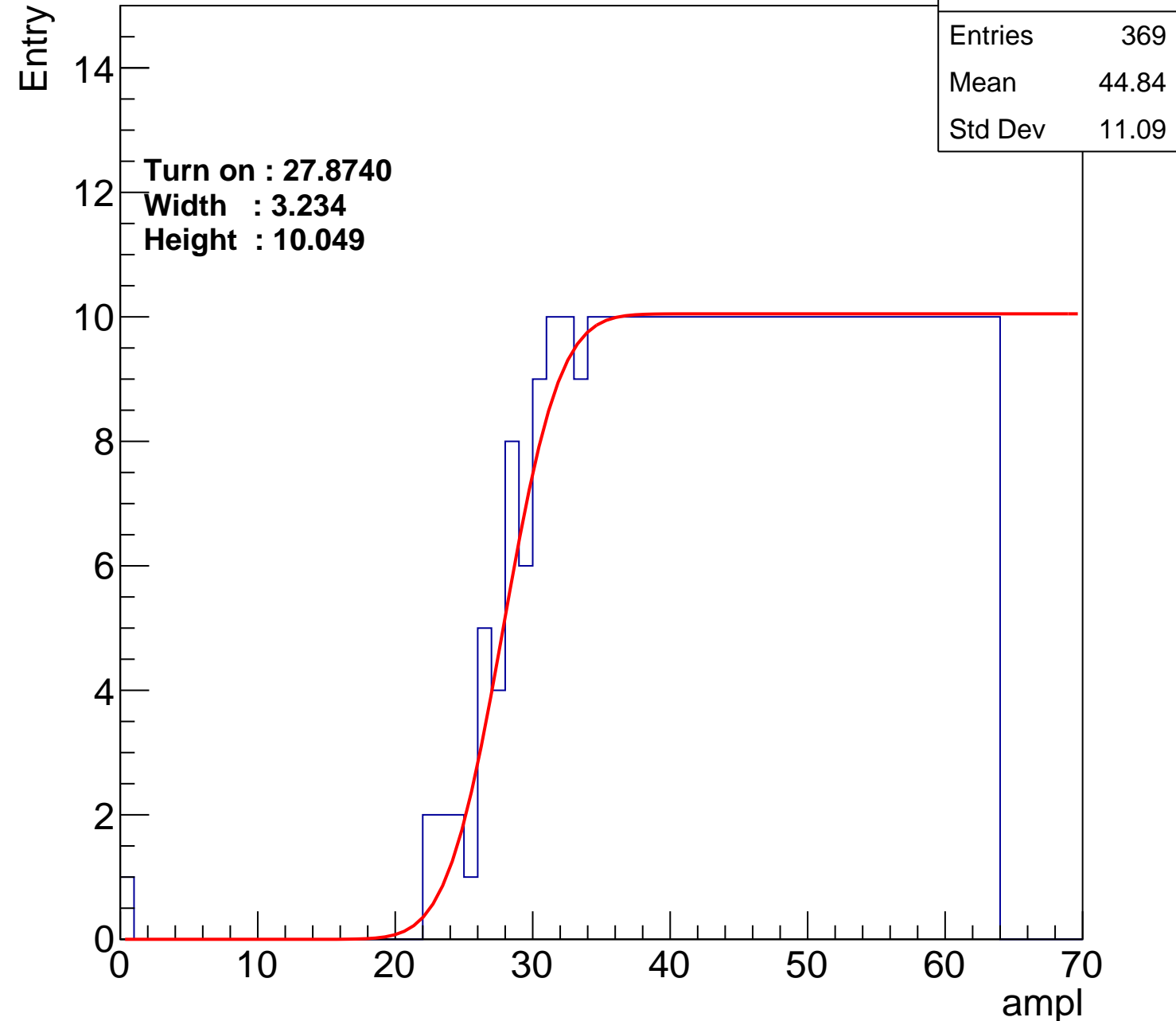
Width : 3.234

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch74

calib\_packv5\_042523\_0143.root, FC#4, port A2

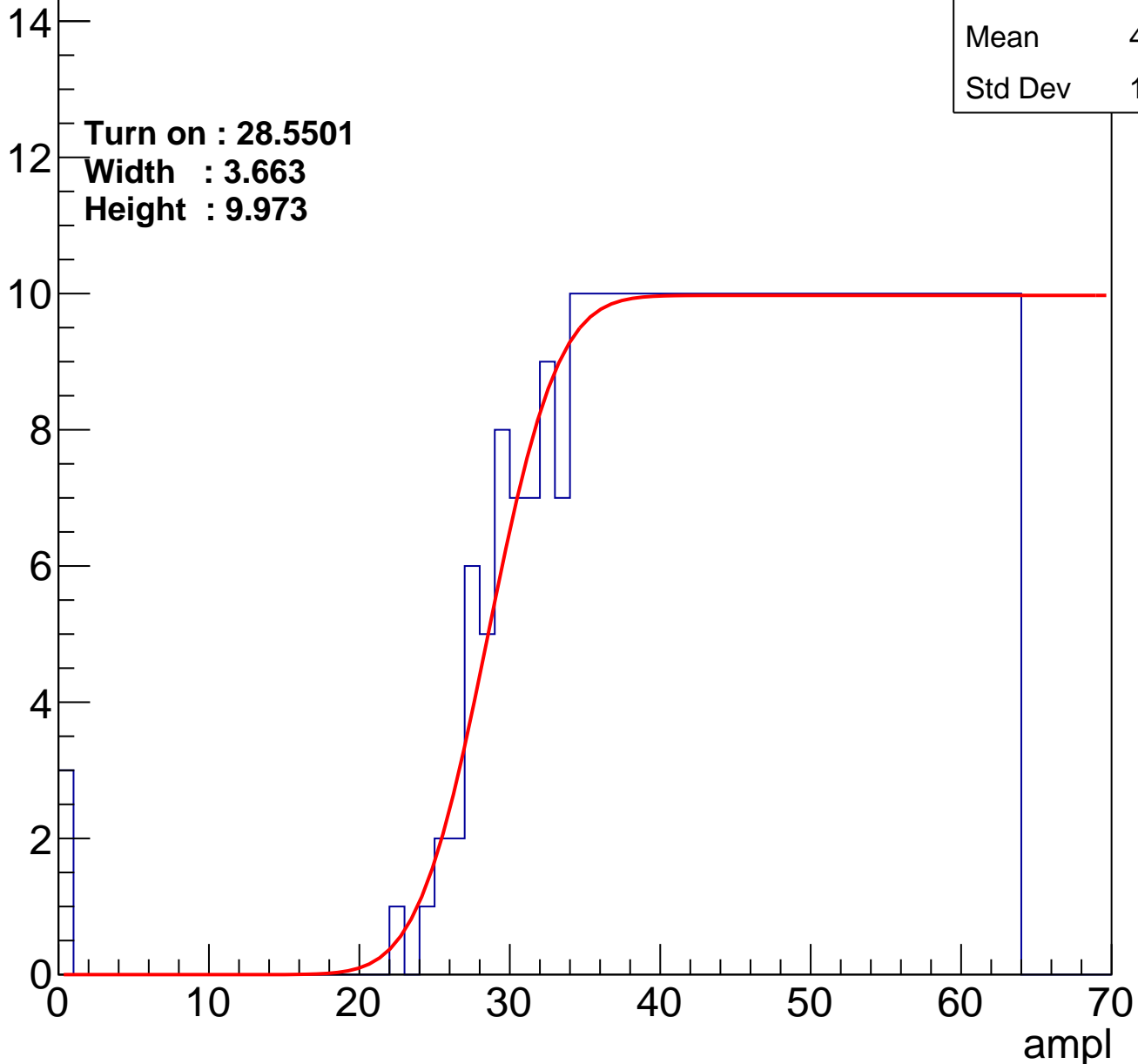
Entries	358
Mean	45.19
Std Dev	11.29

Turn on : 28.5501

Width : 3.663

Height : 9.973

Entry



# B1L100S, U26-ch75

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	390
Mean	43.76
Std Dev	11.8

Turn on : 25.7033

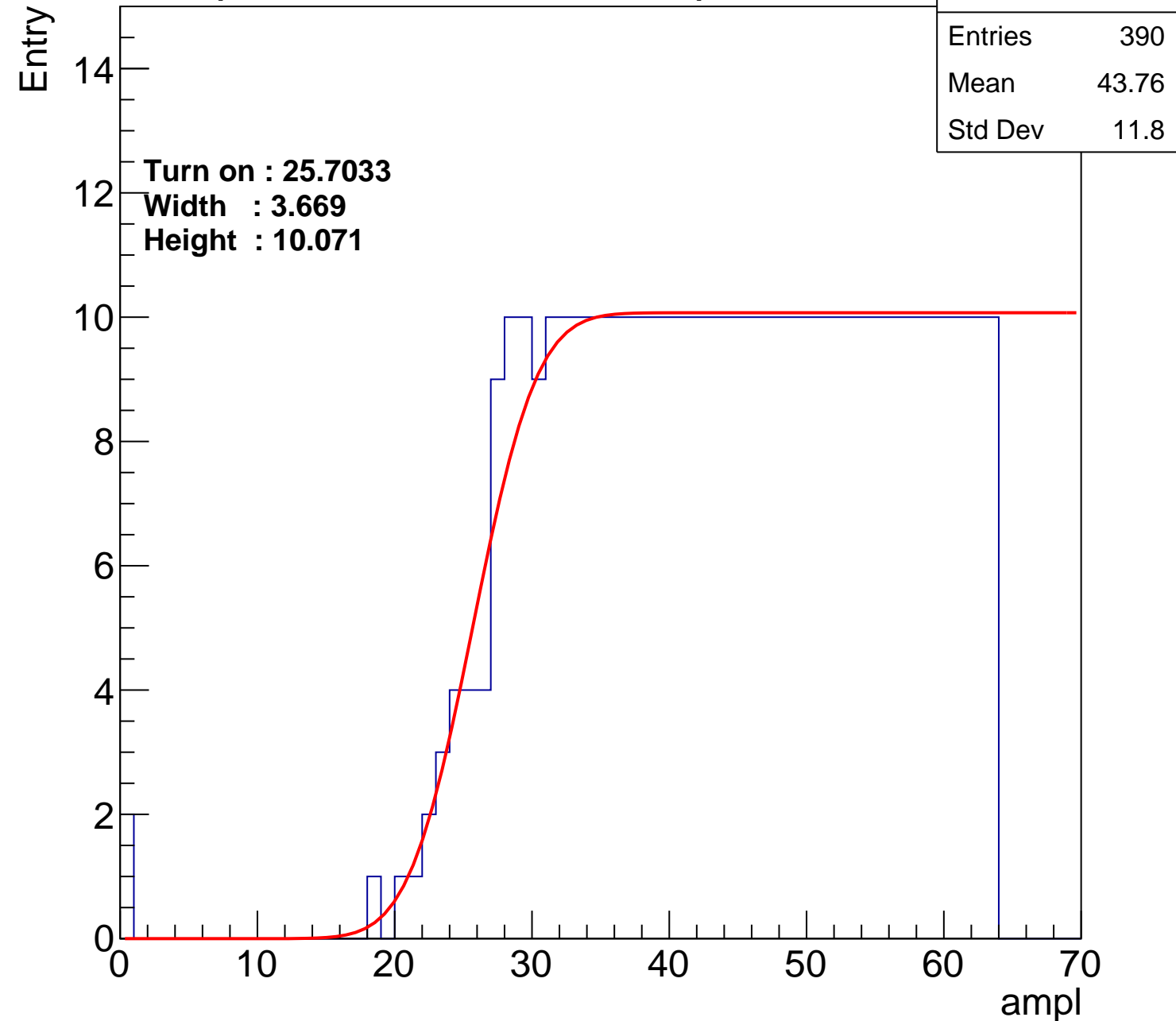
Width : 3.669

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch76

calib\_packv5\_042523\_0143.root, FC#4, port A2

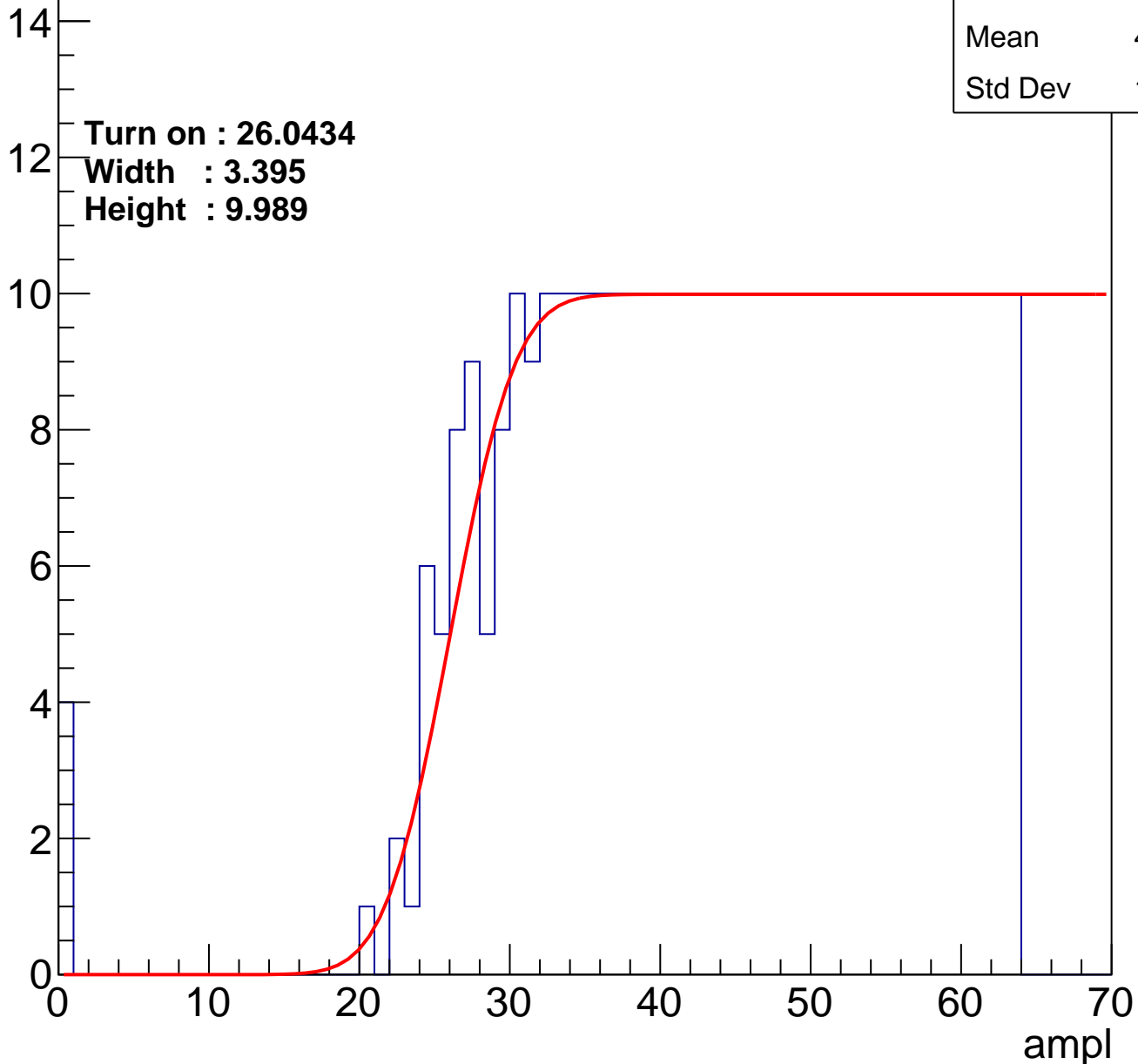
Entries	388
Mean	43.71
Std Dev	12.11

Turn on : 26.0434

Width : 3.395

Height : 9.989

Entry



# B1L100S, U26-ch77

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	367
Mean	44.94
Std Dev	11.03

Turn on : 27.2456

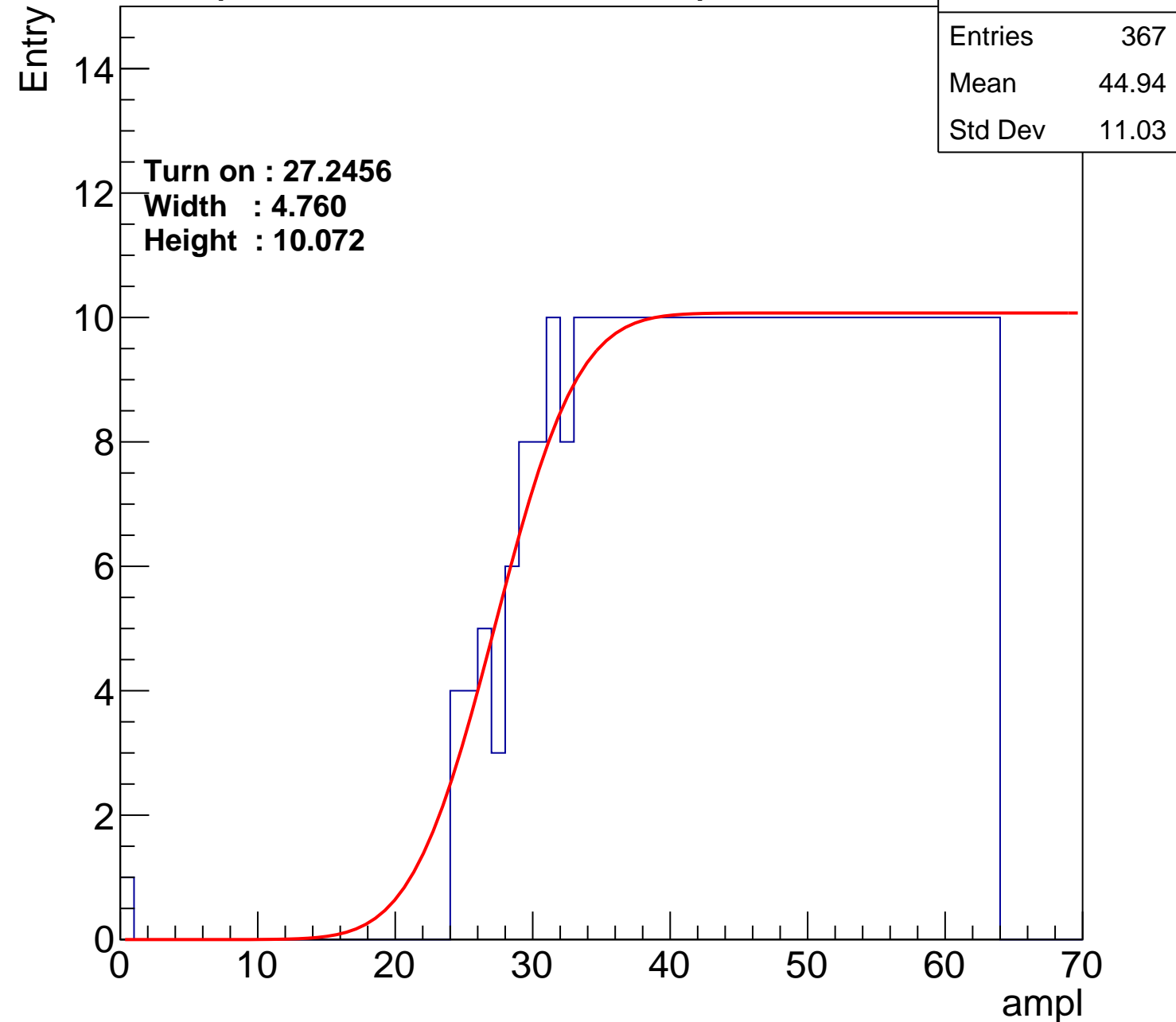
Width : 4.760

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch78

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.3328

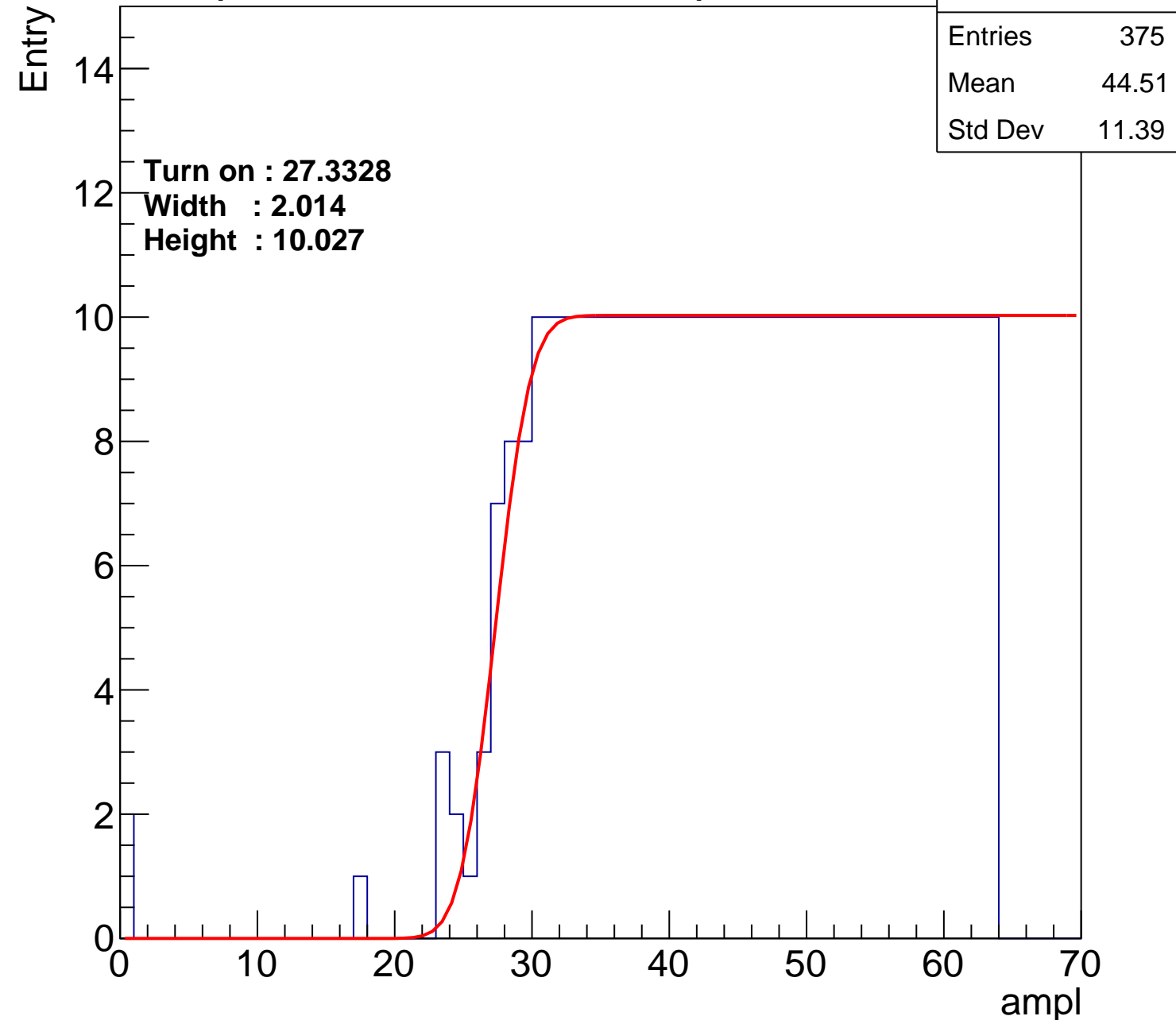
Width : 2.014

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch79

calib\_packv5\_042523\_0143.root, FC#4, port A2

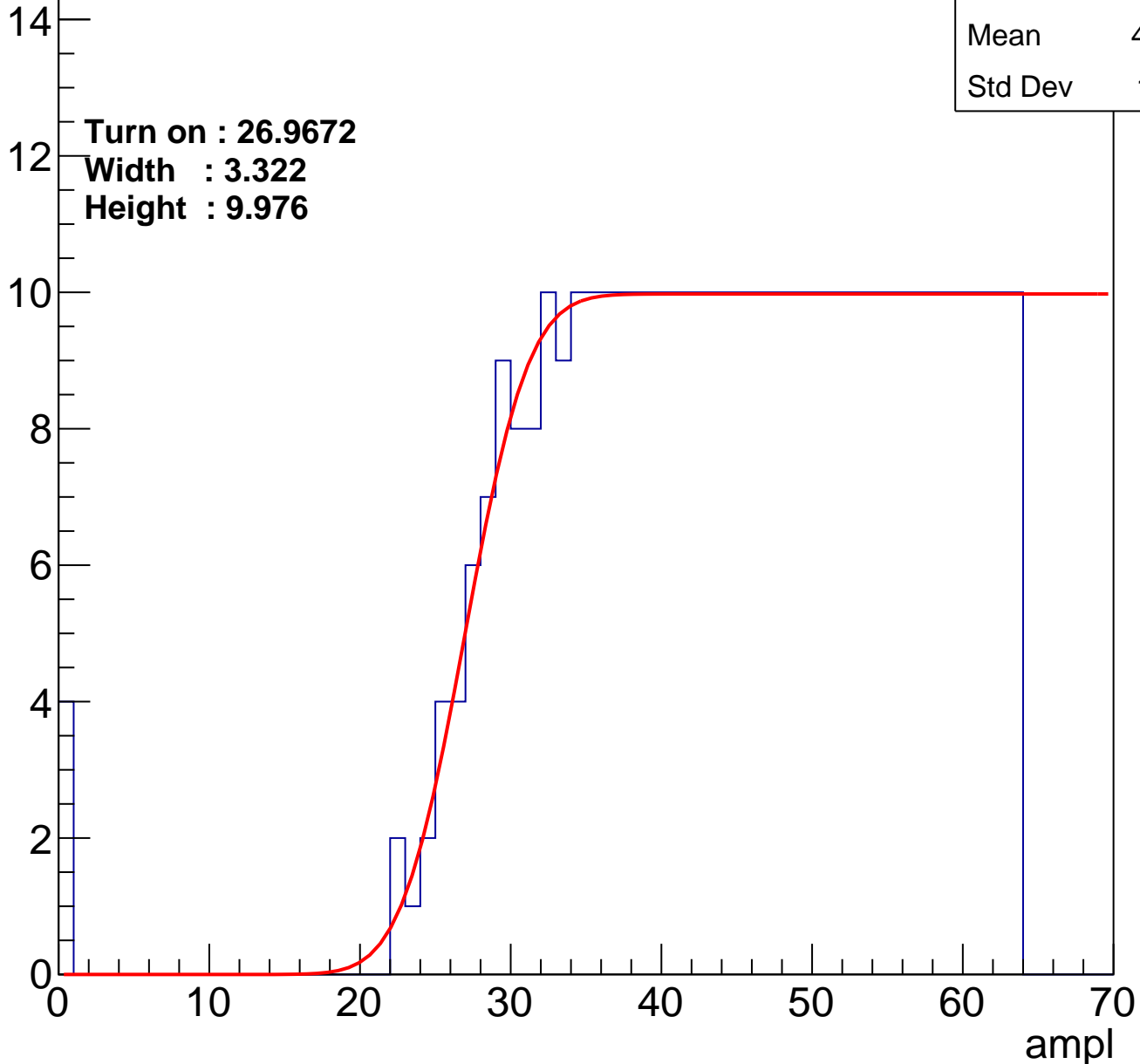
Entries	374
Mean	44.37
Std Dev	11.81

Turn on : 26.9672

Width : 3.322

Height : 9.976

Entry



# B1L100S, U26-ch80

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	394
Mean	43.49
Std Dev	12.07

Turn on : 25.0934

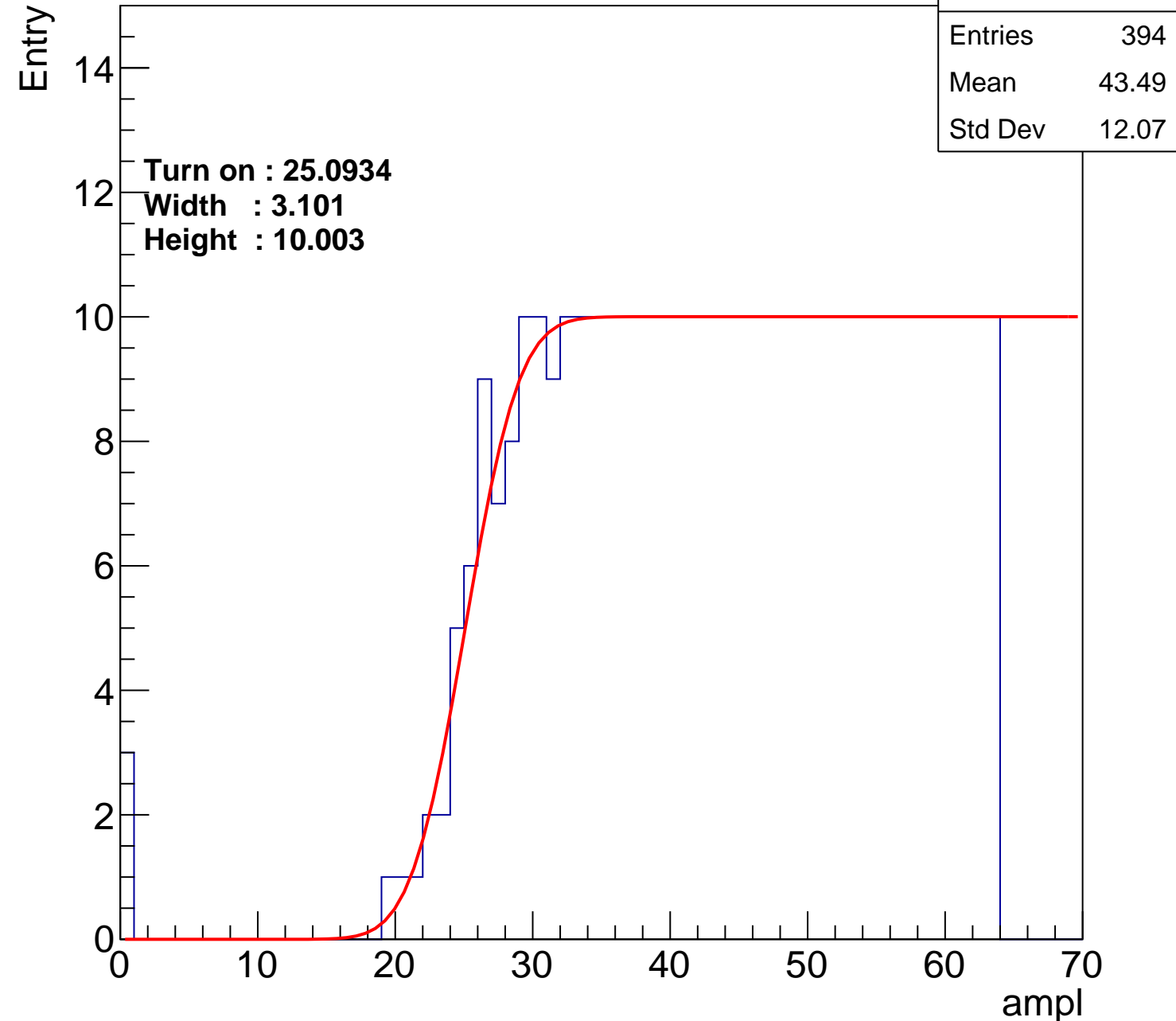
Width : 3.101

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch81

calib\_packv5\_042523\_0143.root, FC#4, port A2

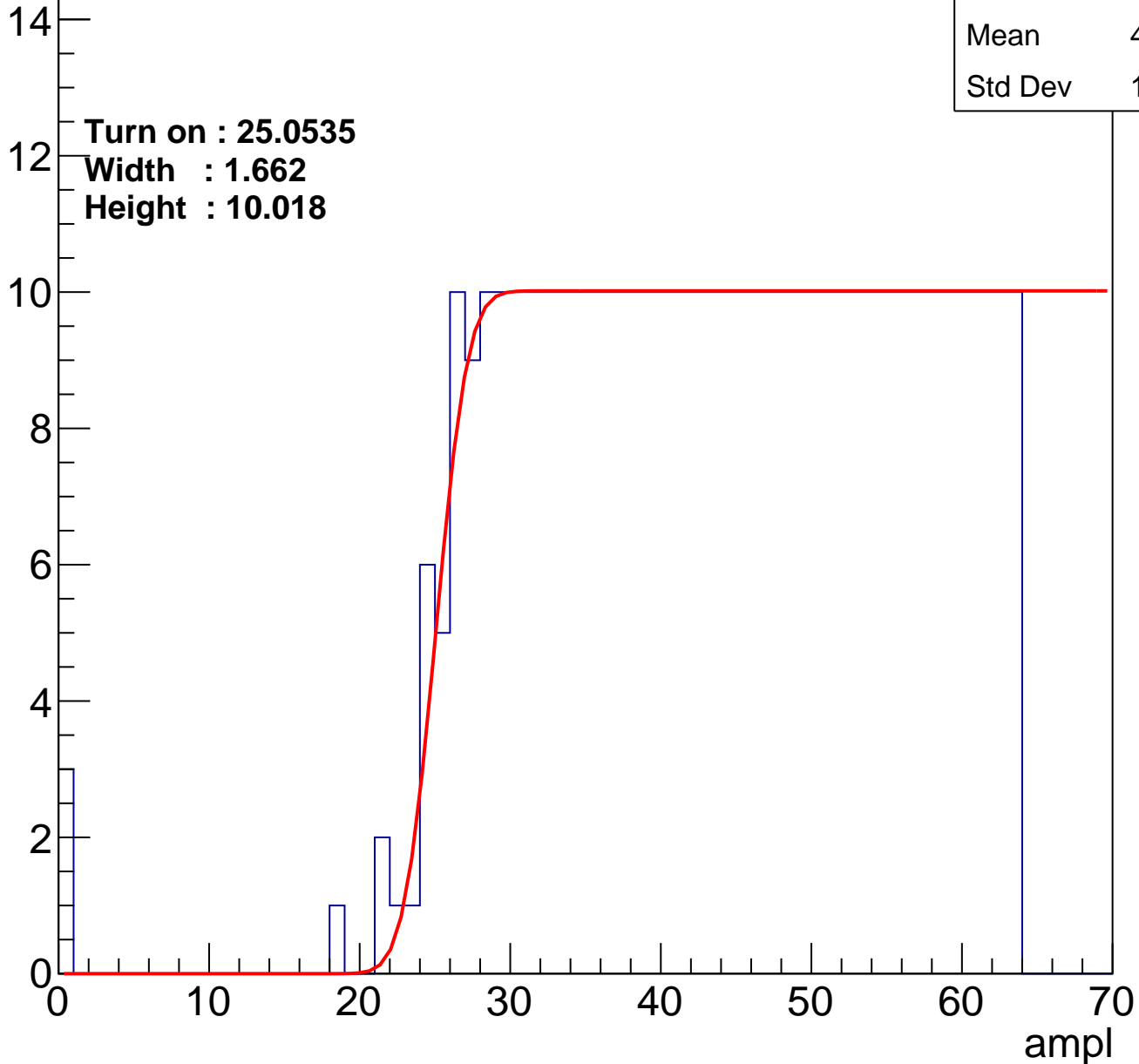
Entries	398
Mean	43.36
Std Dev	12.07

Turn on : 25.0535

Width : 1.662

Height : 10.018

Entry



# B1L100S, U26-ch82

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	387
Mean	43.89
Std Dev	11.75

Turn on : 25.5926

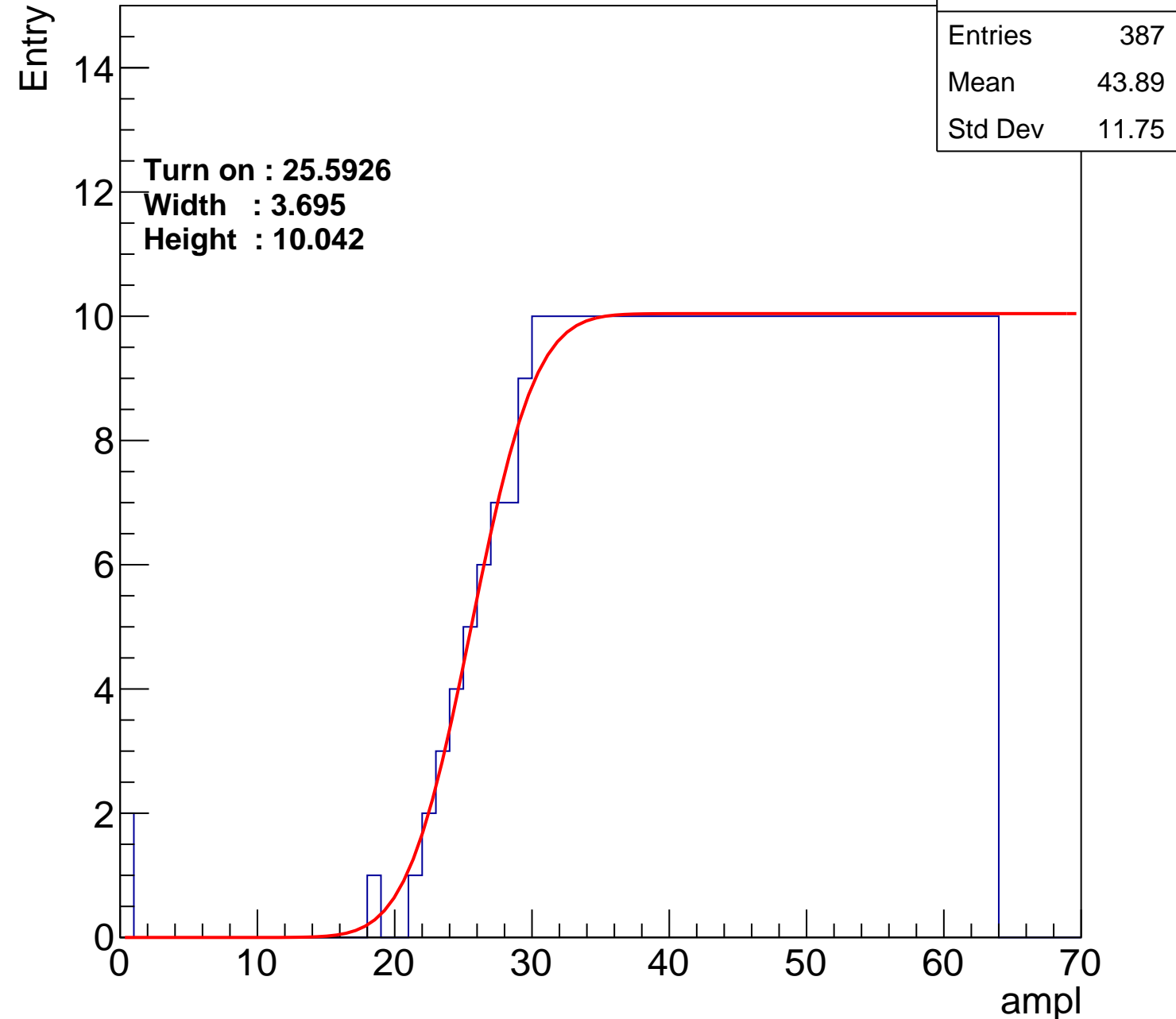
Width : 3.695

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch83

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.1647

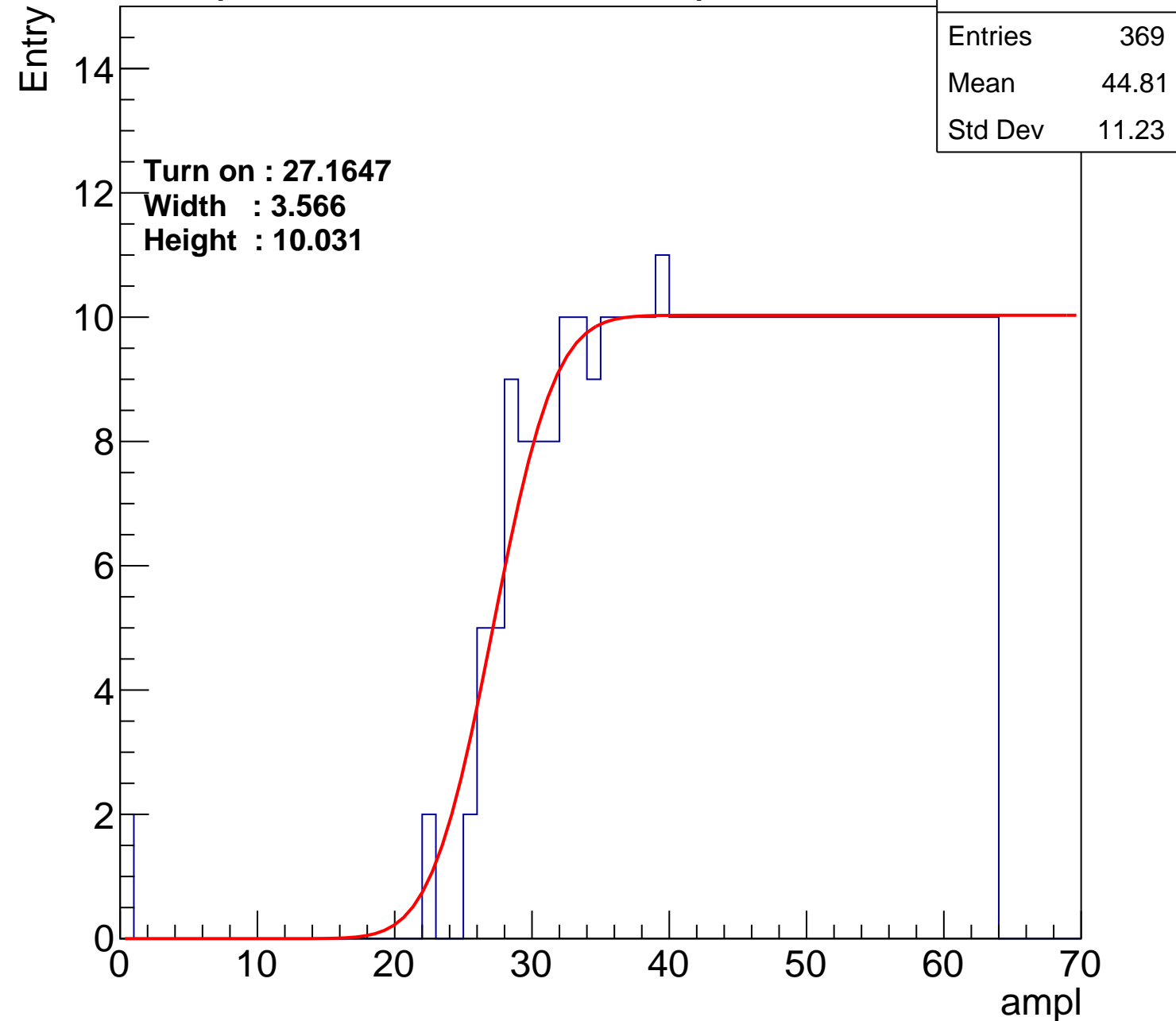
Width : 3.566

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch84

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 26.4010

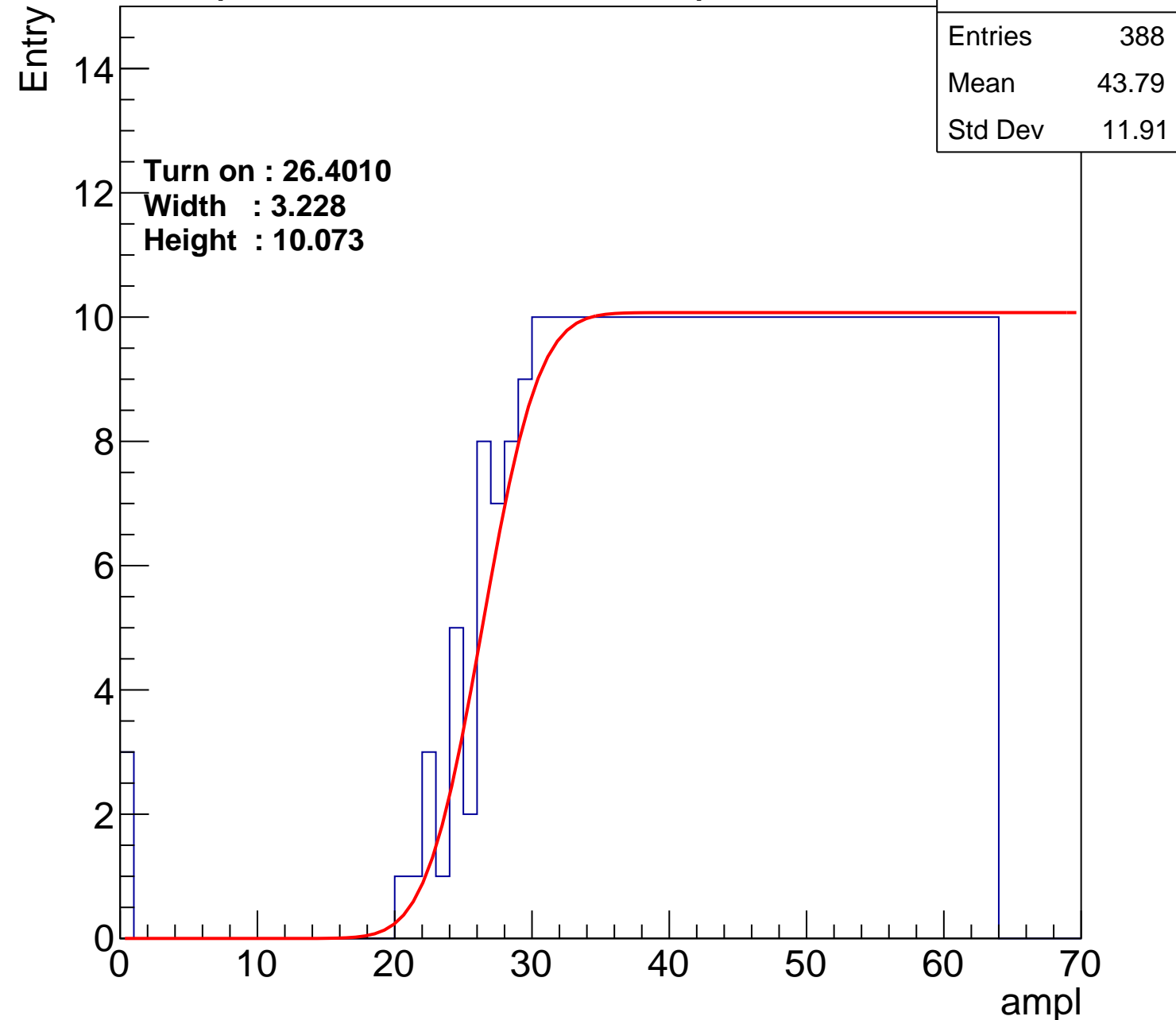
Width : 3.228

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch85

calib\_packv5\_042523\_0143.root, FC#4, port A2

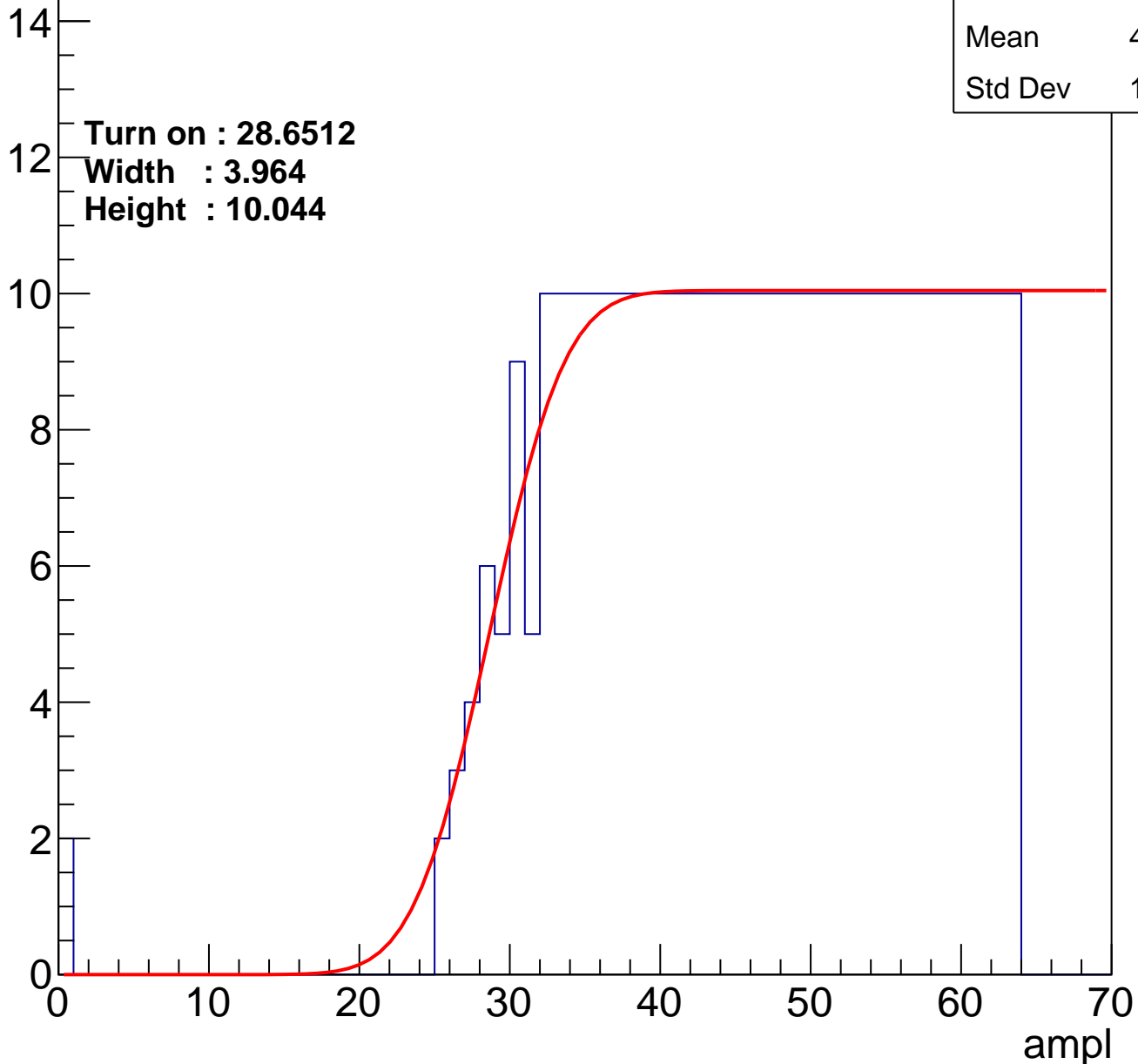
Entries	356
Mean	45.43
Std Dev	10.92

Turn on : 28.6512

Width : 3.964

Height : 10.044

Entry



# B1L100S, U26-ch86

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	364
Mean	45
Std Dev	11.27

**Turn on : 27.4067**

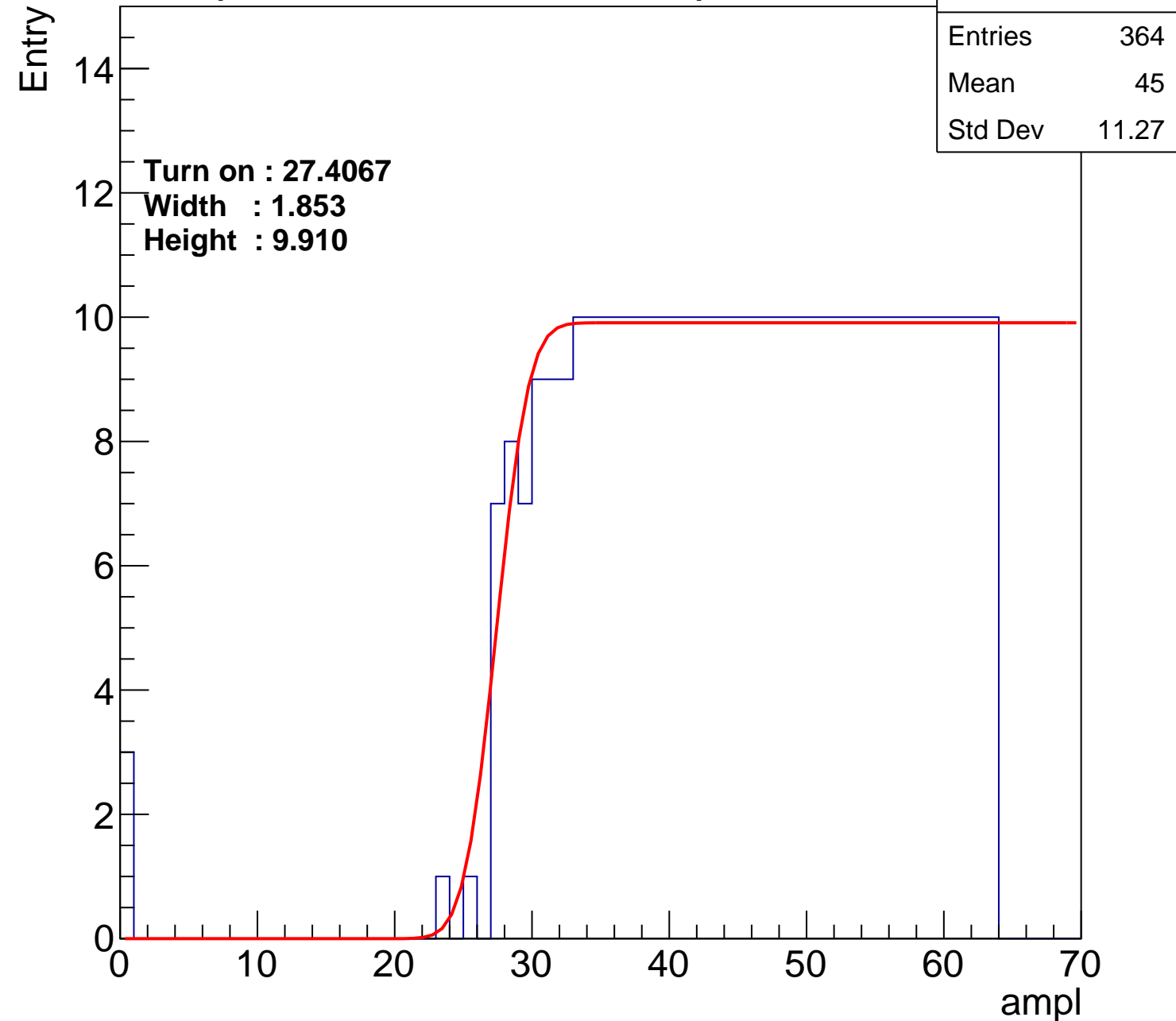
**Width : 1.853**

**Height : 9.910**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch87

calib\_packv5\_042523\_0143.root, FC#4, port A2

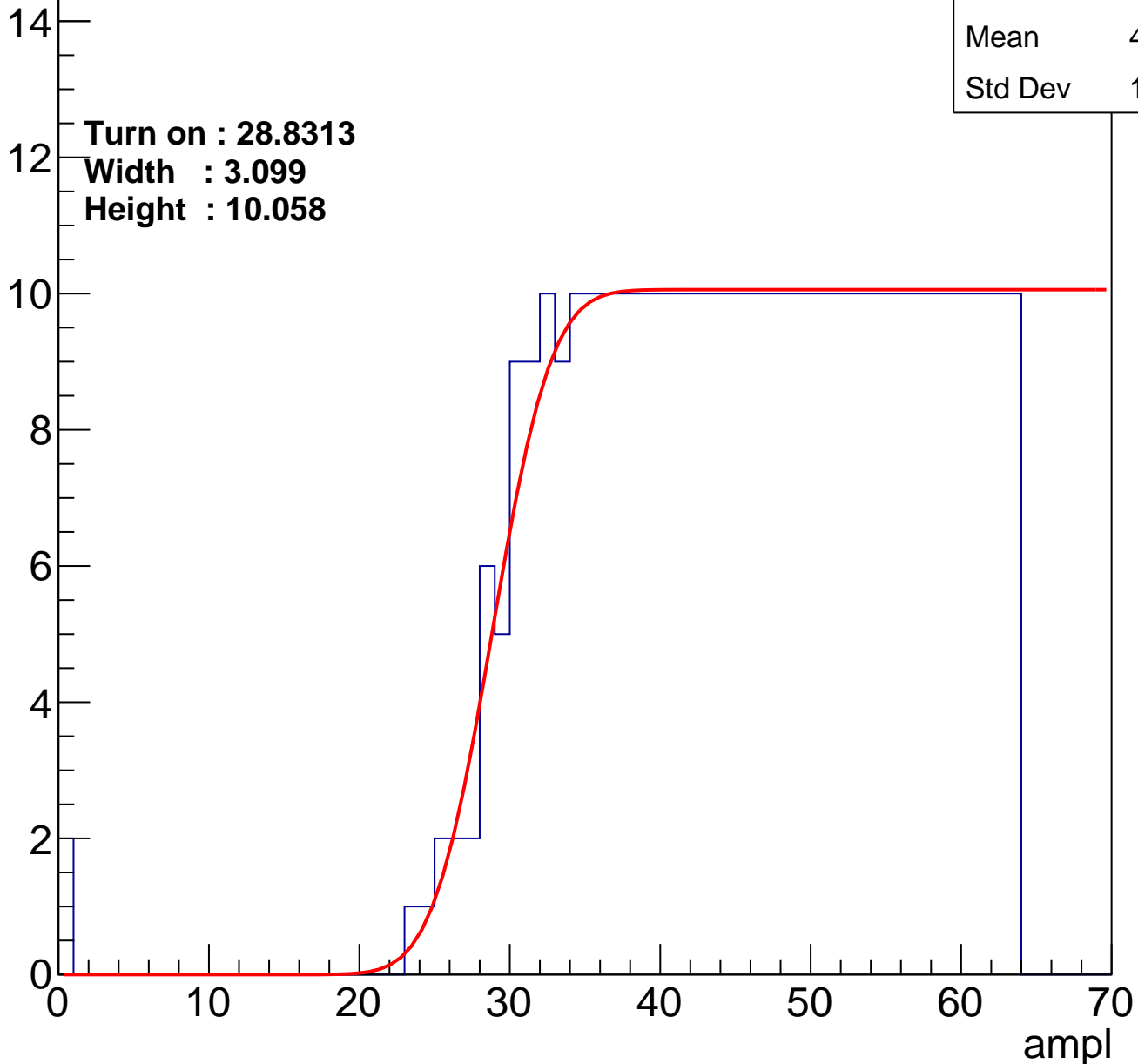
Entries	358
Mean	45.34
Std Dev	10.96

Turn on : 28.8313

Width : 3.099

Height : 10.058

Entry



# B1L100S, U26-ch88

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	387
Mean	43.68
Std Dev	12.27

Turn on : 26.0051

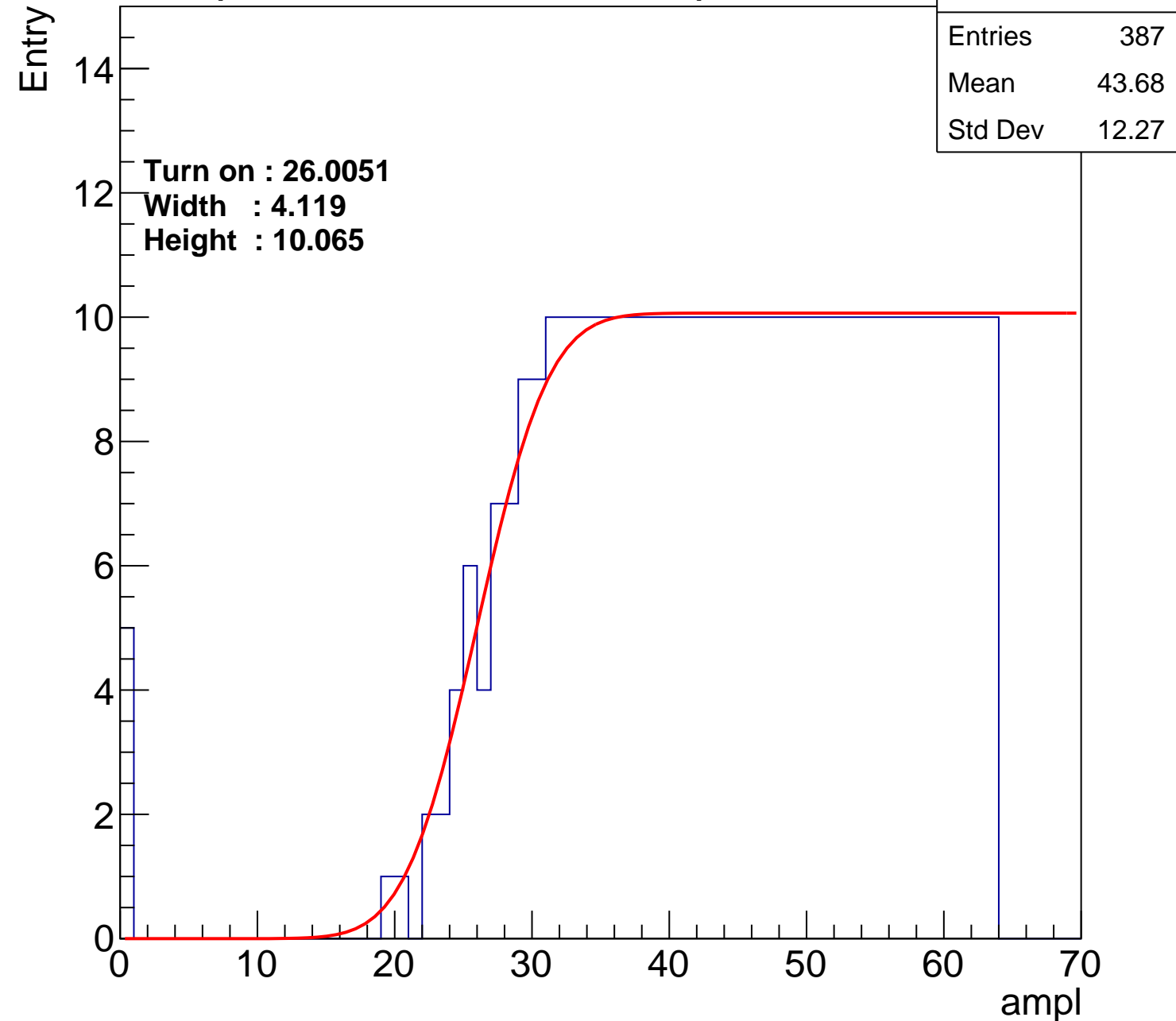
Width : 4.119

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch89

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	378
Mean	44.36
Std Dev	11.47

Turn on : 26.3436

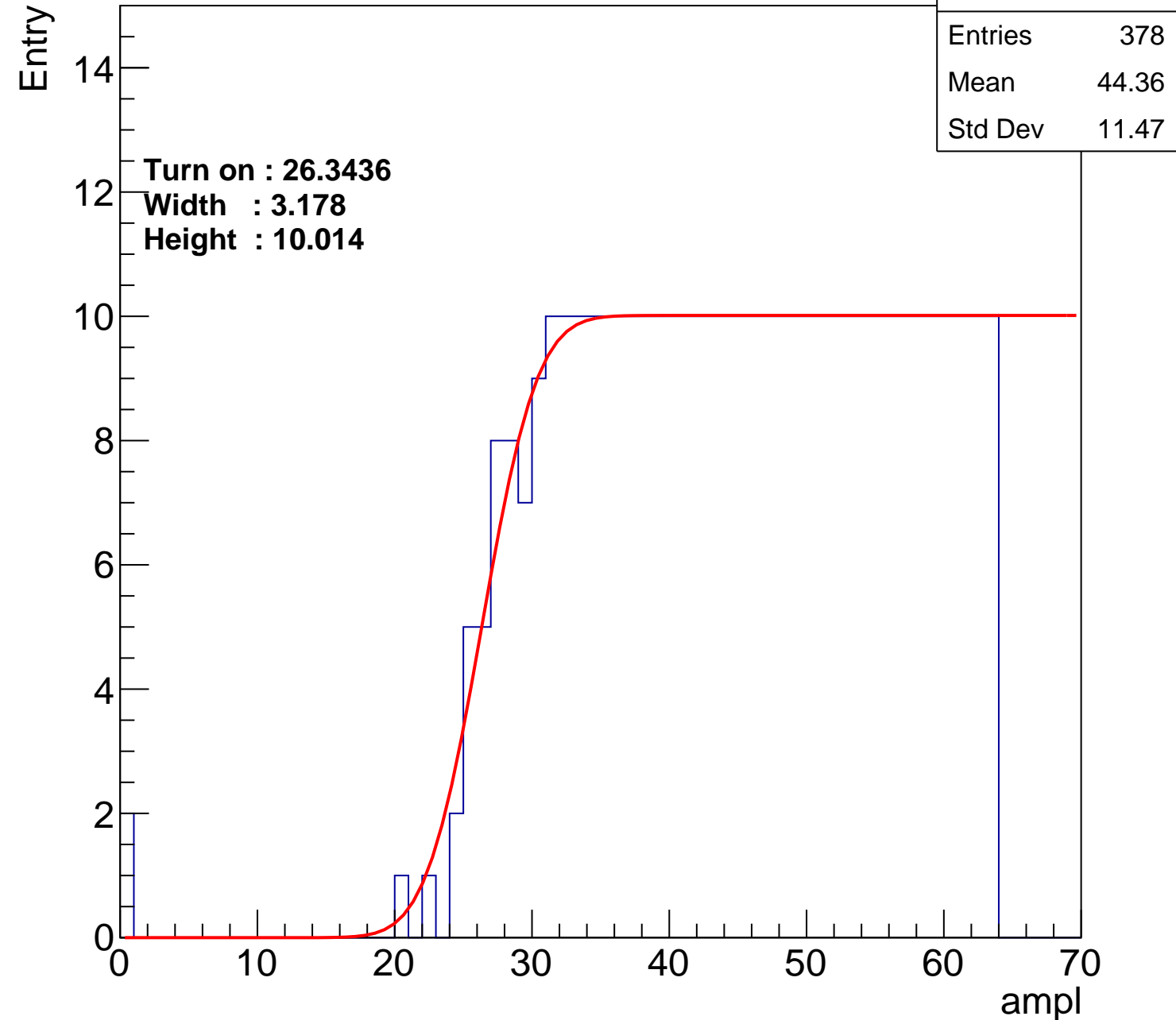
Width : 3.178

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch90

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	394
Mean	43.57
Std Dev	11.89

Turn on : 25.4536

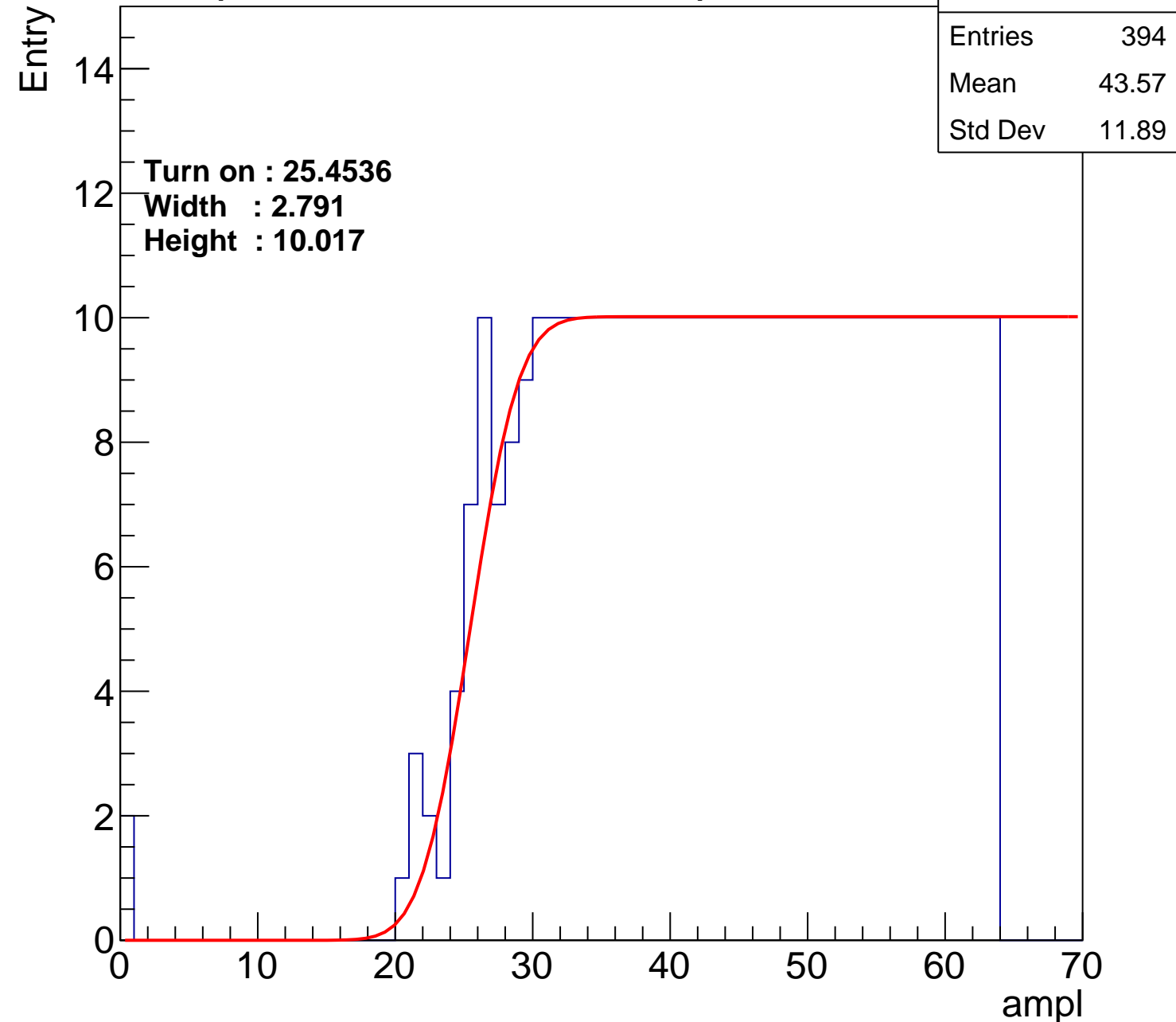
Width : 2.791

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch91

calib\_packv5\_042523\_0143.root, FC#4, port A2

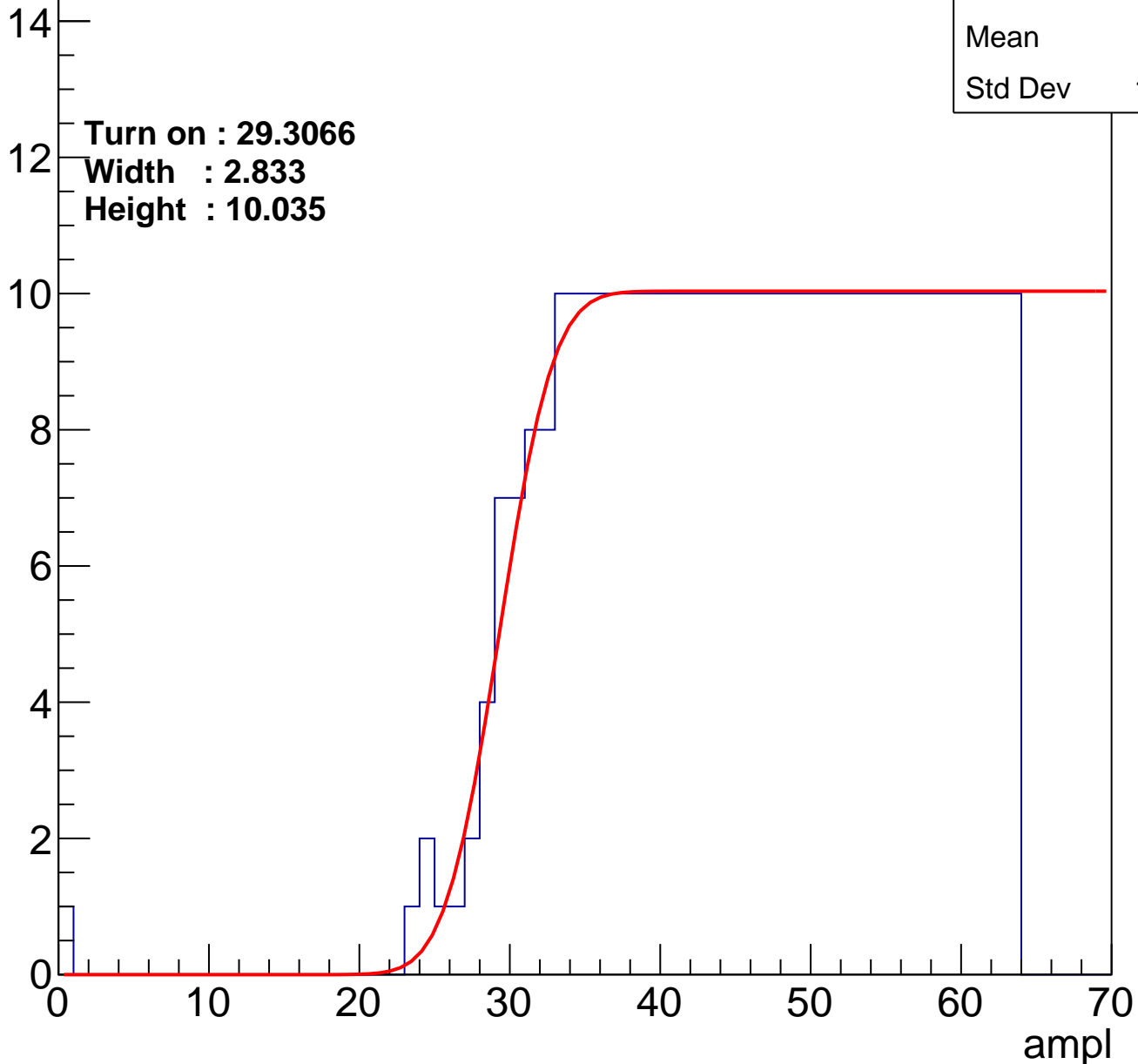
Entries	352
Mean	45.7
Std Dev	10.61

Turn on : 29.3066

Width : 2.833

Height : 10.035

Entry



**calib\_packv5\_042523\_0143.root, FC#4, port A2**

Entries	372
Mean	44.56
Std Dev	11.54

Mean	44.56
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Std Dev	11.54
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**Width : 2.638**

**Height : 9.970**



# B1L100S, U26-ch93

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.71
Std Dev	11.43

Turn on : 27.1673

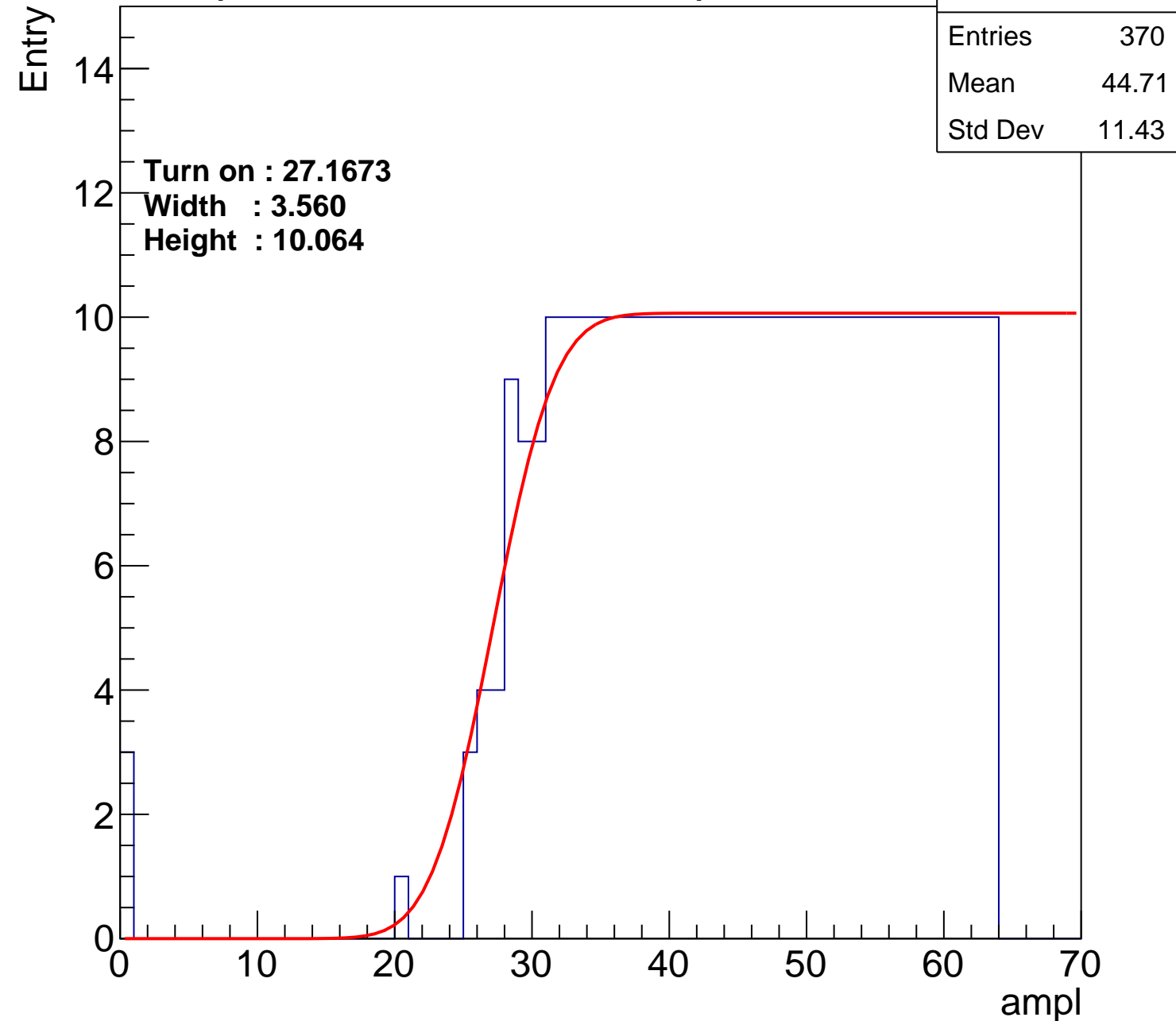
Width : 3.560

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch94

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	387
Mean	43.8
Std Dev	12.01

Turn on : 25.7203

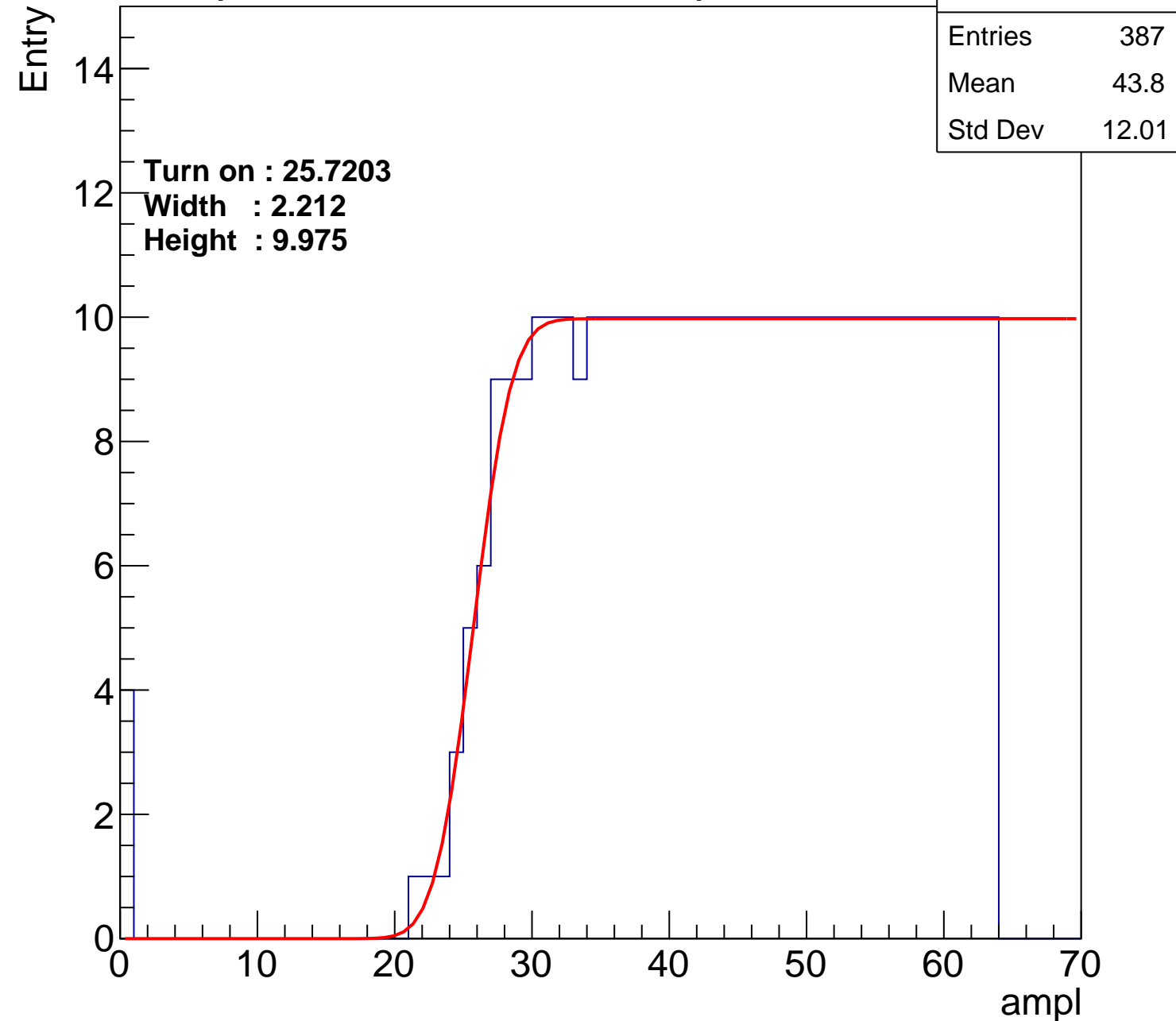
Width : 2.212

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch95

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	352
Mean	45.68
Std Dev	10.64

Turn on : 29.3681

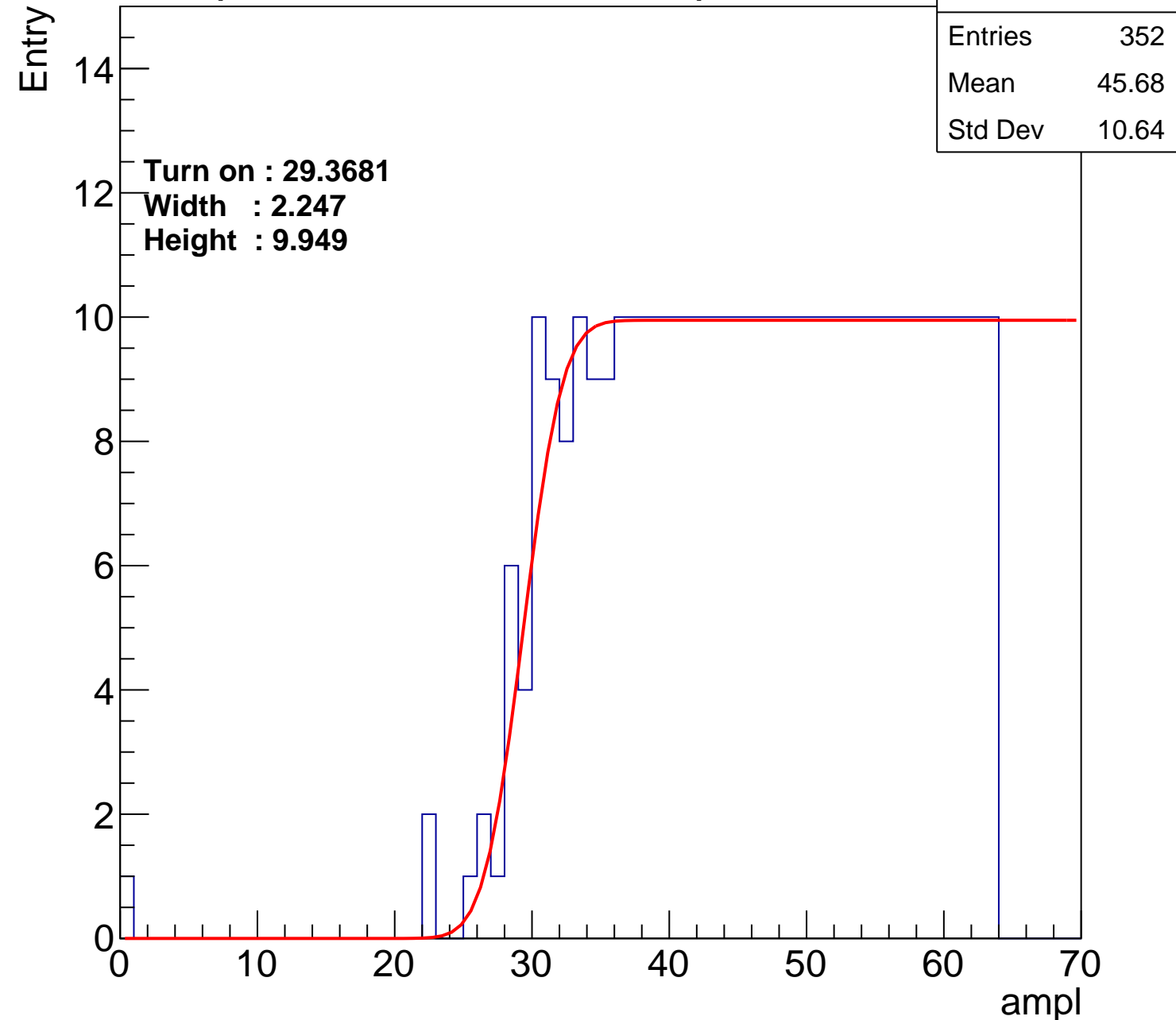
Width : 2.247

Height : 9.949

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch96

calib\_packv5\_042523\_0143.root, FC#4, port A2

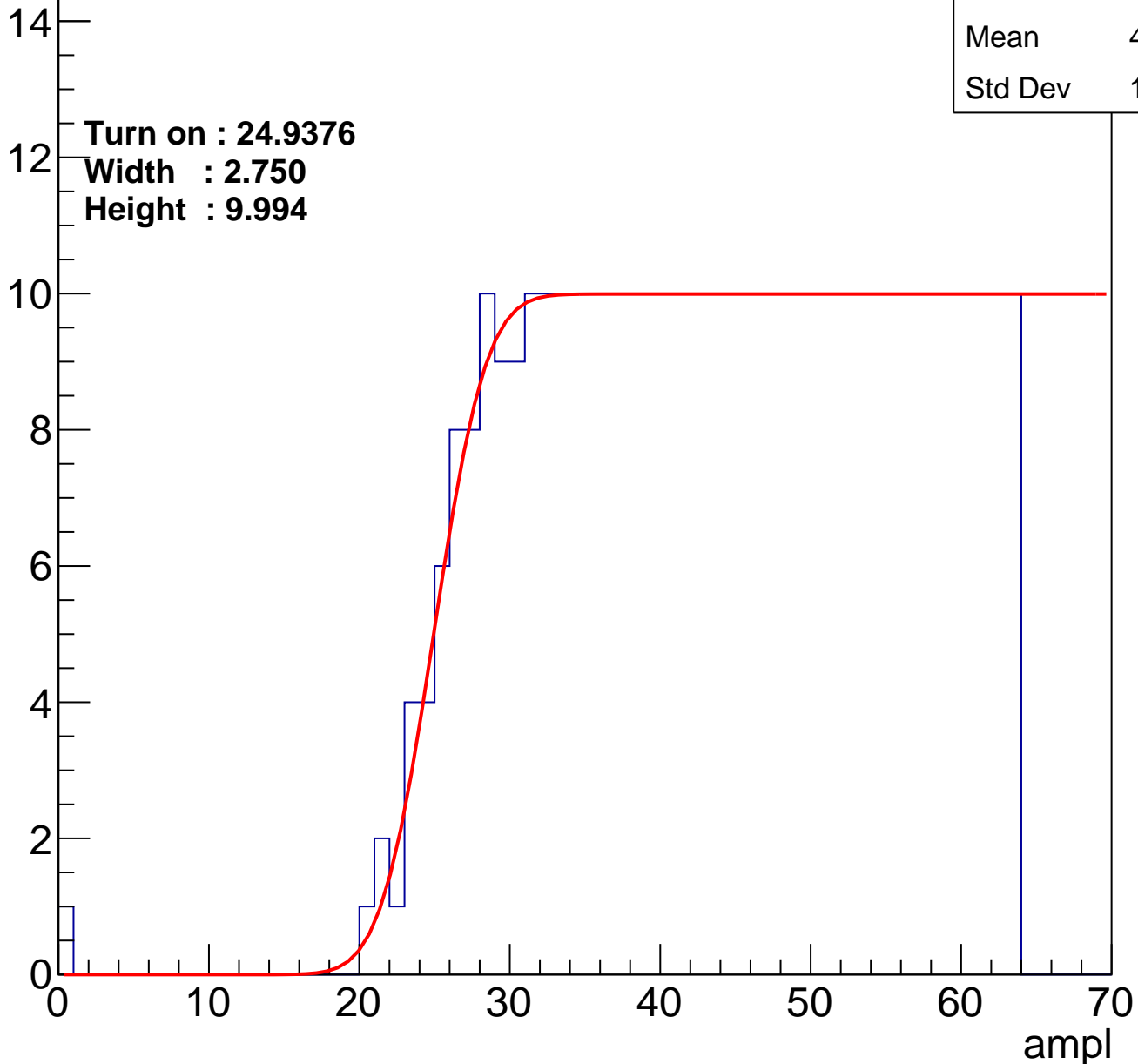
Entries	393
Mean	43.68
Std Dev	11.69

Turn on : 24.9376

Width : 2.750

Height : 9.994

Entry



# B1L100S, U26-ch97

calib\_packv5\_042523\_0143.root, FC#4, port A2

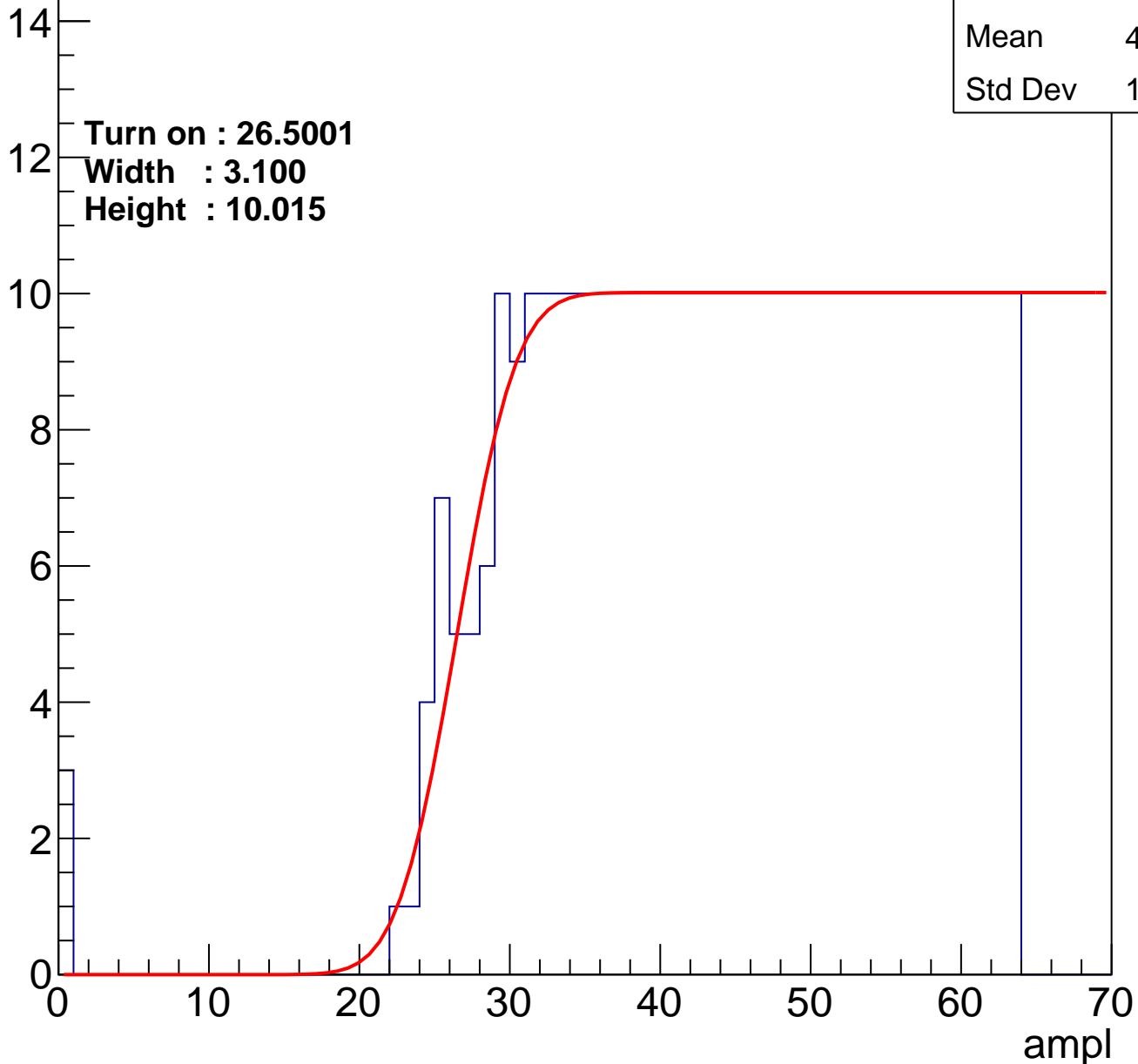
Entries	381
Mean	44.14
Std Dev	11.72

Turn on : 26.5001

Width : 3.100

Height : 10.015

Entry



# B1L100S, U26-ch98

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	367
Mean	44.89
Std Dev	11.19

Turn on : 27.2695

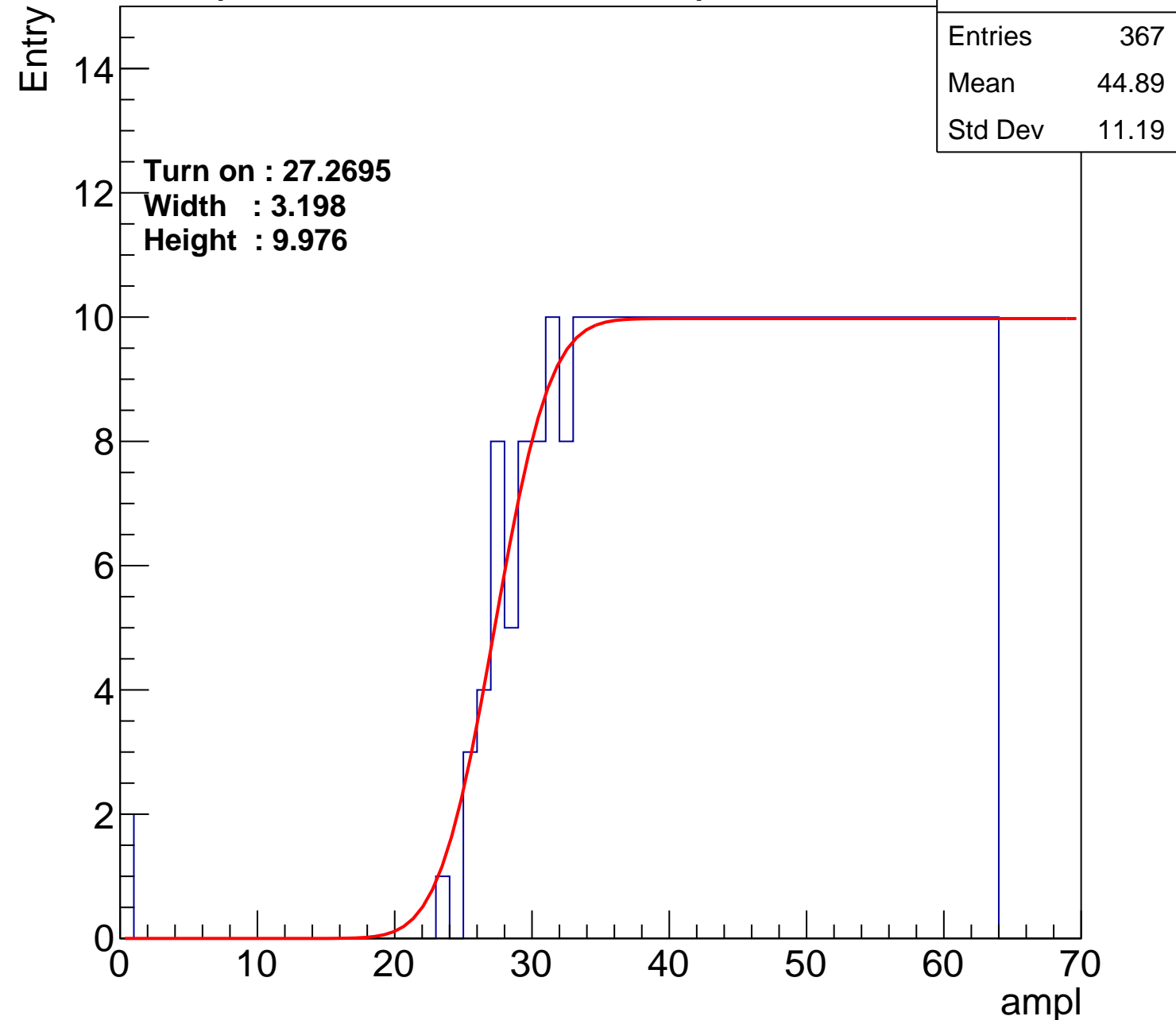
Width : 3.198

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch99

calib\_packv5\_042523\_0143.root, FC#4, port A2

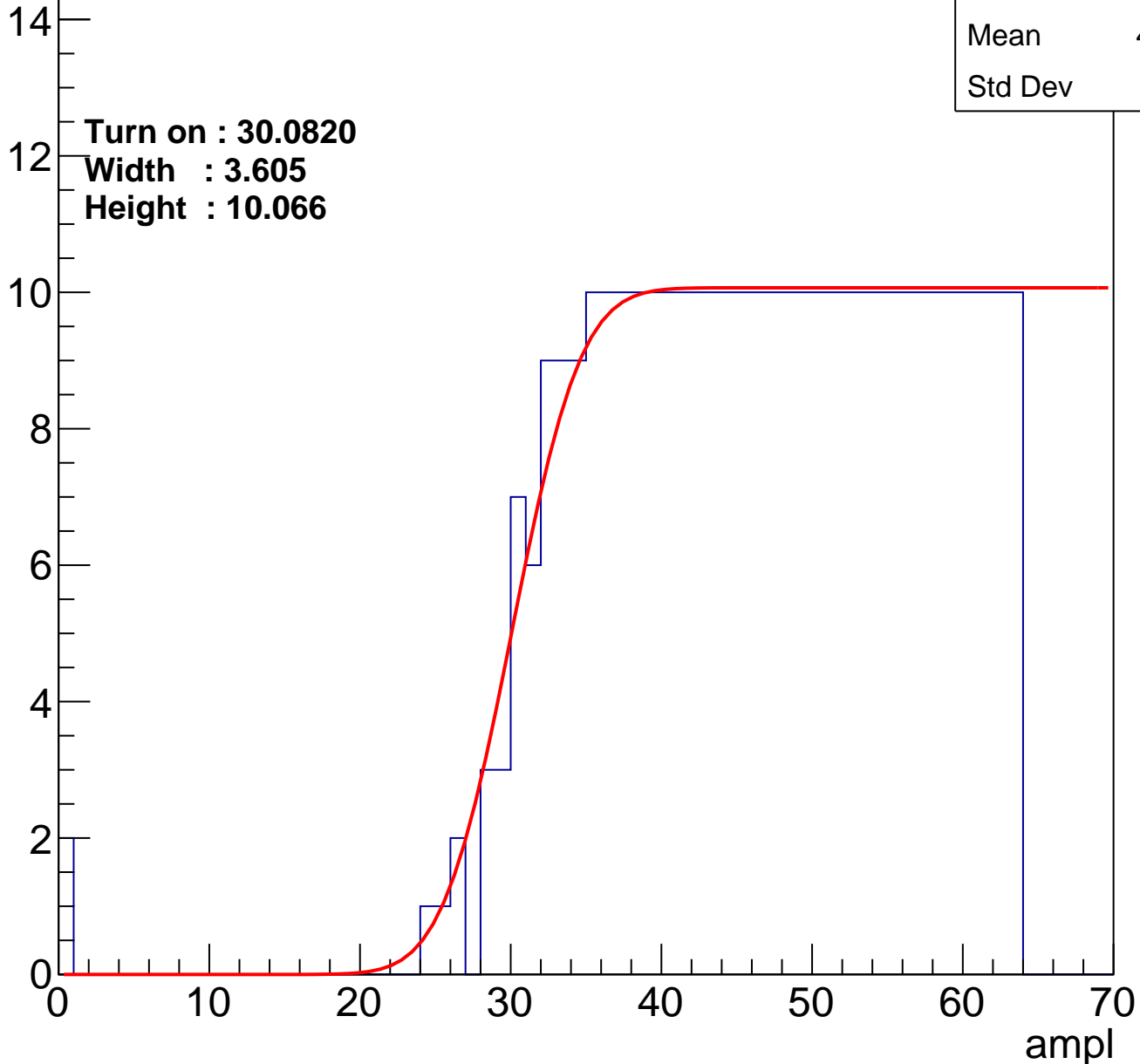
Entries	342
Mean	46.11
Std Dev	10.6

Turn on : 30.0820

Width : 3.605

Height : 10.066

Entry



# B1L100S, U26-ch100

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	354
Mean	45.54
Std Dev	10.74

**Turn on : 28.7370**

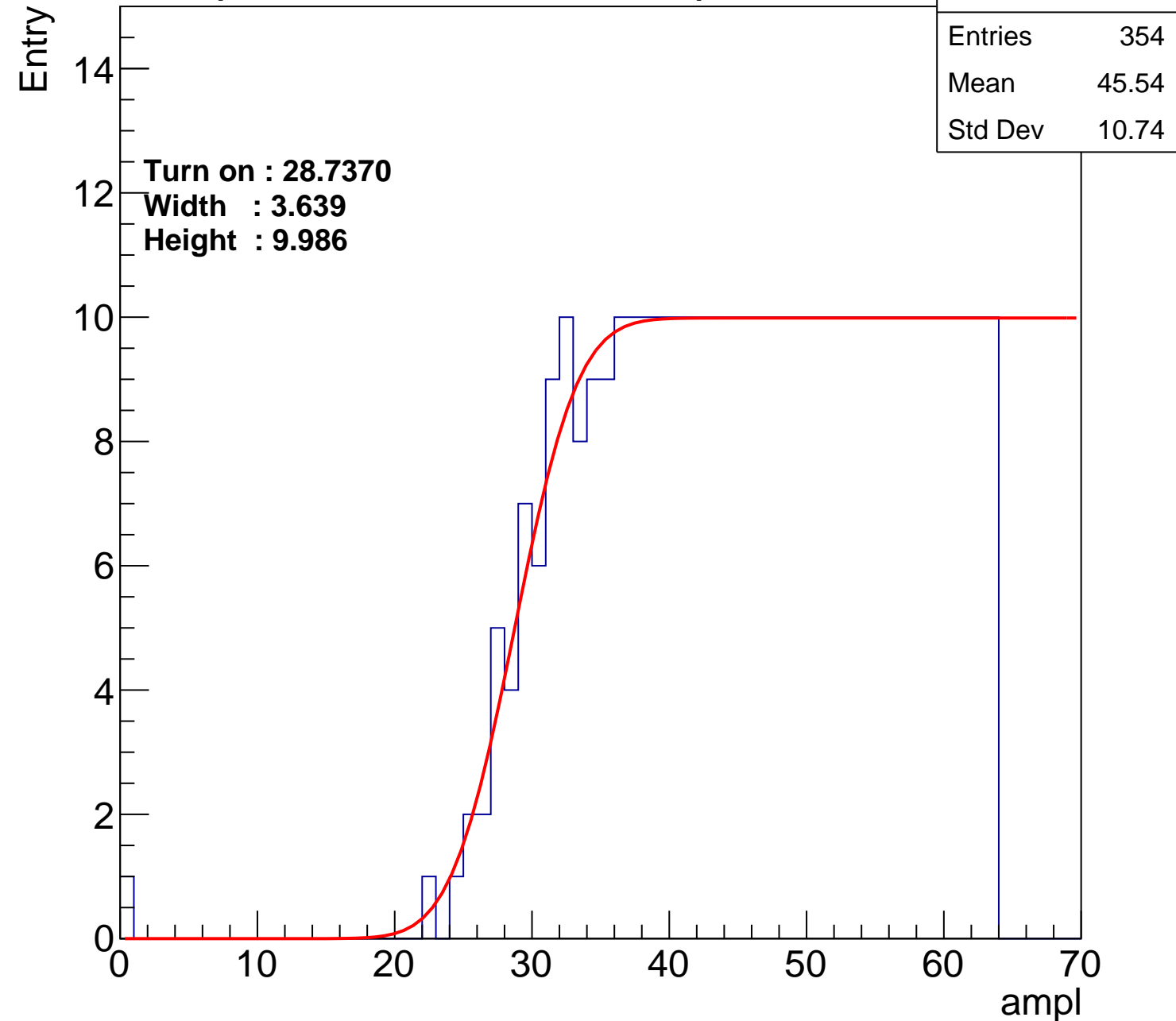
**Width : 3.639**

**Height : 9.986**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch101

calib\_packv5\_042523\_0143.root, FC#4, port A2

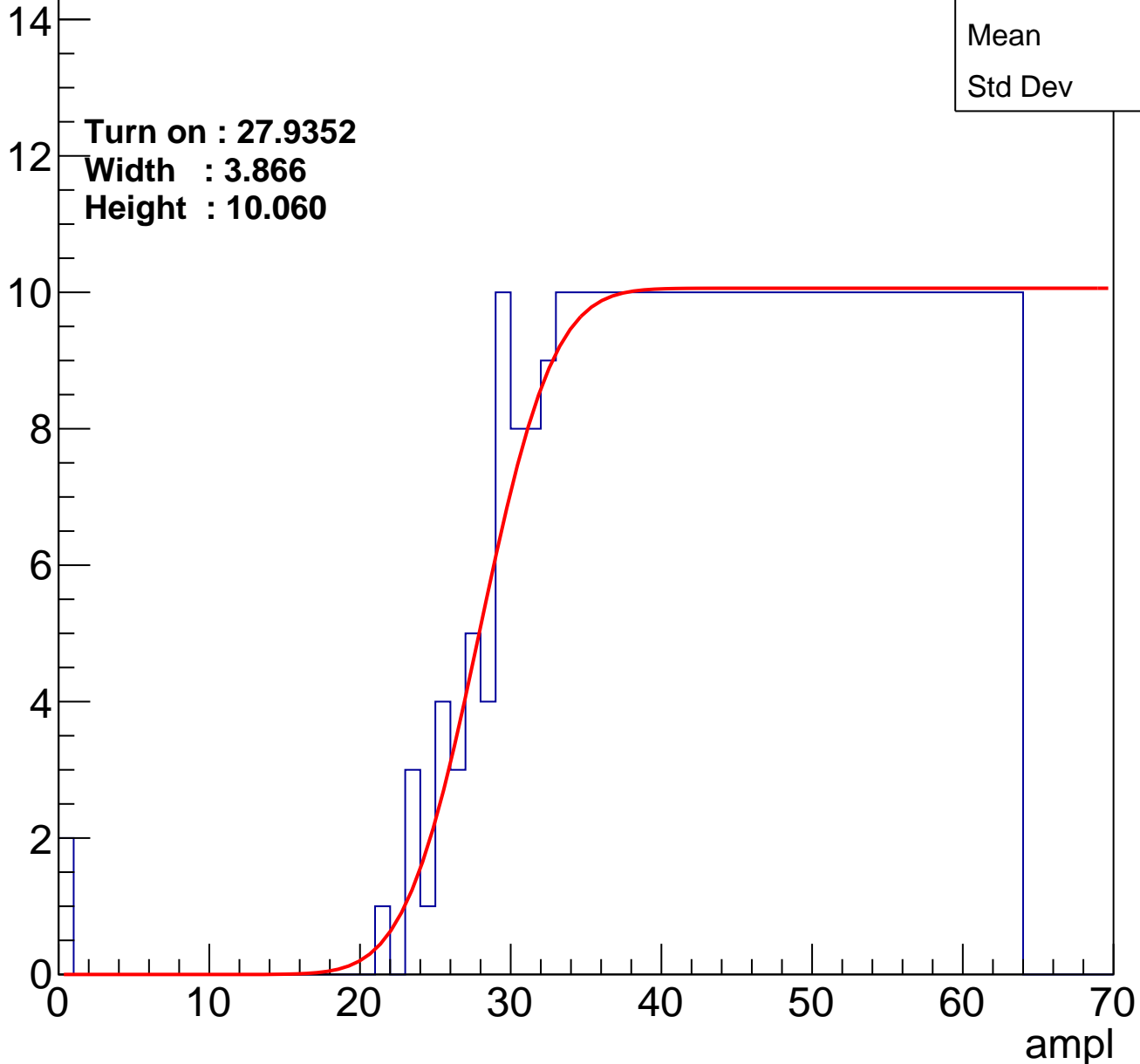
Entries	368
Mean	44.8
Std Dev	11.3

Turn on : 27.9352

Width : 3.866

Height : 10.060

Entry



# B1L100S, U26-ch102

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	367
Mean	44.97
Std Dev	10.98

Turn on : 27.5868

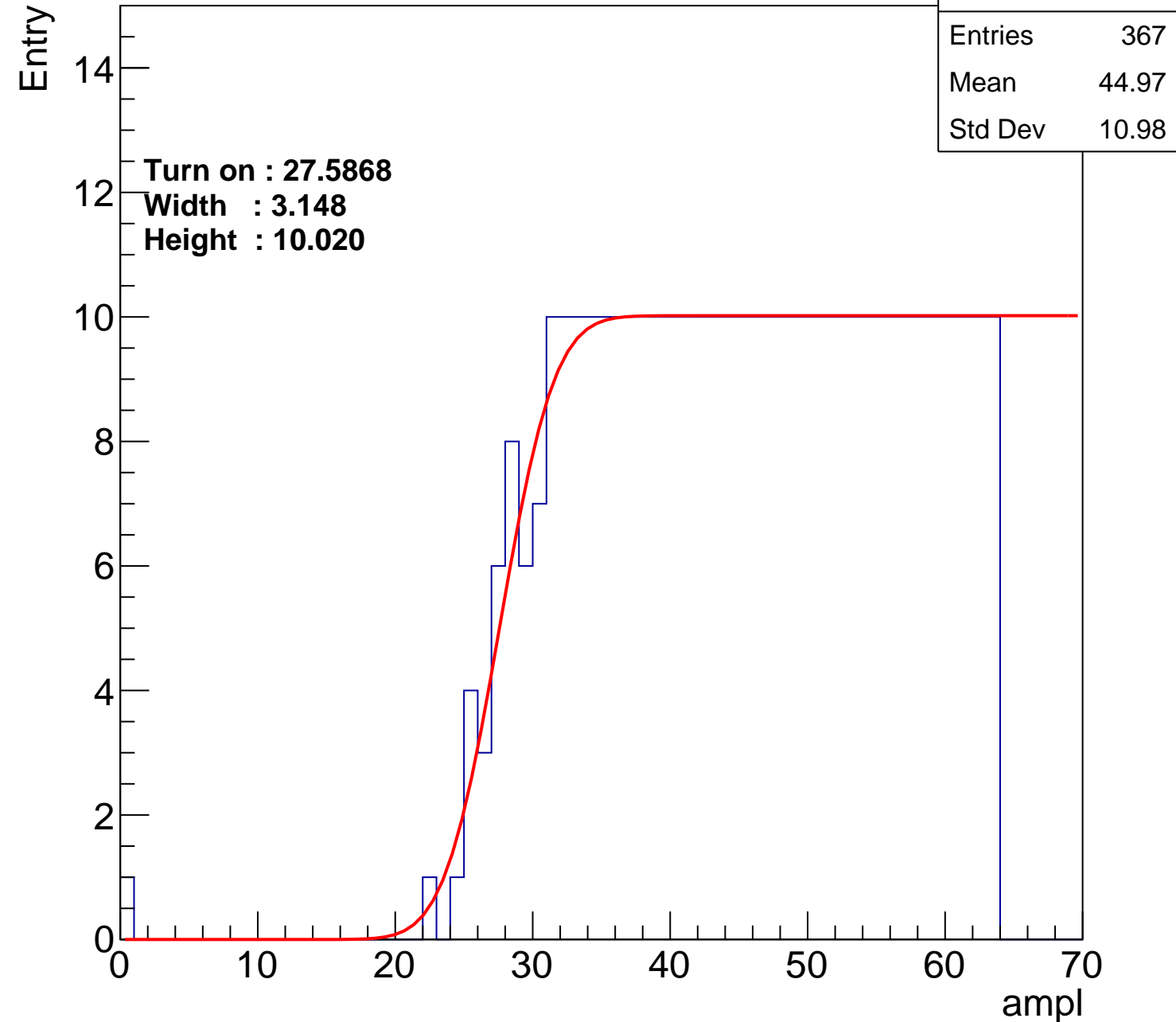
Width : 3.148

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch103

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	360
Mean	45.29
Std Dev	10.83

Turn on : 28.5305

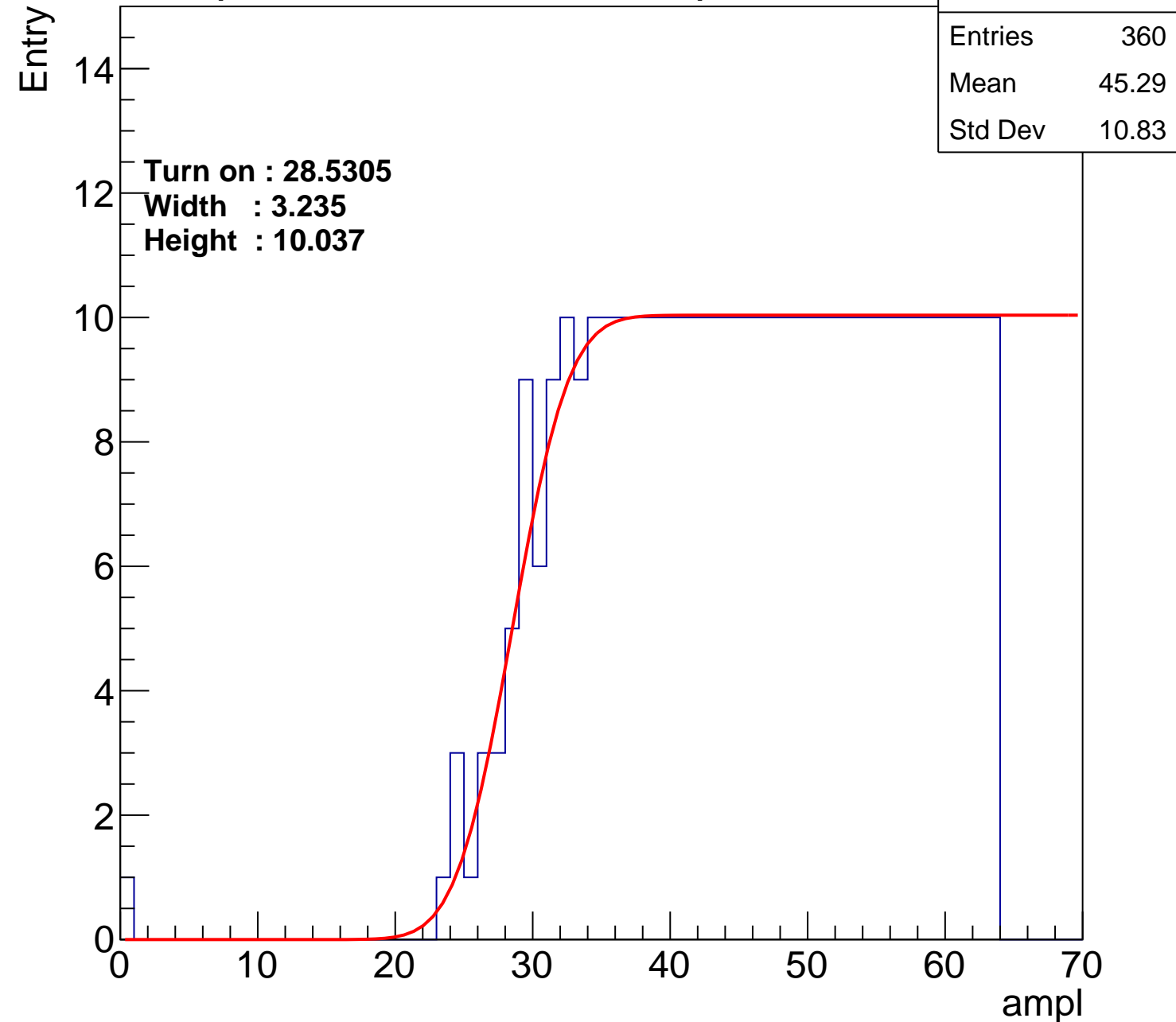
Width : 3.235

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch104

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 27.1574

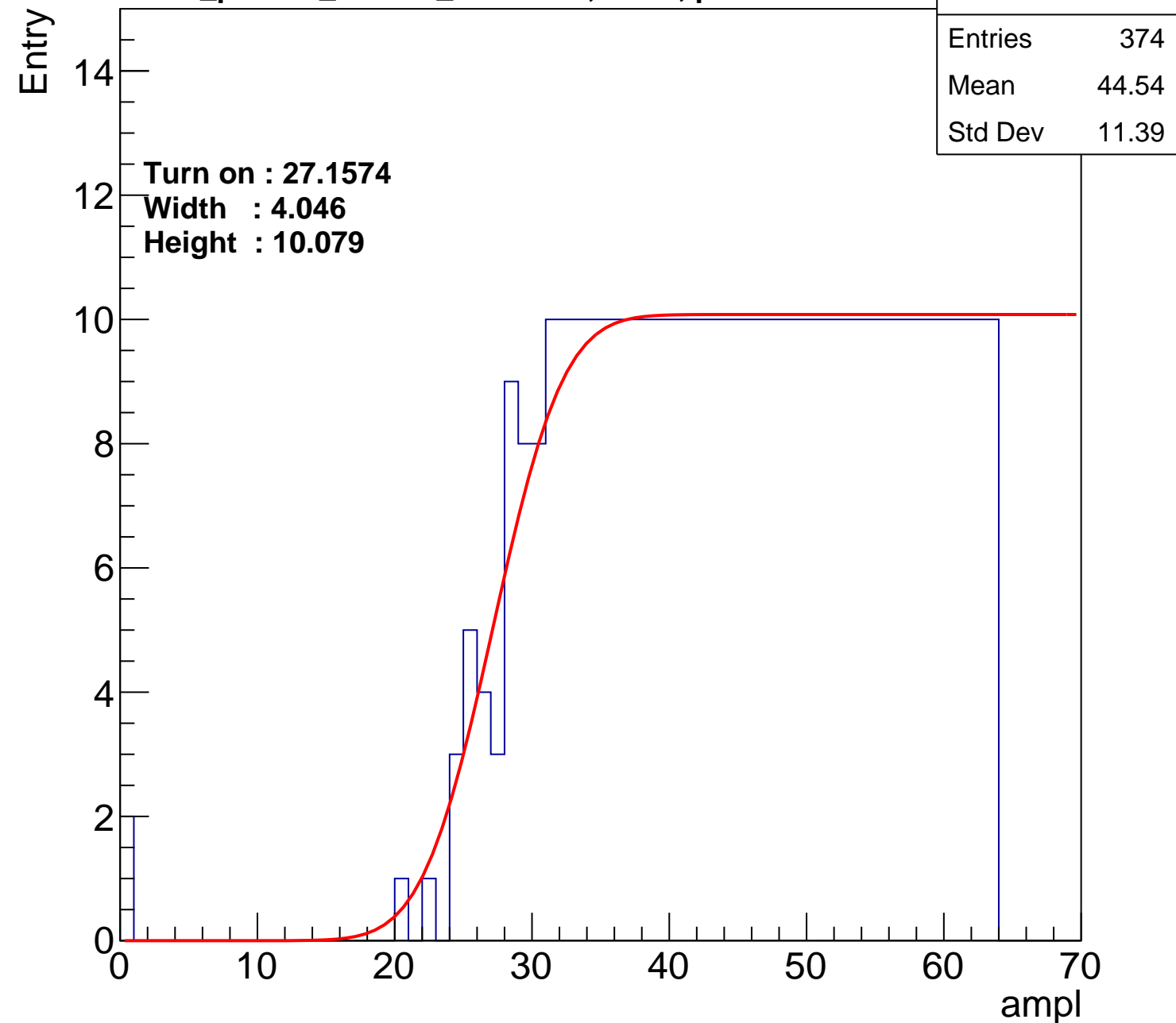
Width : 4.046

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch105

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	356
Mean	45.36
Std Dev	11.13

Turn on : 29.0832

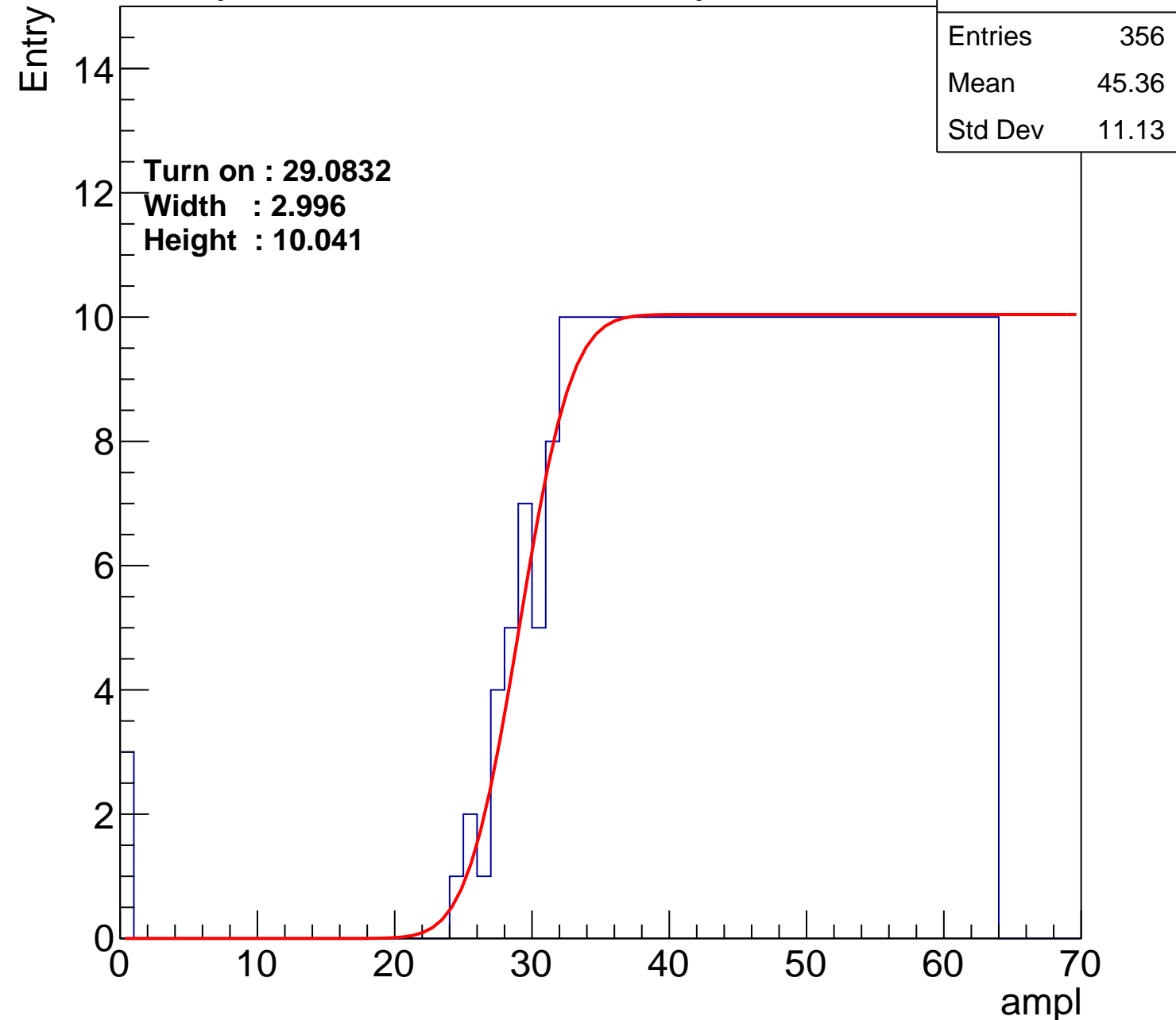
Width : 2.996

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch106

calib\_packv5\_042523\_0143.root, FC#4, port A2

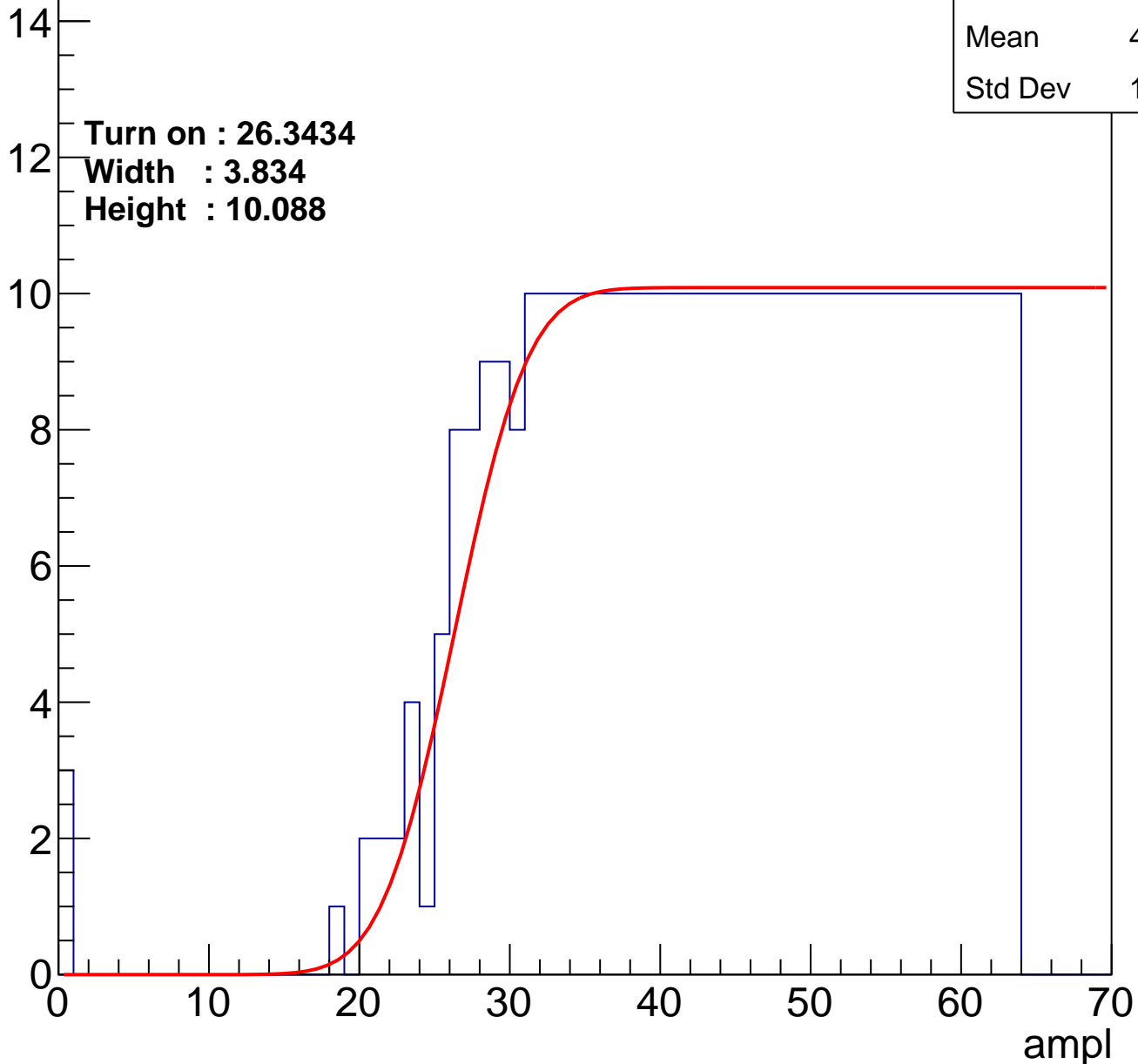
Entries	392
Mean	43.55
Std Dev	12.08

**Turn on : 26.3434**

**Width : 3.834**

**Height : 10.088**

Entry



# B1L100S, U26-ch107

calib\_packv5\_042523\_0143.root, FC#4, port A2

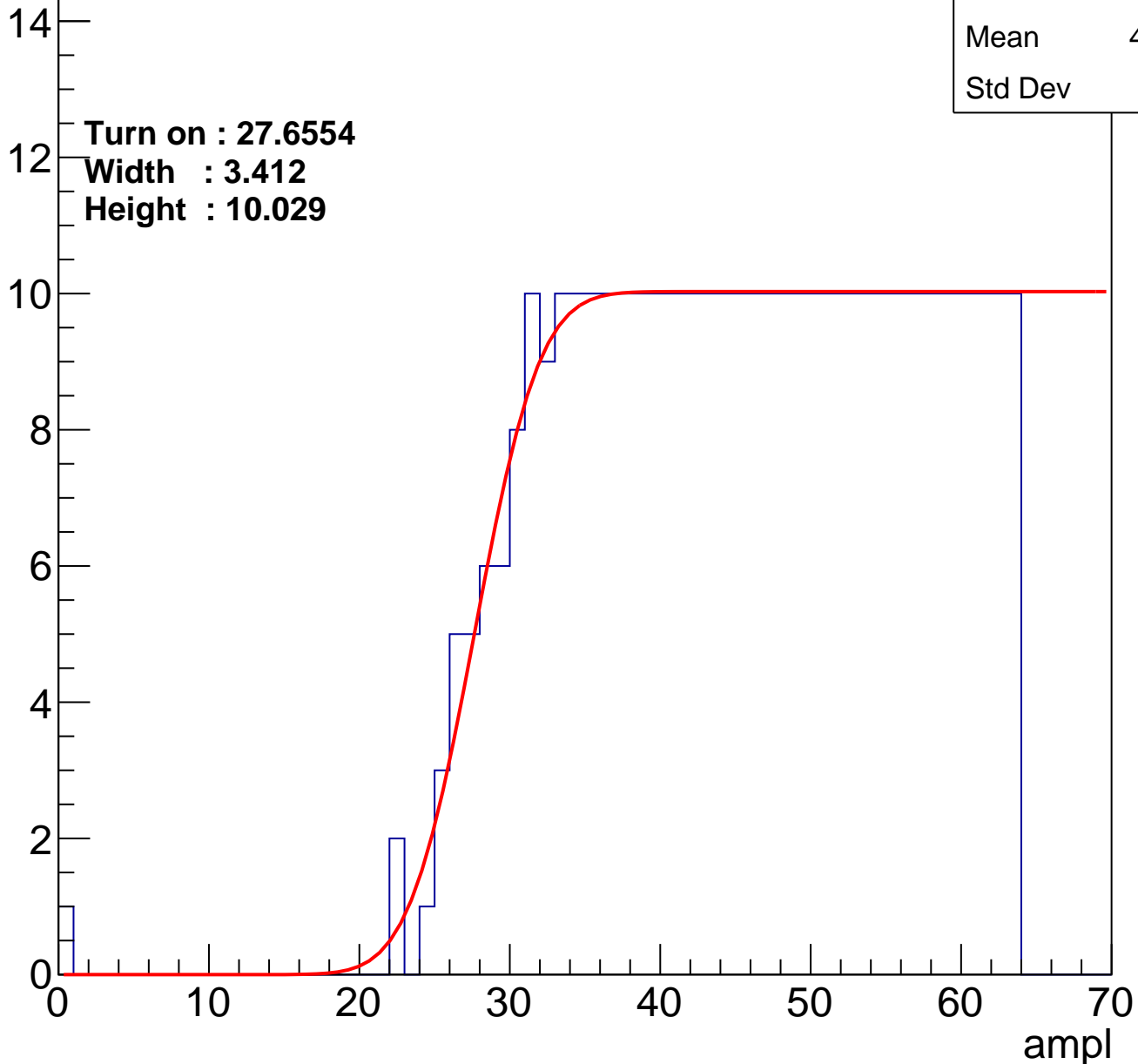
Entries	366
Mean	44.99
Std Dev	11

Turn on : 27.6554

Width : 3.412

Height : 10.029

Entry



# B1L100S, U26-ch108

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	392
Mean	43.55
Std Dev	12.09

Turn on : 25.1811

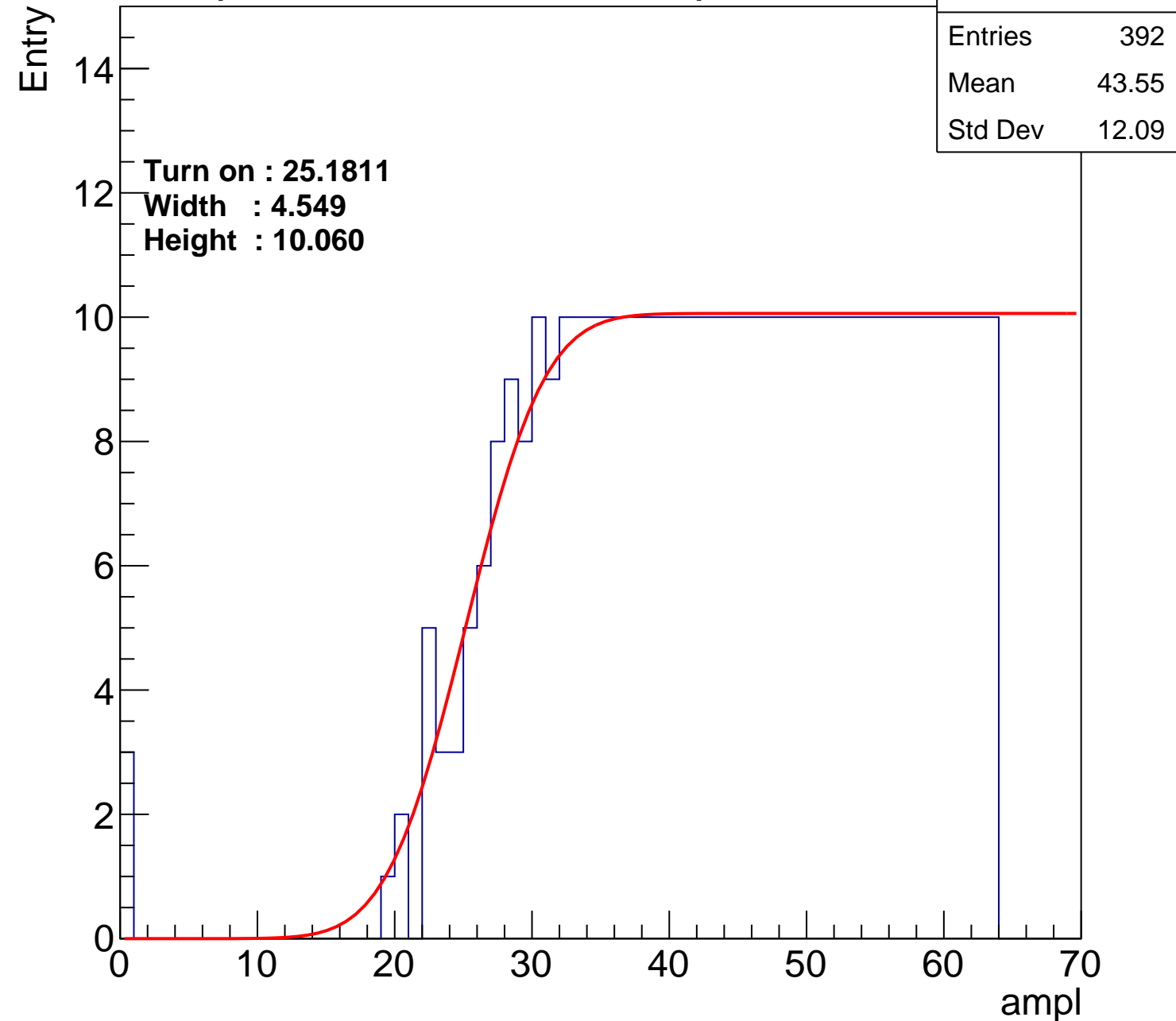
Width : 4.549

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch109

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.36
Std Dev	11.6

Turn on : 26.5343

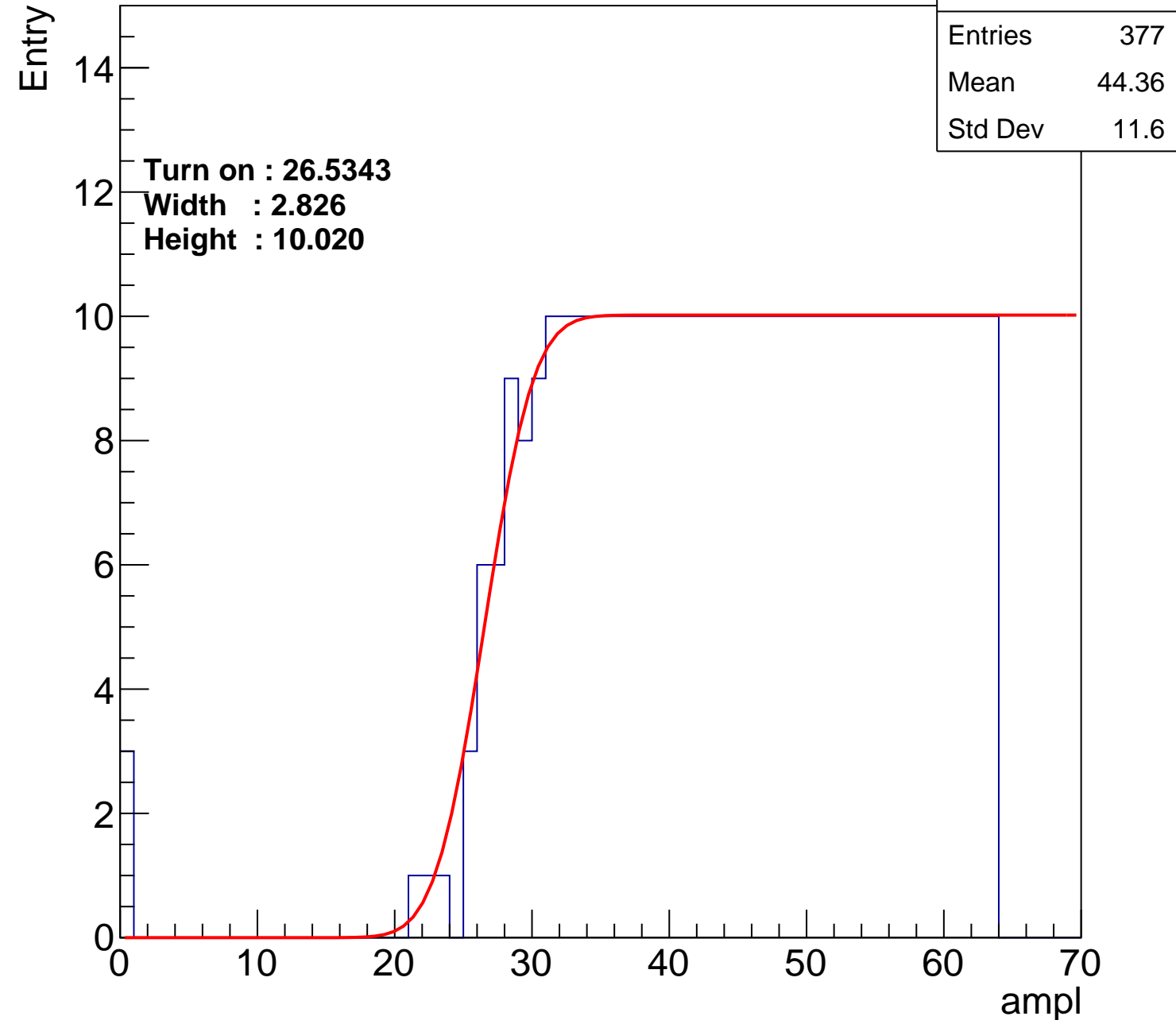
Width : 2.826

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch110

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.7915

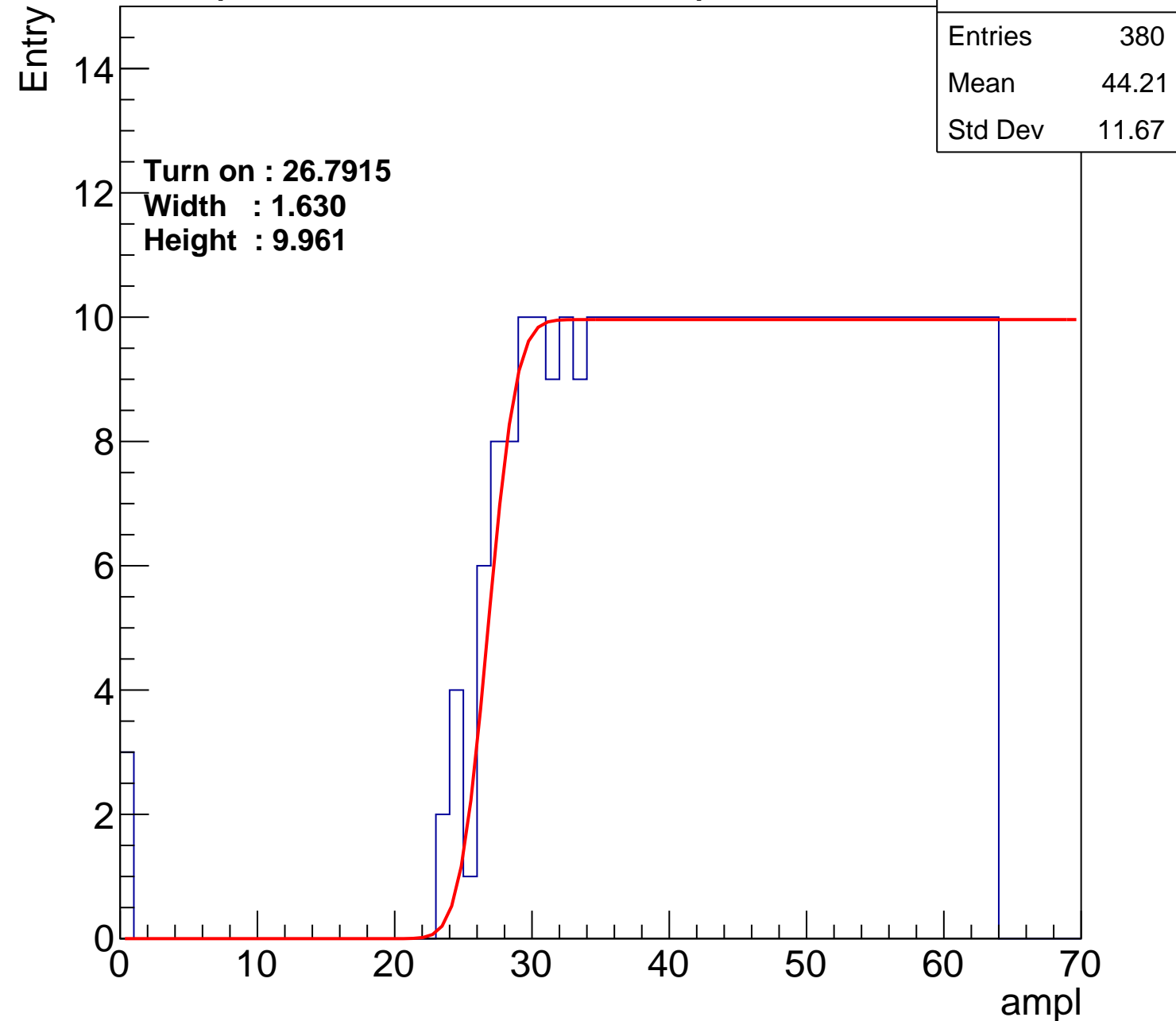
Width : 1.630

Height : 9.961

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch111

calib\_packv5\_042523\_0143.root, FC#4, port A2

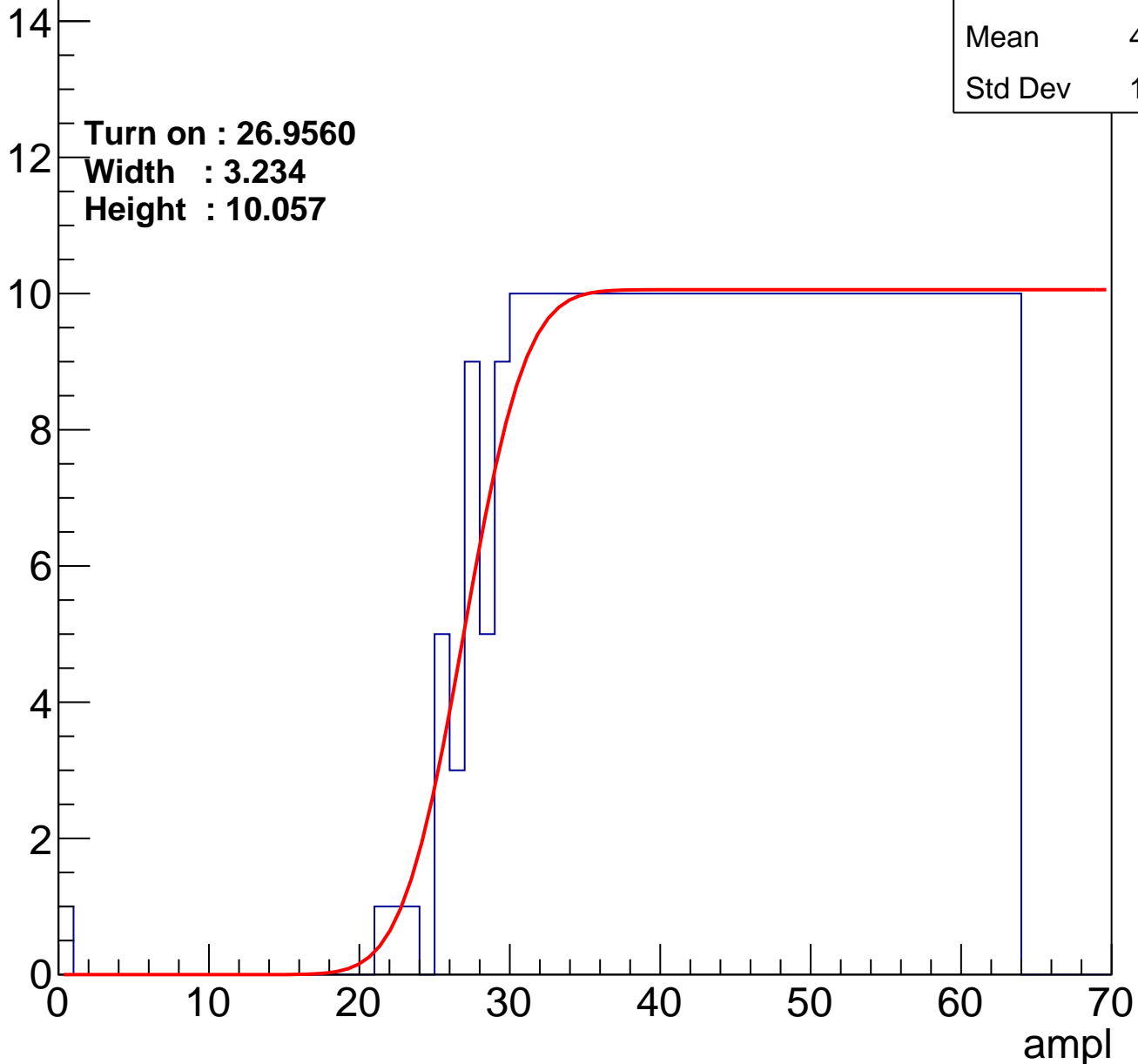
Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.9560

Width : 3.234

Height : 10.057

Entry



# B1L100S, U26-ch112

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	377
Mean	44.46
Std Dev	11.28

Turn on : 26.4666

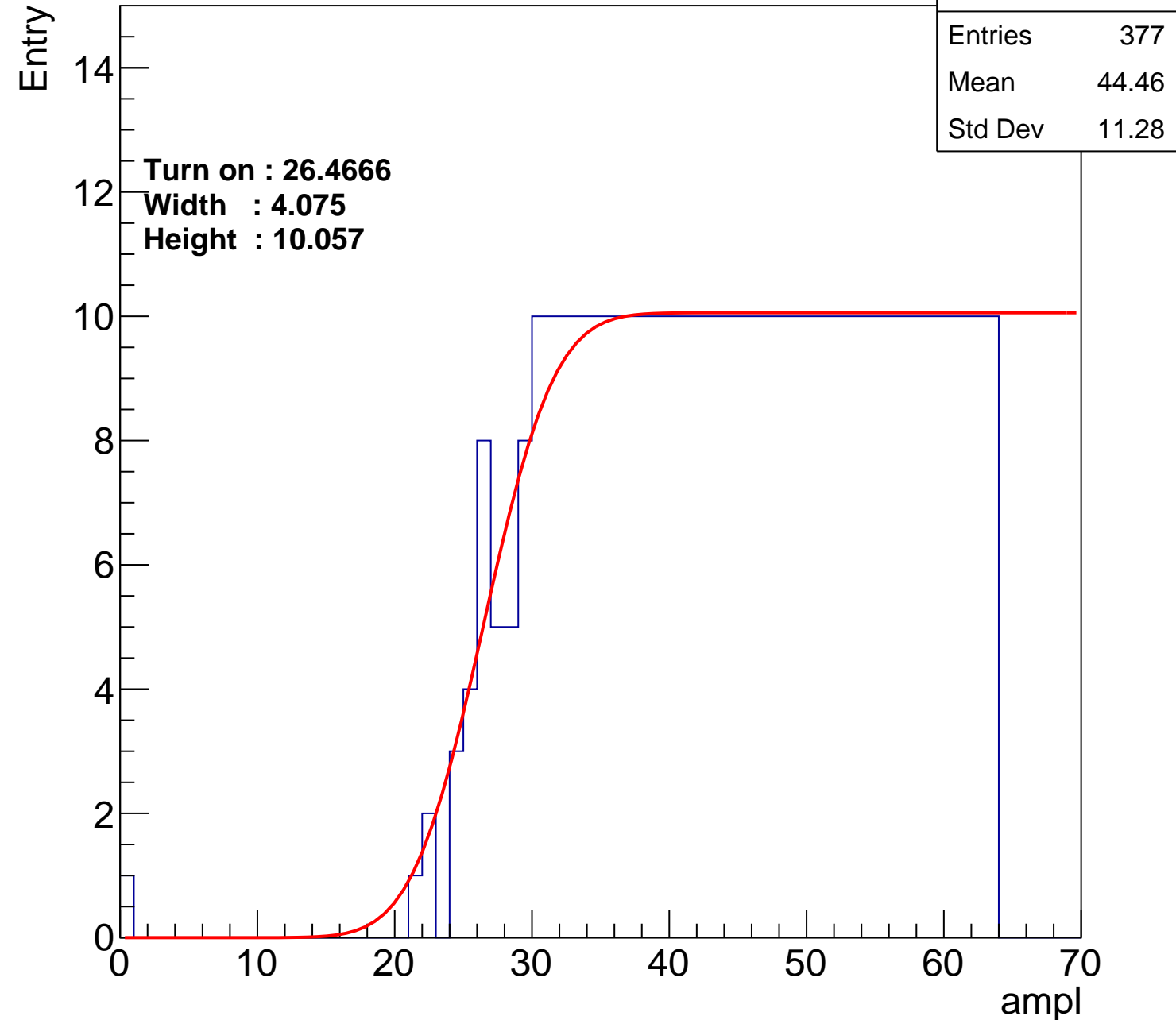
Width : 4.075

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch113

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	344
Mean	45.99
Std Dev	10.68

Turn on : 30.0983

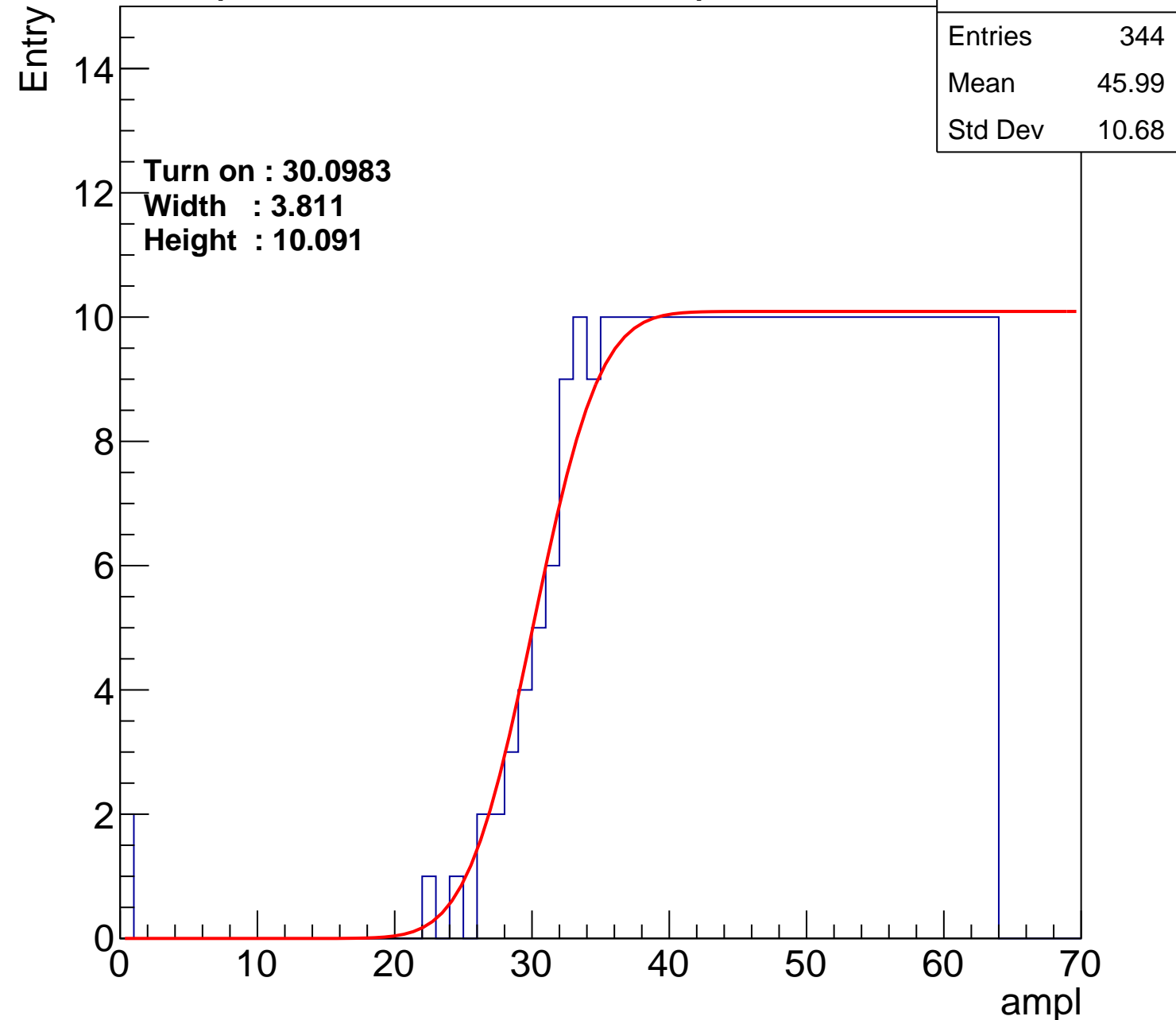
Width : 3.811

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch114

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	374
Mean	44.34
Std Dev	11.94

Turn on : 27.6954

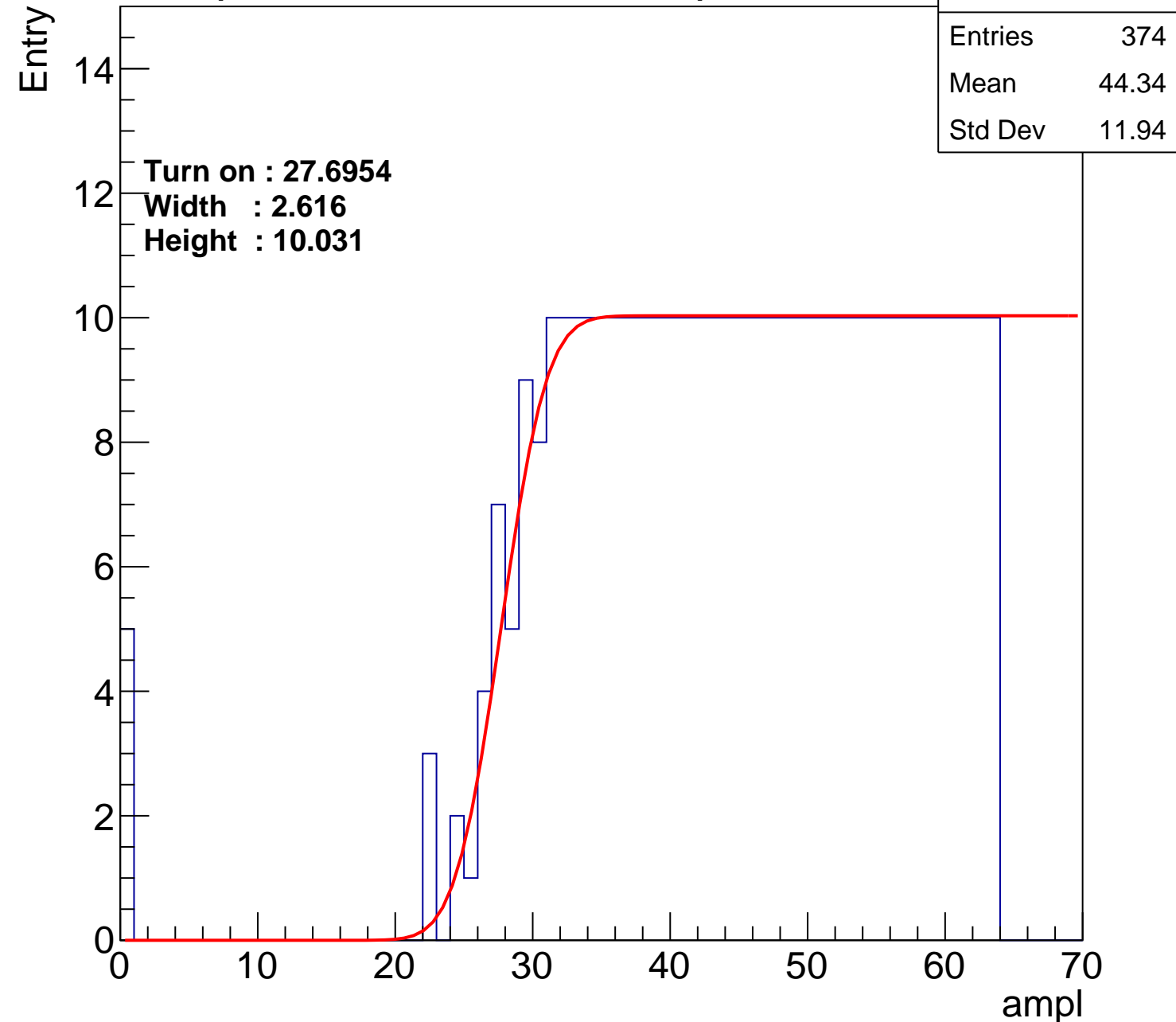
Width : 2.616

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch115

calib\_packv5\_042523\_0143.root, FC#4, port A2

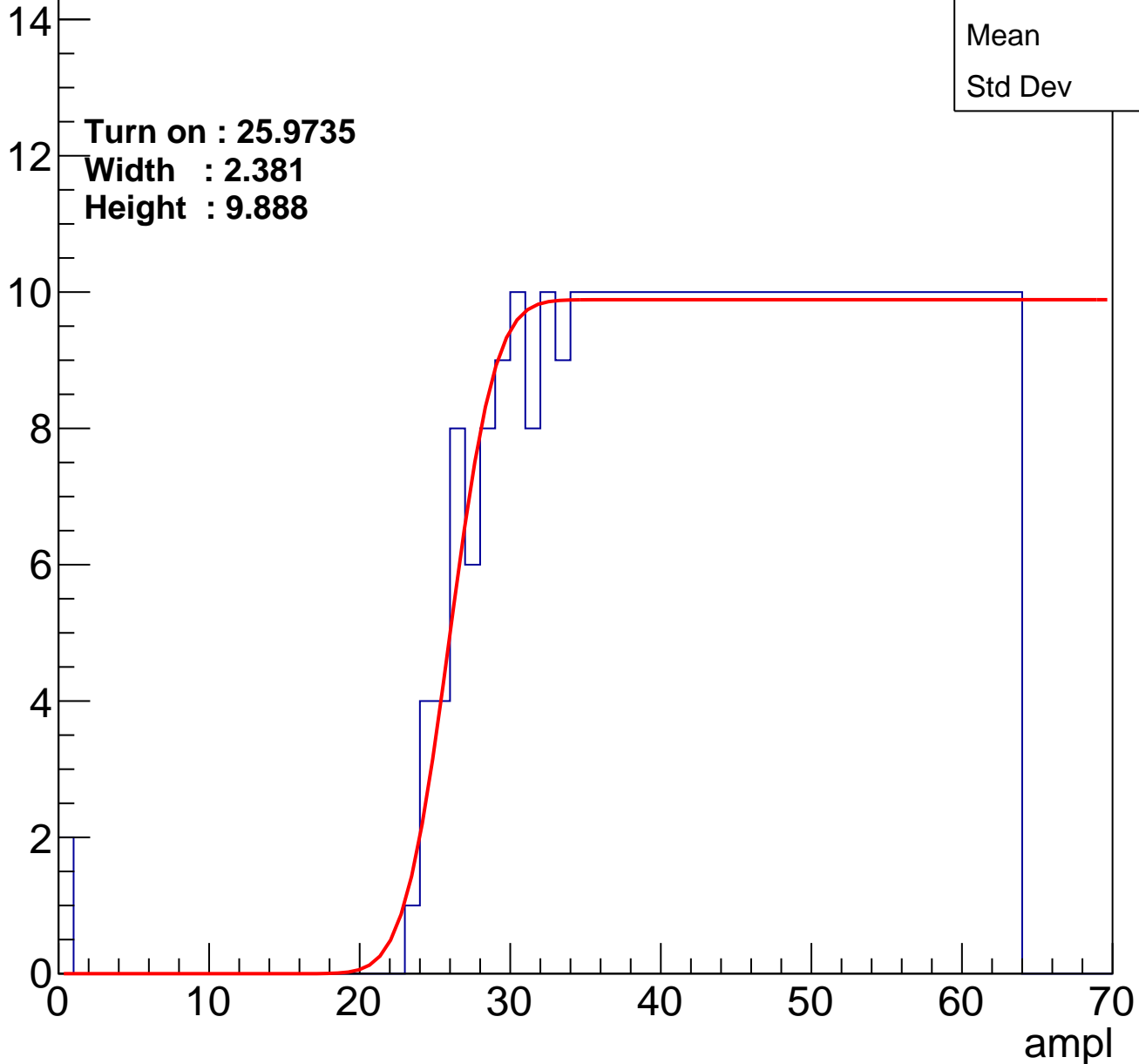
Entries	379
Mean	44.3
Std Dev	11.5

Turn on : 25.9735

Width : 2.381

Height : 9.888

Entry



# B1L100S, U26-ch116

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	372
Mean	44.77
Std Dev	11.04

Turn on : 27.2687

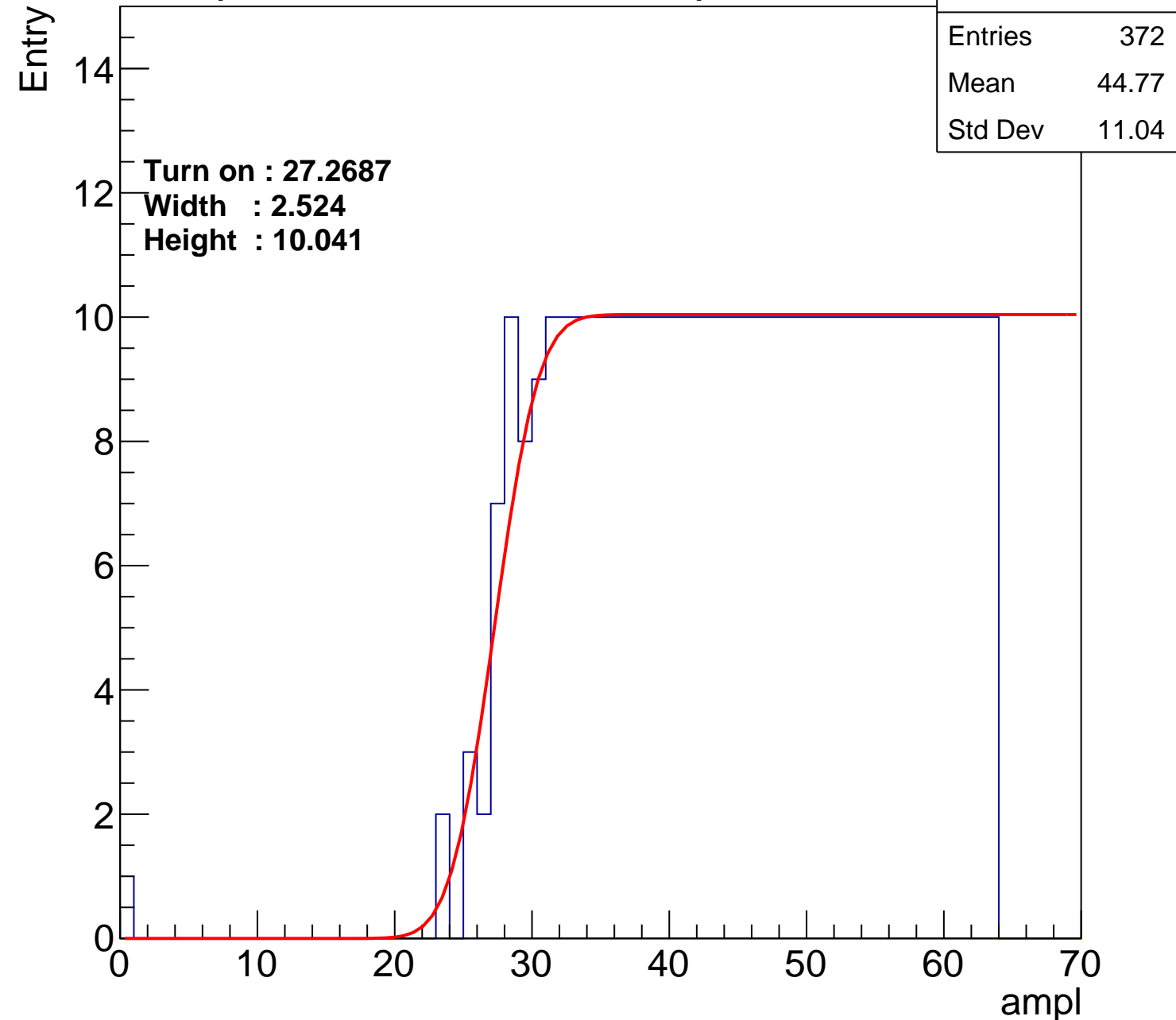
Width : 2.524

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch117

calib\_packv5\_042523\_0143.root, FC#4, port A2

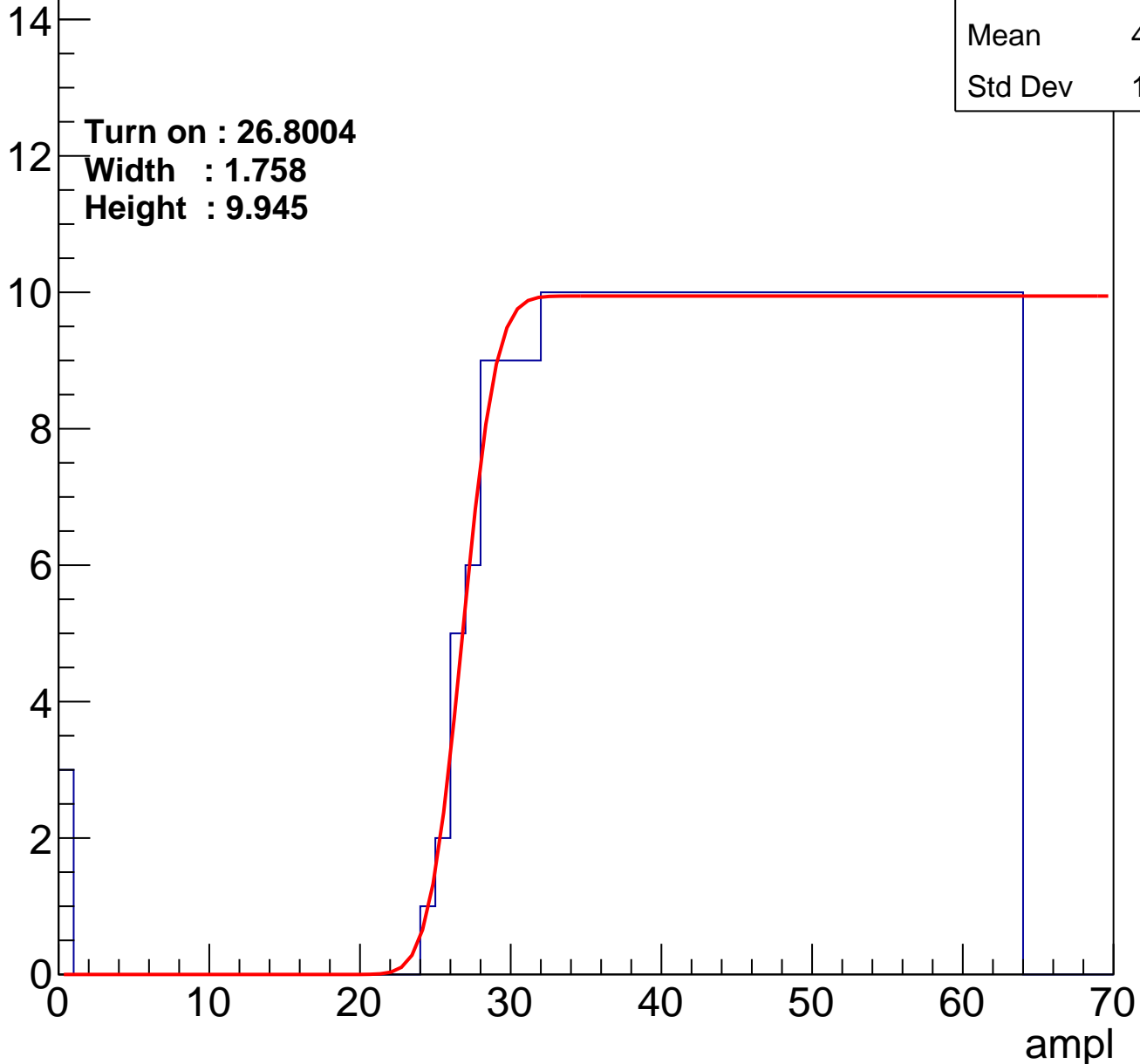
Entries	373
Mean	44.58
Std Dev	11.46

**Turn on : 26.8004**

**Width : 1.758**

**Height : 9.945**

Entry



# B1L100S, U26-ch118

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	376
Mean	44.5
Std Dev	11.26

Turn on : 26.1597

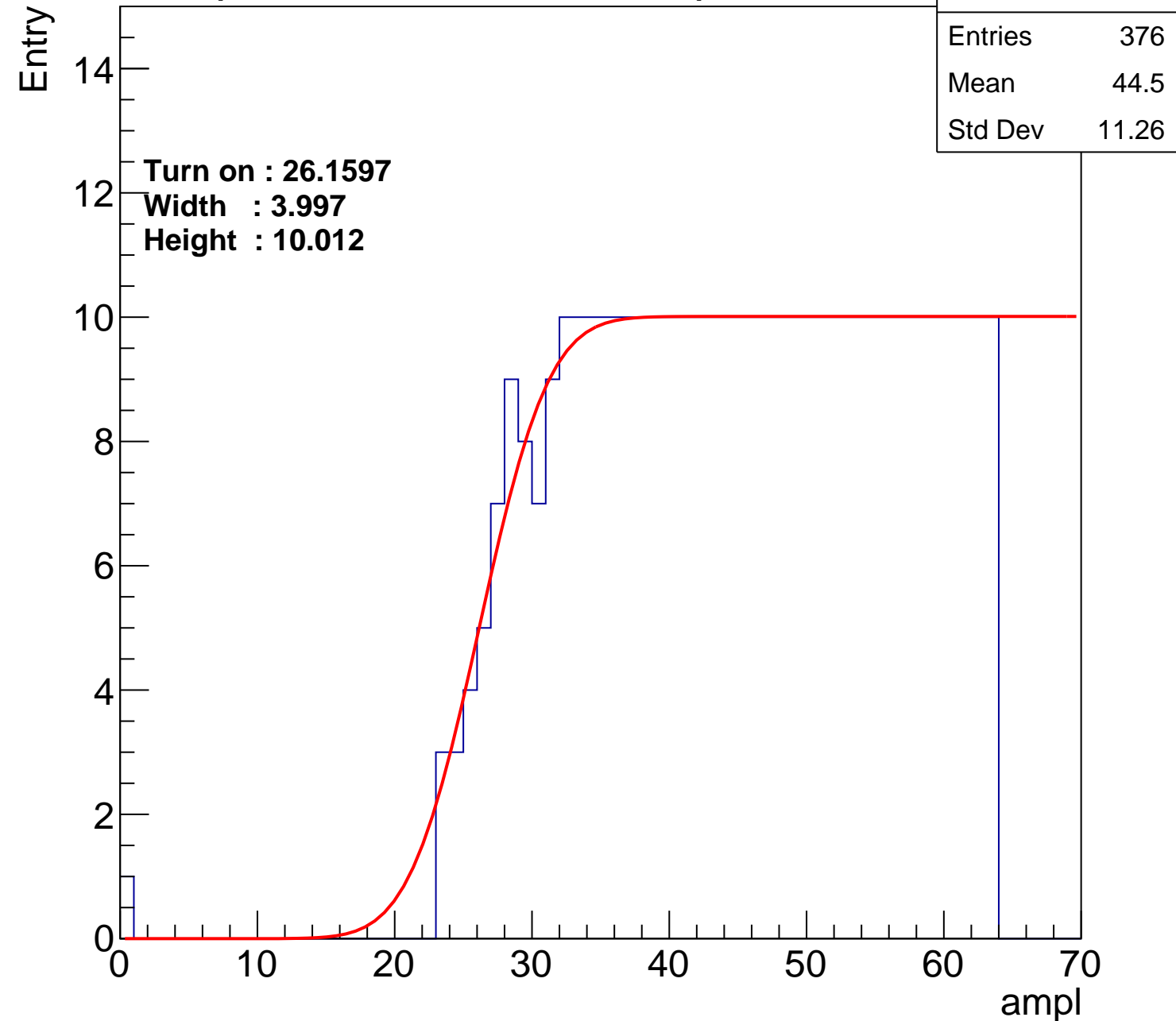
Width : 3.997

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L100S, U26-ch119

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	356
Mean	45.49
Std Dev	10.73

Turn on : 28.6156

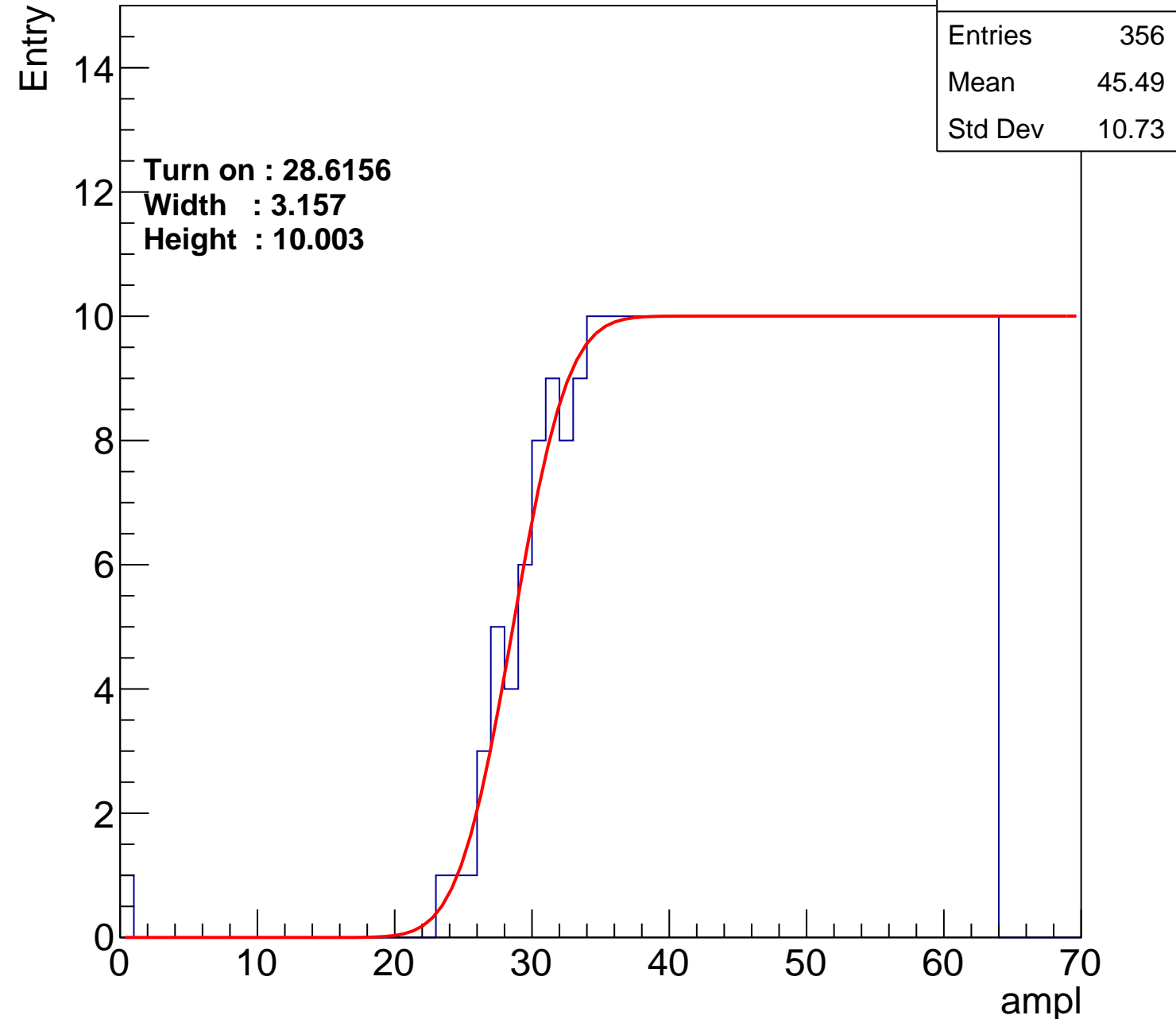
Width : 3.157

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch120

calib\_packv5\_042523\_0143.root, FC#4, port A2

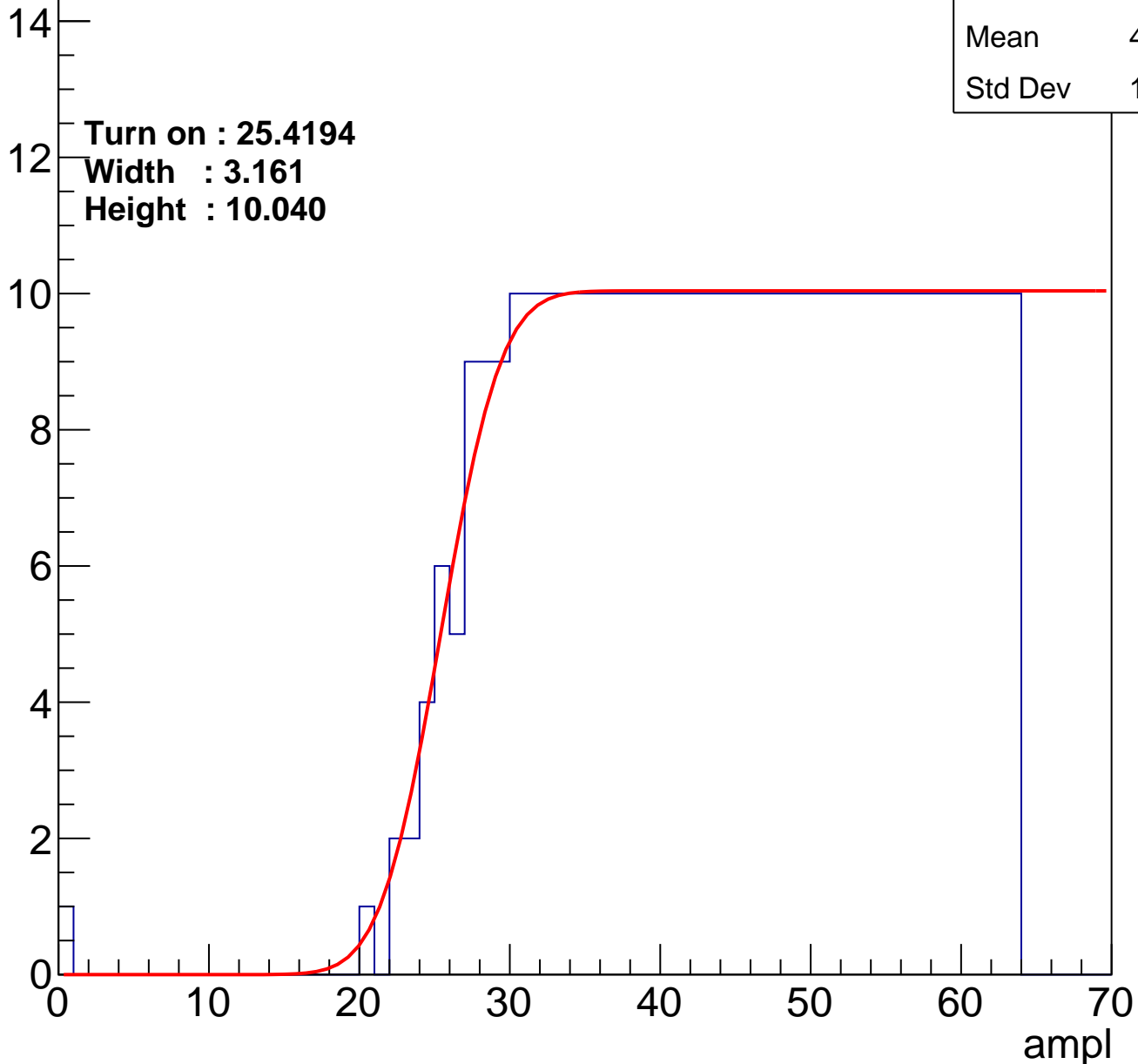
Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 25.4194

Width : 3.161

Height : 10.040

Entry



# B1L100S, U26-ch121

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	370
Mean	44.69
Std Dev	11.37

Turn on : 27.8781

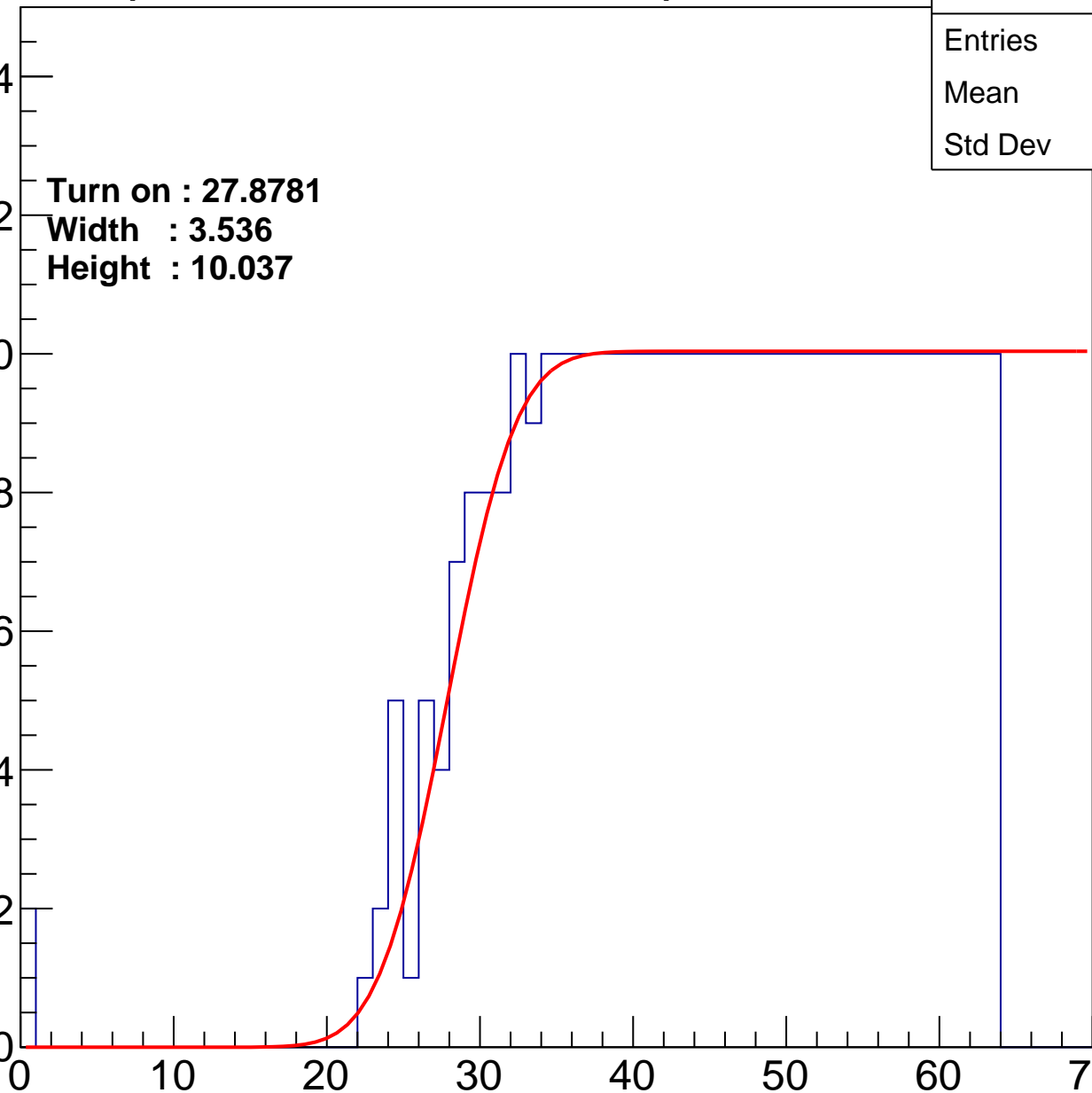
Width : 3.536

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch122

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	371
Mean	44.6
Std Dev	11.53

Turn on : 27.4650

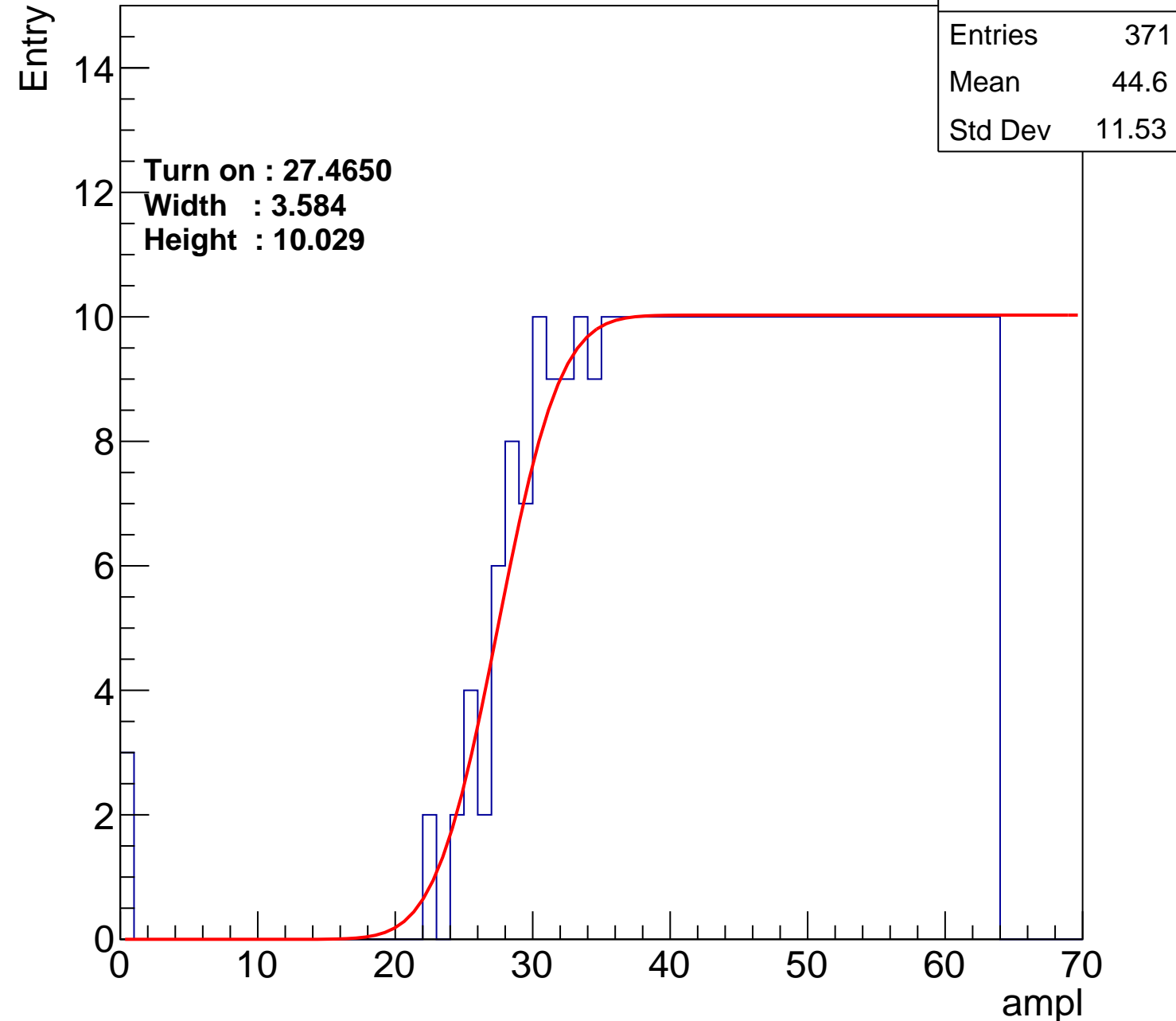
Width : 3.584

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch123

calib\_packv5\_042523\_0143.root, FC#4, port A2

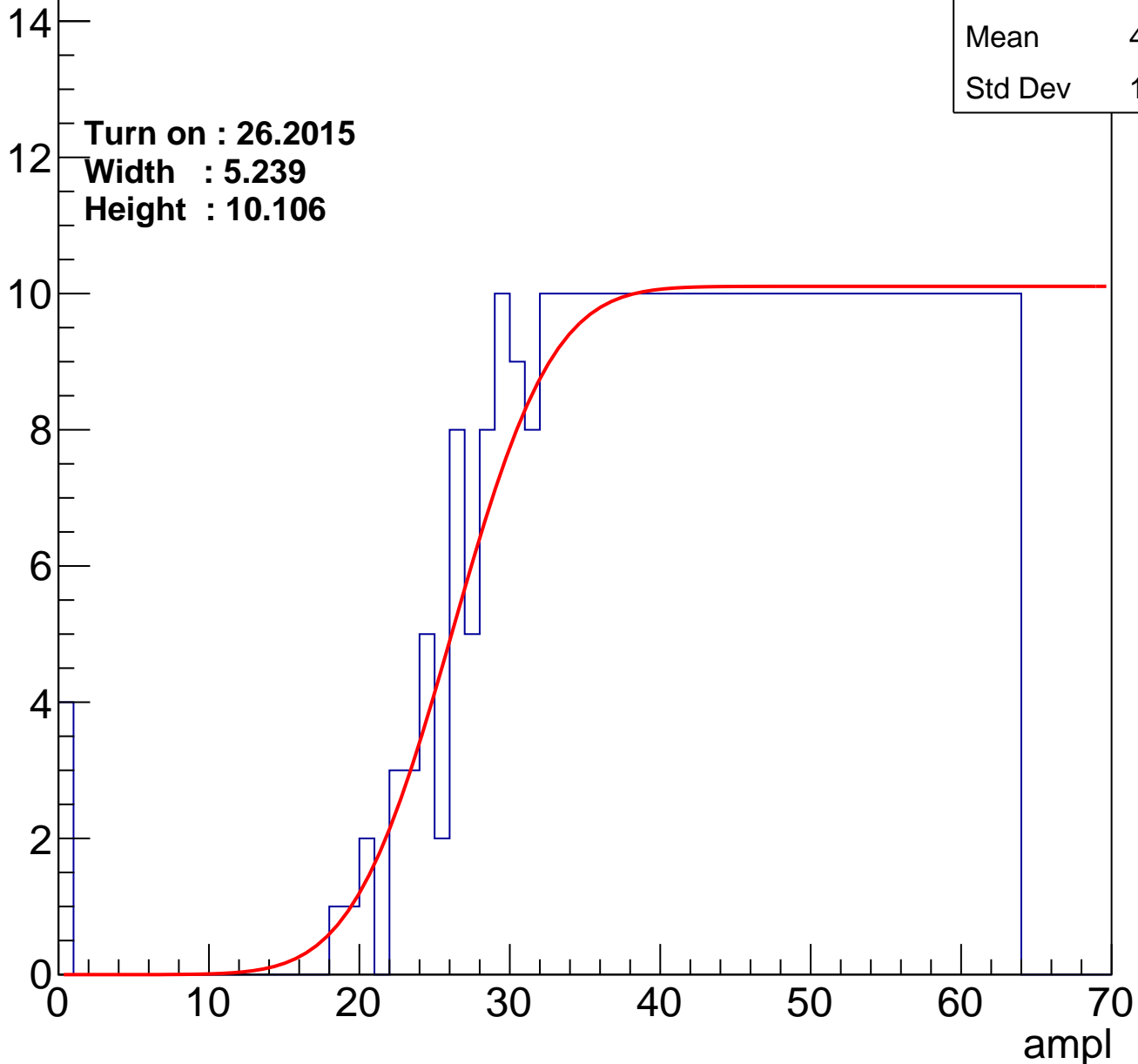
Entries	389
Mean	43.59
Std Dev	12.24

Turn on : 26.2015

Width : 5.239

Height : 10.106

Entry



# B1L100S, U26-ch124

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 26.7817

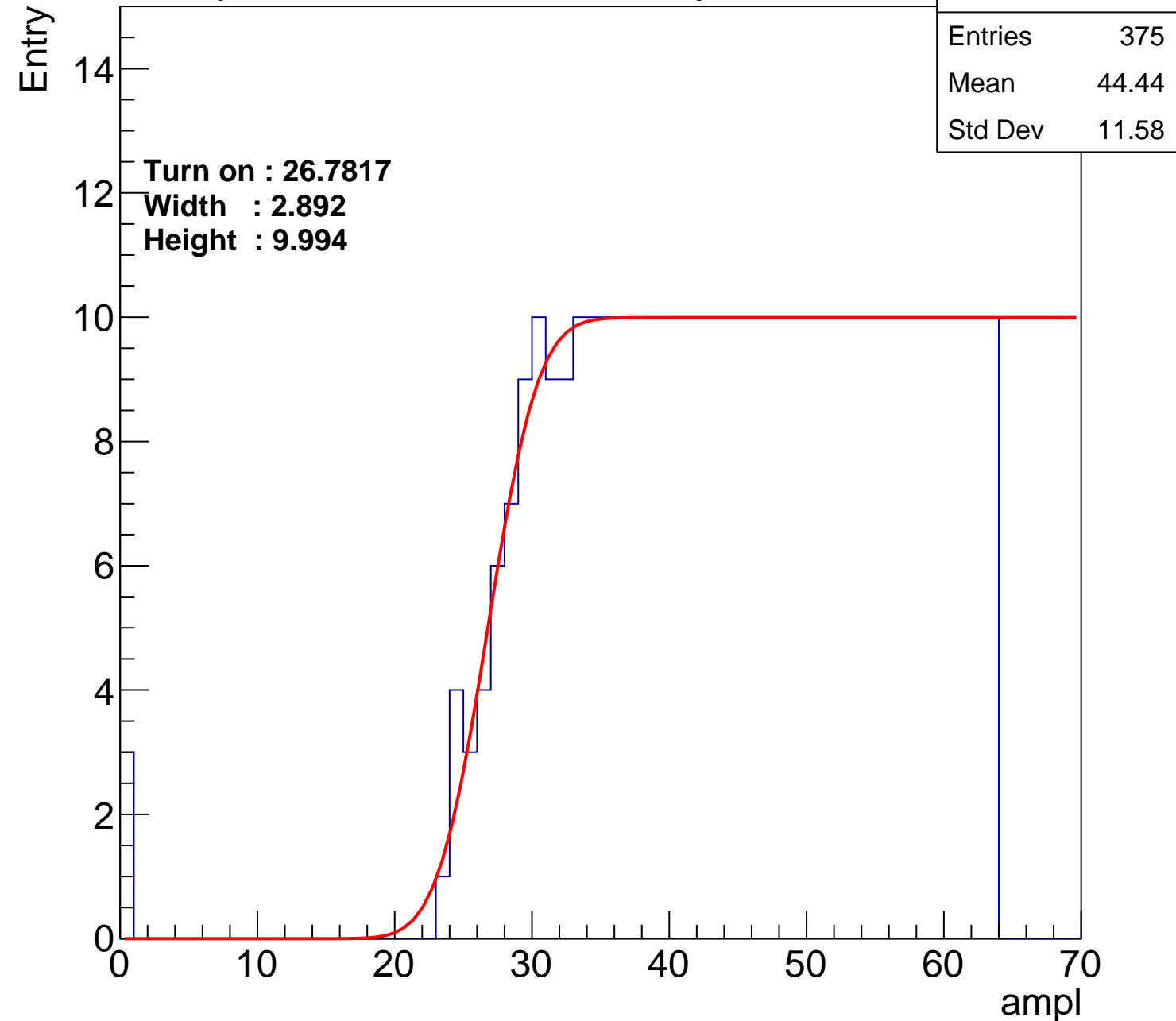
Width : 2.892

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch125

calib\_packv5\_042523\_0143.root, FC#4, port A2

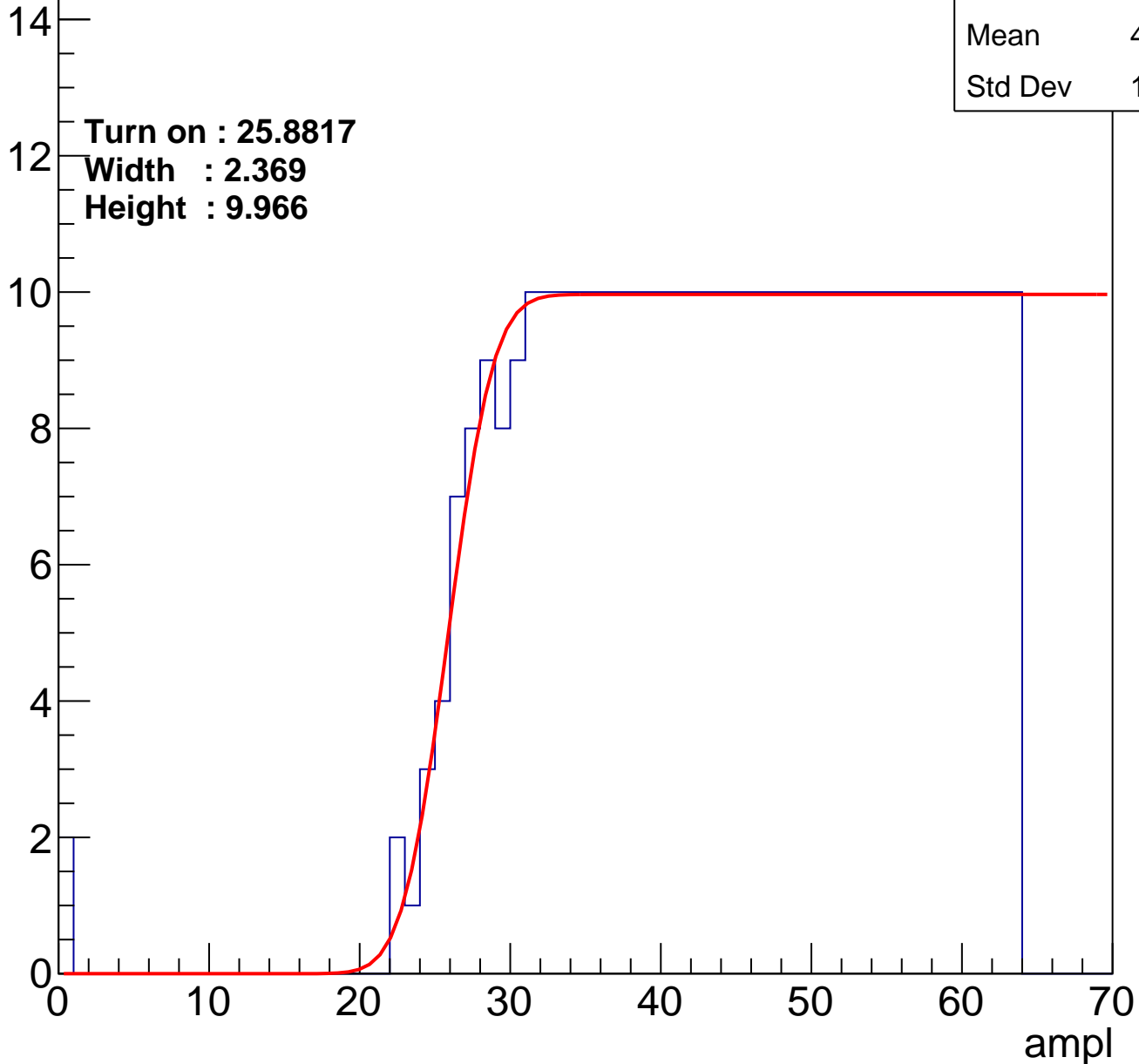
Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.8817

Width : 2.369

Height : 9.966

Entry



# B1L100S, U26-ch126

calib\_packv5\_042523\_0143.root, FC#4, port A2

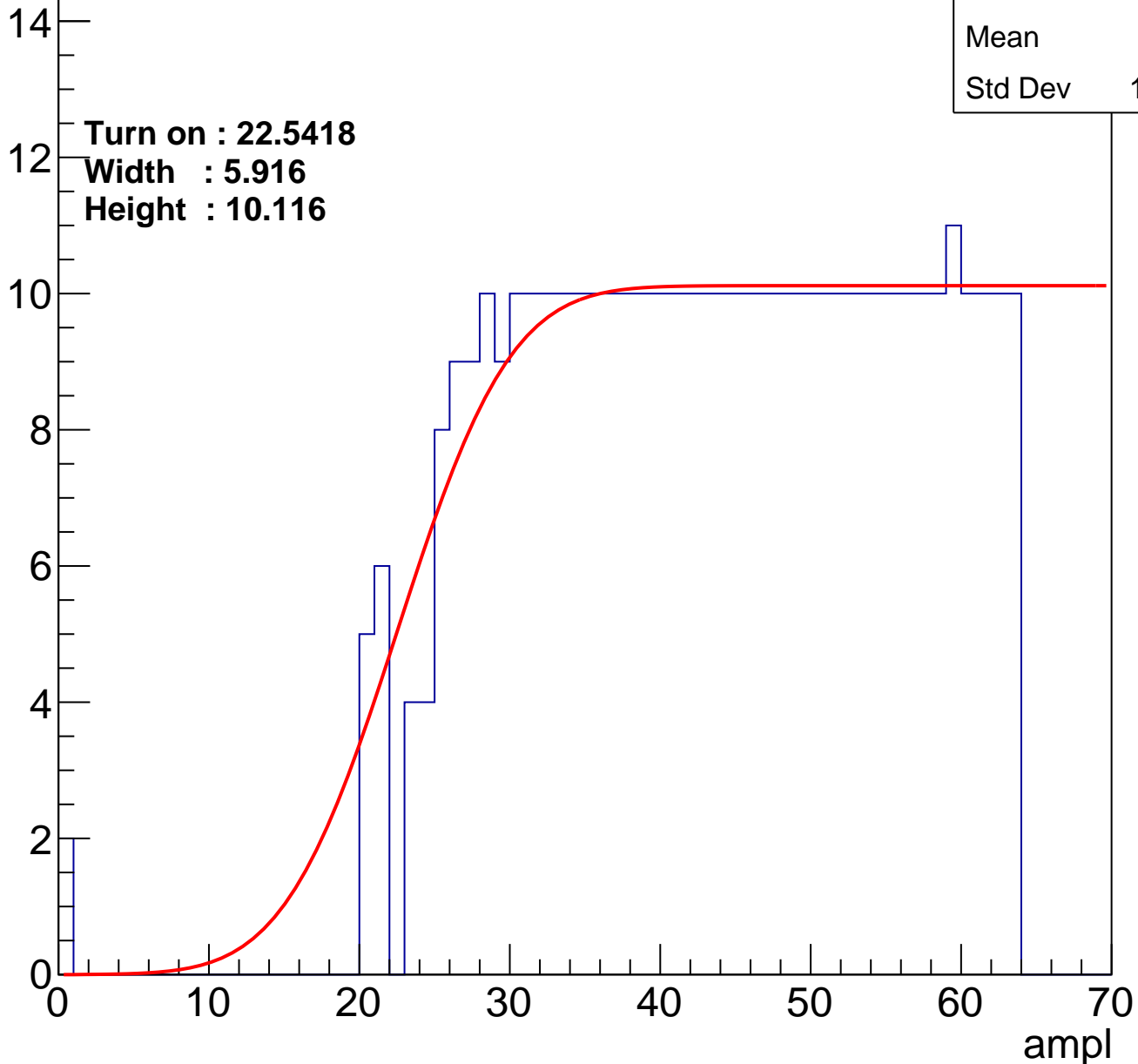
Entries	407
Mean	43
Std Dev	12.24

Turn on : 22.5418

Width : 5.916

Height : 10.116

Entry





# B1L100S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.09
Std Dev	11.93

Turn on : 26.8159

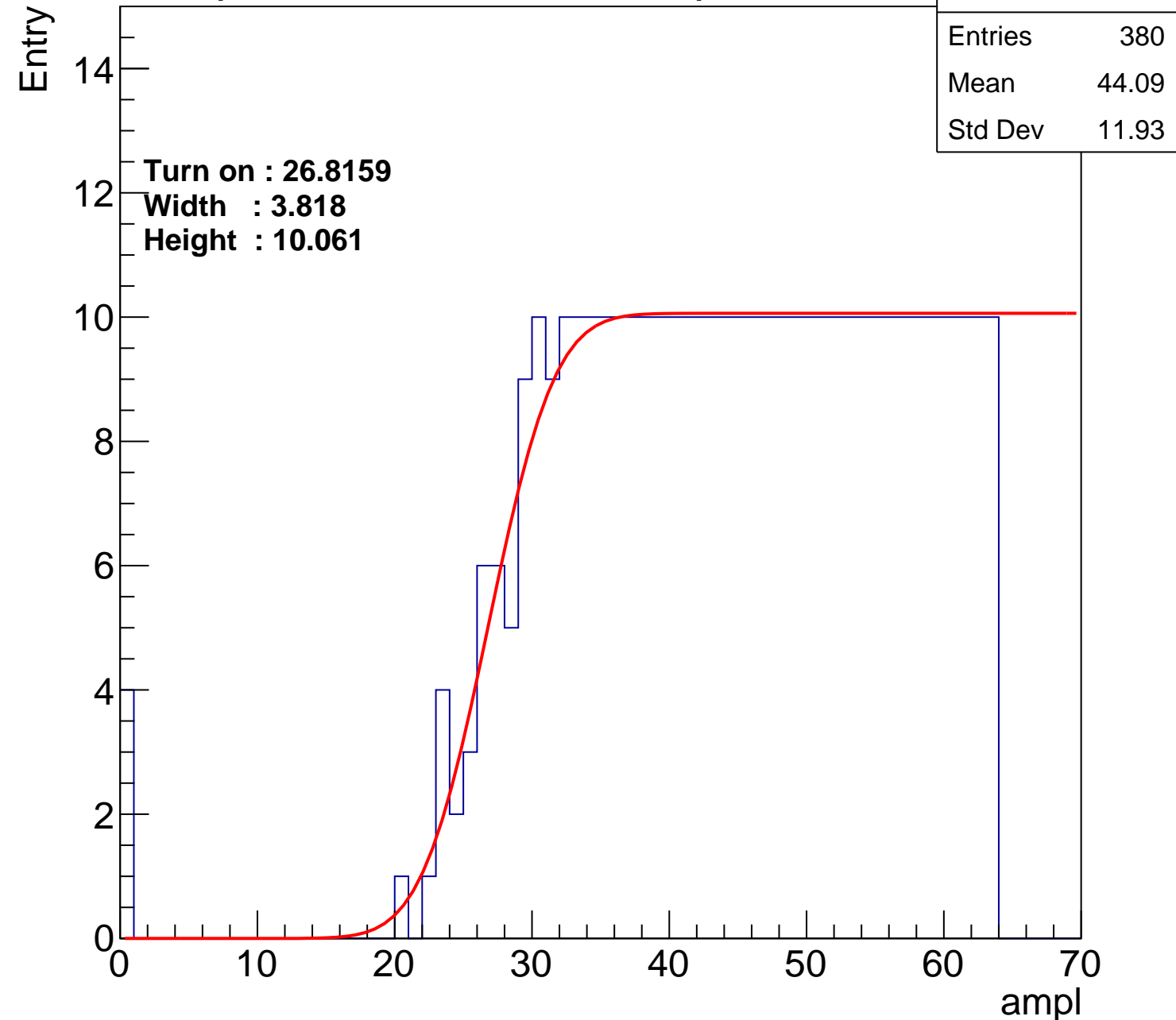
Width : 3.818

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L100S, U26-ch127

calib\_packv5\_042523\_0143.root, FC#4, port A2

Entries	380
Mean	44.09
Std Dev	11.93

Turn on : 26.8159

Width : 3.818

Height : 10.061

Entry

