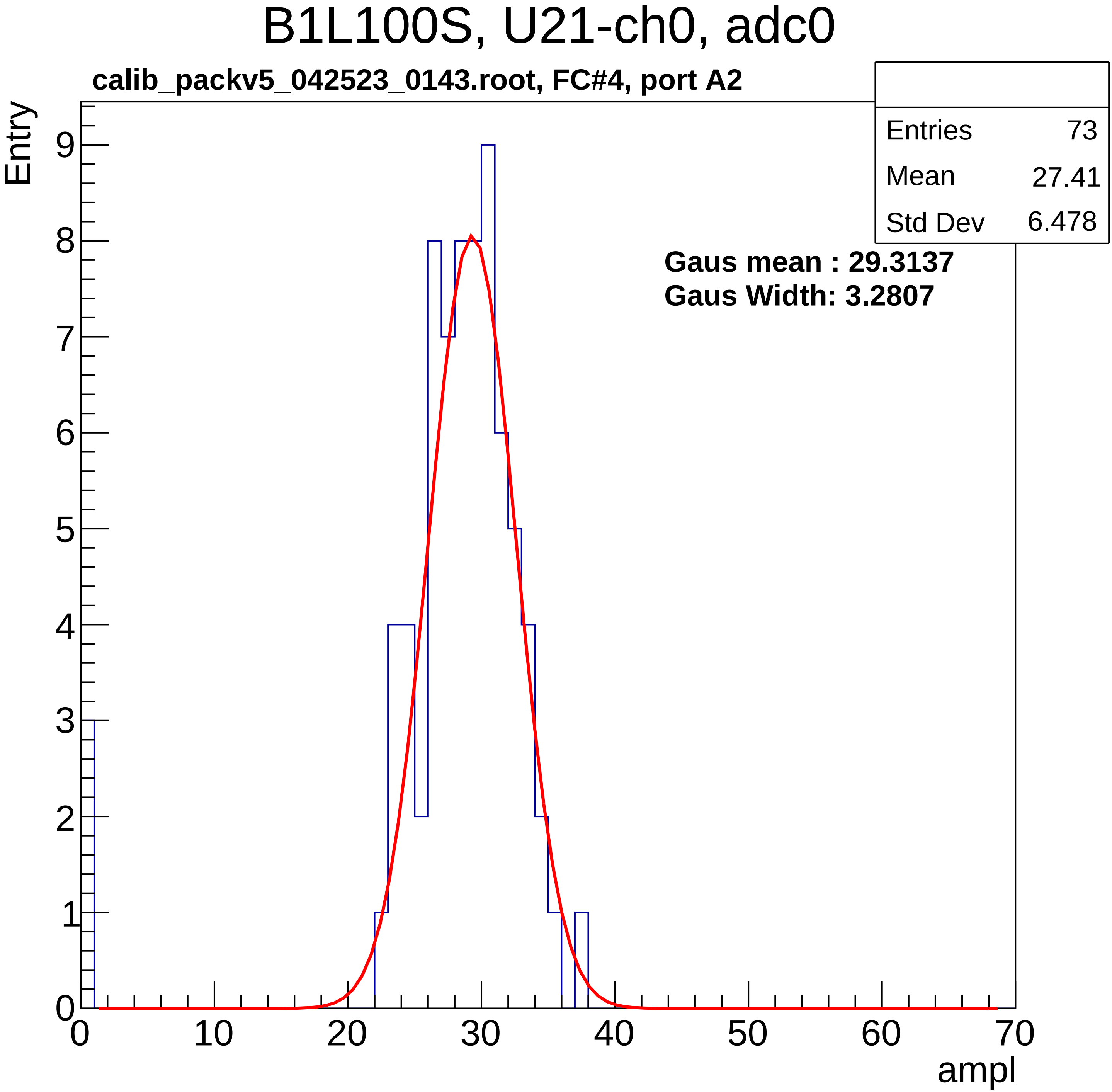
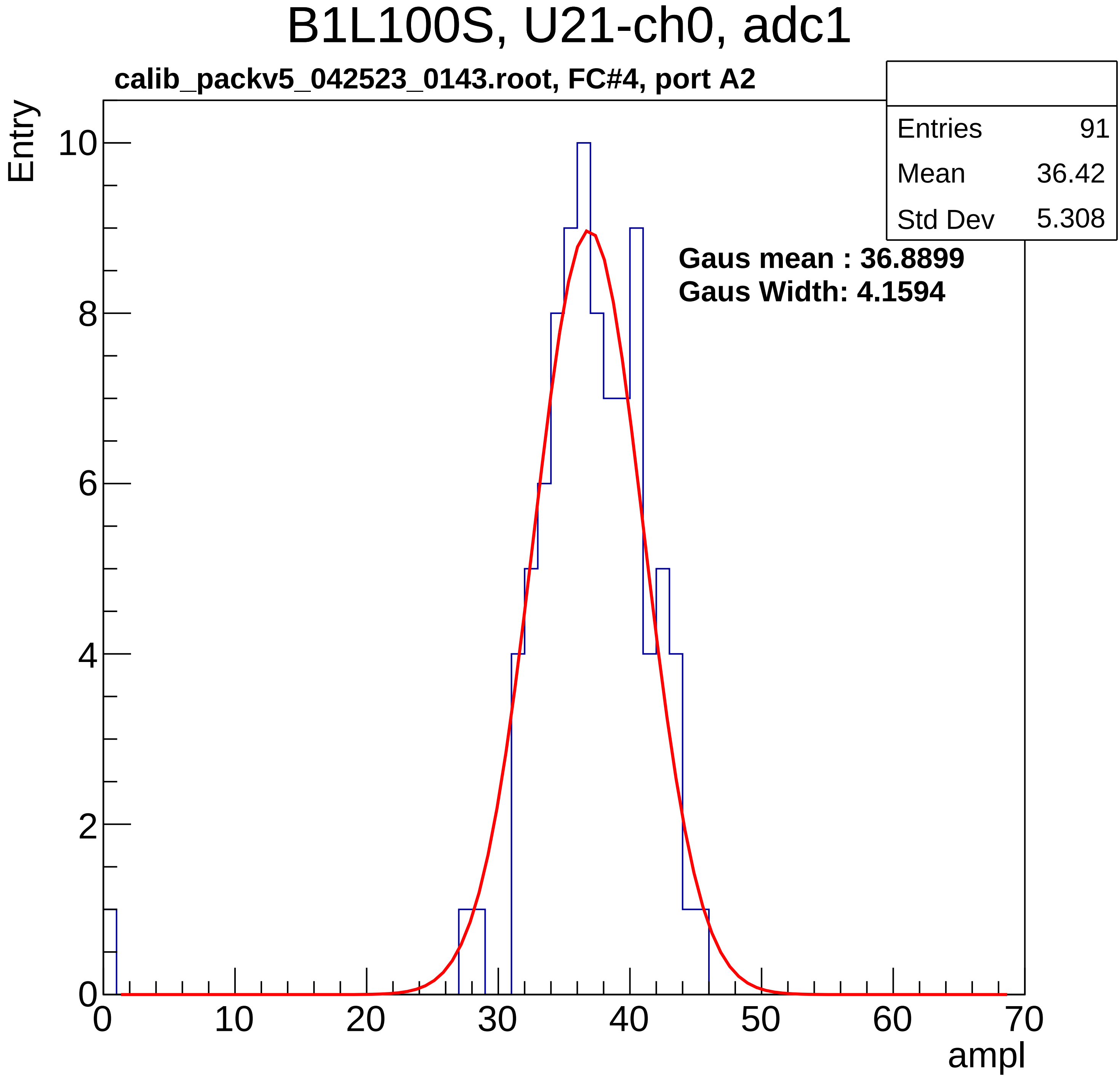
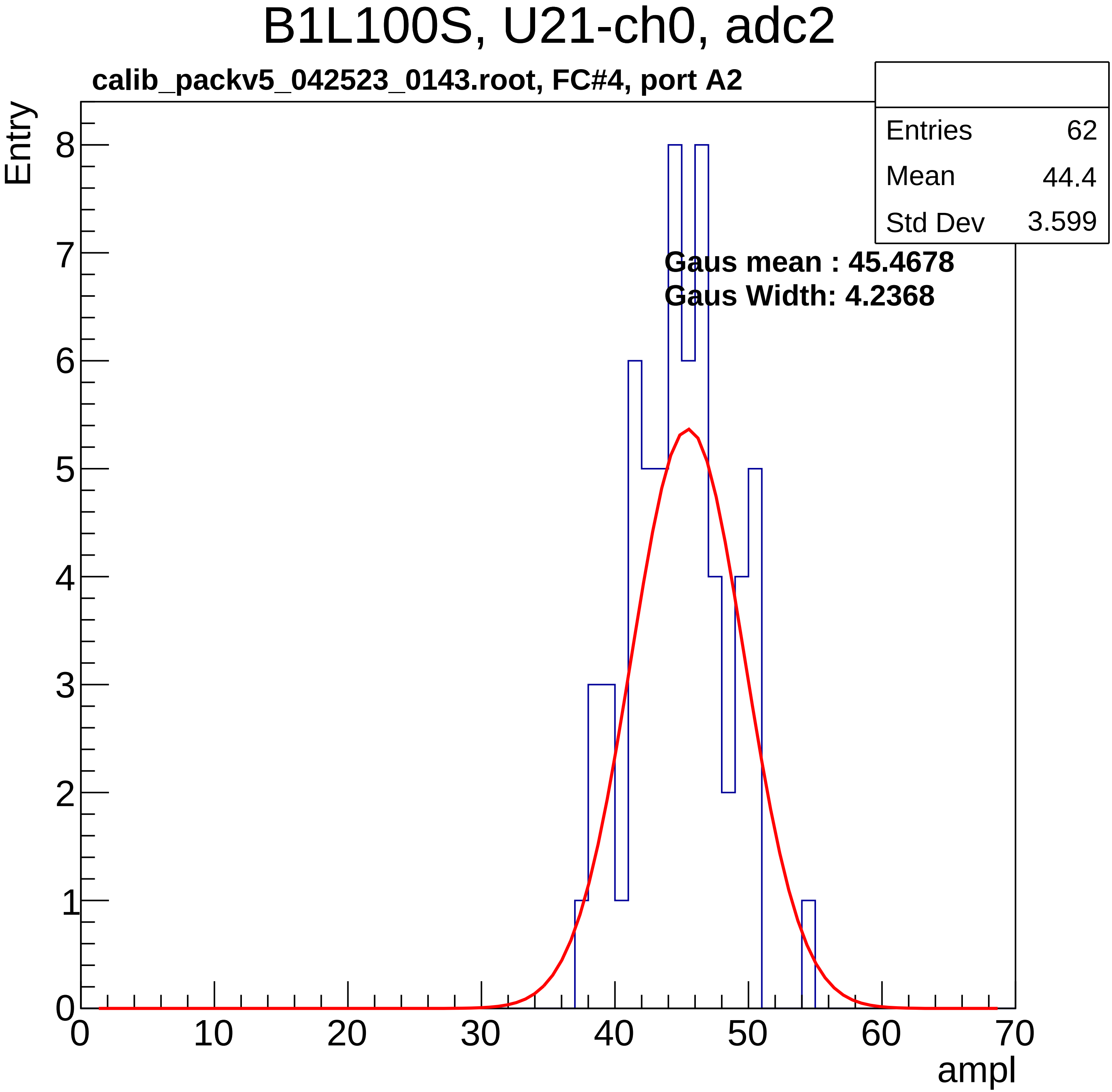
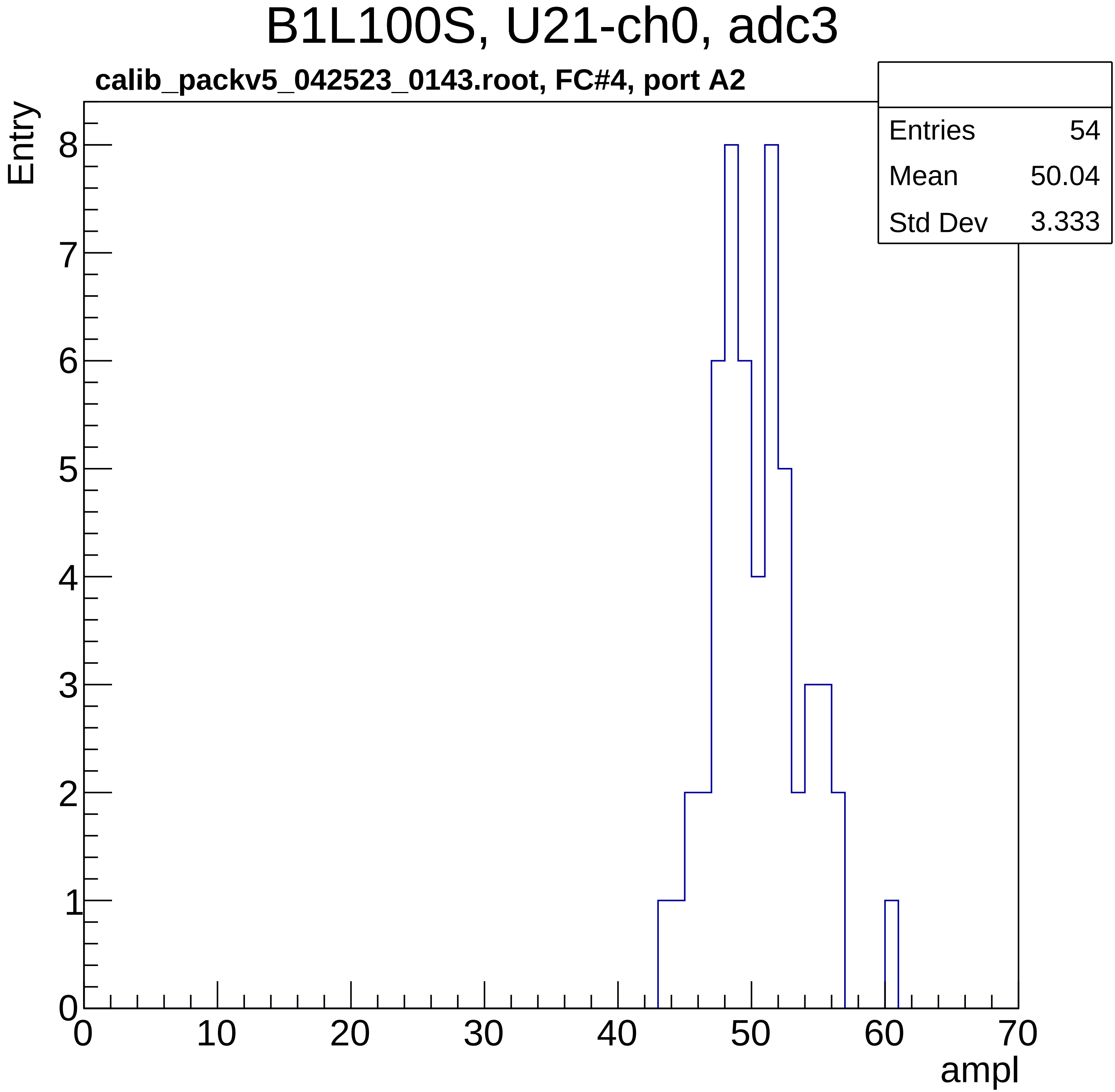
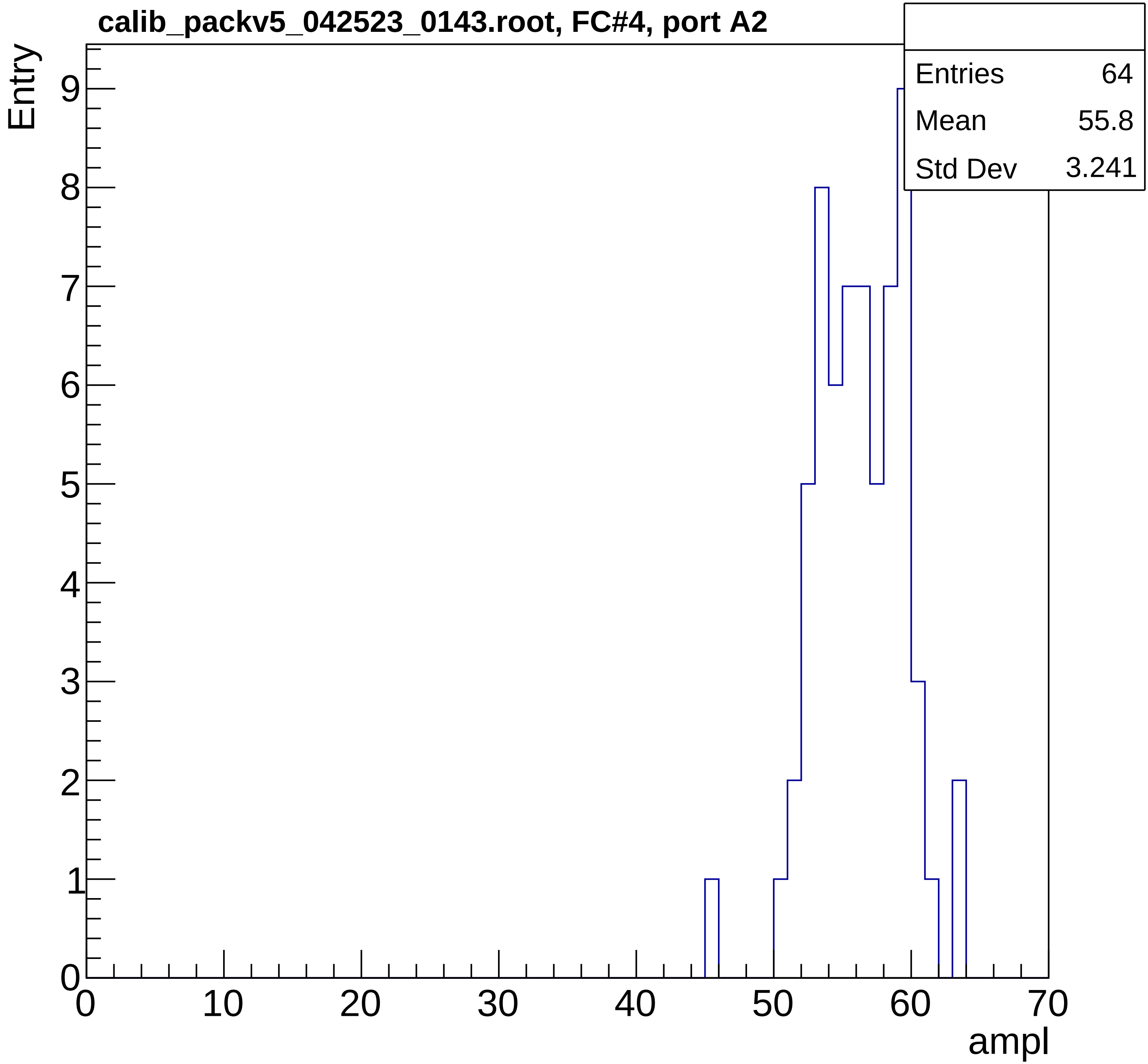

B1L100S, U21-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 27.41
Std Dev 6.478
Gaus mean : 29.3137
Gaus Width: 3.2807
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 36.42
Std Dev 5.308
Gaus mean : 36.8899
Gaus Width: 4.1594
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
Entries 62
Mean 44.4
Std Dev 3.599
Gaus mean : 45.4678
Gaus Width: 4.2368

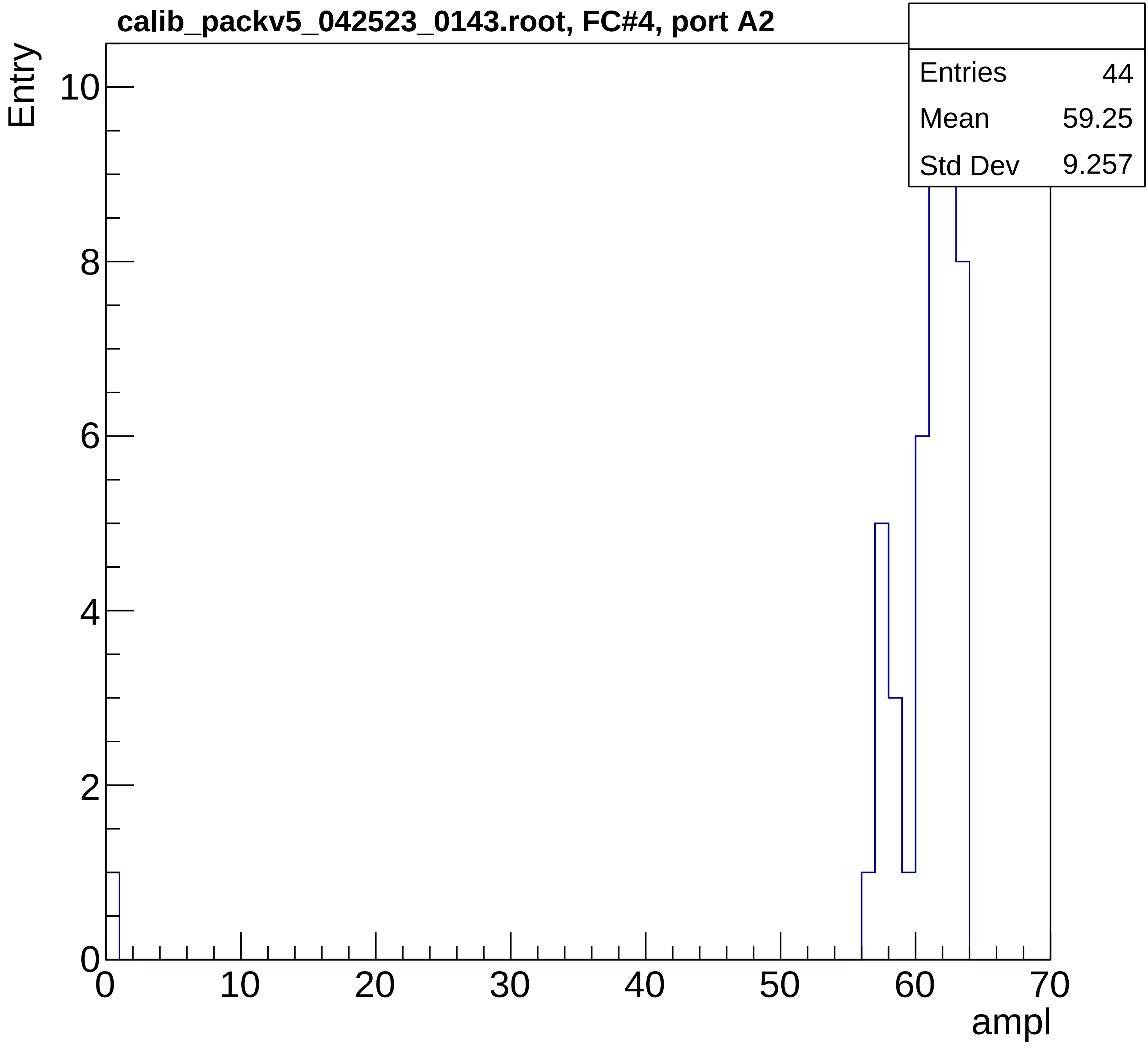

B1L100S, U21-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 50.04
Std Dev 3.333
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

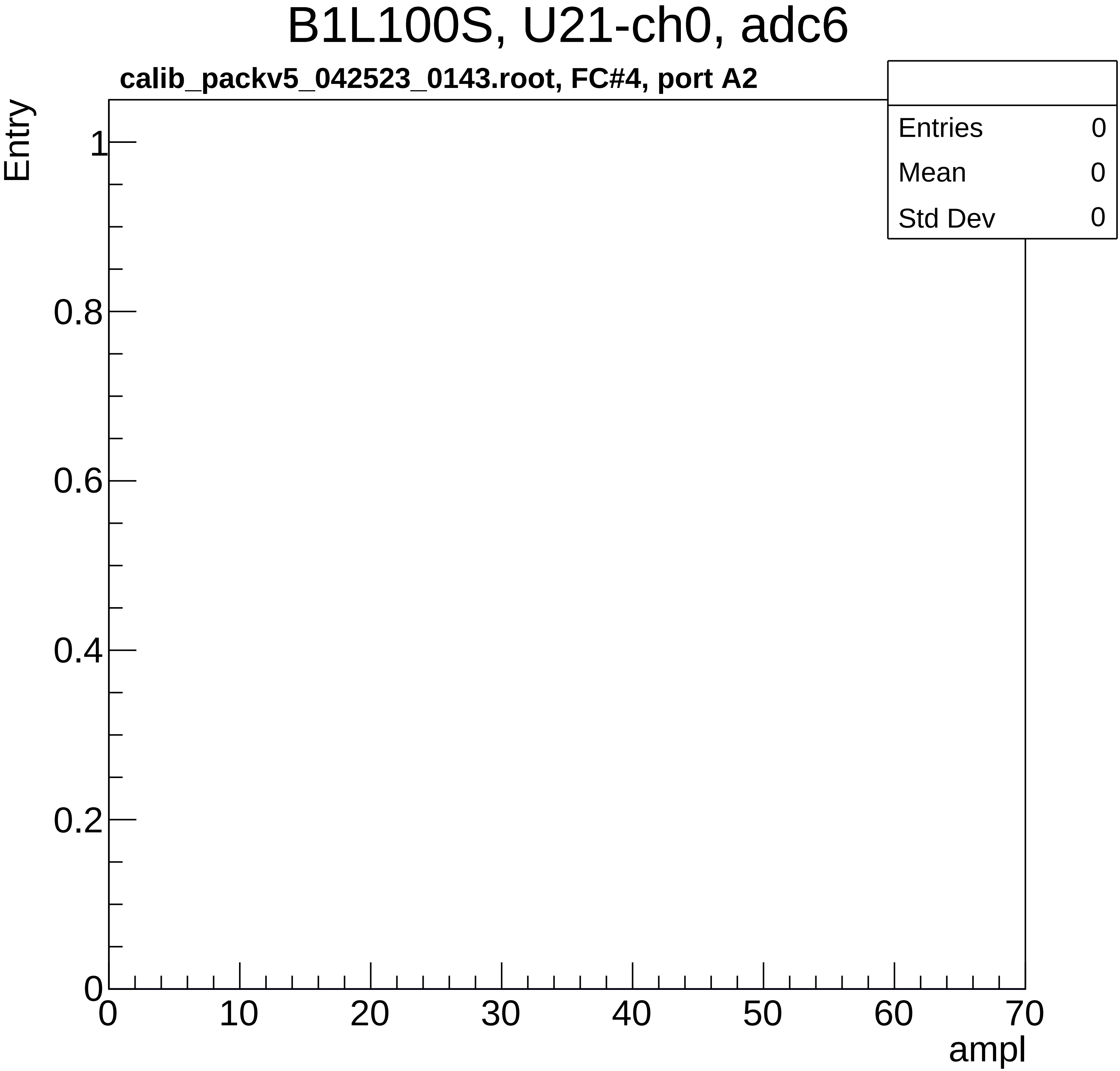
B1L100S, U21-ch0, adc4



B1L100S, U21-ch0, adc5

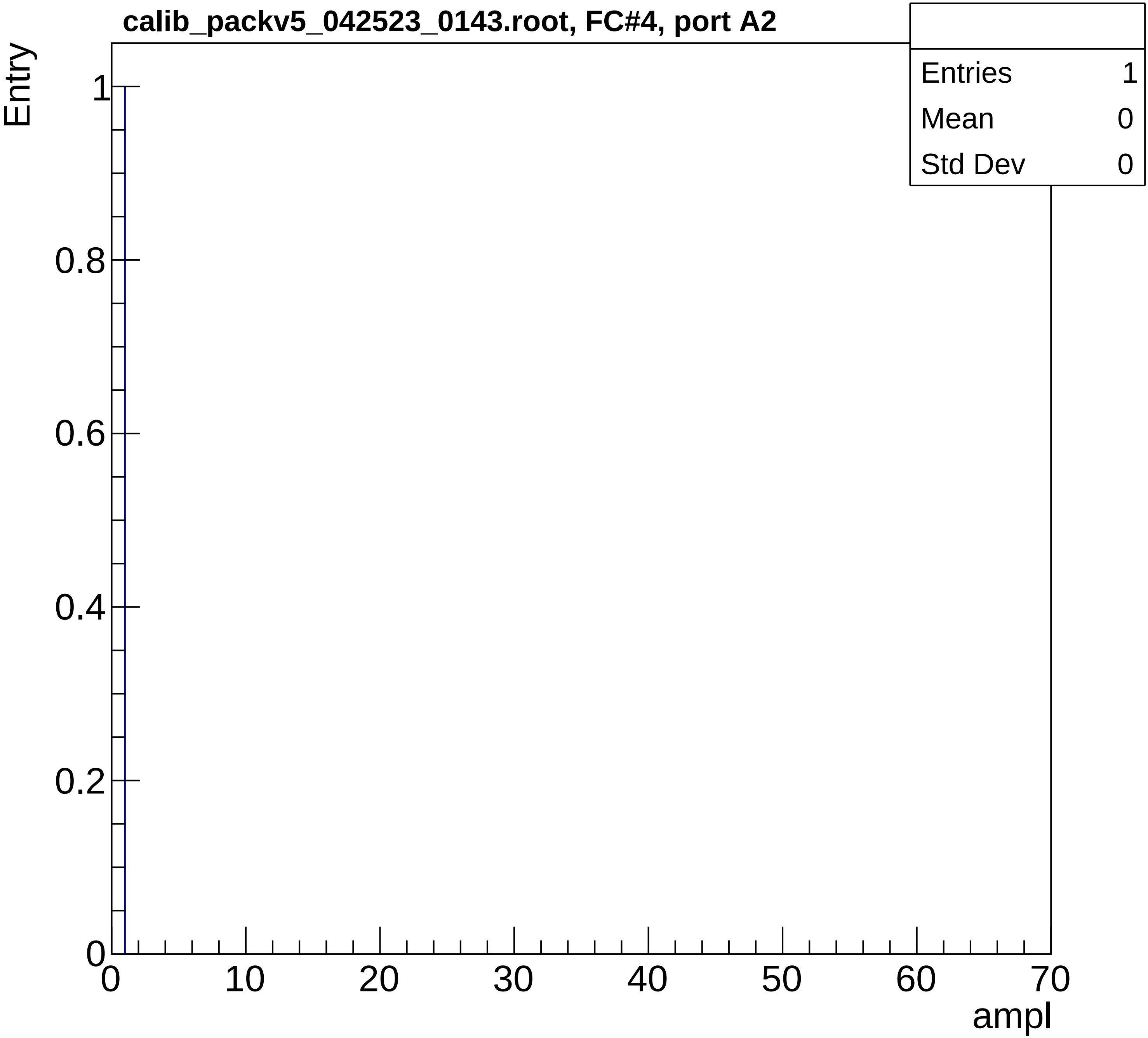
calib_packv5_042523_0143.root, FC#4, port A2

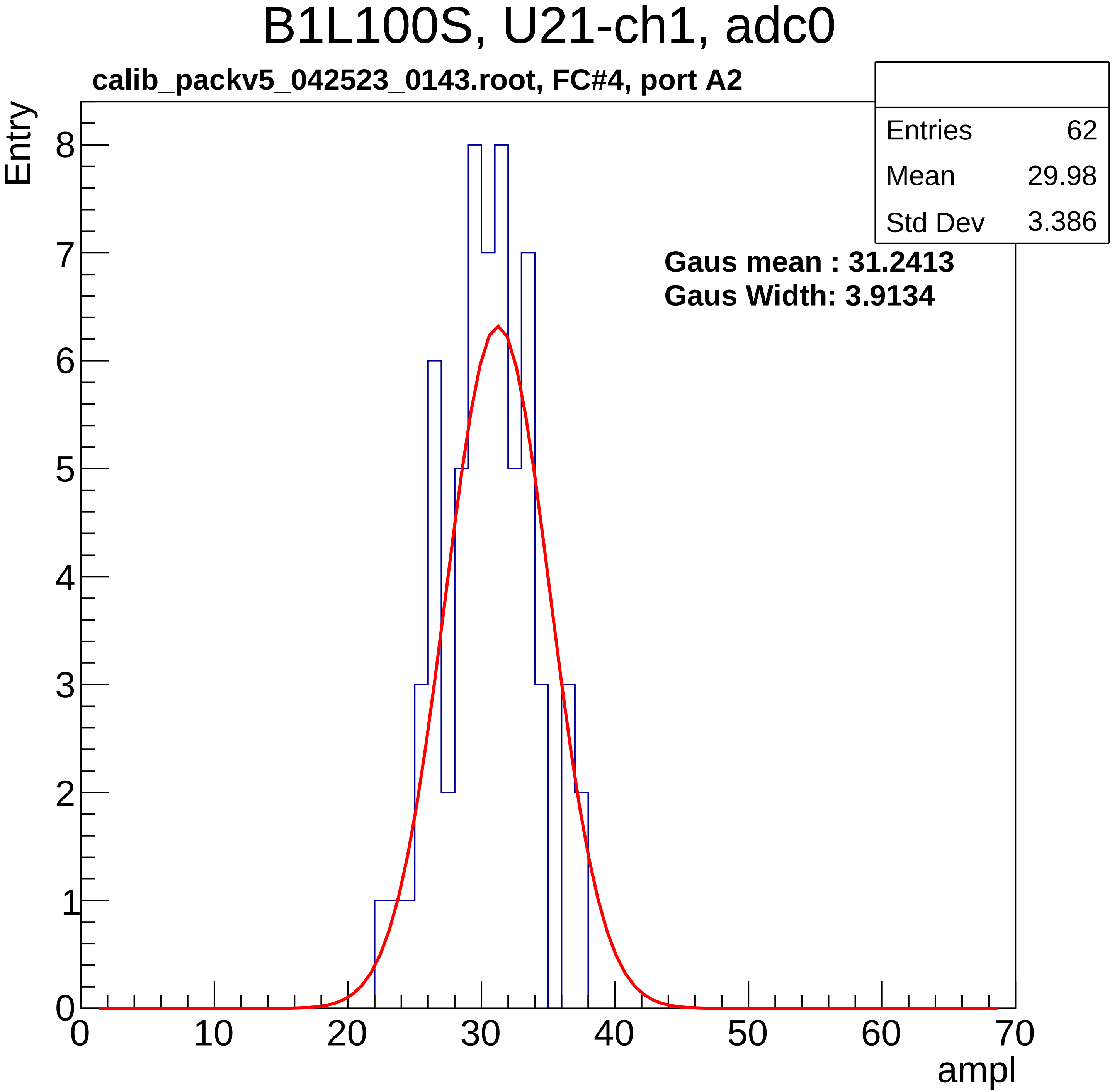


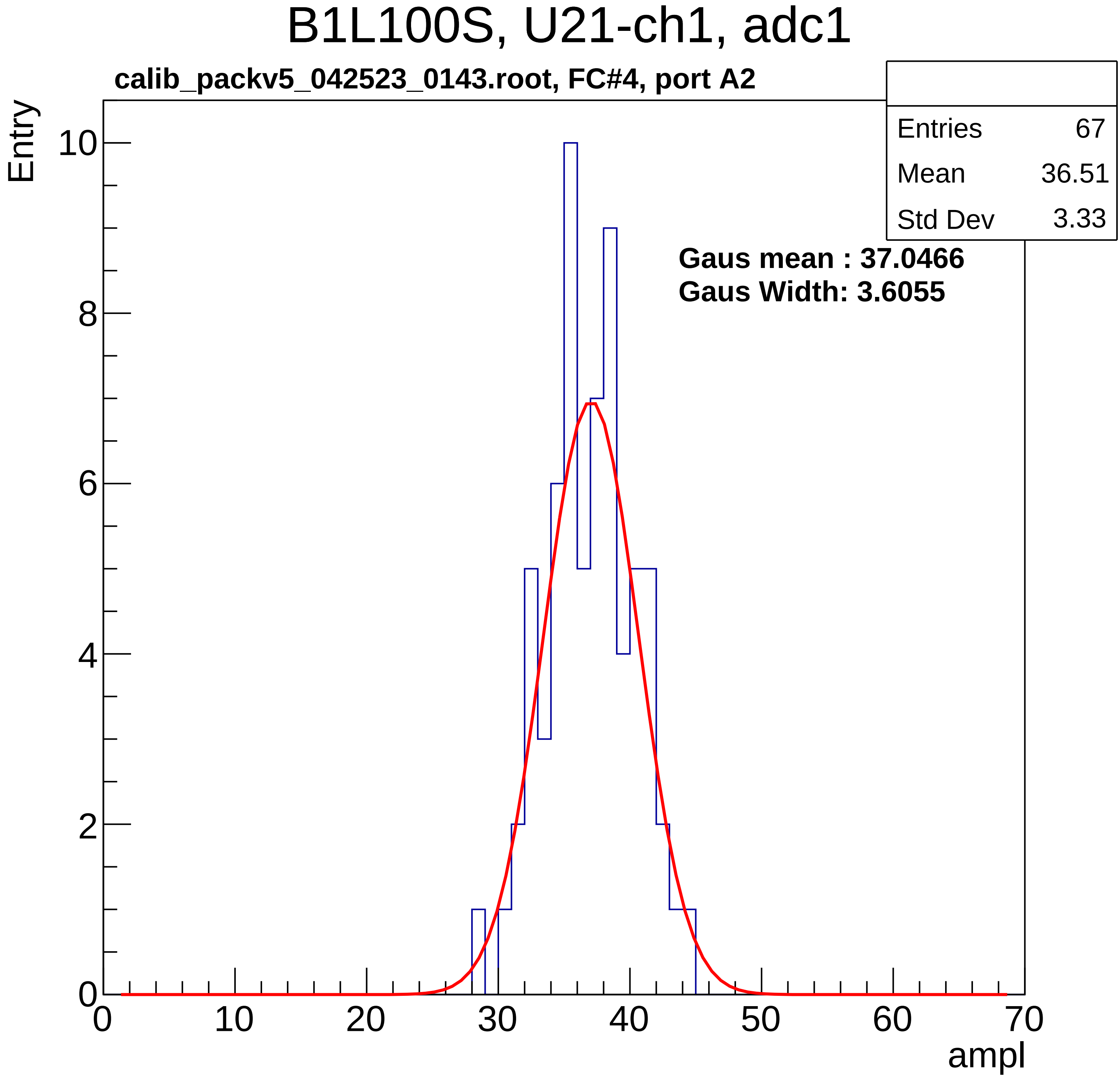

B1L100S, U21-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

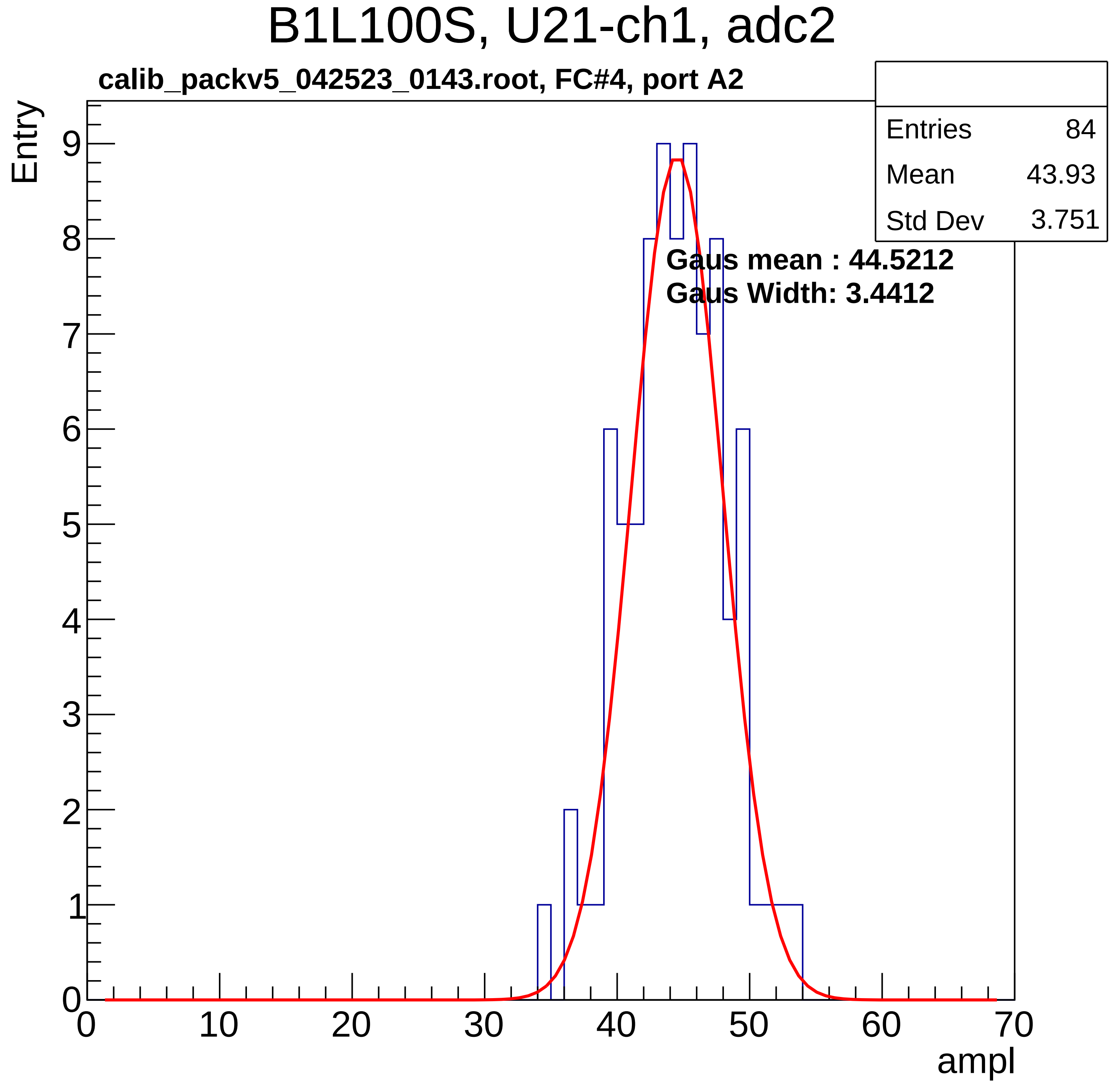
B1L100S, U21-ch0, adc7

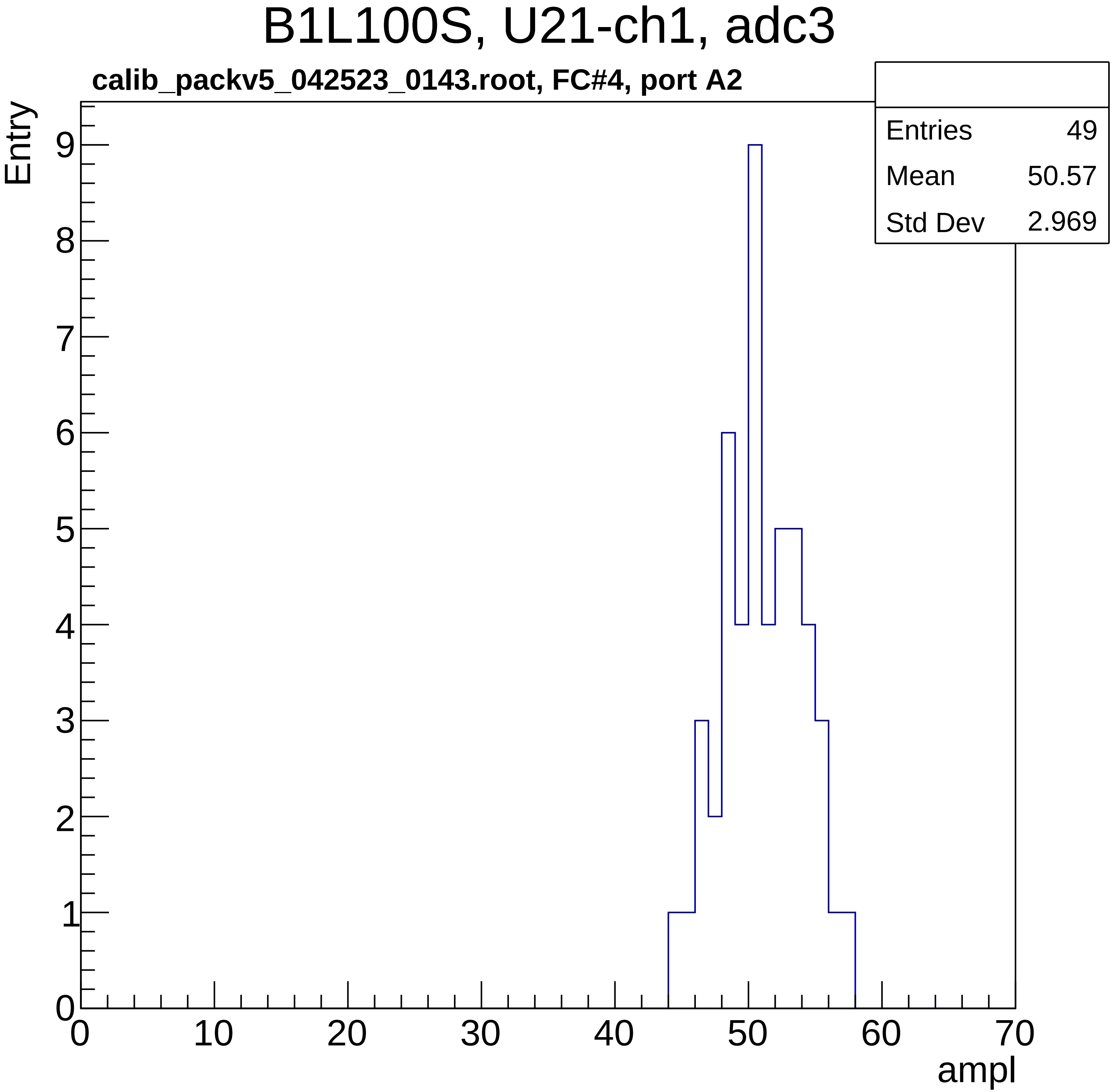
calib_packv5_042523_0143.root, FC#4, port A2

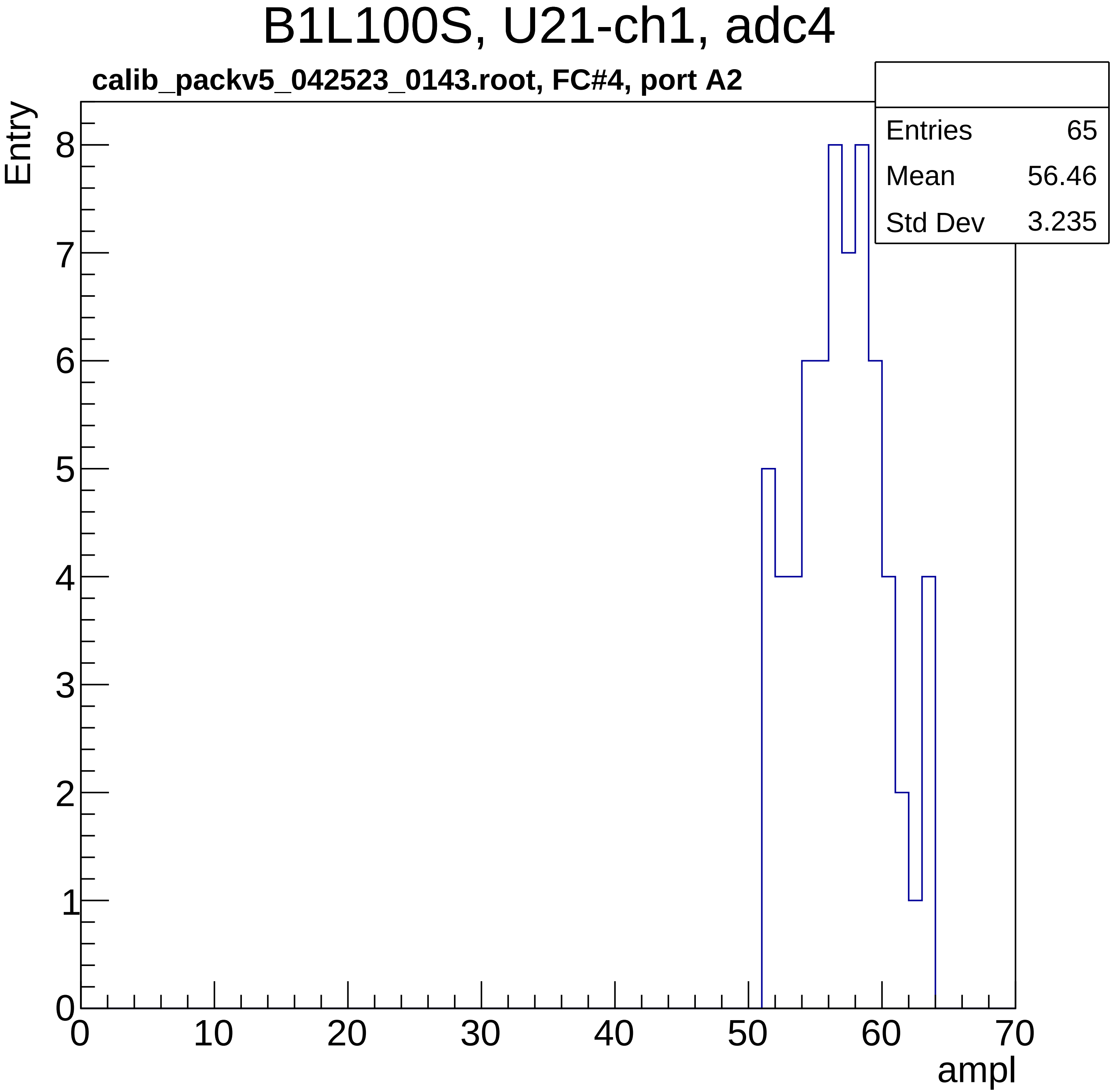


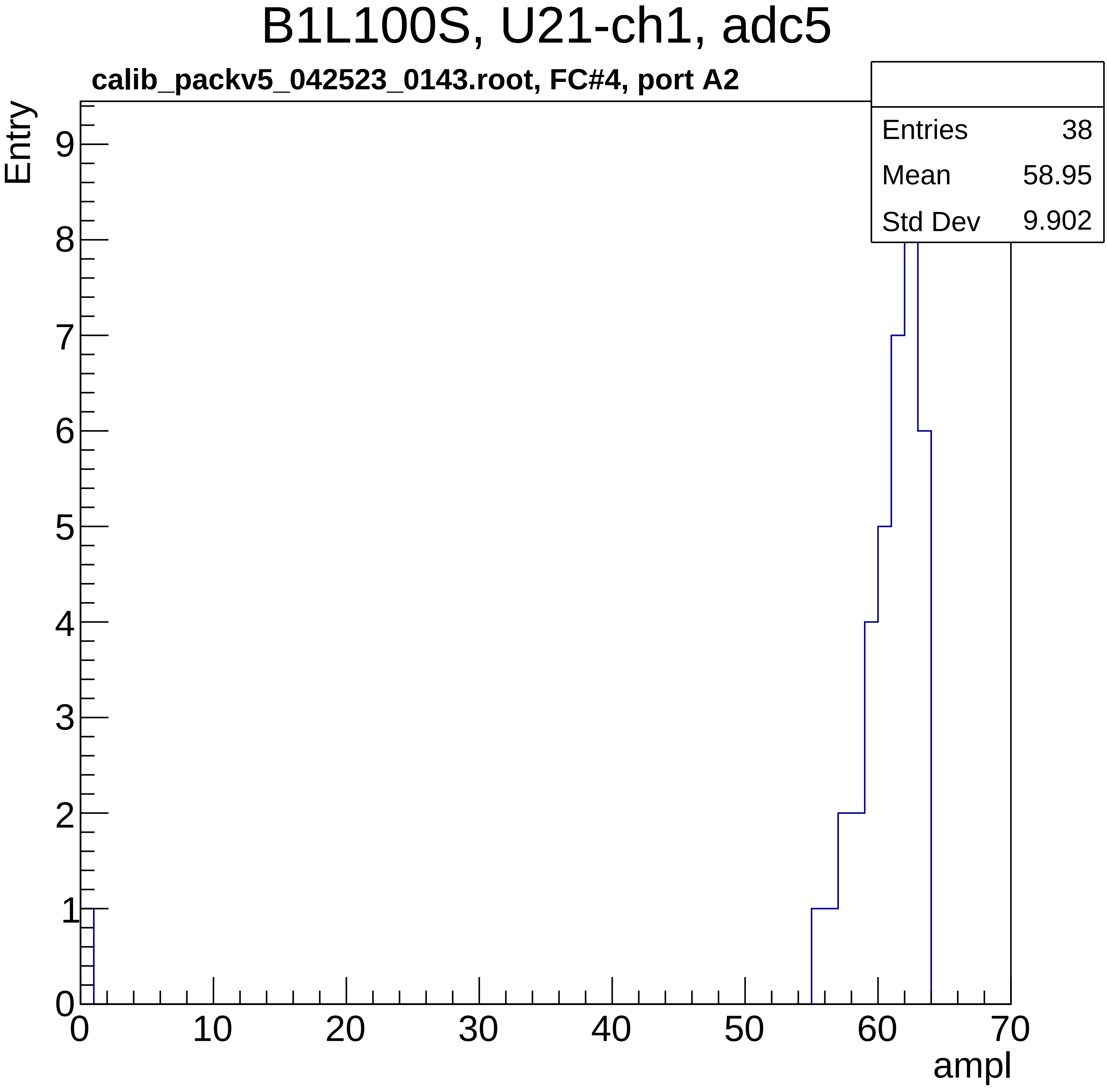

B1L100S, U21-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.98
Std Dev 3.386
Gaus mean : 31.2413
Gaus Width: 3.9134
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

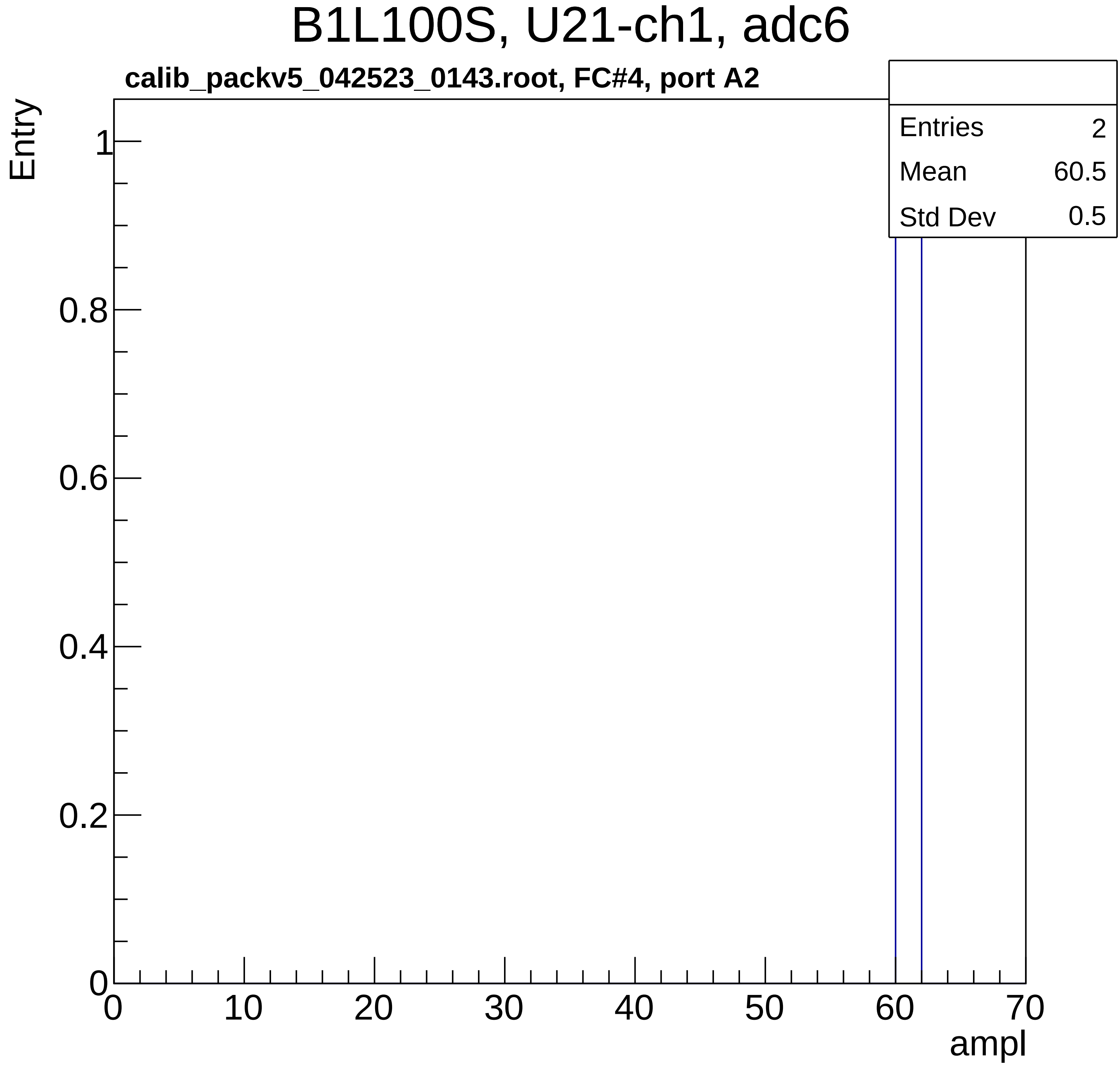

B1L100S, U21-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.51
Std Dev 3.33
Gaus mean : 37.0466
Gaus Width: 3.6055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 43.93
Std Dev 3.751
Gaus mean : 44.5212
Gaus Width: 3.4412
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

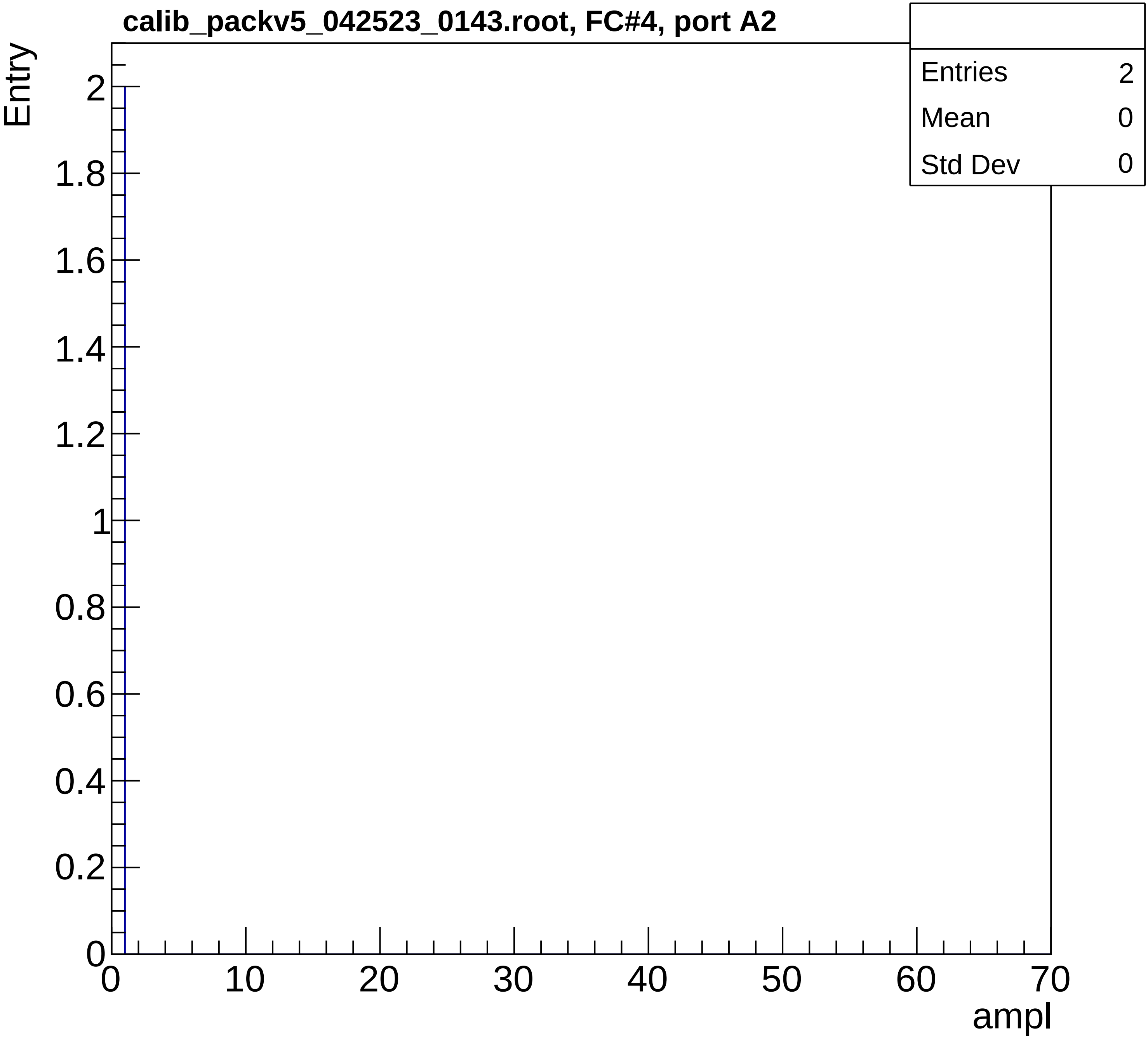

B1L100S, U21-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 50.57
Std Dev 2.969
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

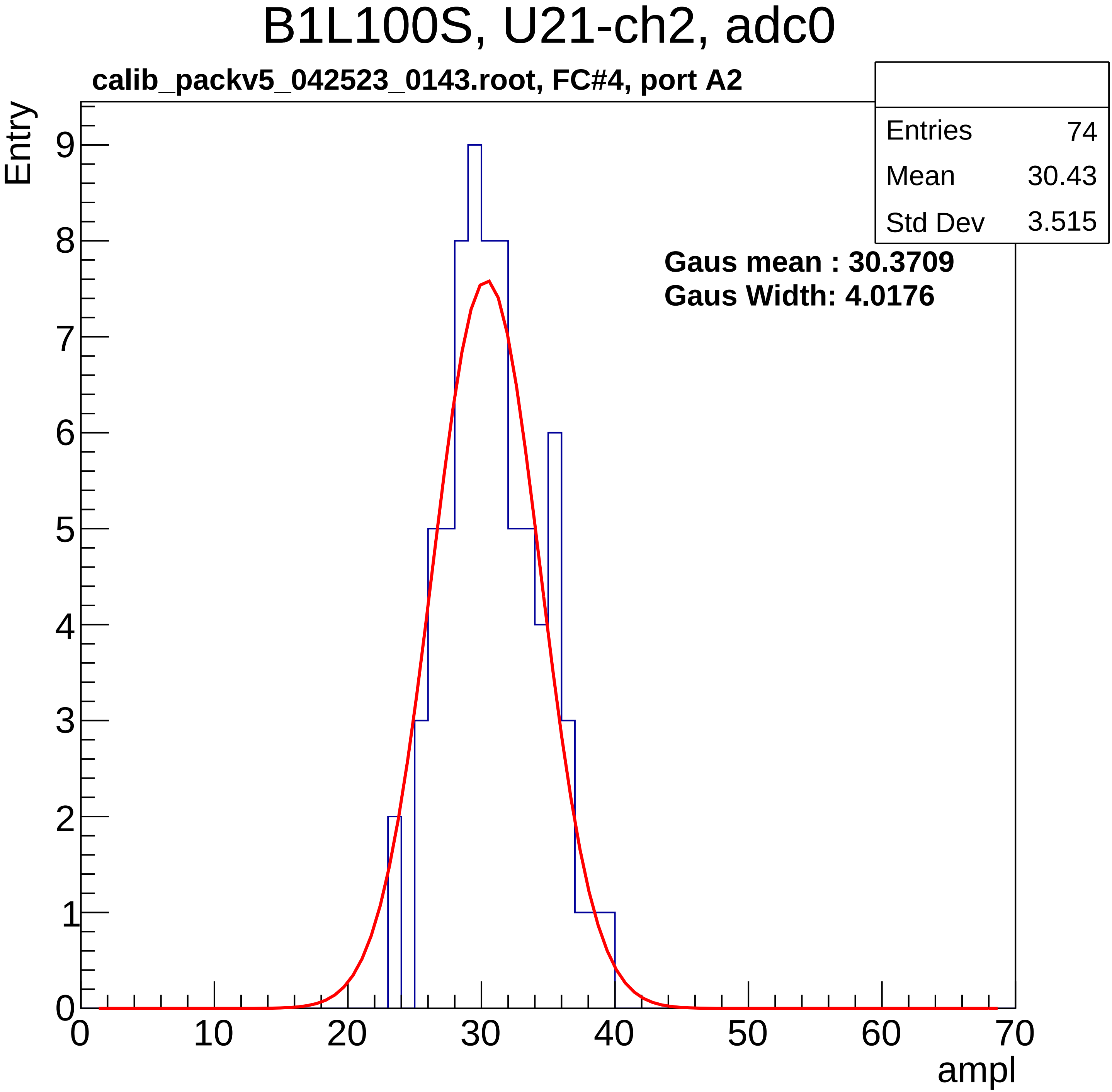

B1L100S, U21-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.46
Std Dev 3.235
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

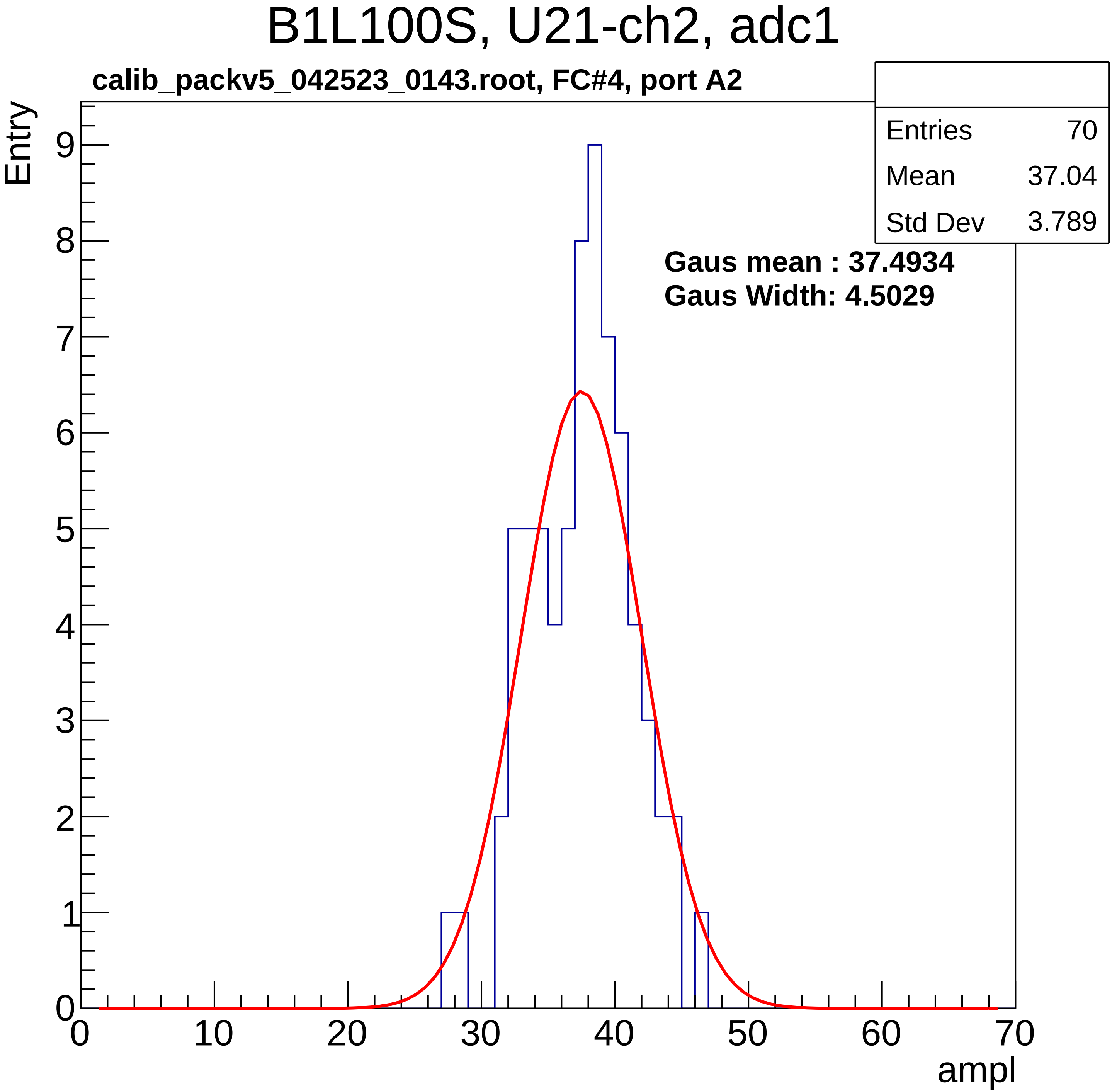

B1L100S, U21-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.95
Std Dev 9.902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

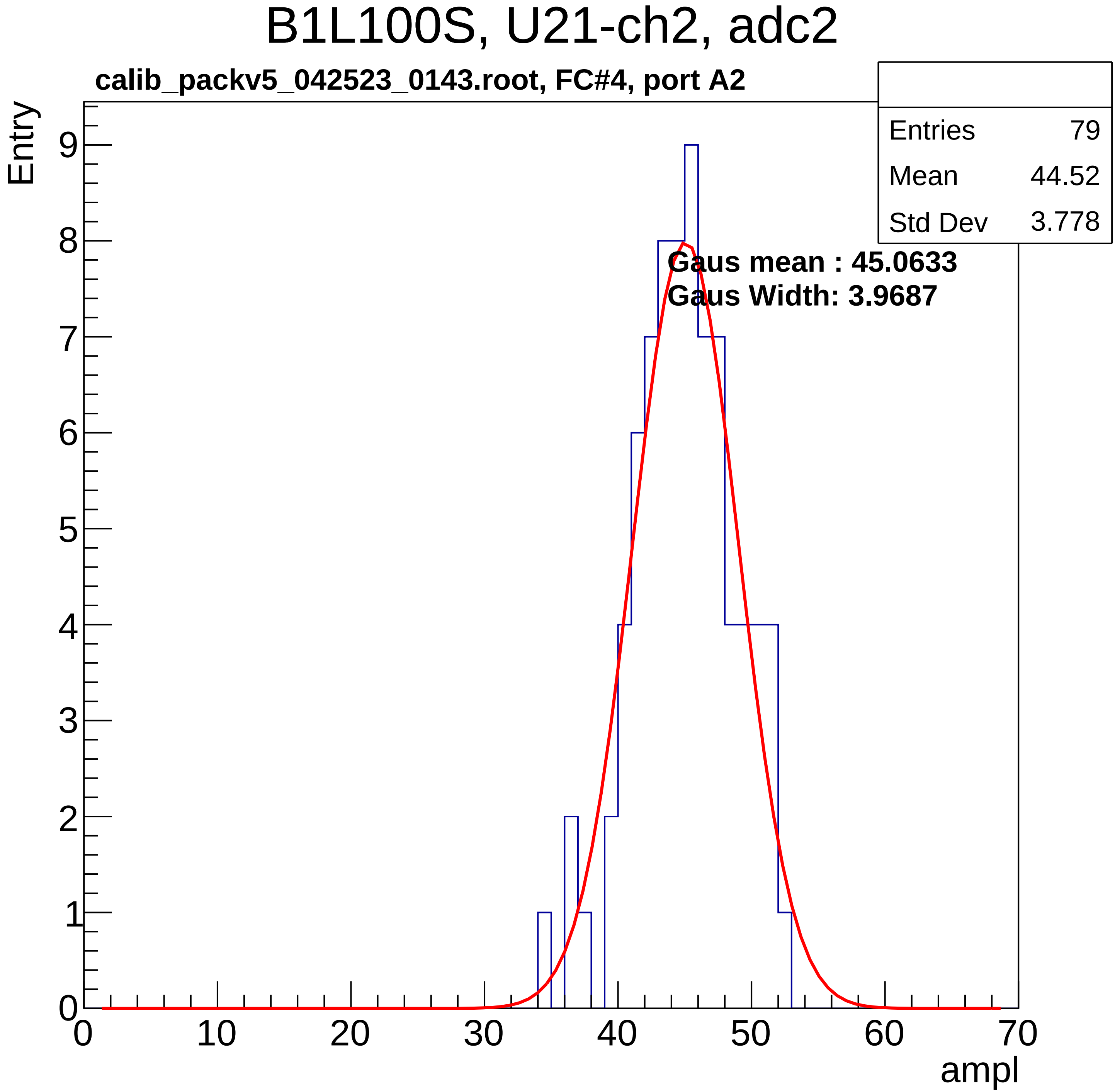

B1L100S, U21-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

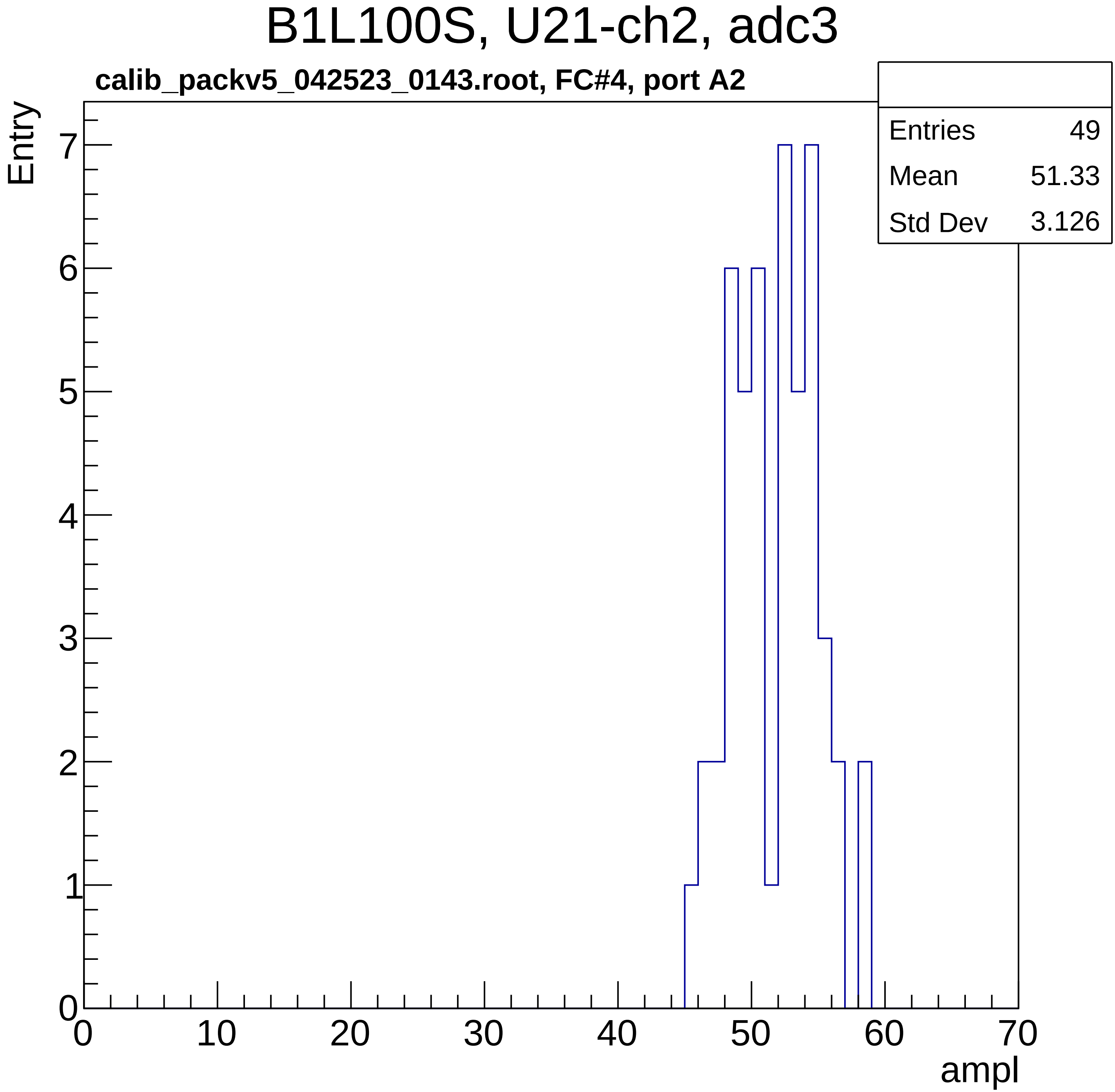
B1L100S, U21-ch1, adc7

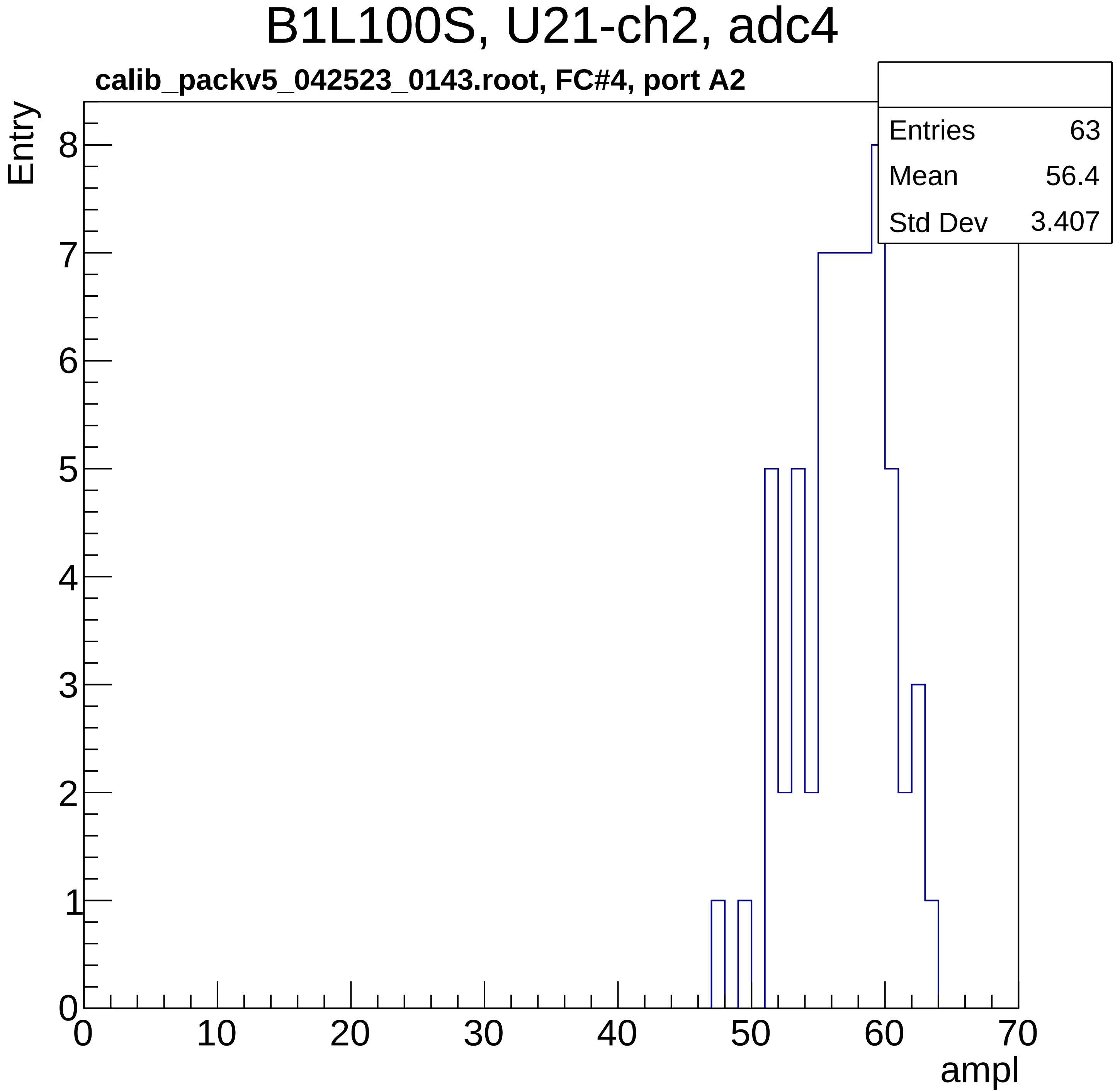


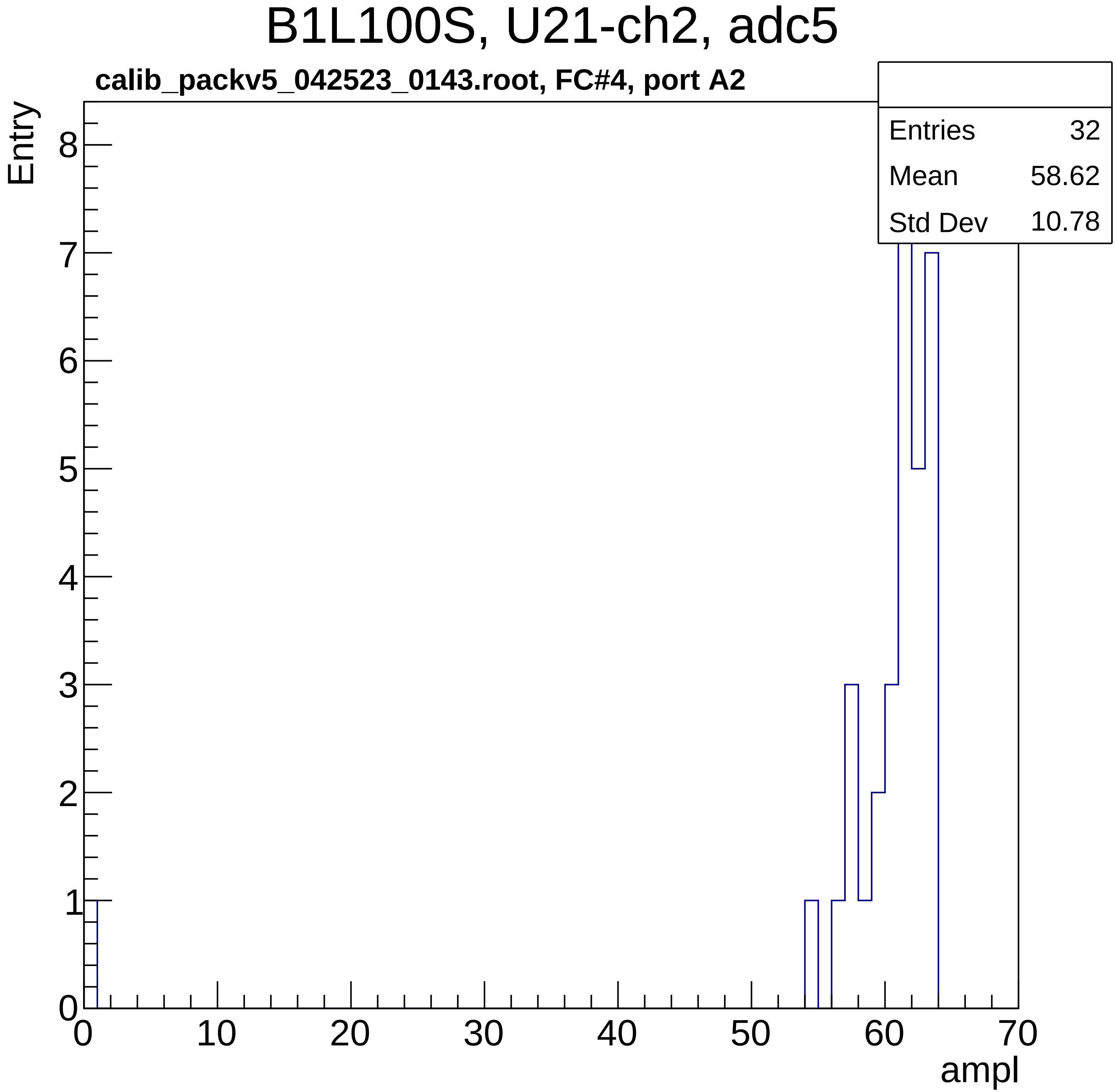

B1L100S, U21-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 30.43
Std Dev 3.515
Gaus mean : 30.3709
Gaus Width: 4.0176
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.04
Std Dev 3.789
Gaus mean : 37.4934
Gaus Width: 4.5029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 44.52
Std Dev 3.778
Gaus mean : 45.0633
Gaus Width: 3.9687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

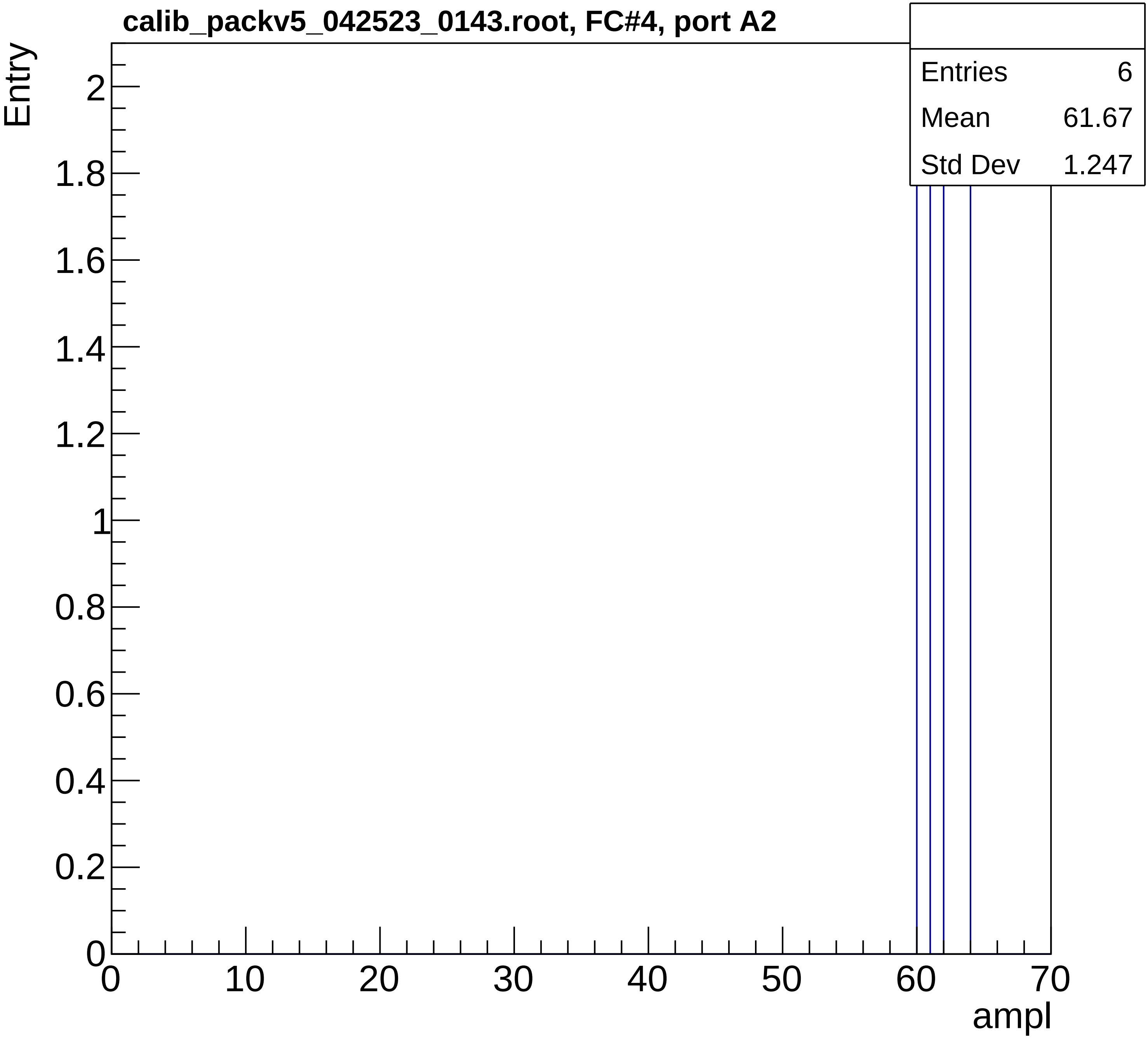

B1L100S, U21-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 51.33
Std Dev 3.126
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

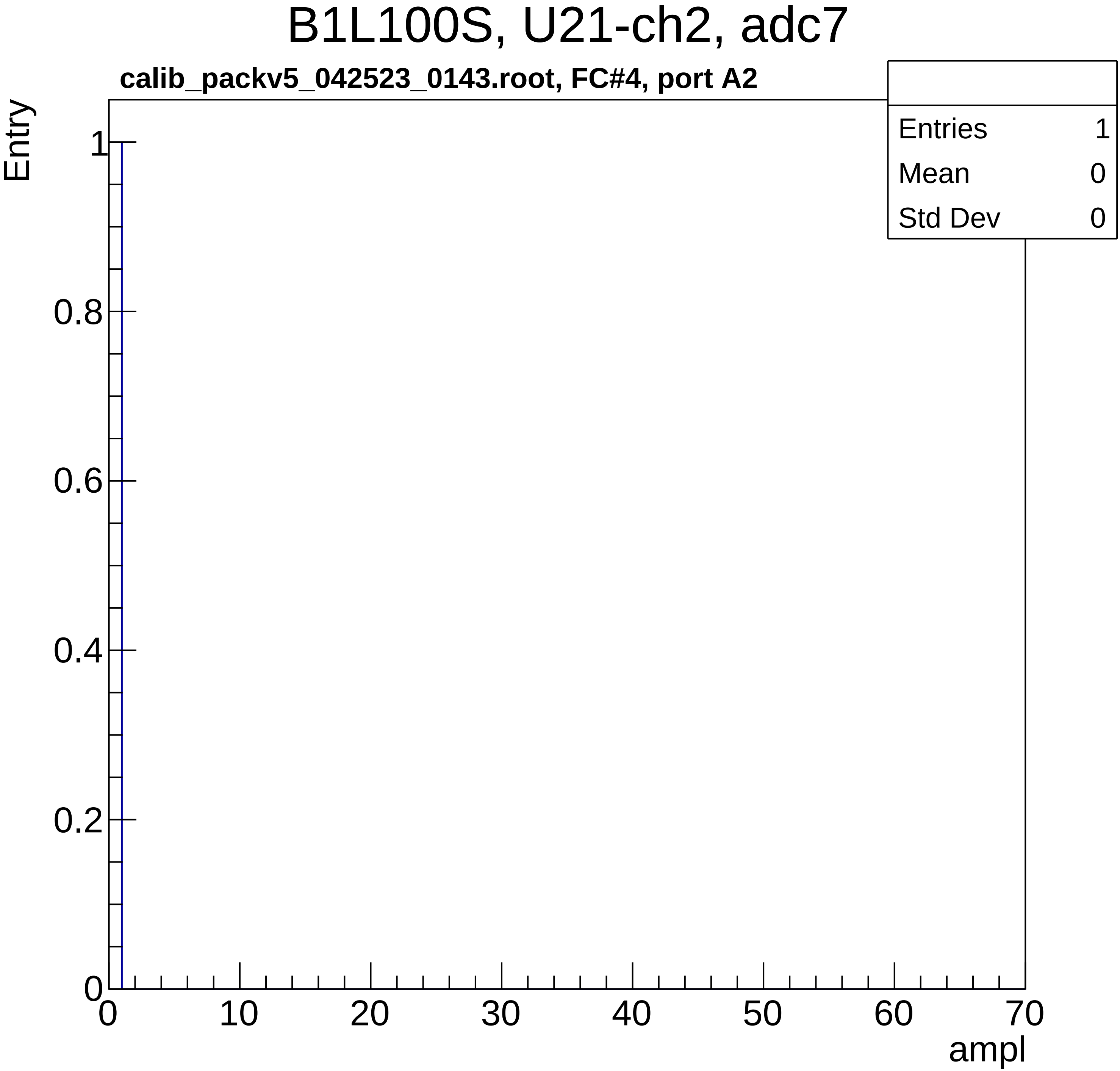

B1L100S, U21-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.4
Std Dev 3.407
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

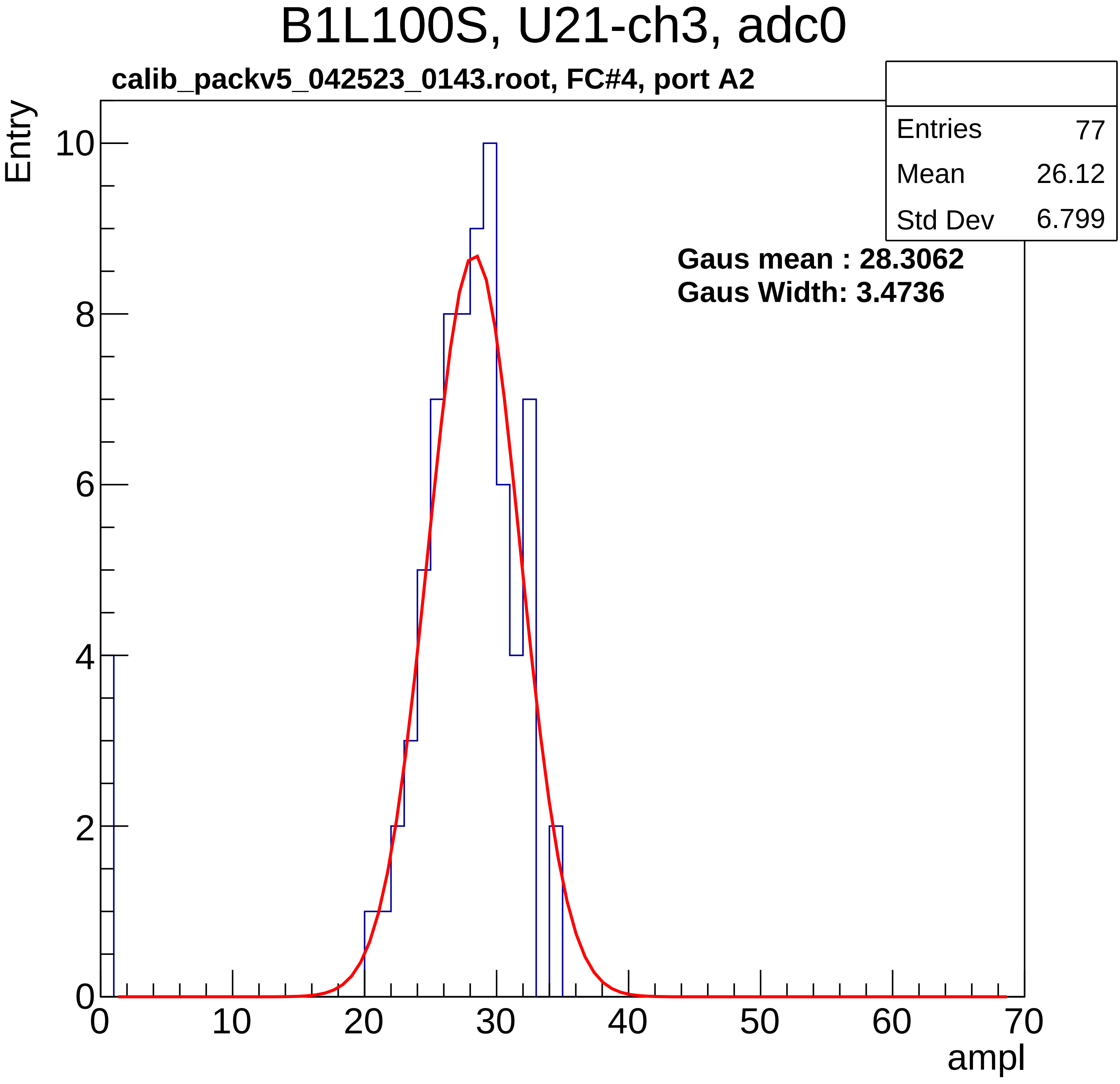

B1L100S, U21-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.62
Std Dev 10.78
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

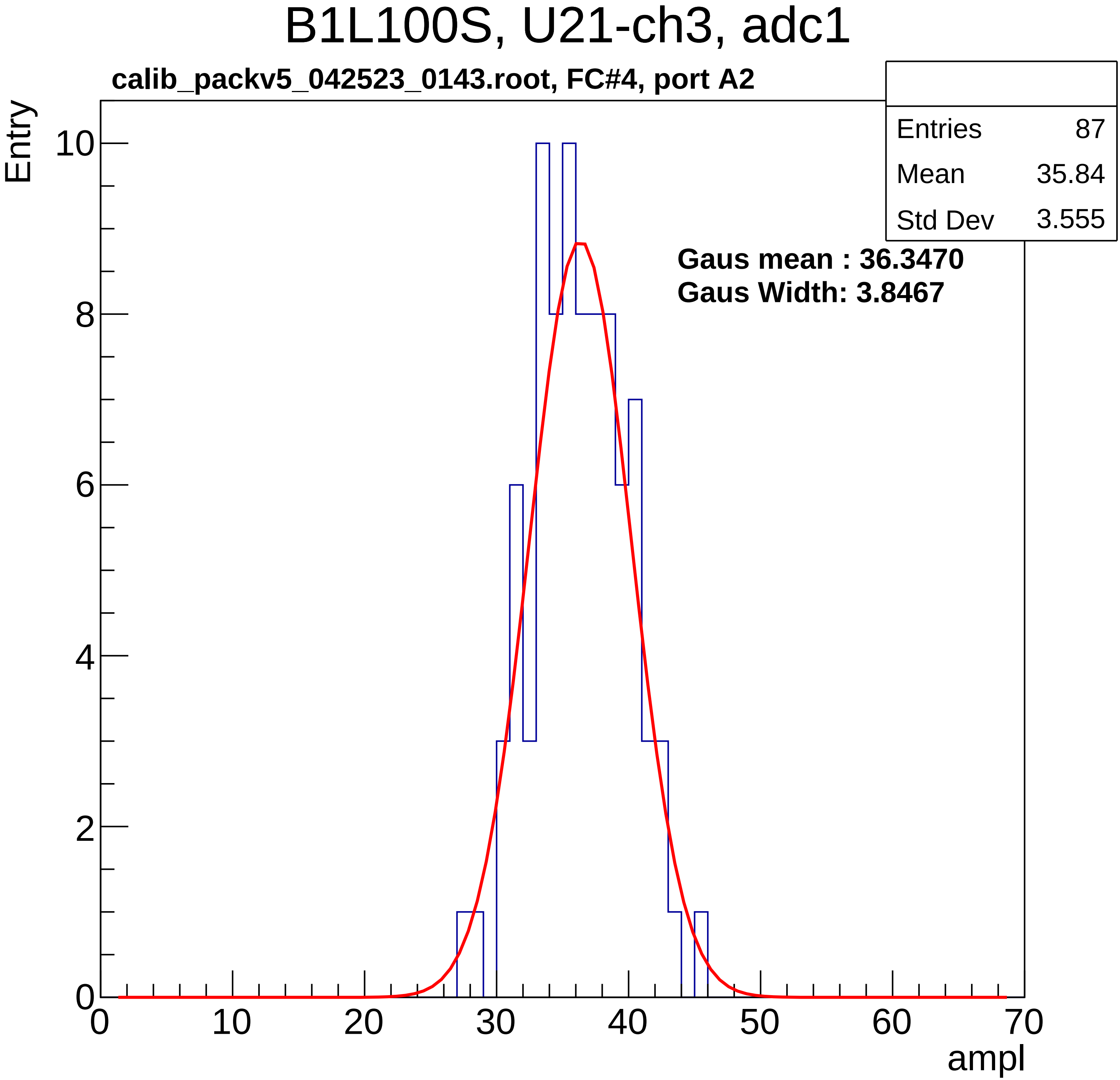
B1L100S, U21-ch2, adc6

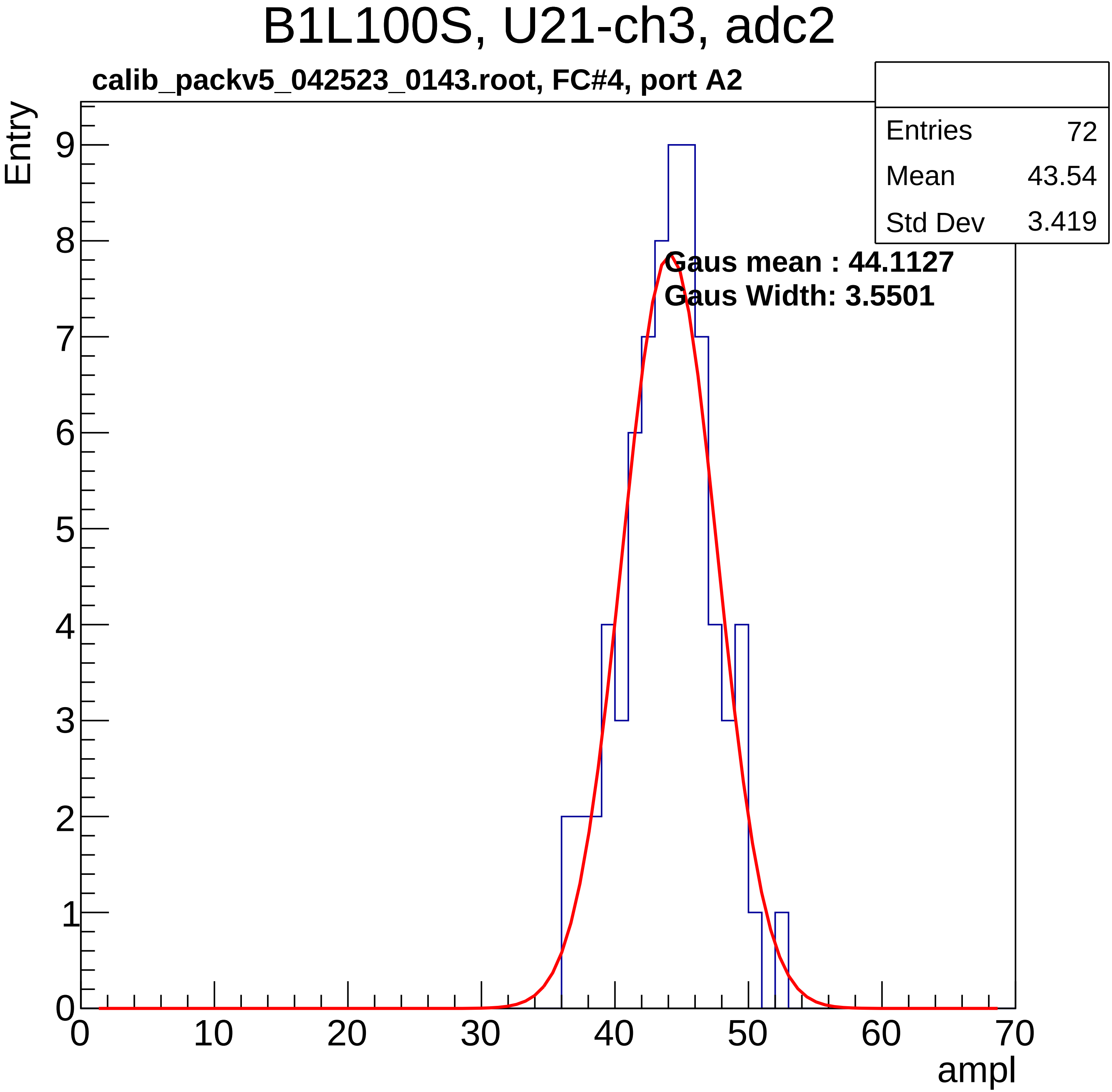
calib_packv5_042523_0143.root, FC#4, port A2

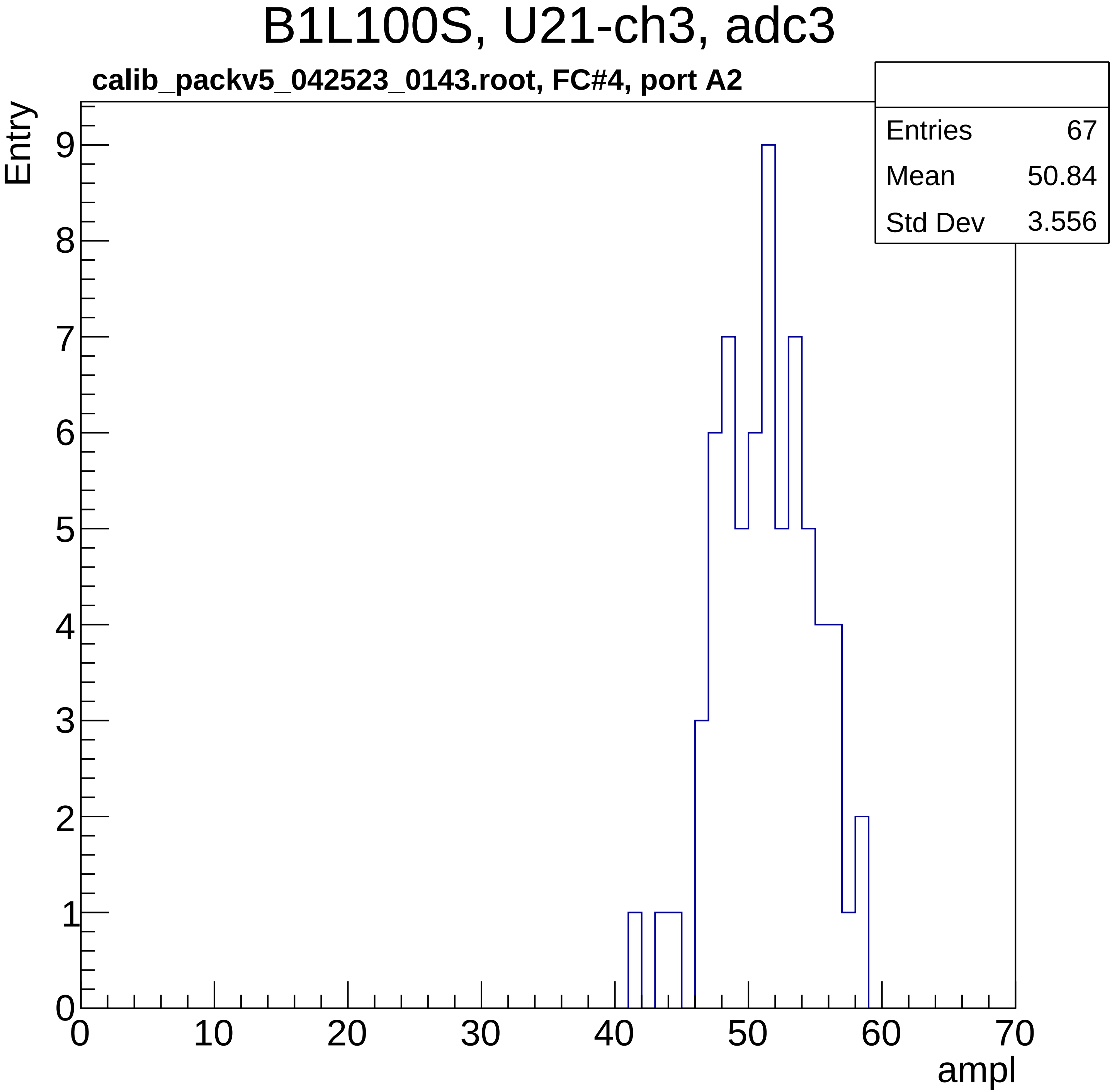


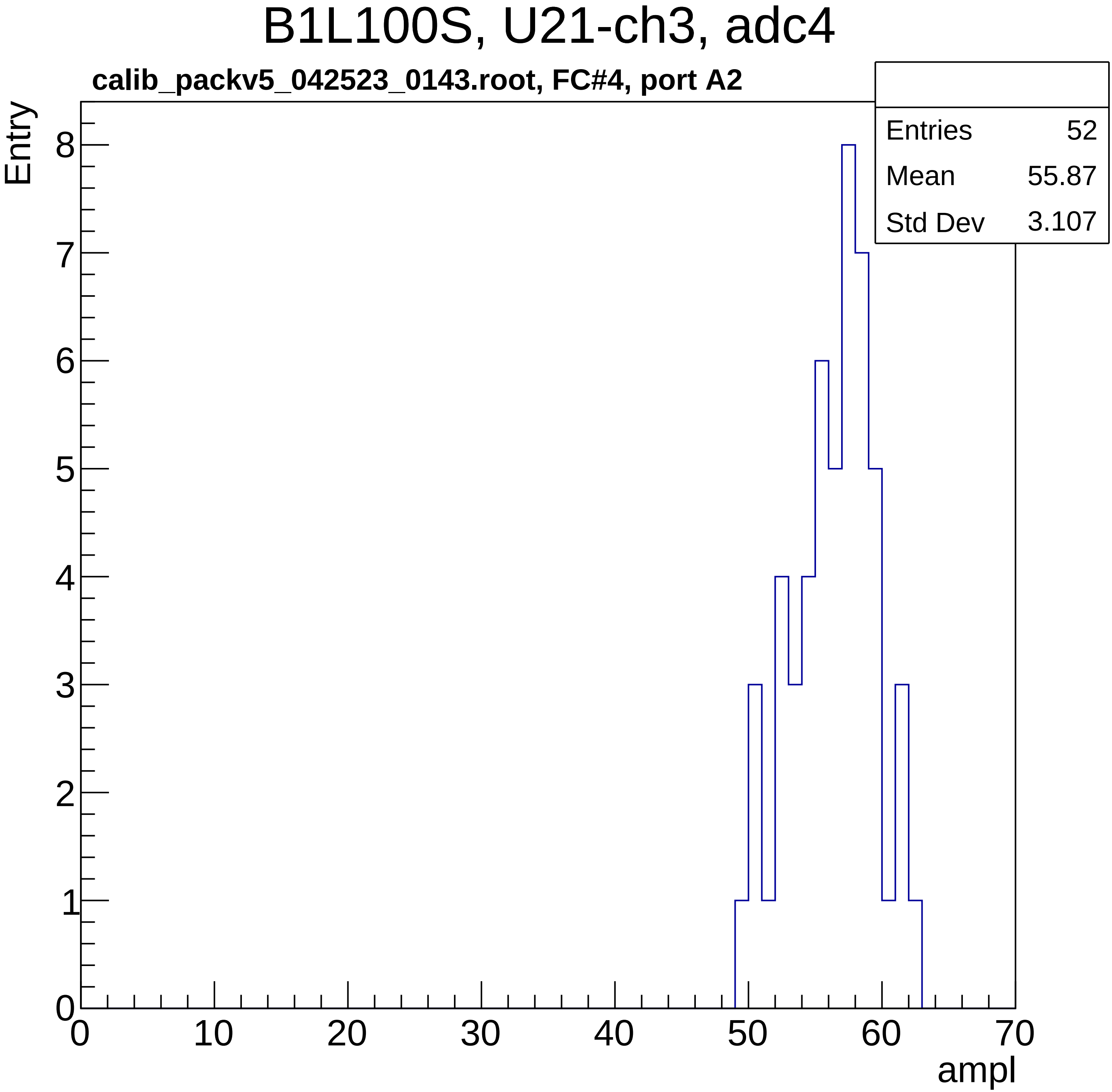

B1L100S, U21-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 26.12
Std Dev 6.799
Gaus mean : 28.3062
Gaus Width: 3.4736
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 35.84
Std Dev 3.555
Gaus mean : 36.3470
Gaus Width: 3.8467
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 43.54
Std Dev 3.419
Gaus mean : 44.1127
Gaus Width: 3.5501
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

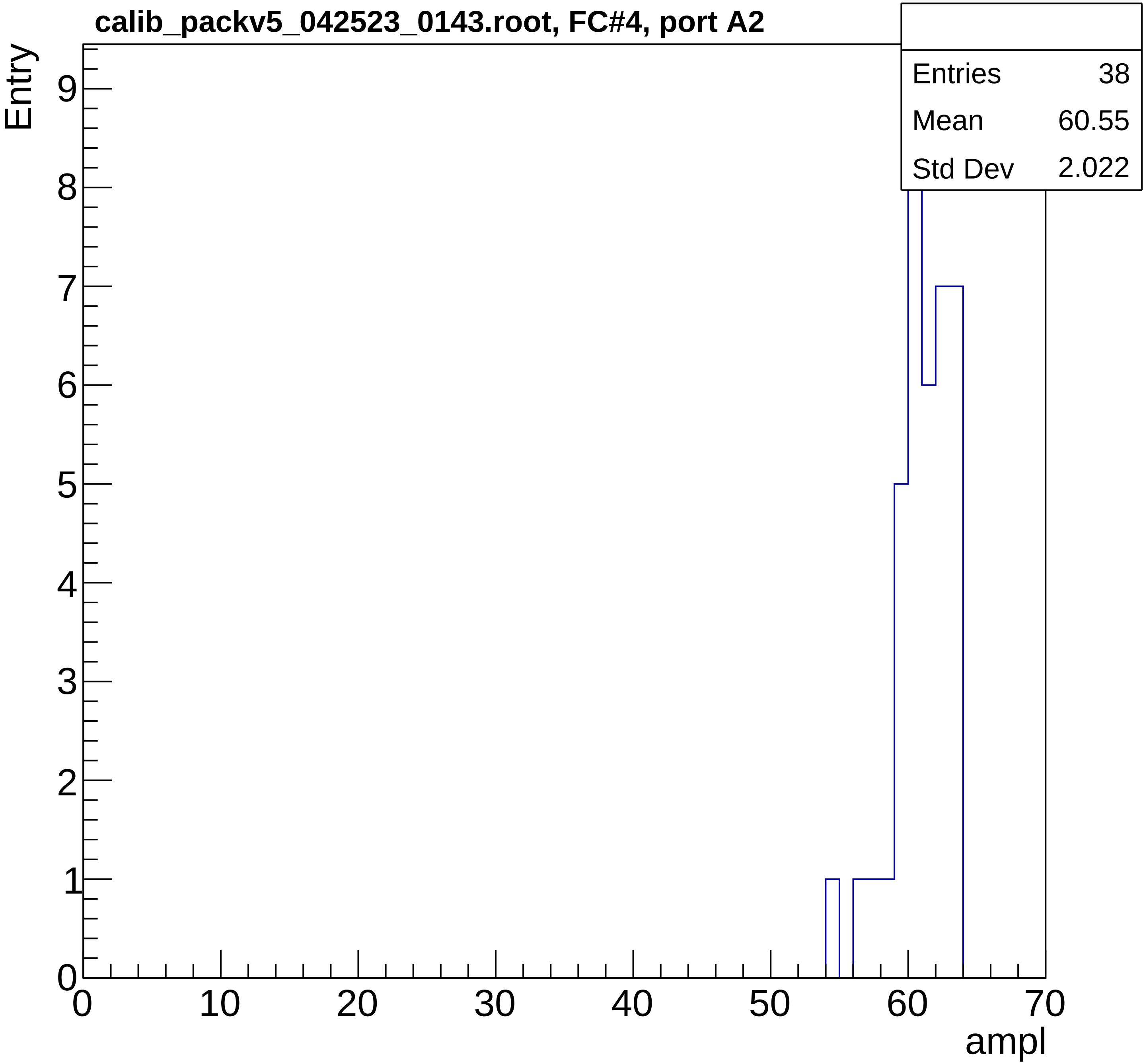

B1L100S, U21-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.84
Std Dev 3.556
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

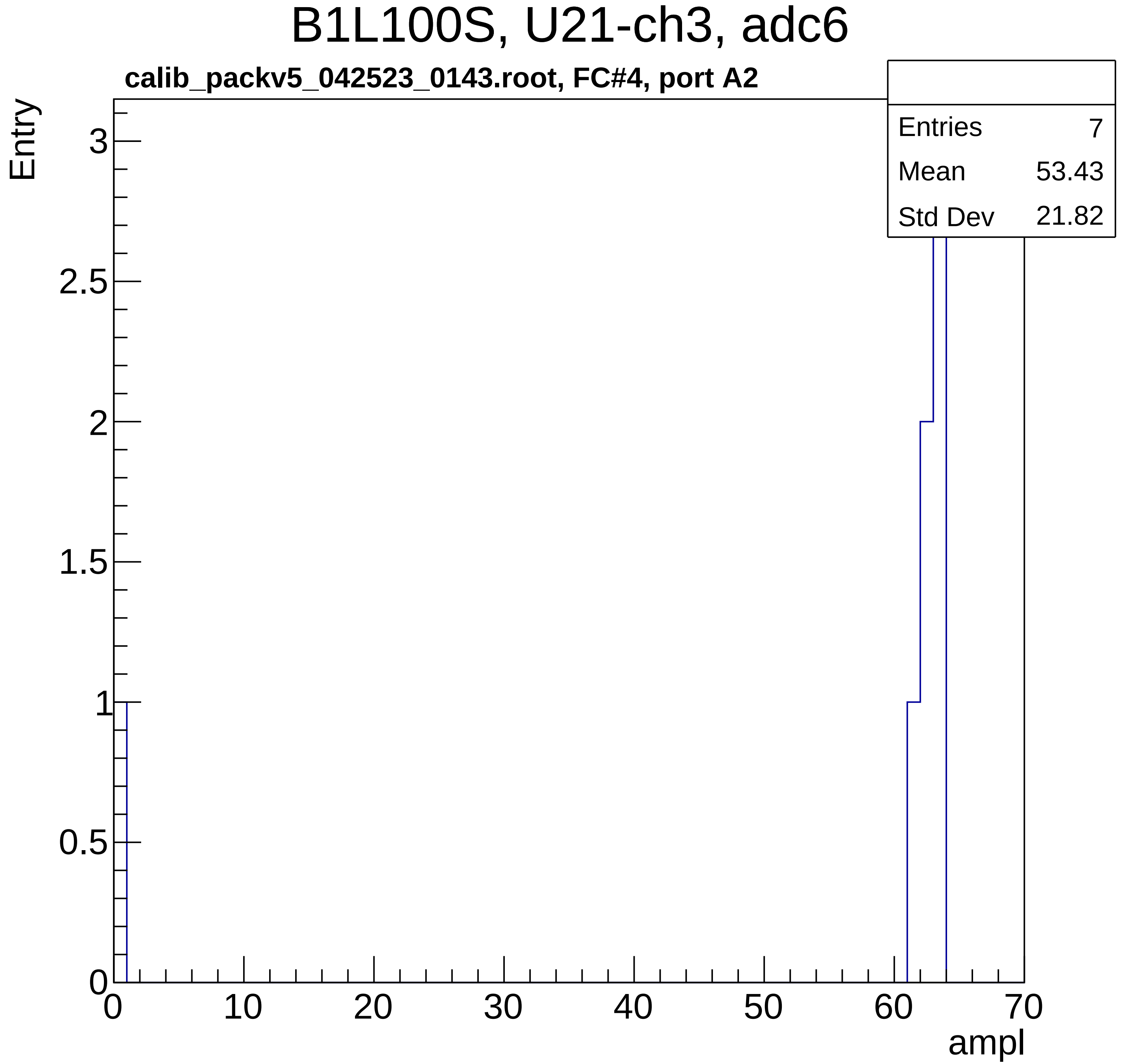


B1L100S, U21-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.87
Std Dev 3.107
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

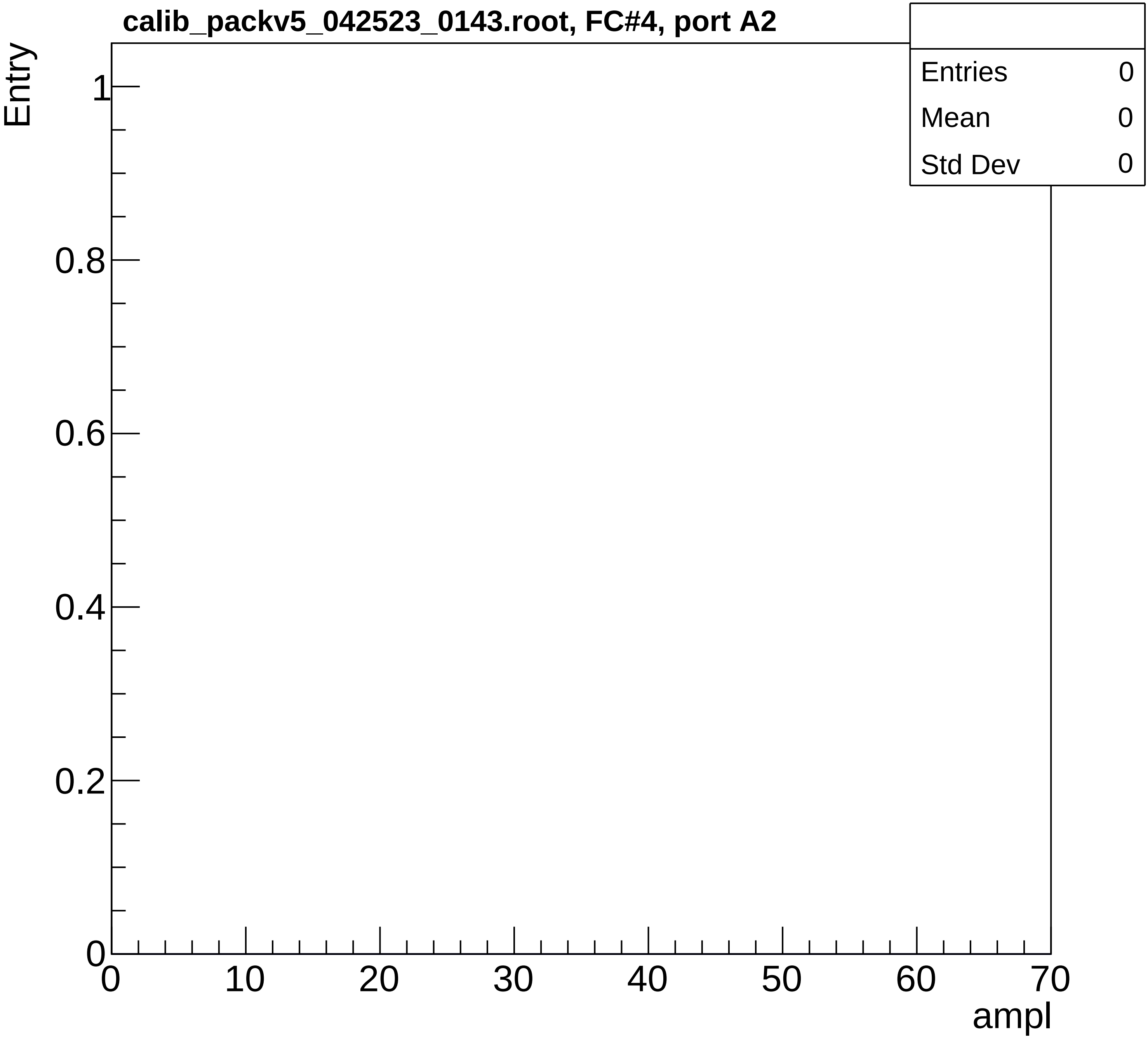
B1L100S, U21-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

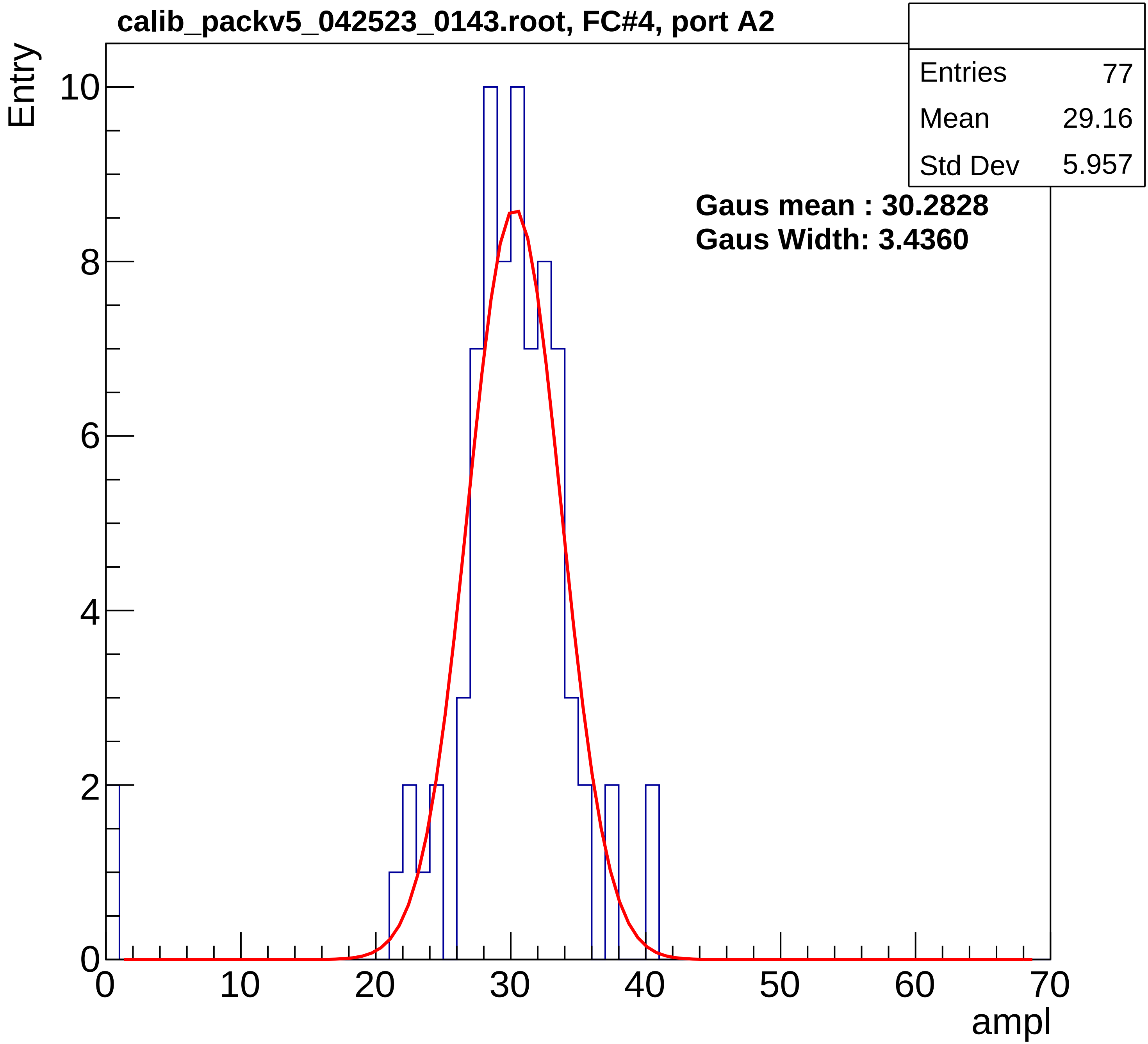


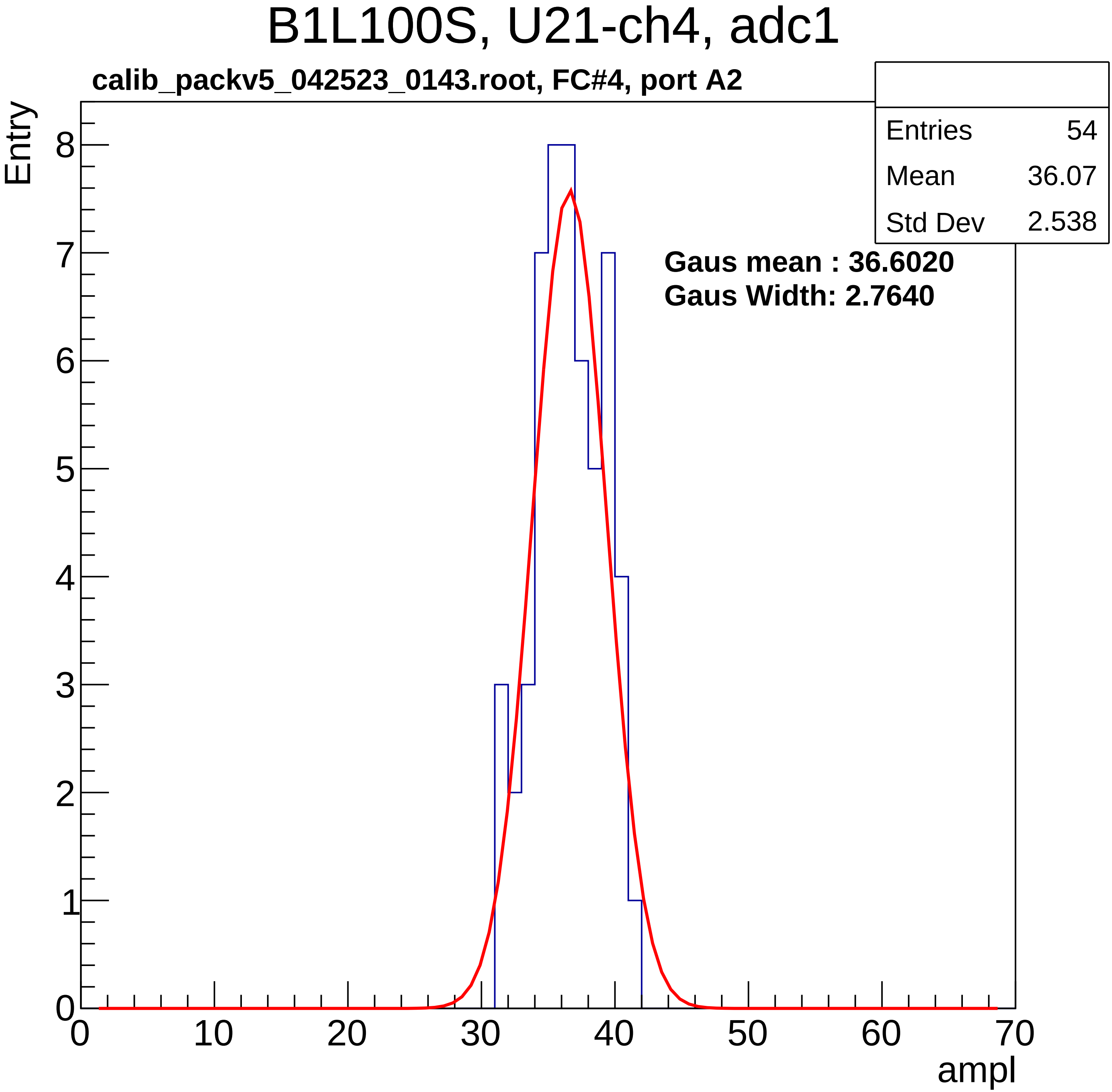

B1L100S, U21-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.43
Std Dev 21.82
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

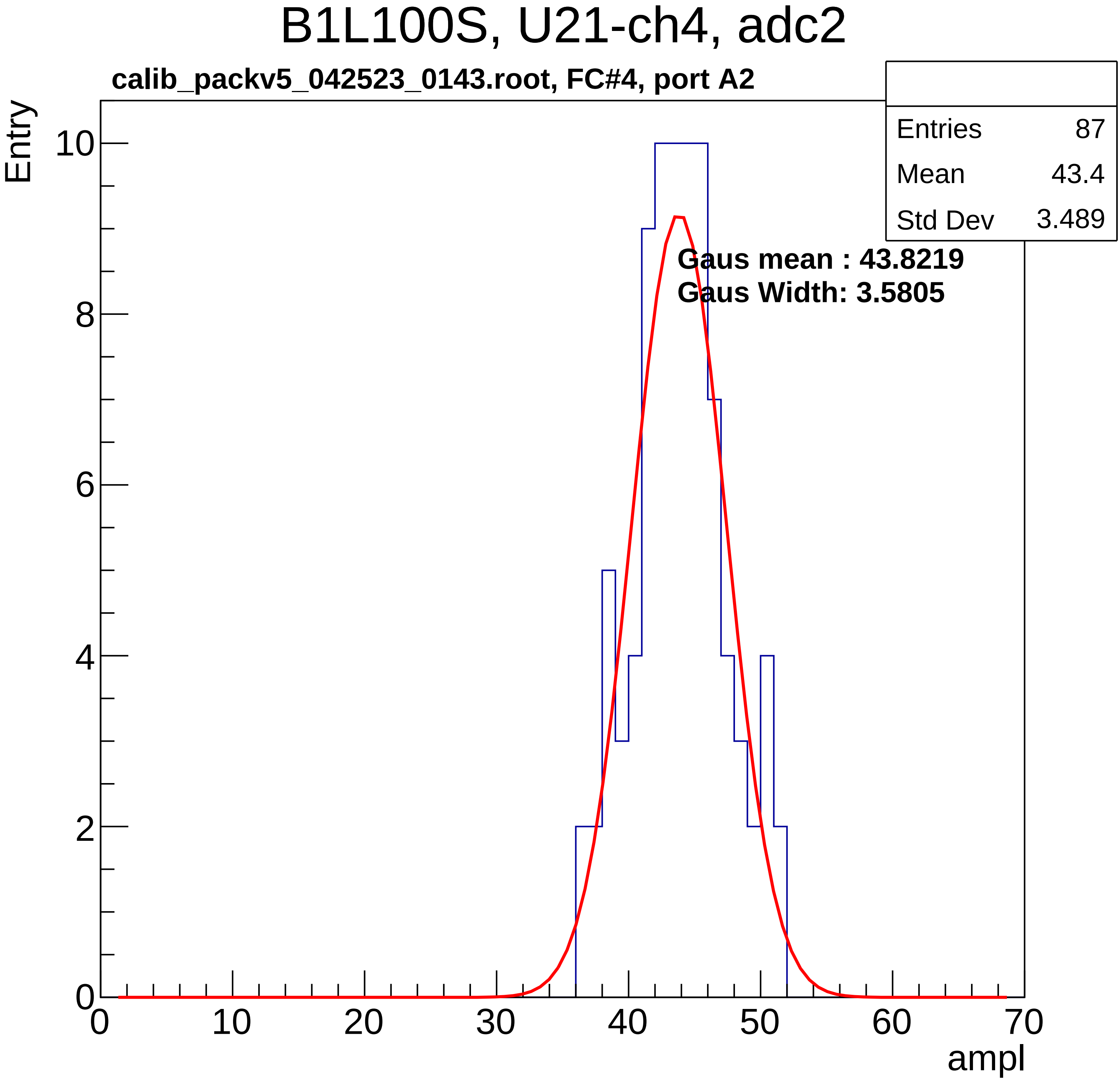
B1L100S, U21-ch3, adc7



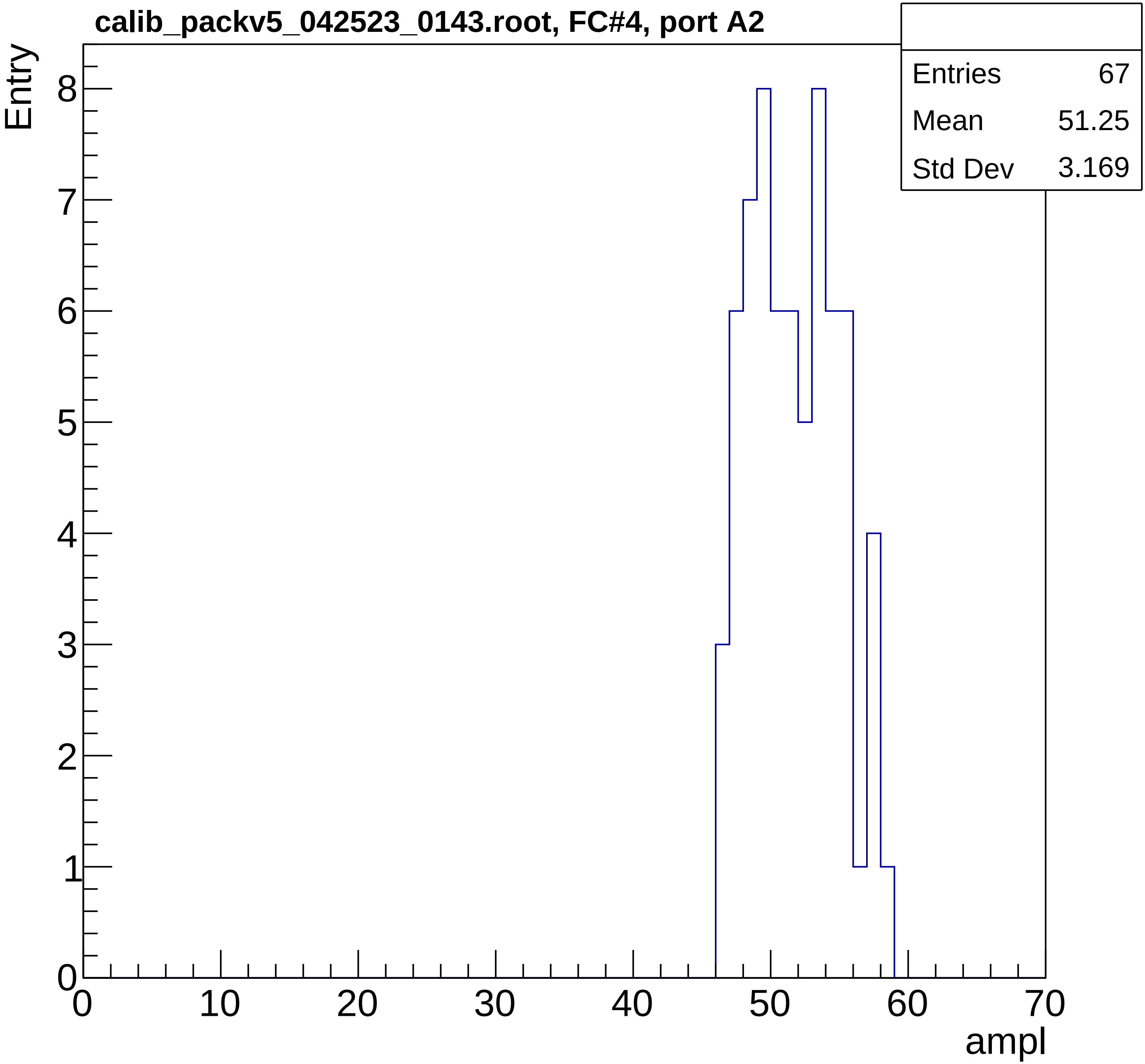
B1L100S, U21-ch4, adc0

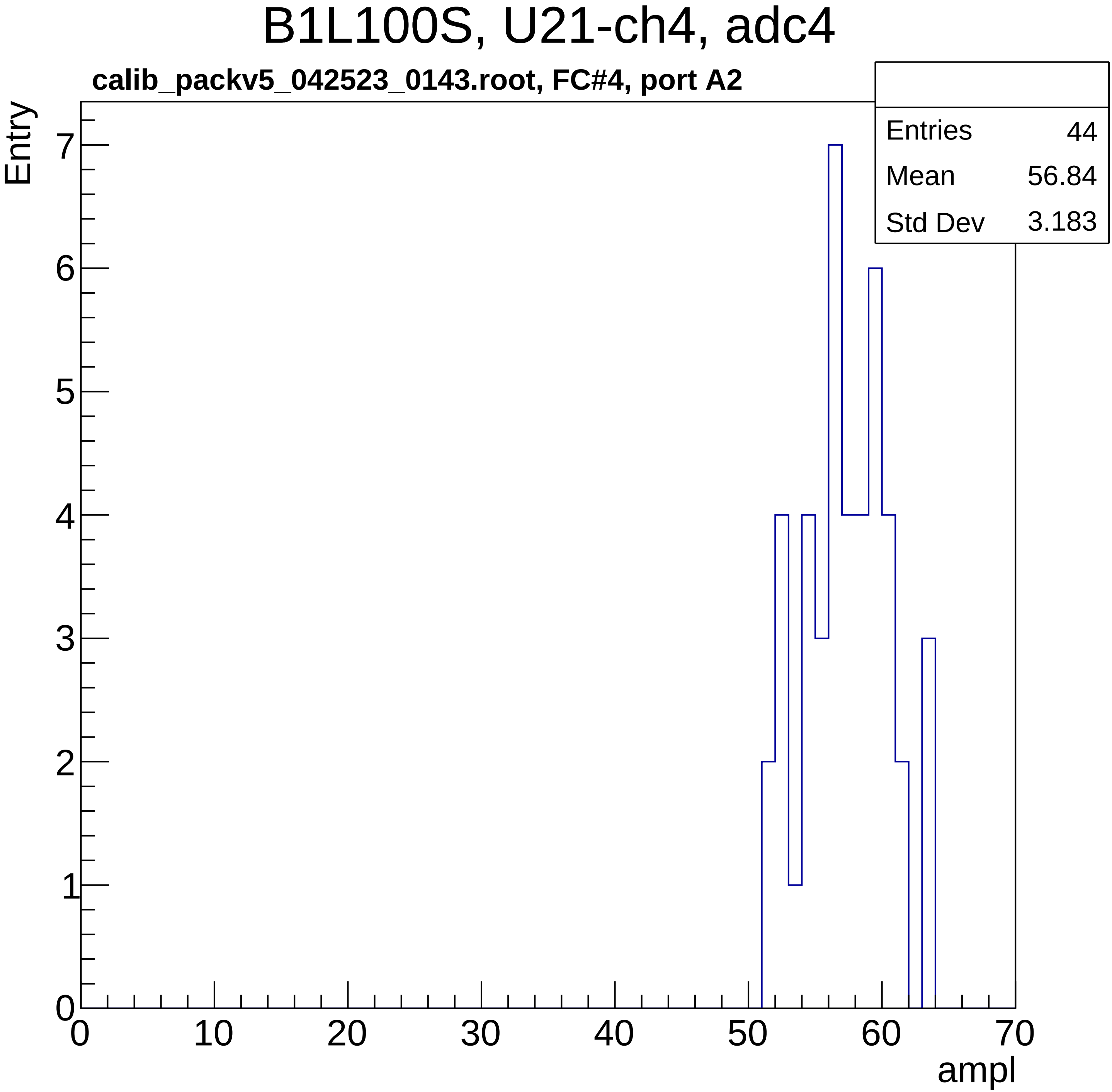


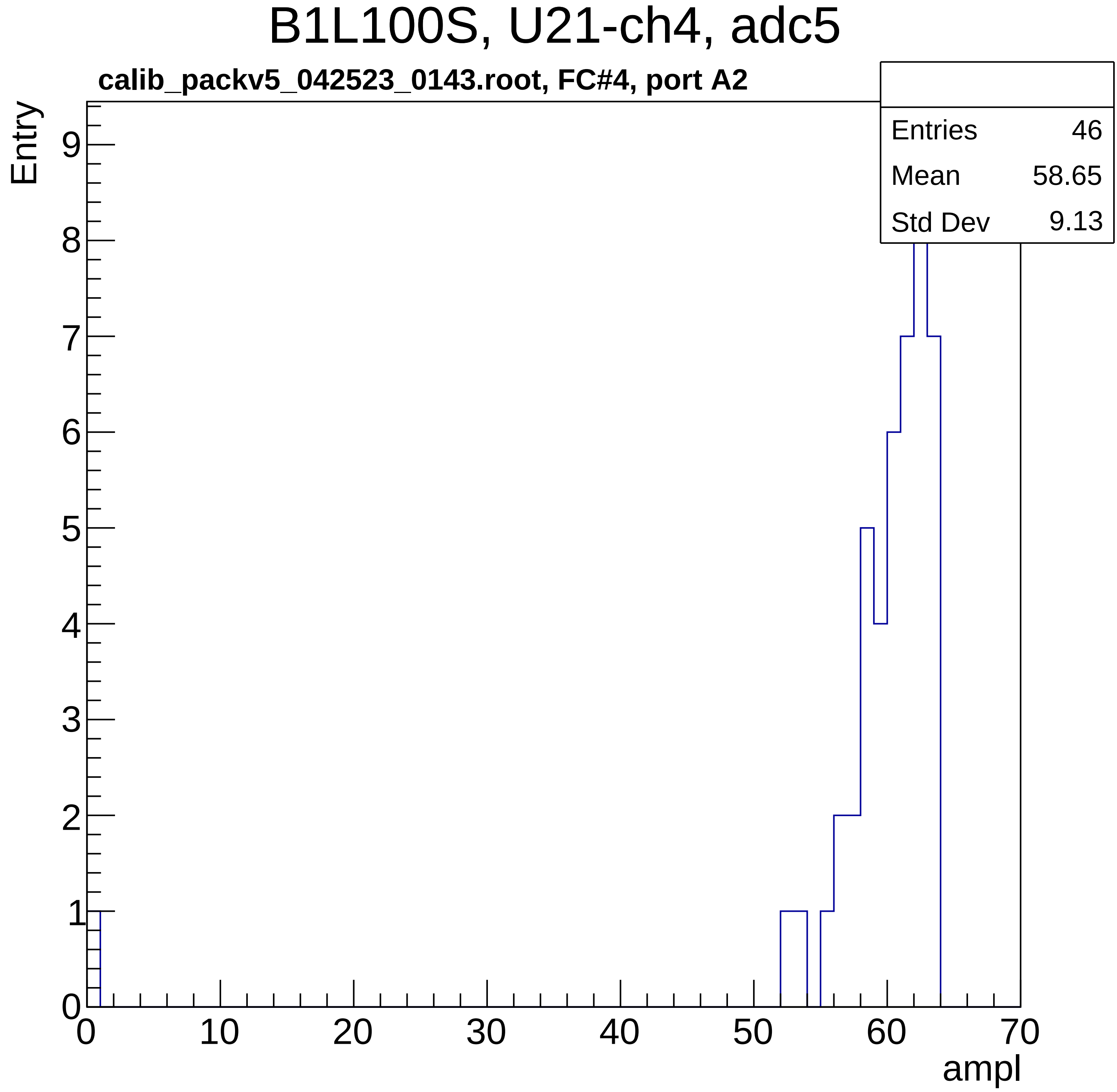

B1L100S, U21-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 36.07
Std Dev 2.538
Gaus mean : 36.6020
Gaus Width: 2.7640
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 43.4
Std Dev 3.489
Gaus mean : 43.8219
Gaus Width: 3.5805
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch4, adc3

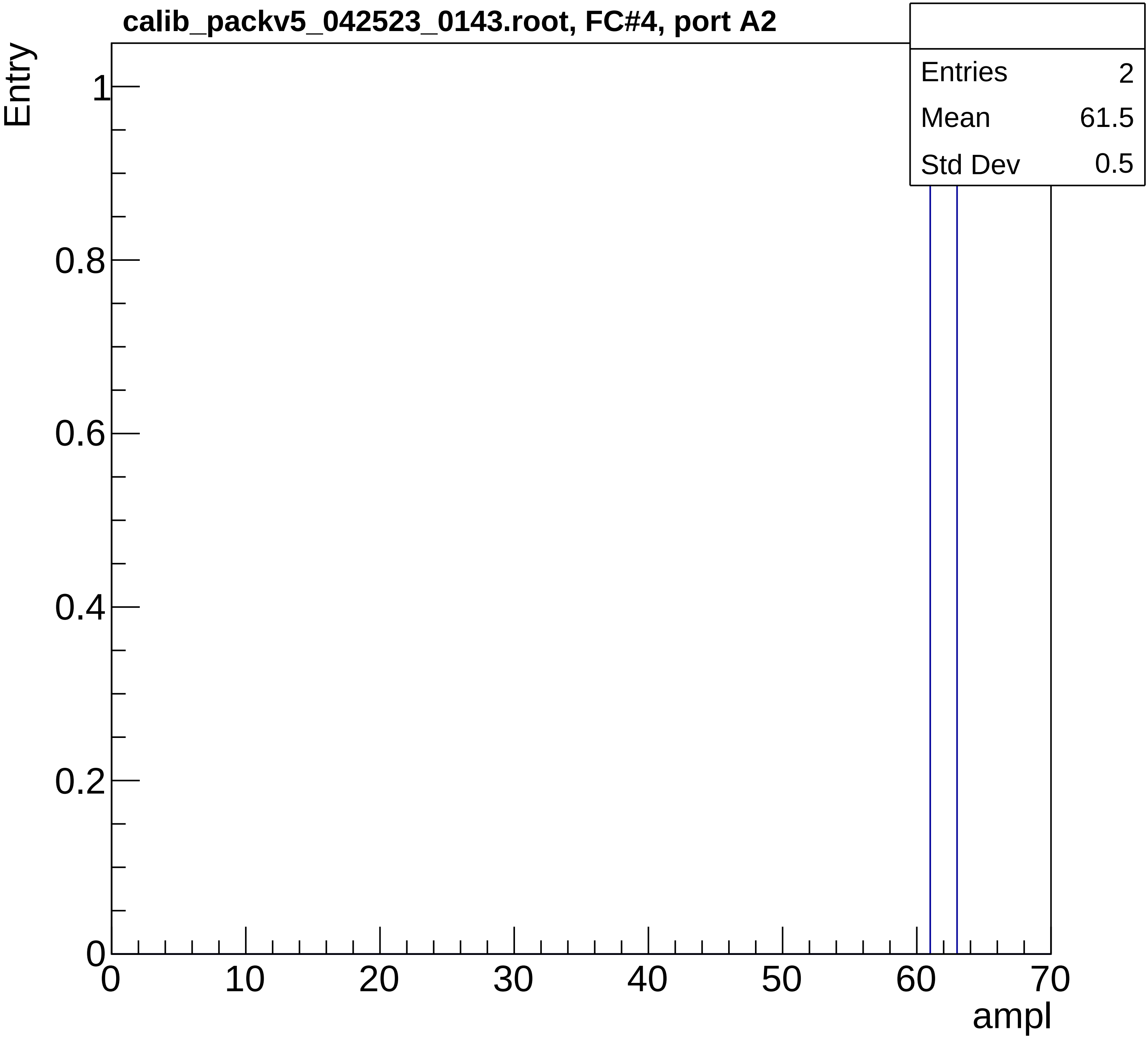


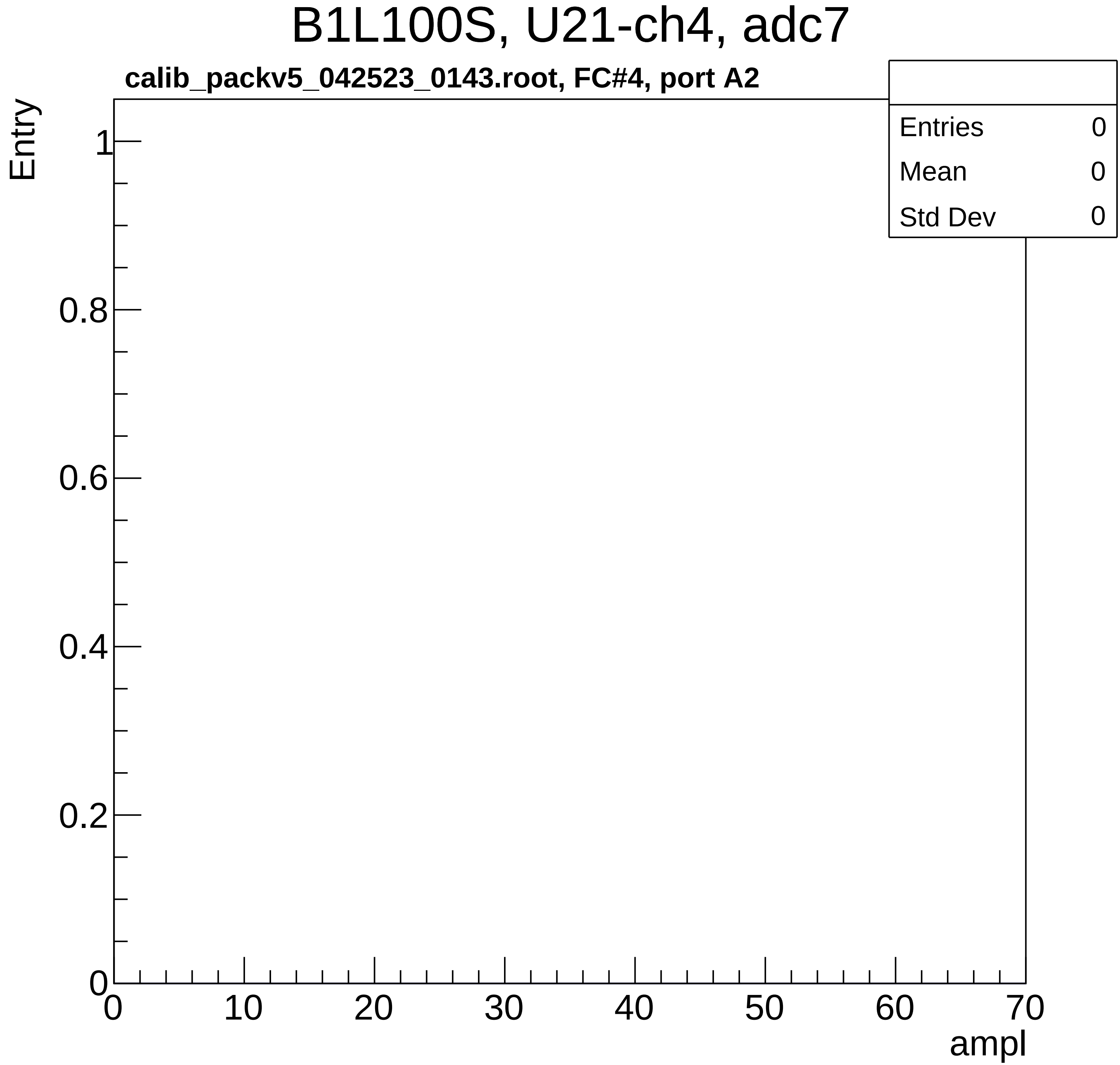

B1L100S, U21-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 56.84
Std Dev 3.183
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

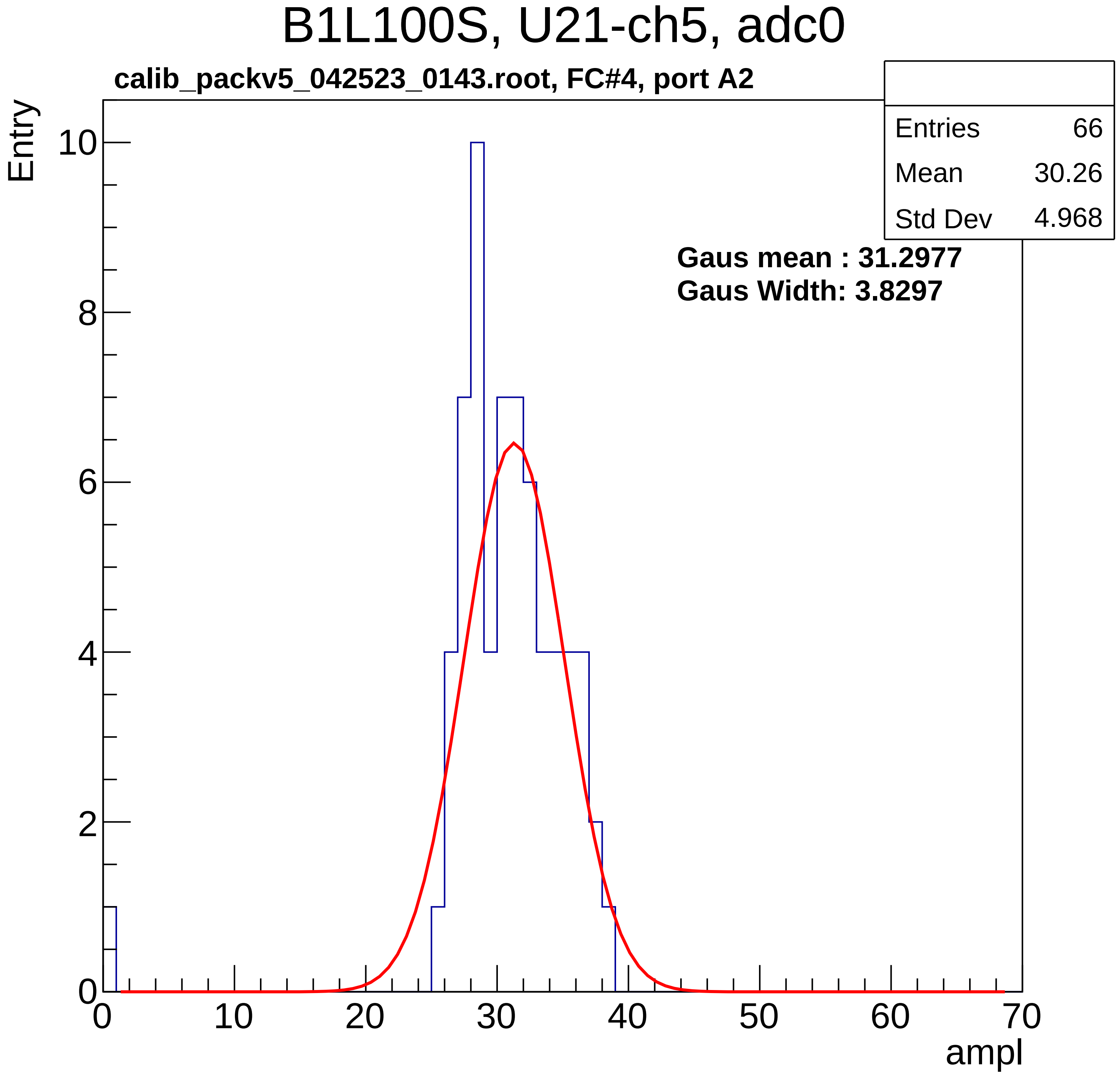

B1L100S, U21-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.65
Std Dev 9.13
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

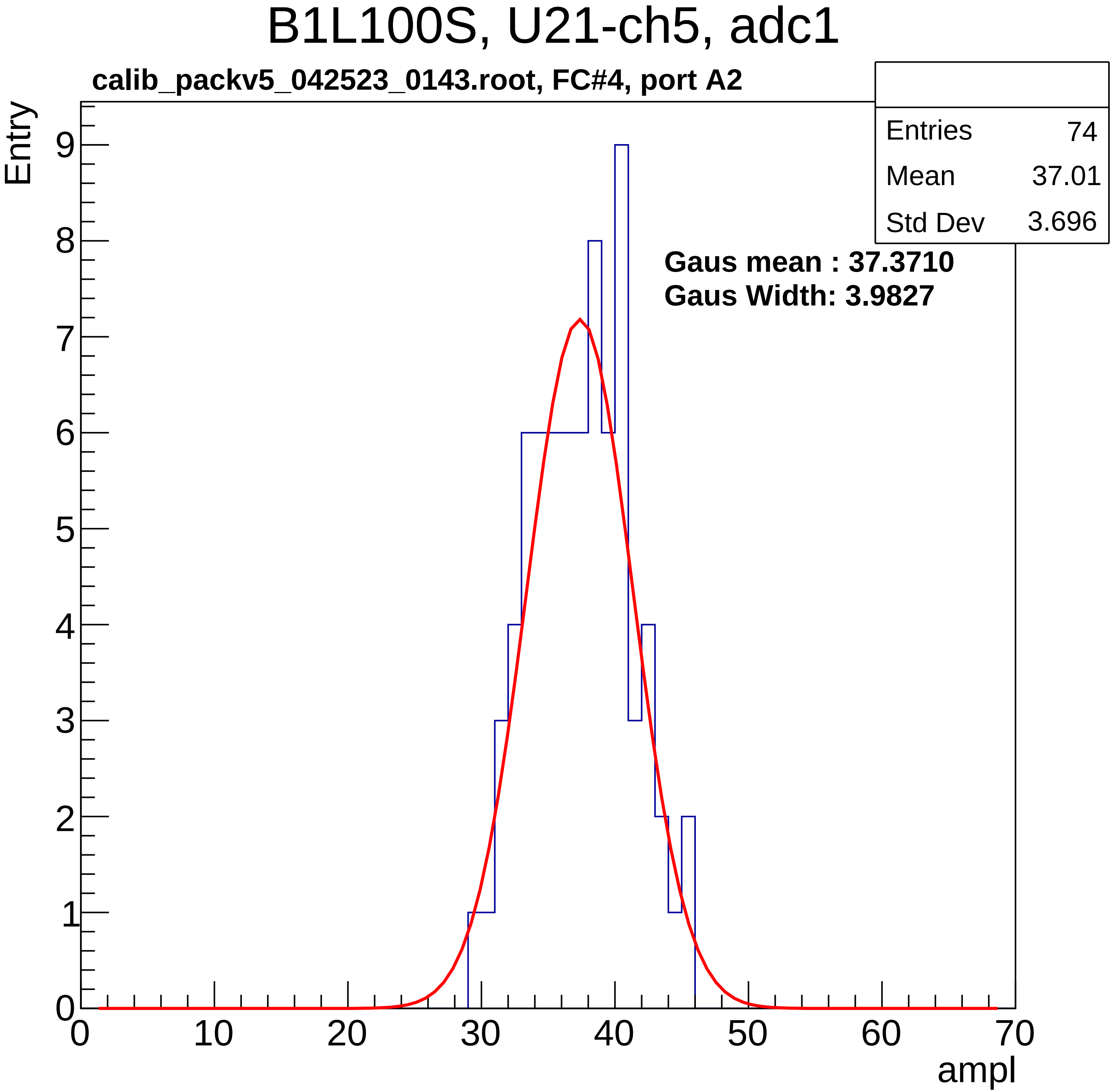
B1L100S, U21-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2



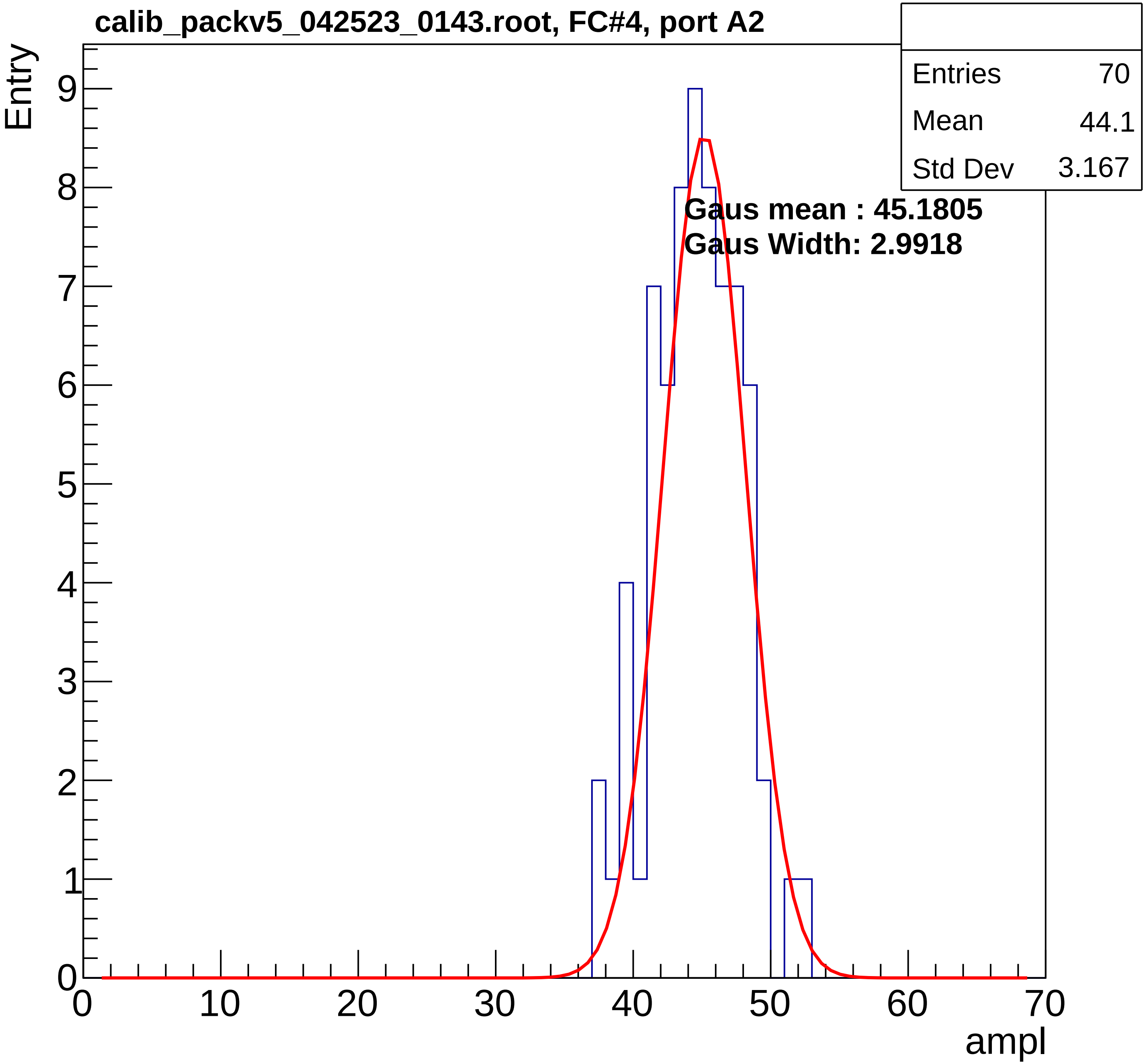

B1L100S, U21-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.26
Std Dev 4.968
Gaus mean : 31.2977
Gaus Width: 3.8297
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

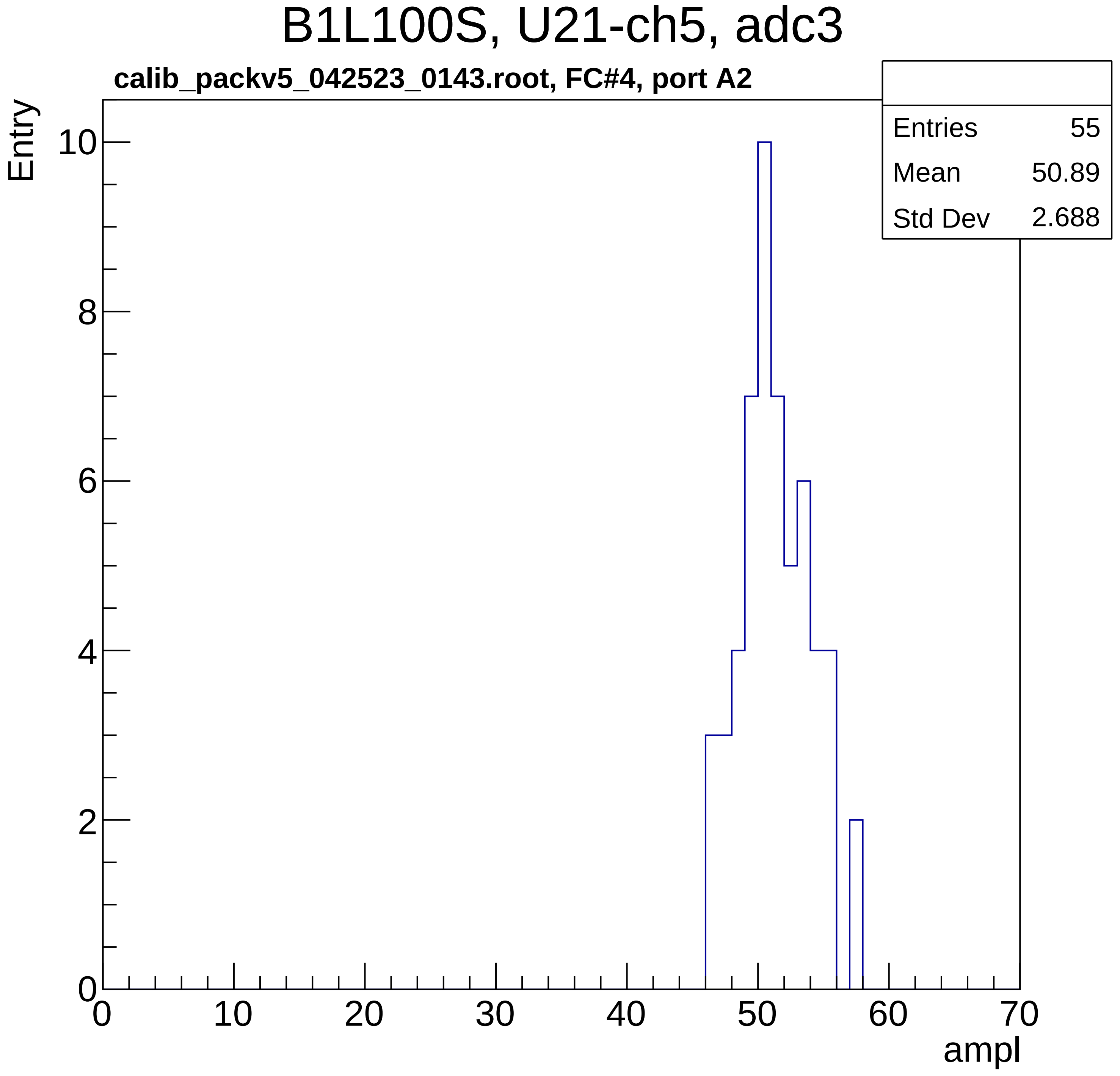

B1L100S, U21-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 74
Mean 37.01
Std Dev 3.696
Gaus mean : 37.3710
Gaus Width: 3.9827
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

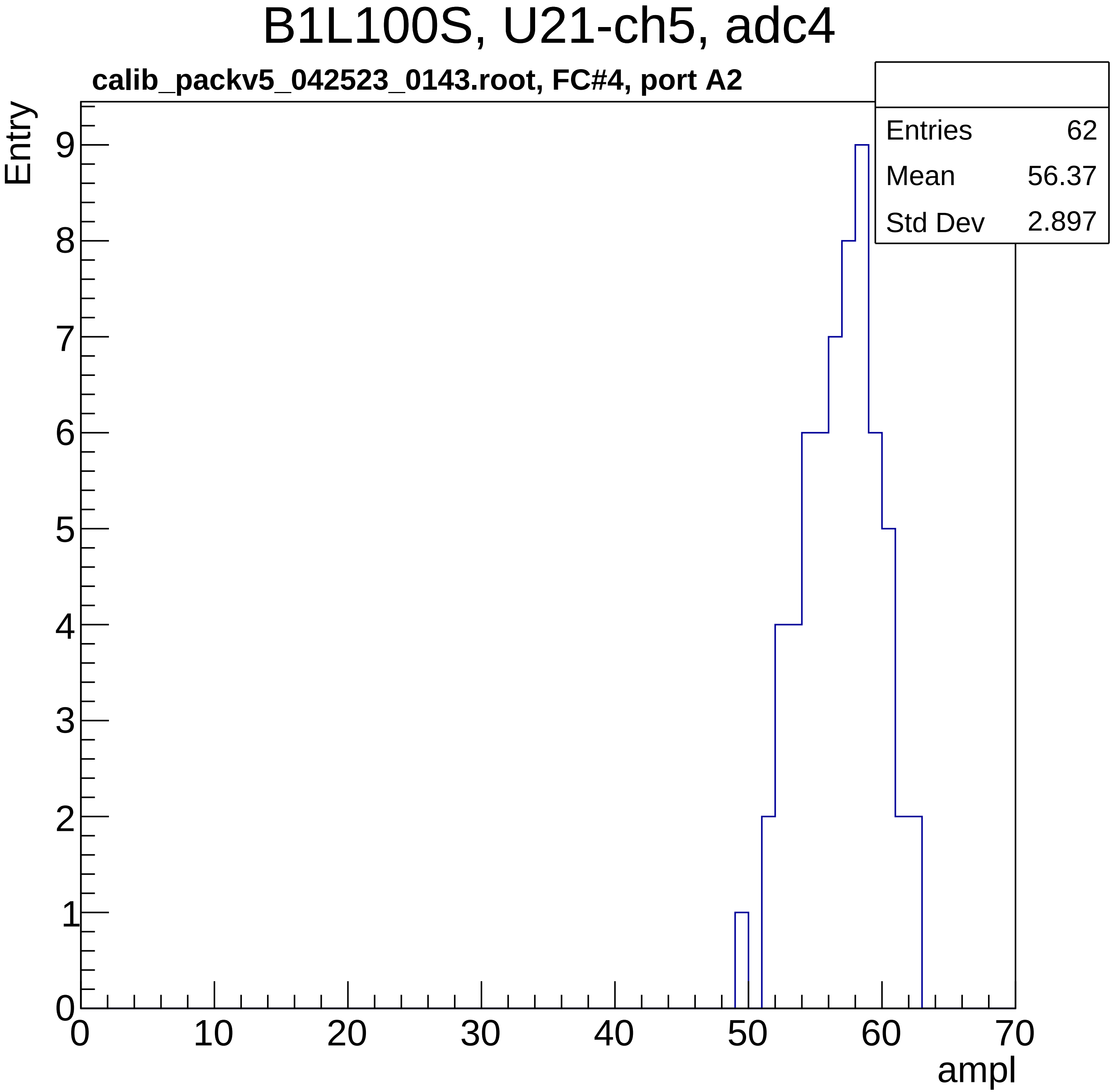
B1L100S, U21-ch5, adc2

calib_packv5_042523_0143.root, FC#4, port A2



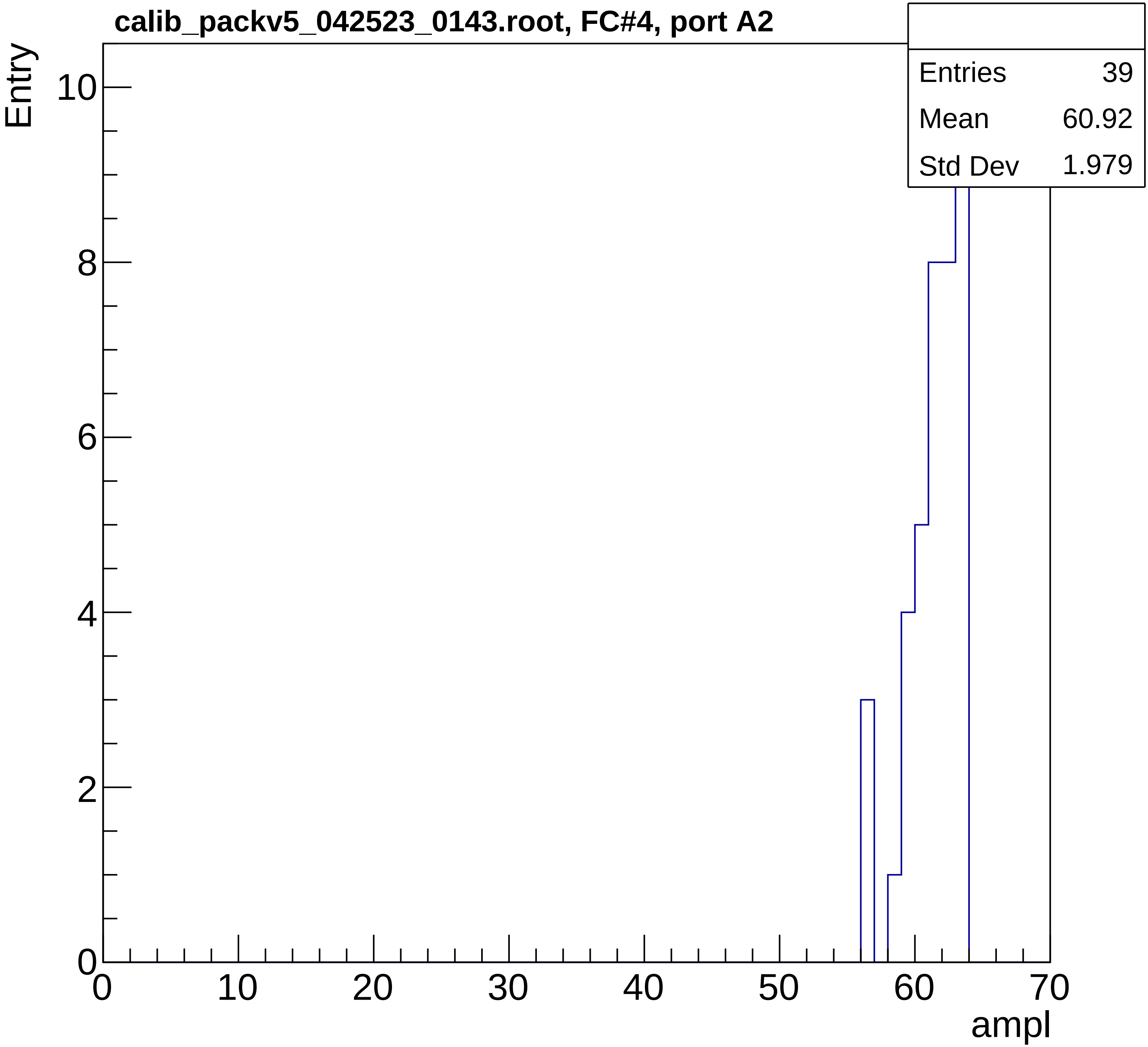
Entries	70
Mean	44.1
Std Dev	3.167

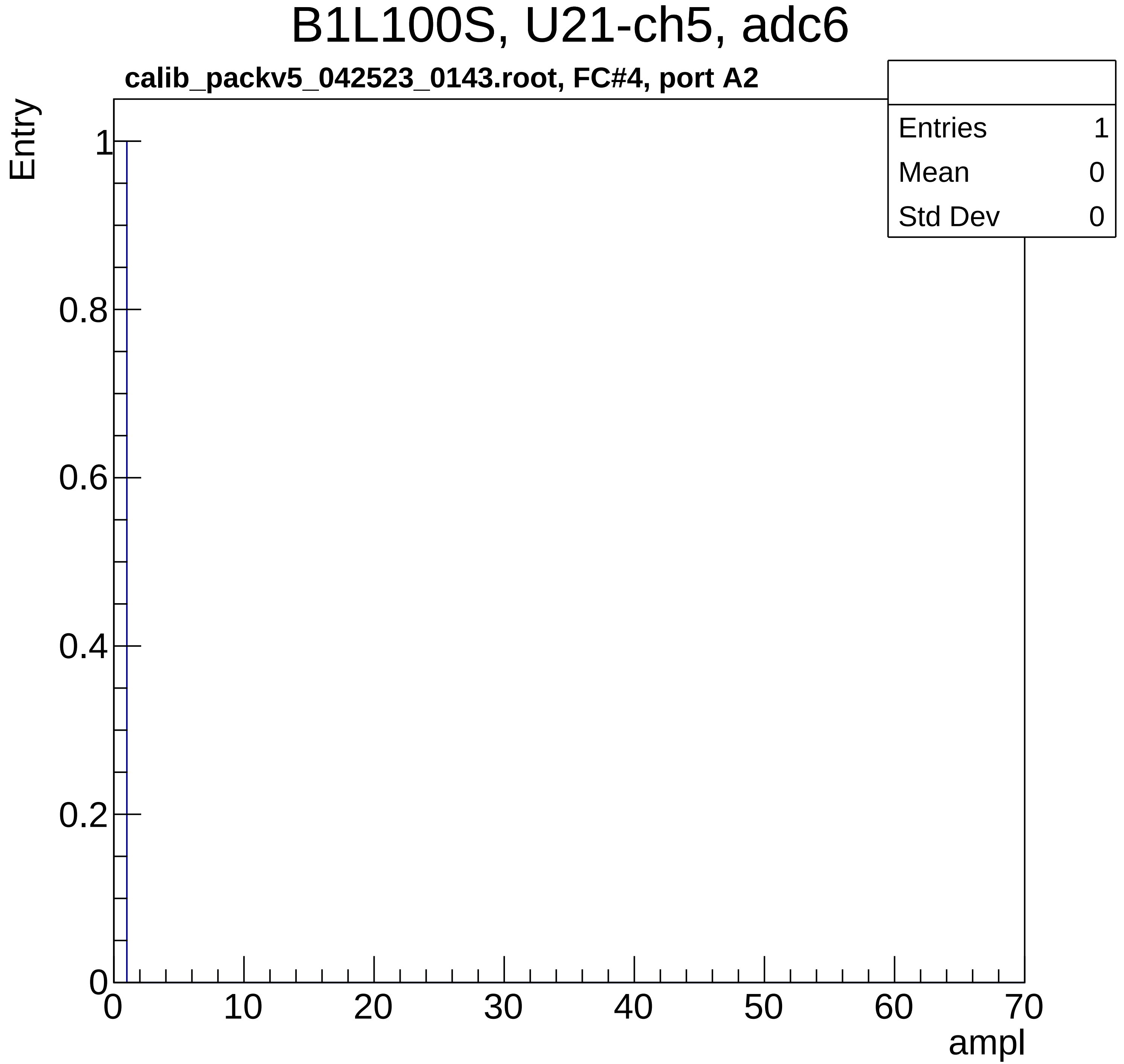

B1L100S, U21-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.89
Std Dev 2.688
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 56.37
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch5, adc5

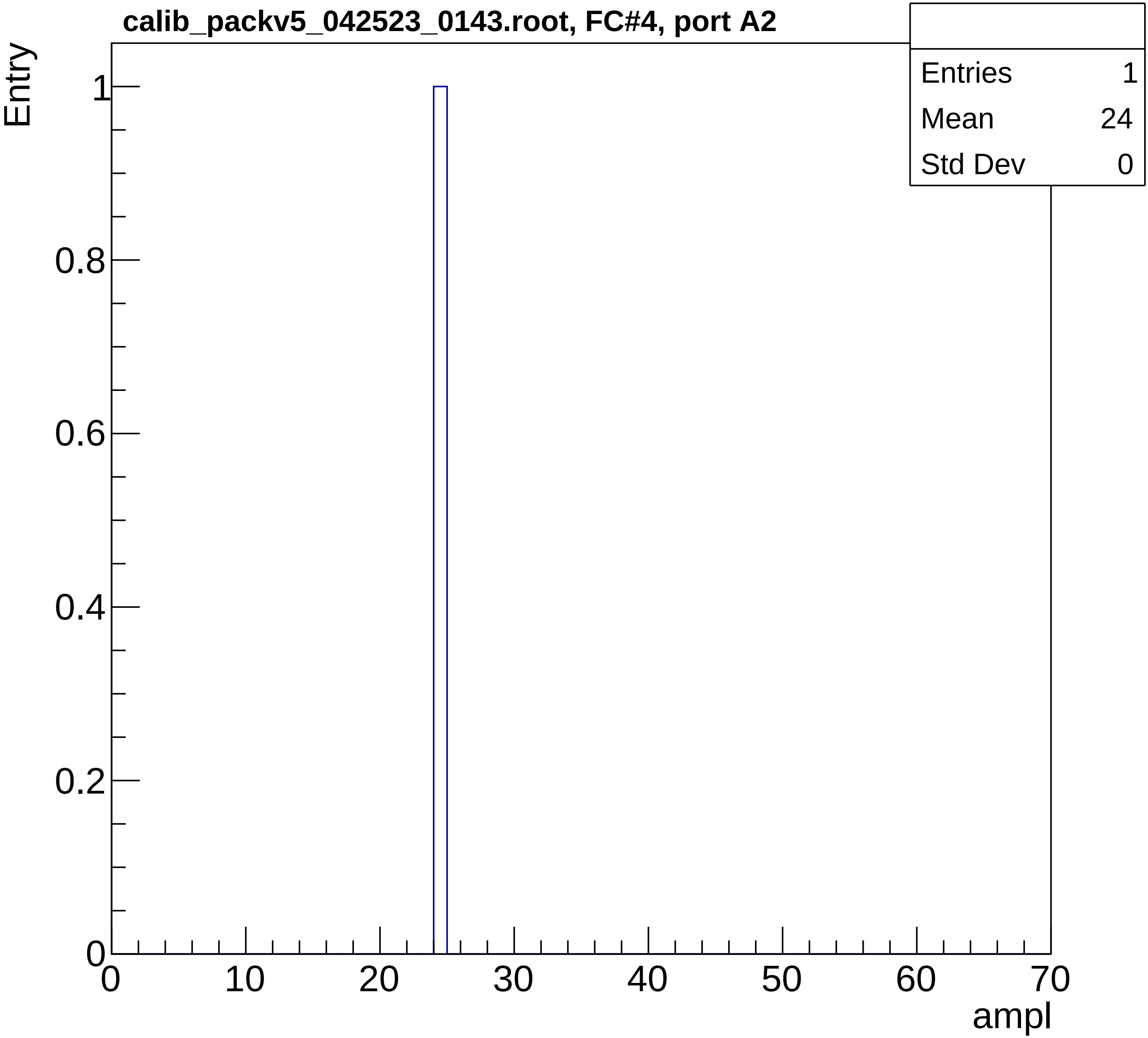
calib_packv5_042523_0143.root, FC#4, port A2

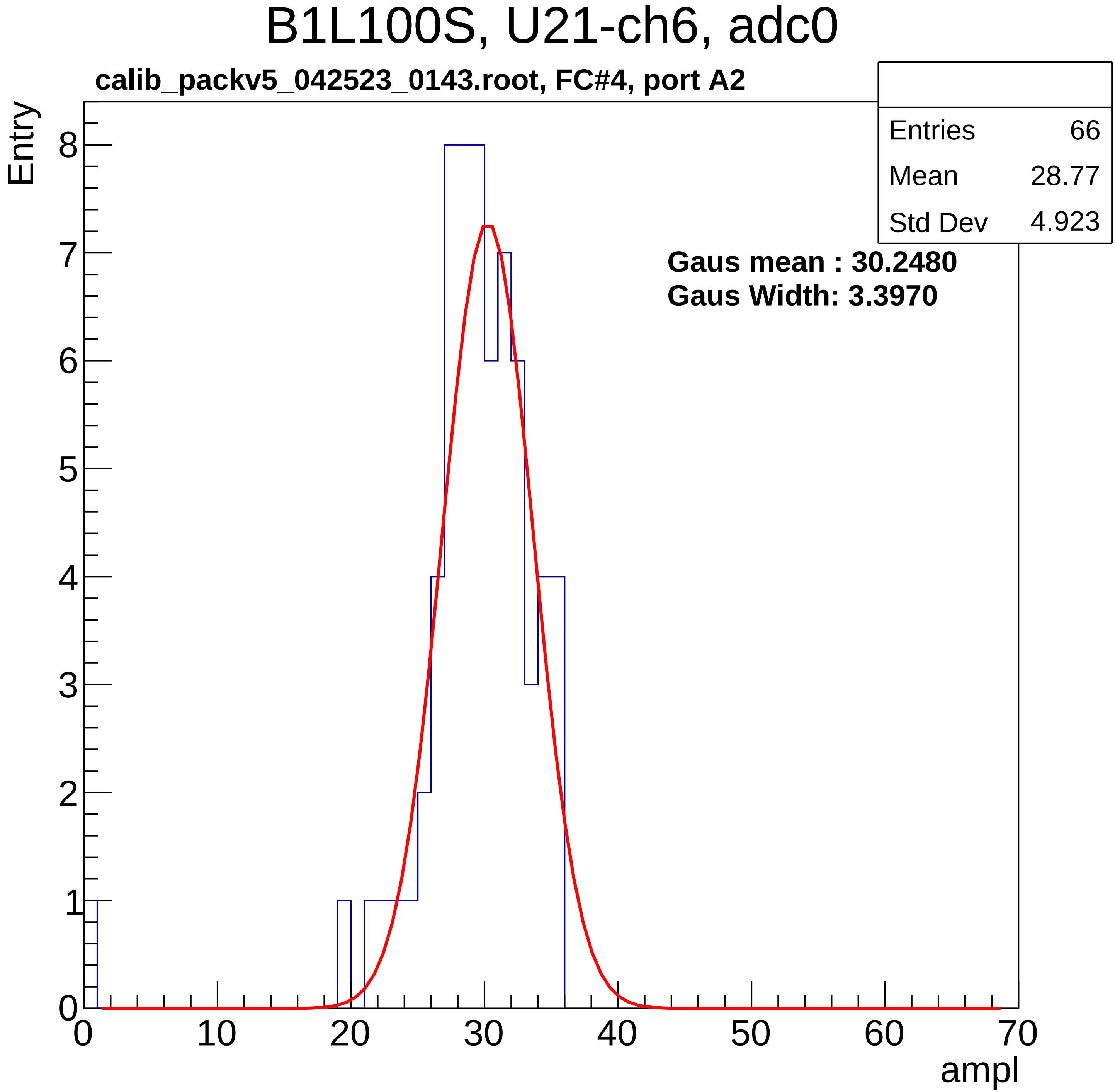


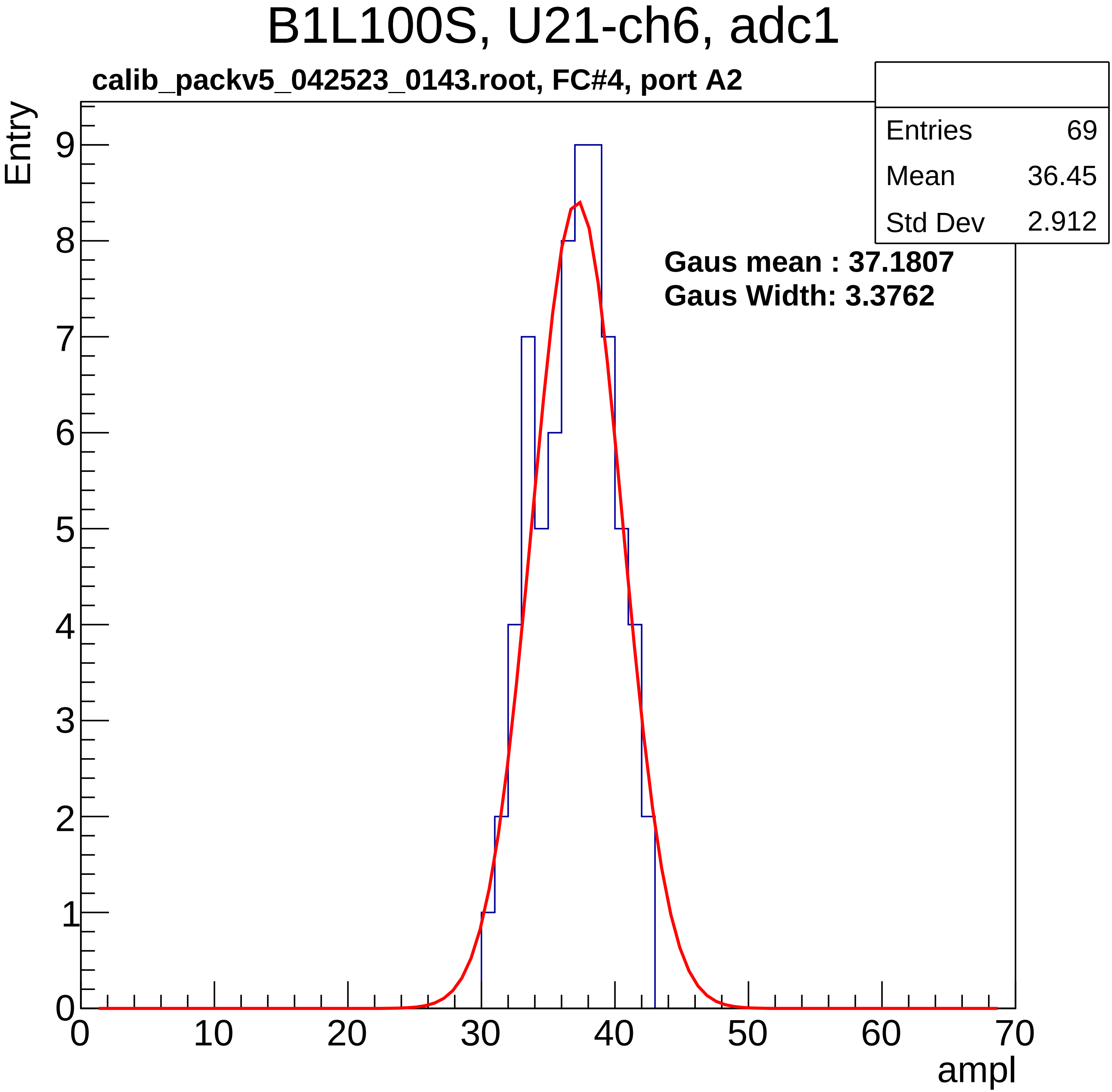

B1L100S, U21-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2



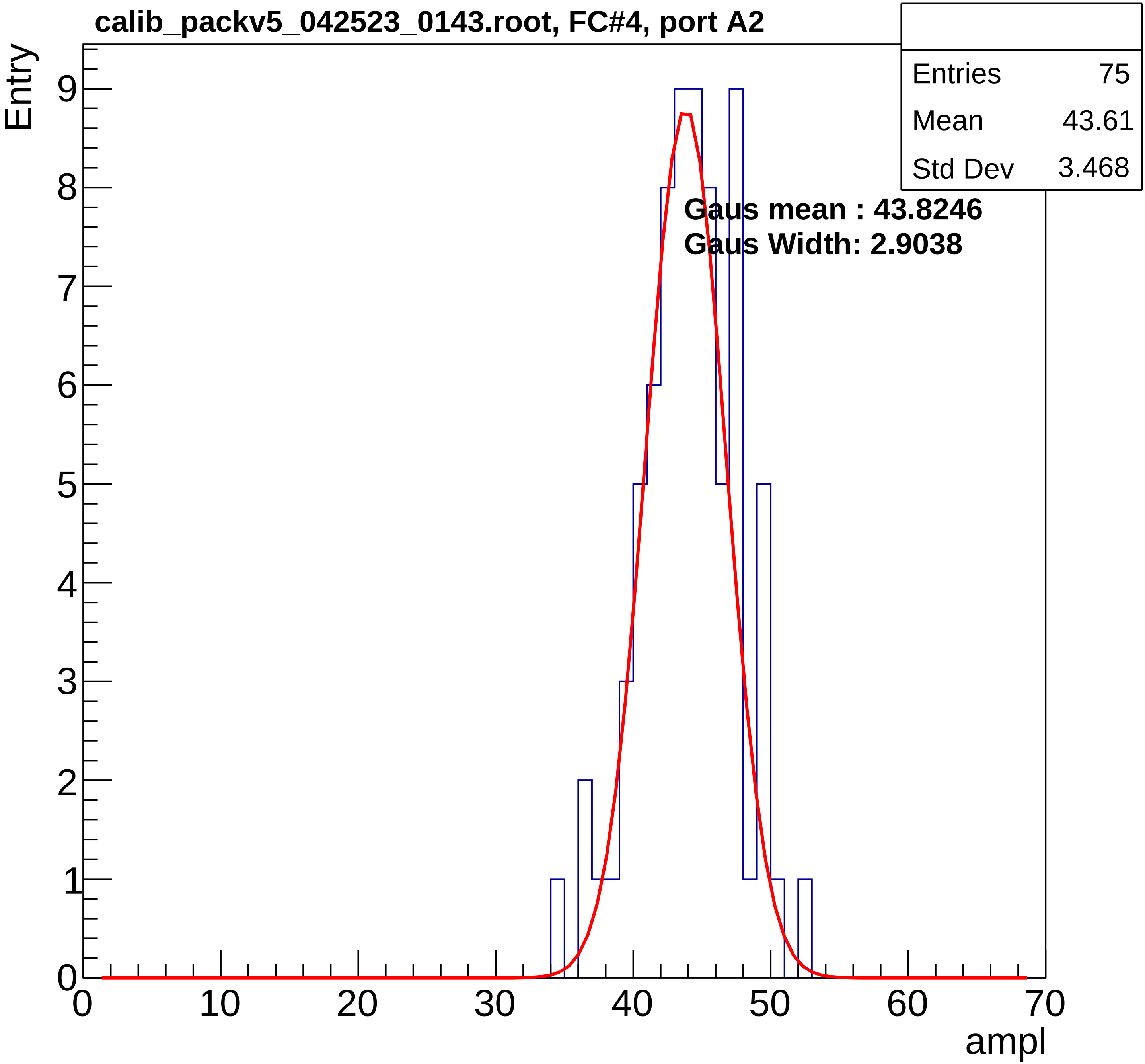

B1L100S, U21-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.77
Std Dev 4.923
Gaus mean : 30.2480
Gaus Width: 3.3970
Entry
8
7
6
5
4
3
2
1
0
0
10
20
30
40
50
60
70
ampl



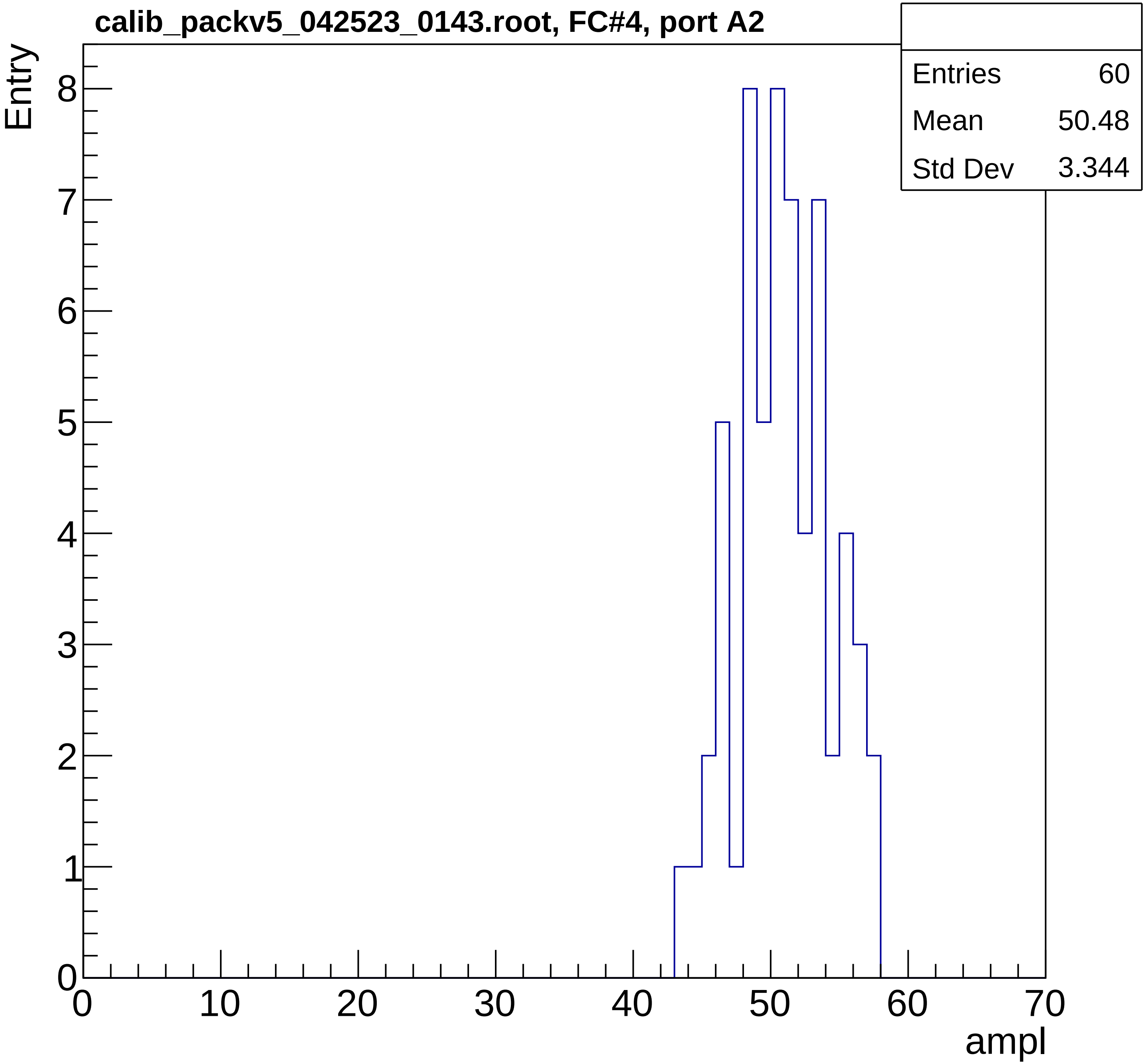
B1L100S, U21-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.45
Std Dev 2.912
Gaus mean : 37.1807
Gaus Width: 3.3762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

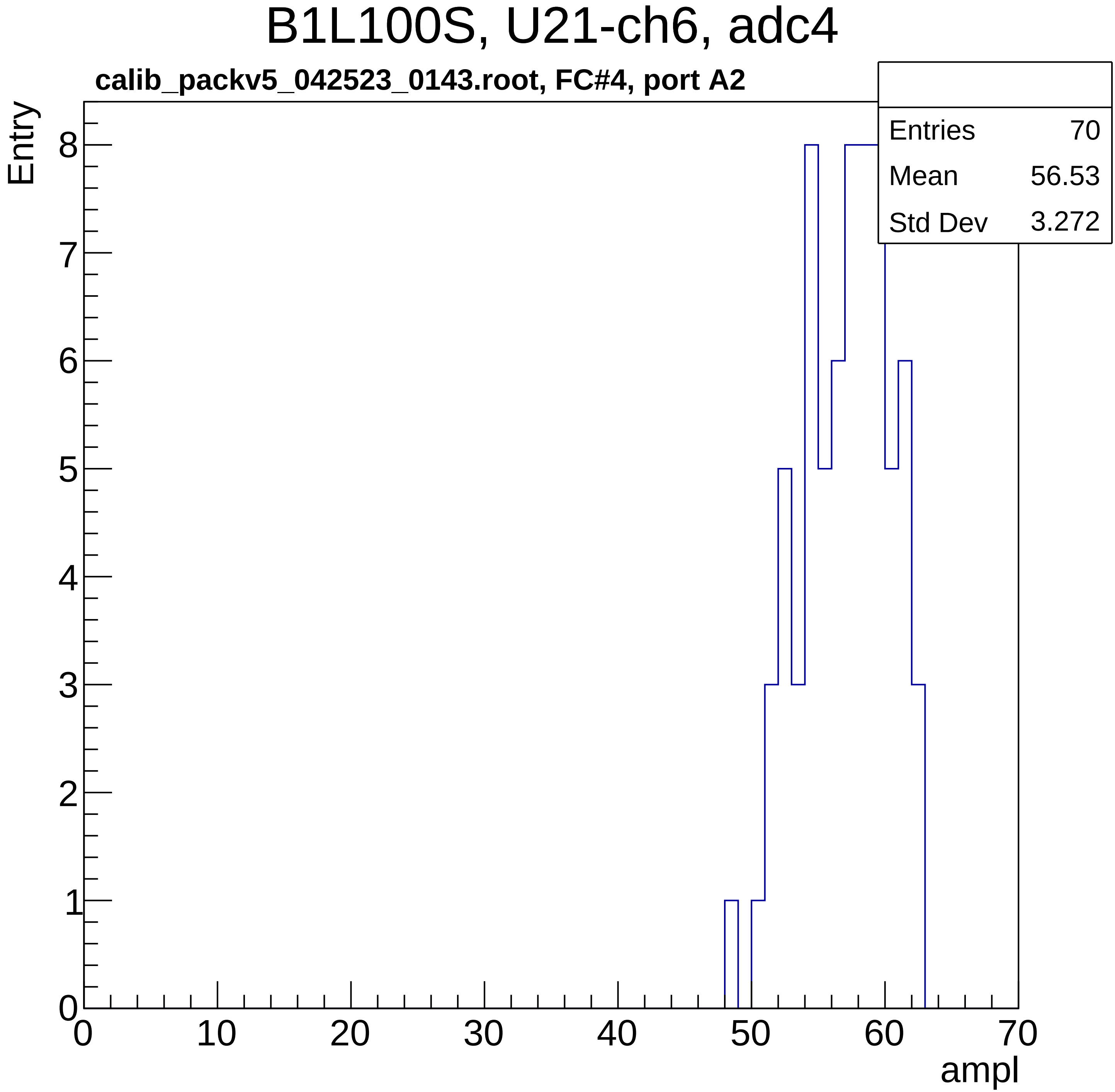
B1L100S, U21-ch6, adc2

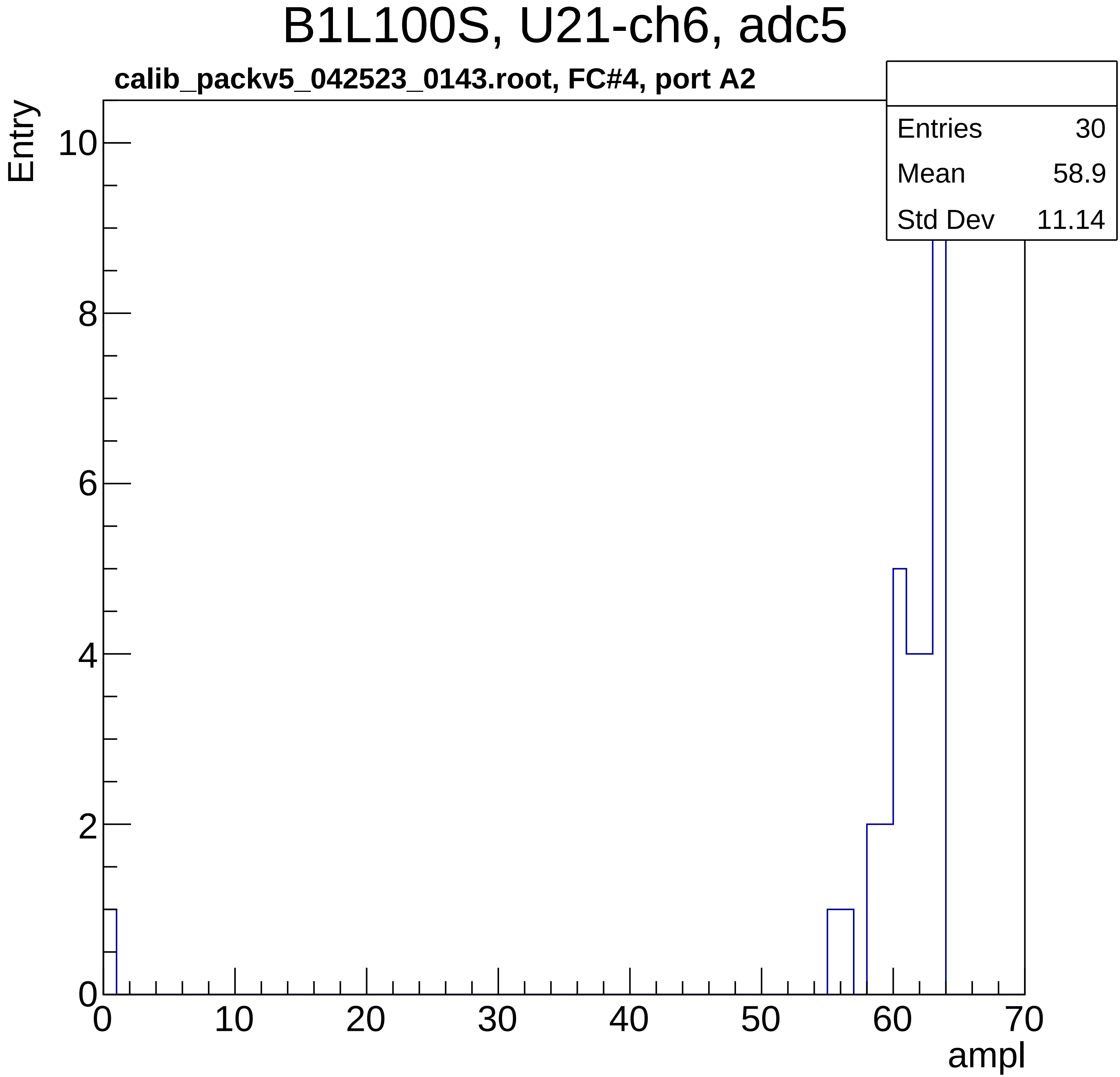
calib_packv5_042523_0143.root, FC#4, port A2

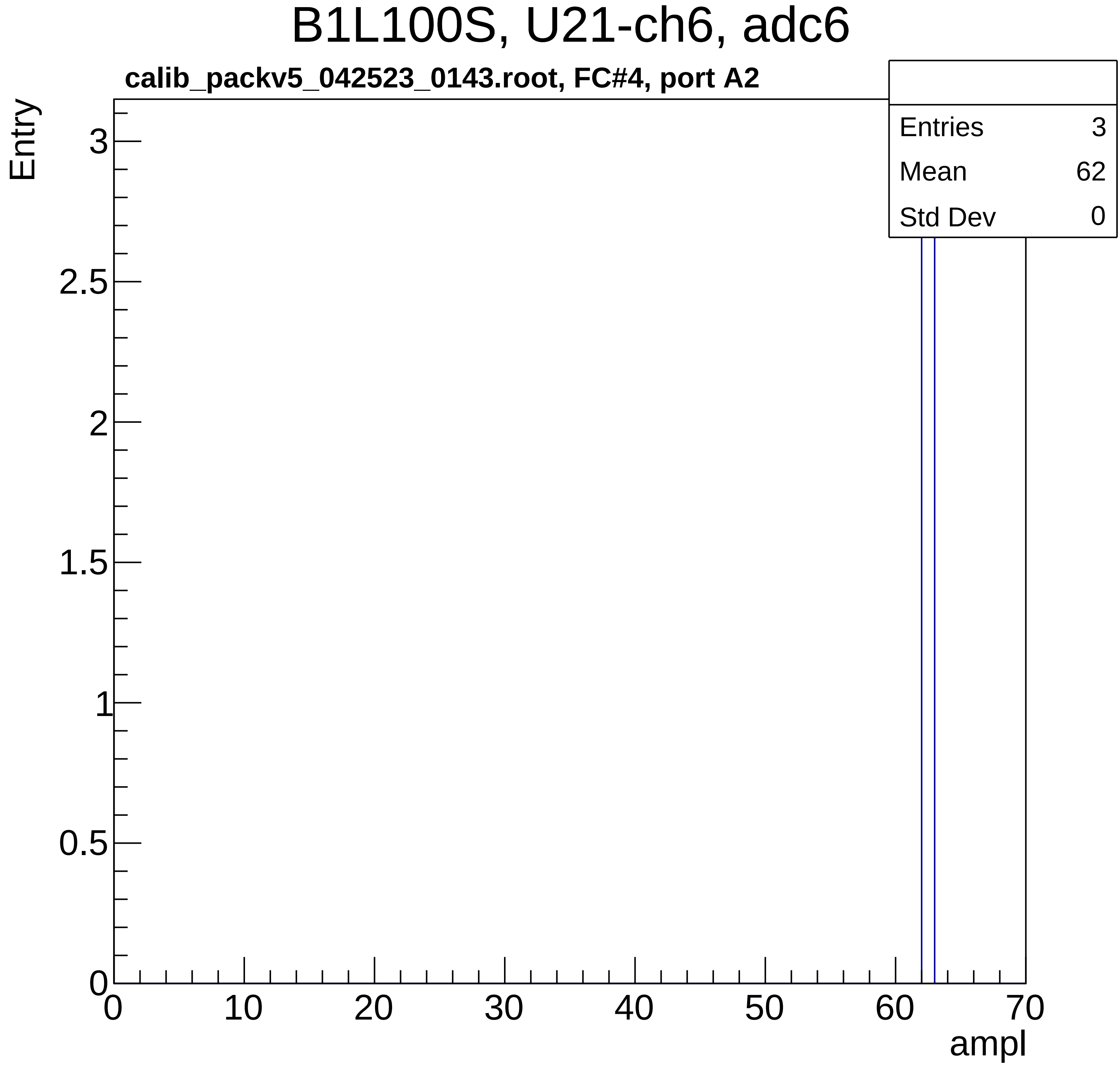


B1L100S, U21-ch6, adc3

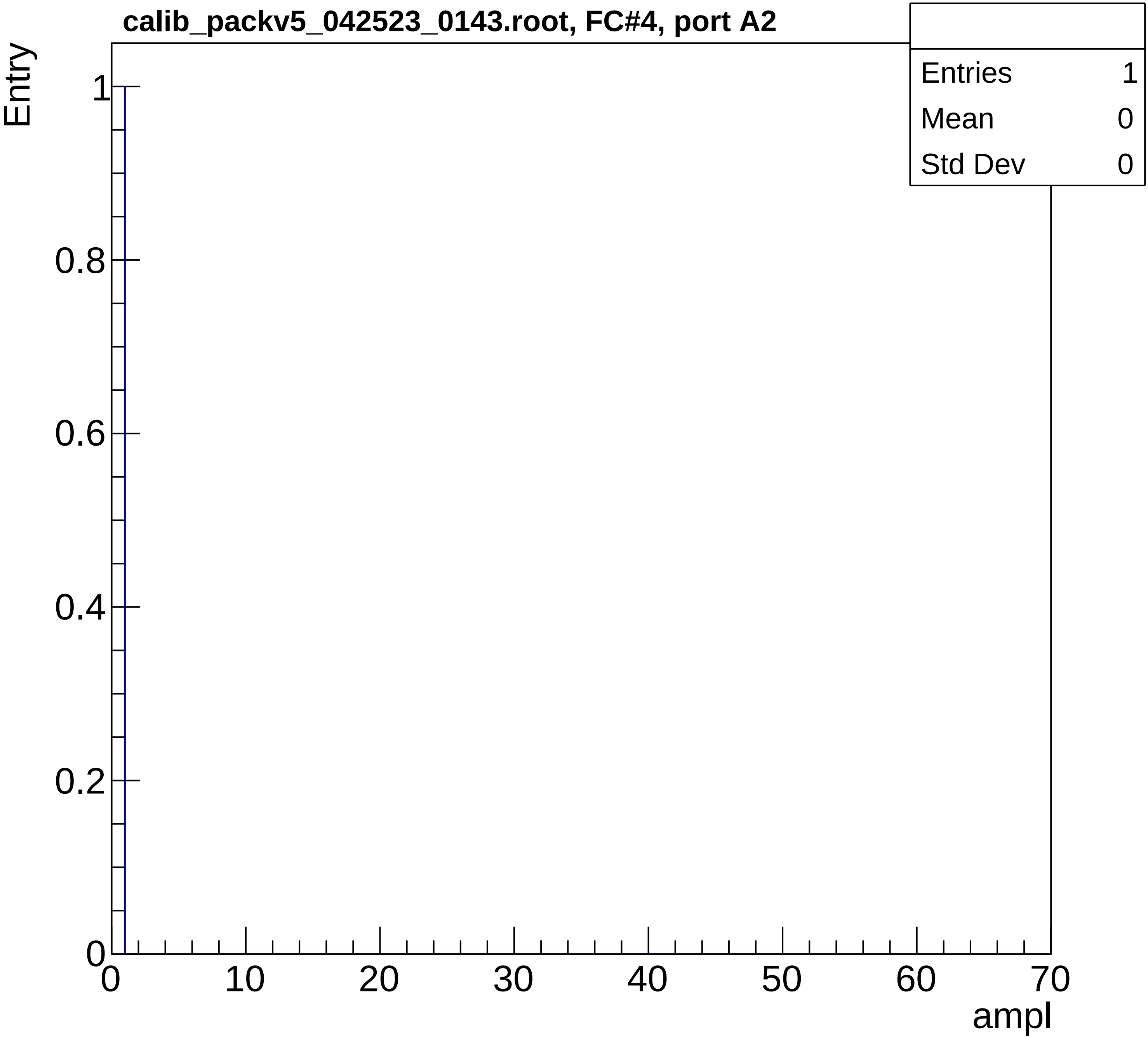


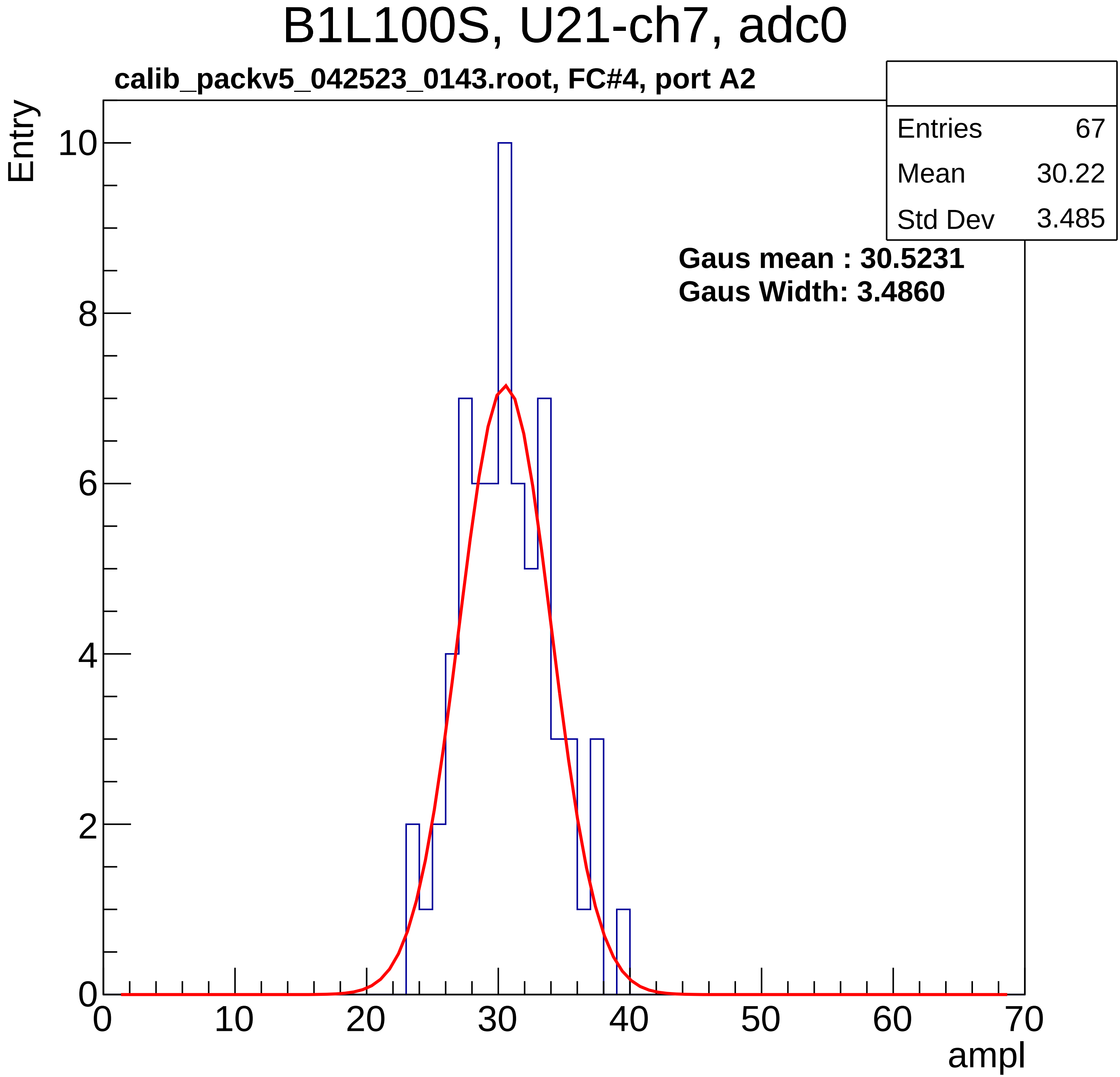

B1L100S, U21-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 56.53
Std Dev 3.272
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.9
Std Dev 11.14
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

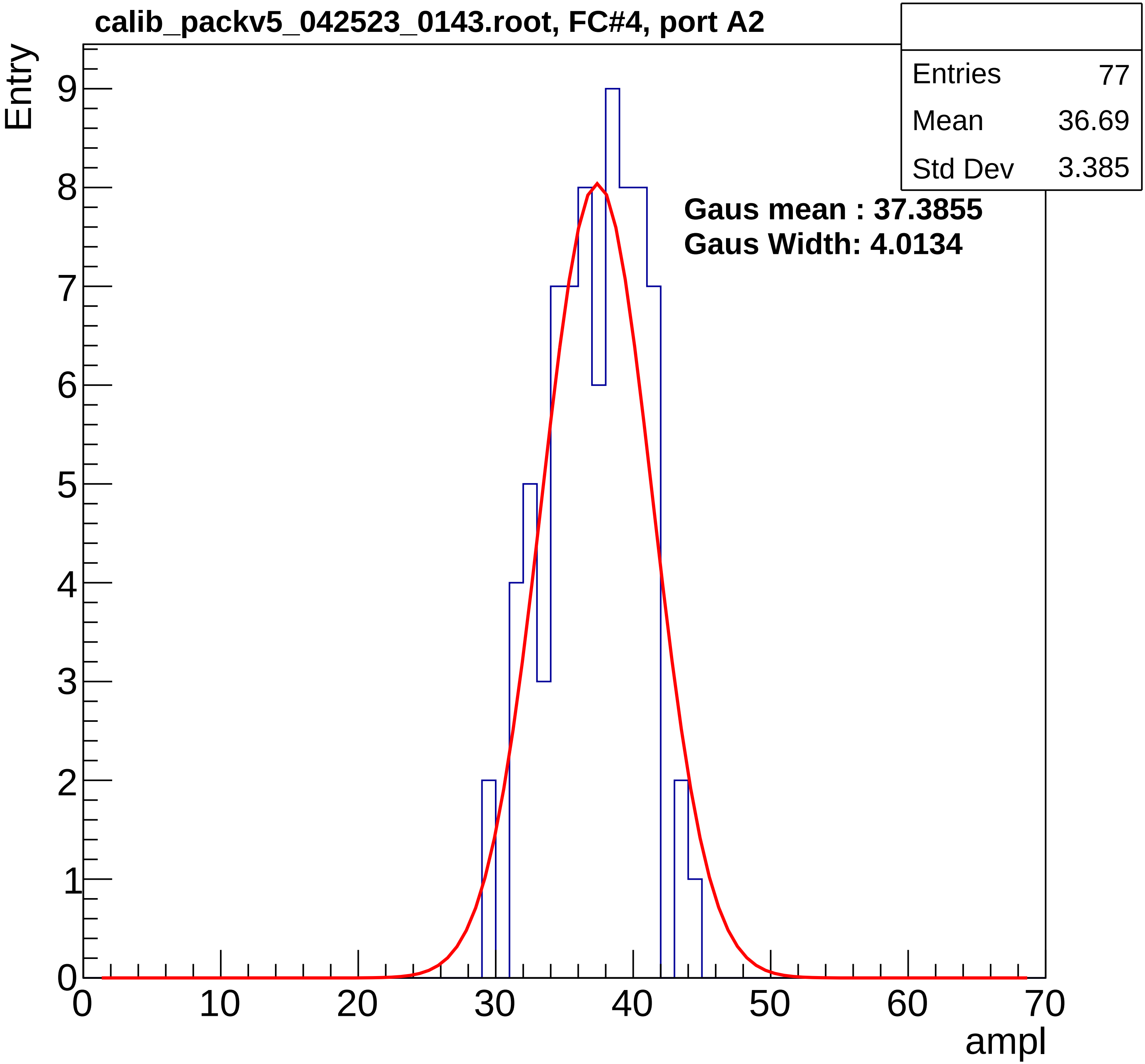

B1L100S, U21-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

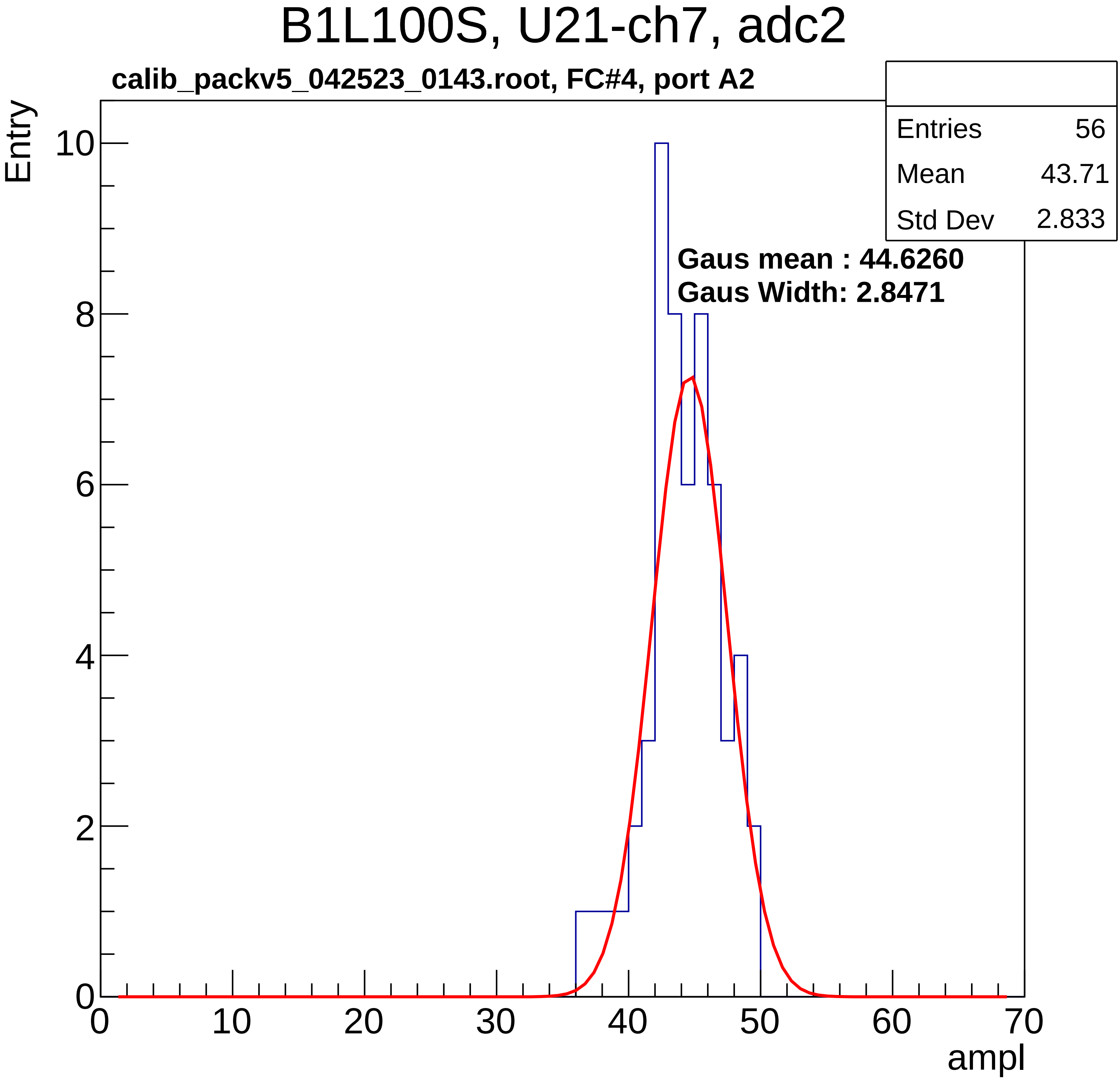
B1L100S, U21-ch6, adc7



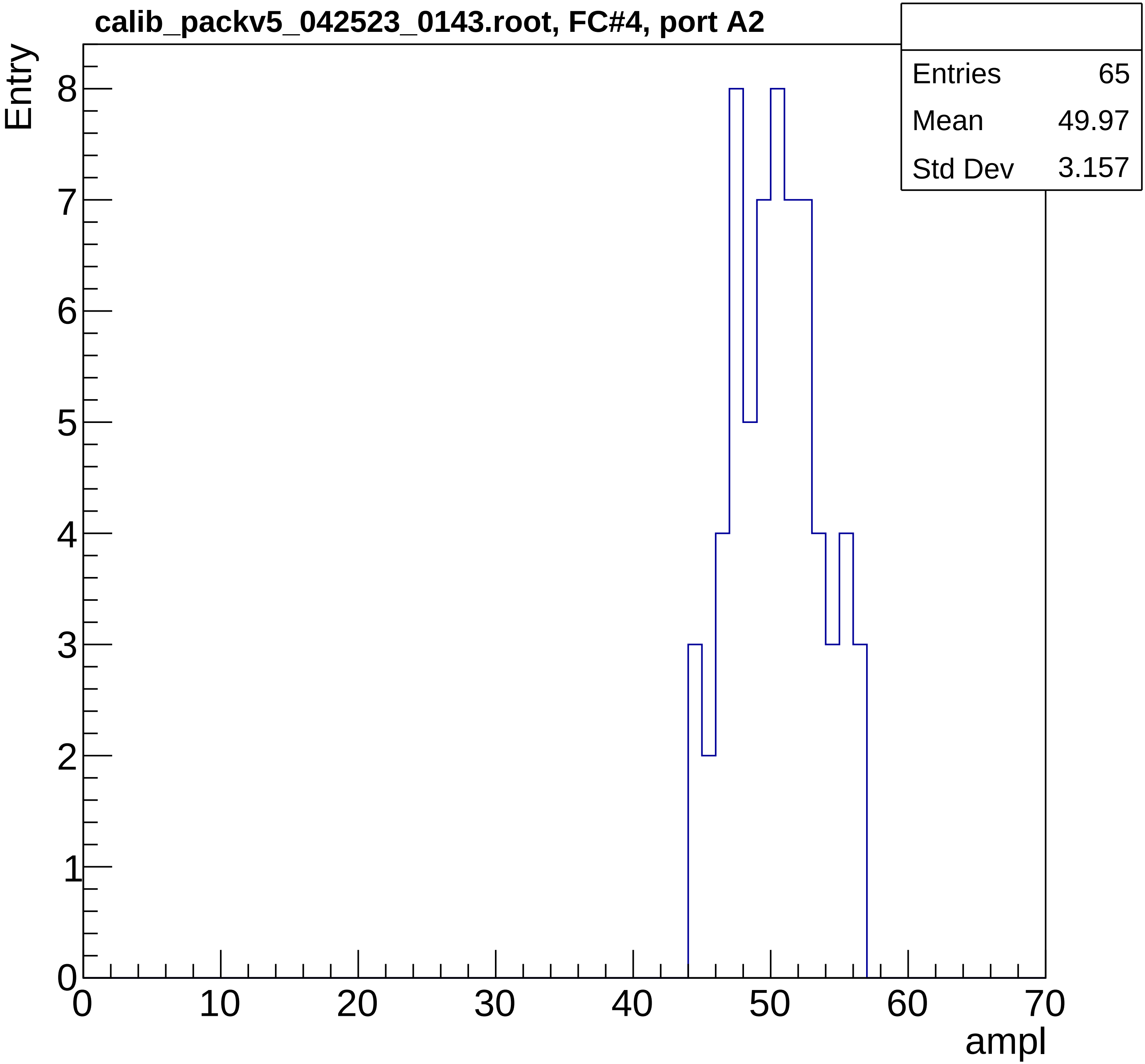

B1L100S, U21-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.22
Std Dev 3.485
Gaus mean : 30.5231
Gaus Width: 3.4860
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

B1L100S, U21-ch7, adc1



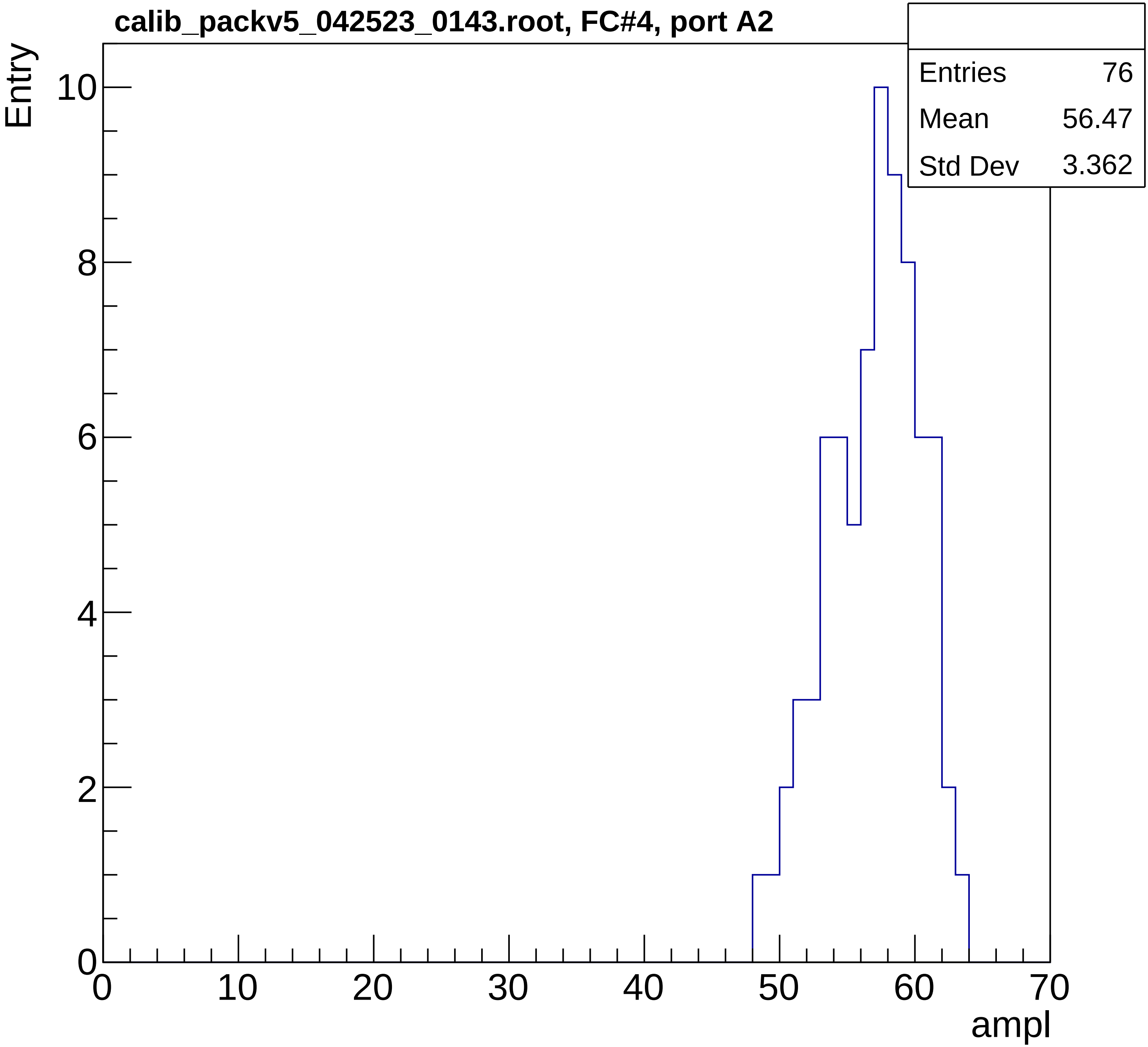

B1L100S, U21-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.71
Std Dev 2.833
Gaus mean : 44.6260
Gaus Width: 2.8471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

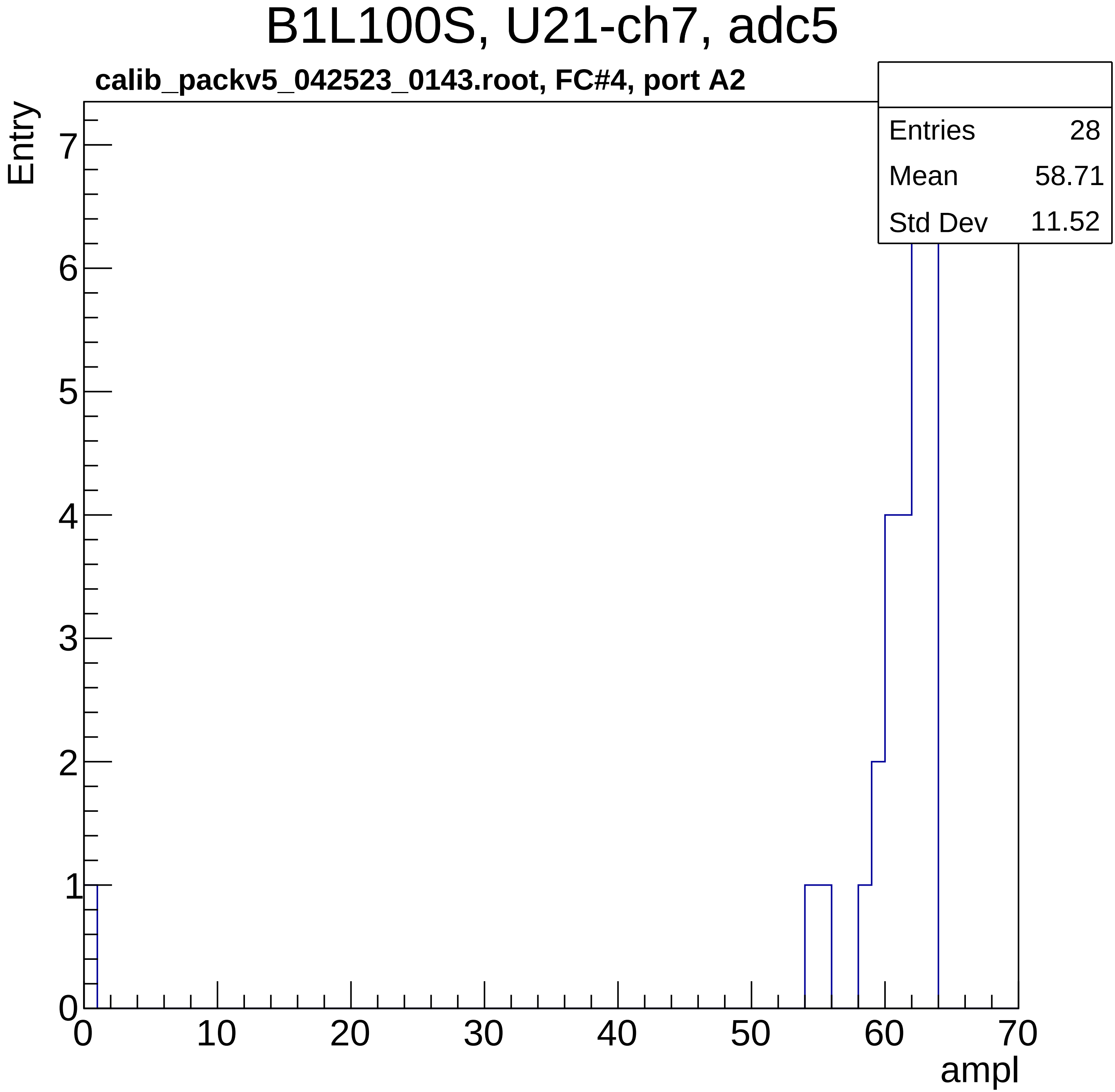
B1L100S, U21-ch7, adc3

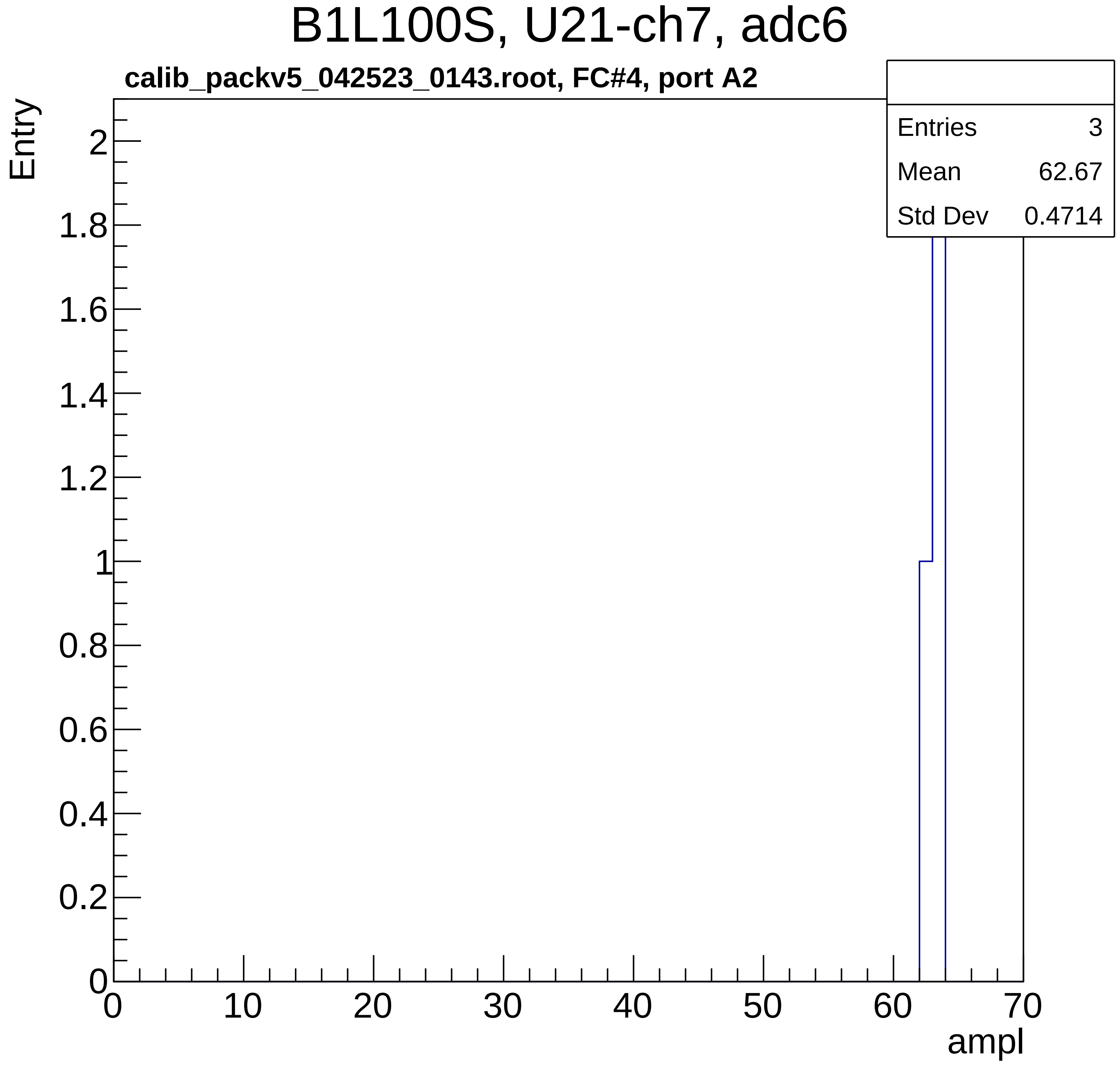


B1L100S, U21-ch7, adc4

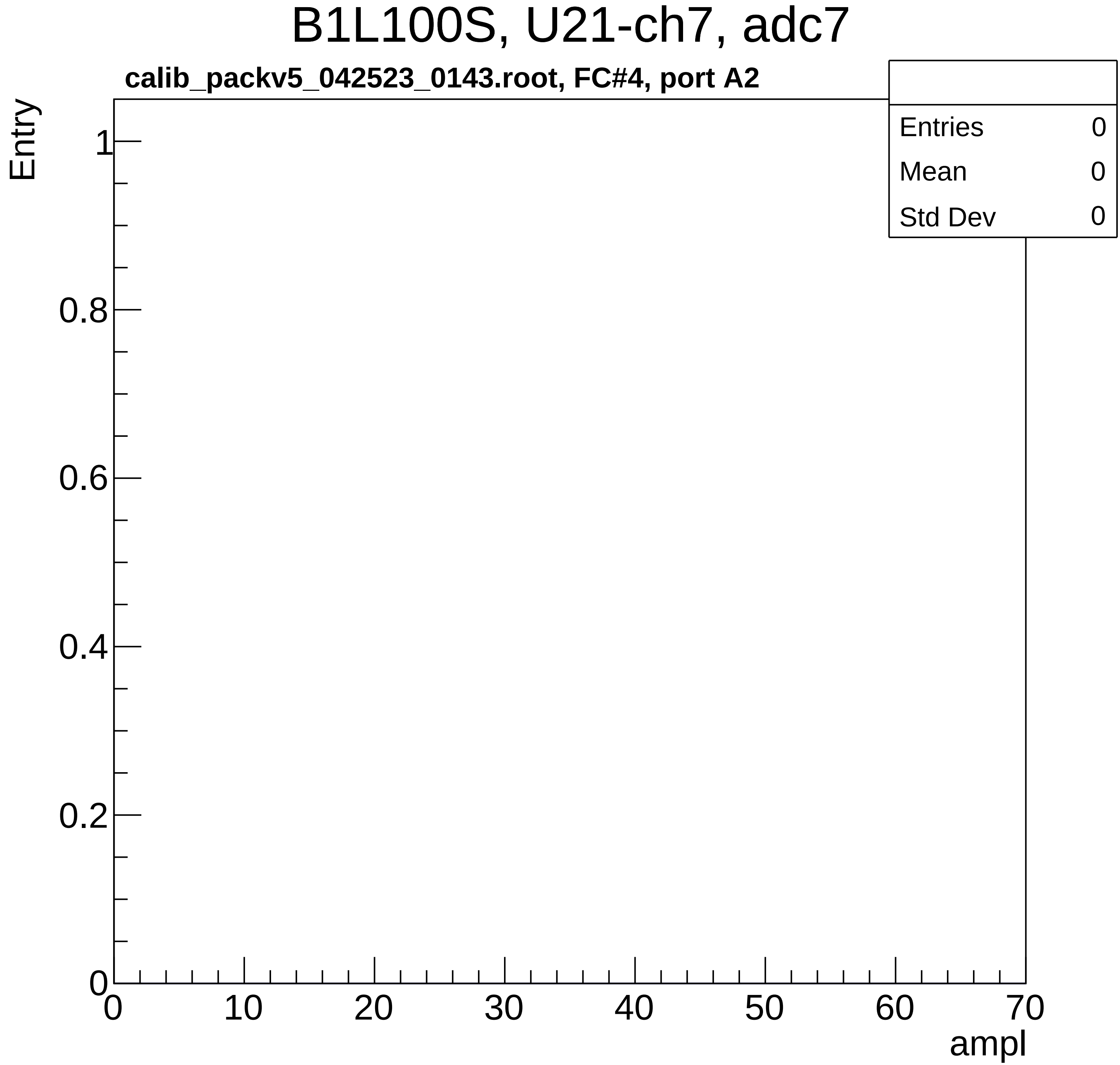
calib_packv5_042523_0143.root, FC#4, port A2

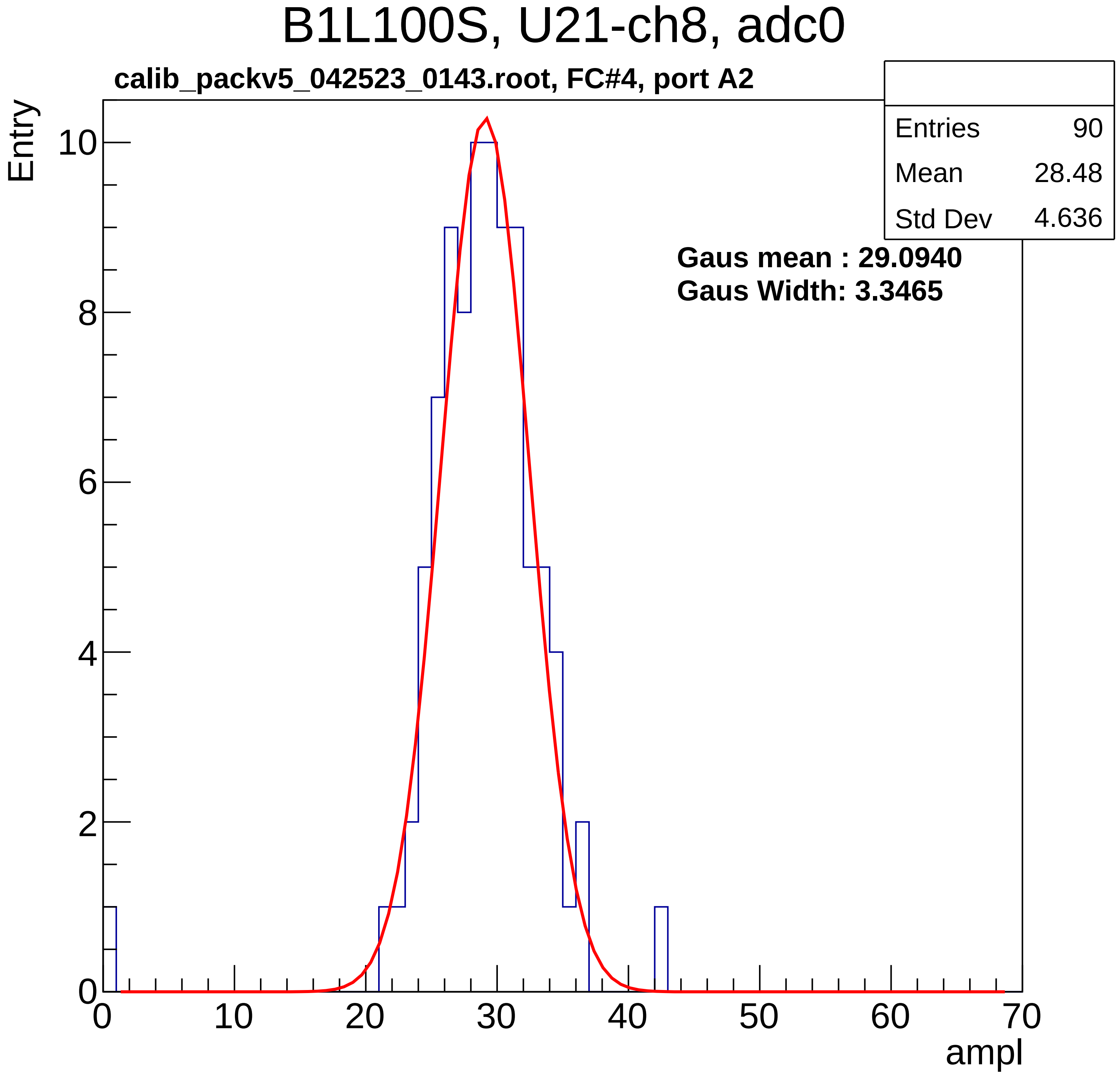


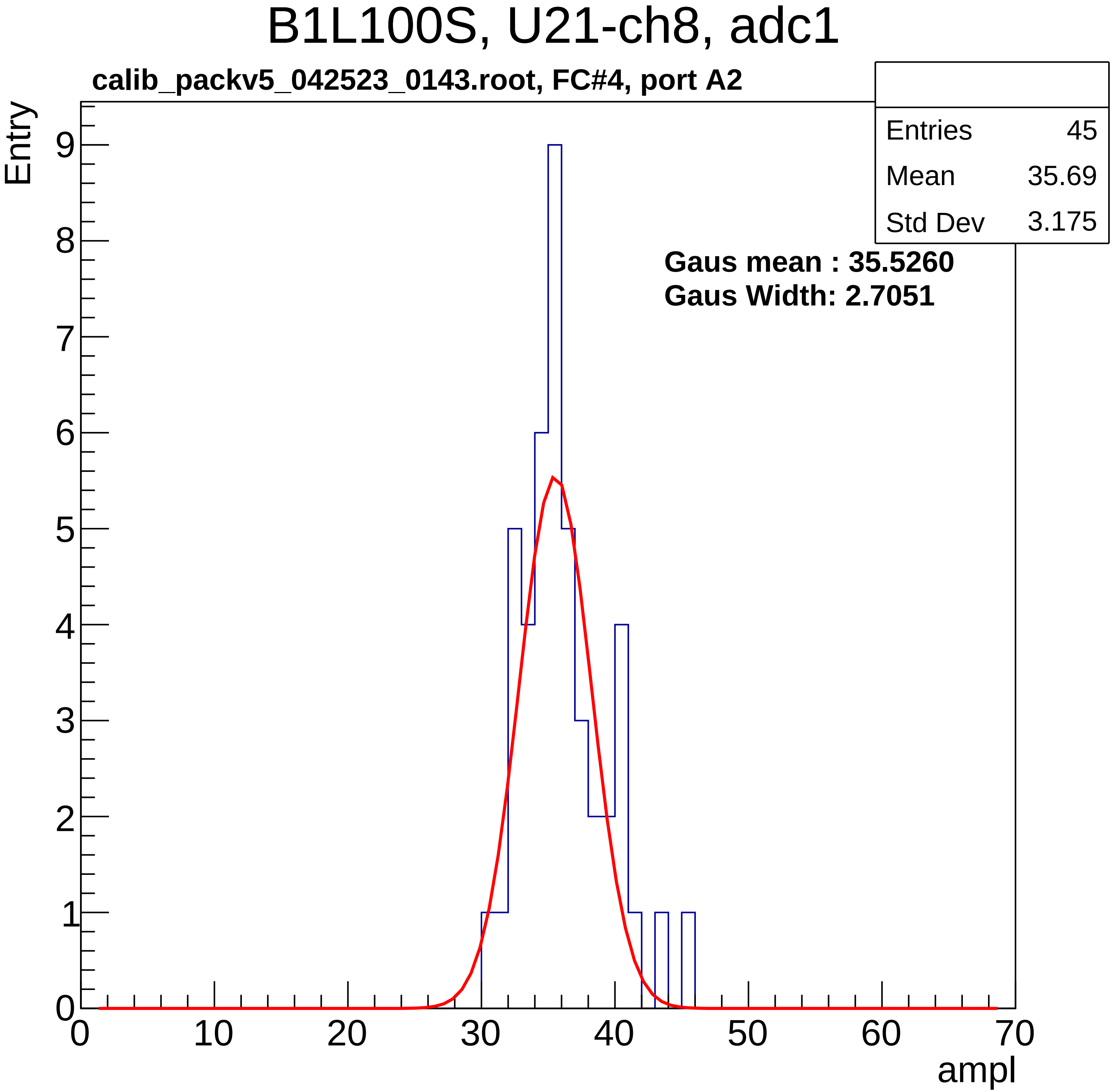

B1L100S, U21-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.71
Std Dev 11.52
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

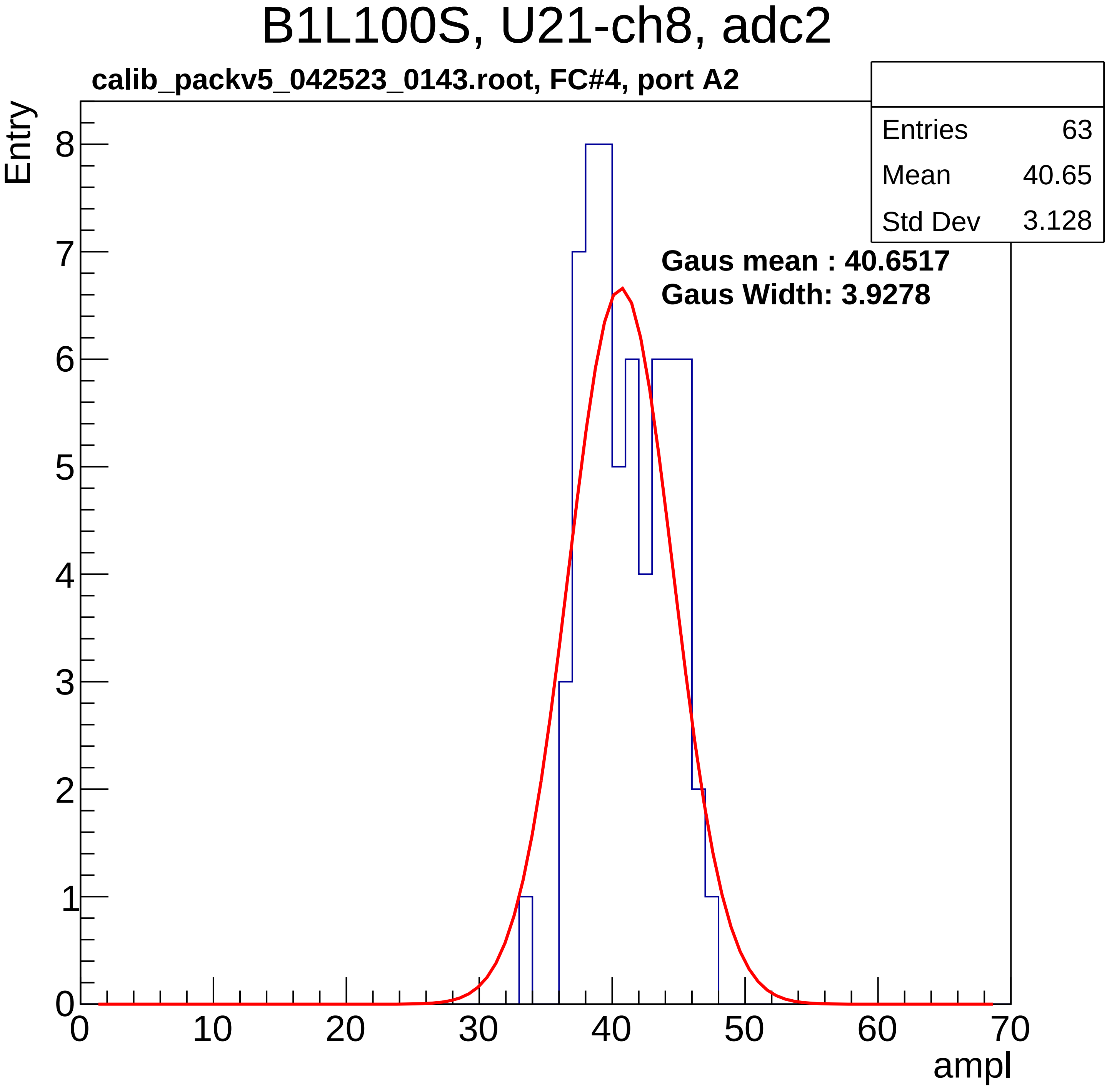


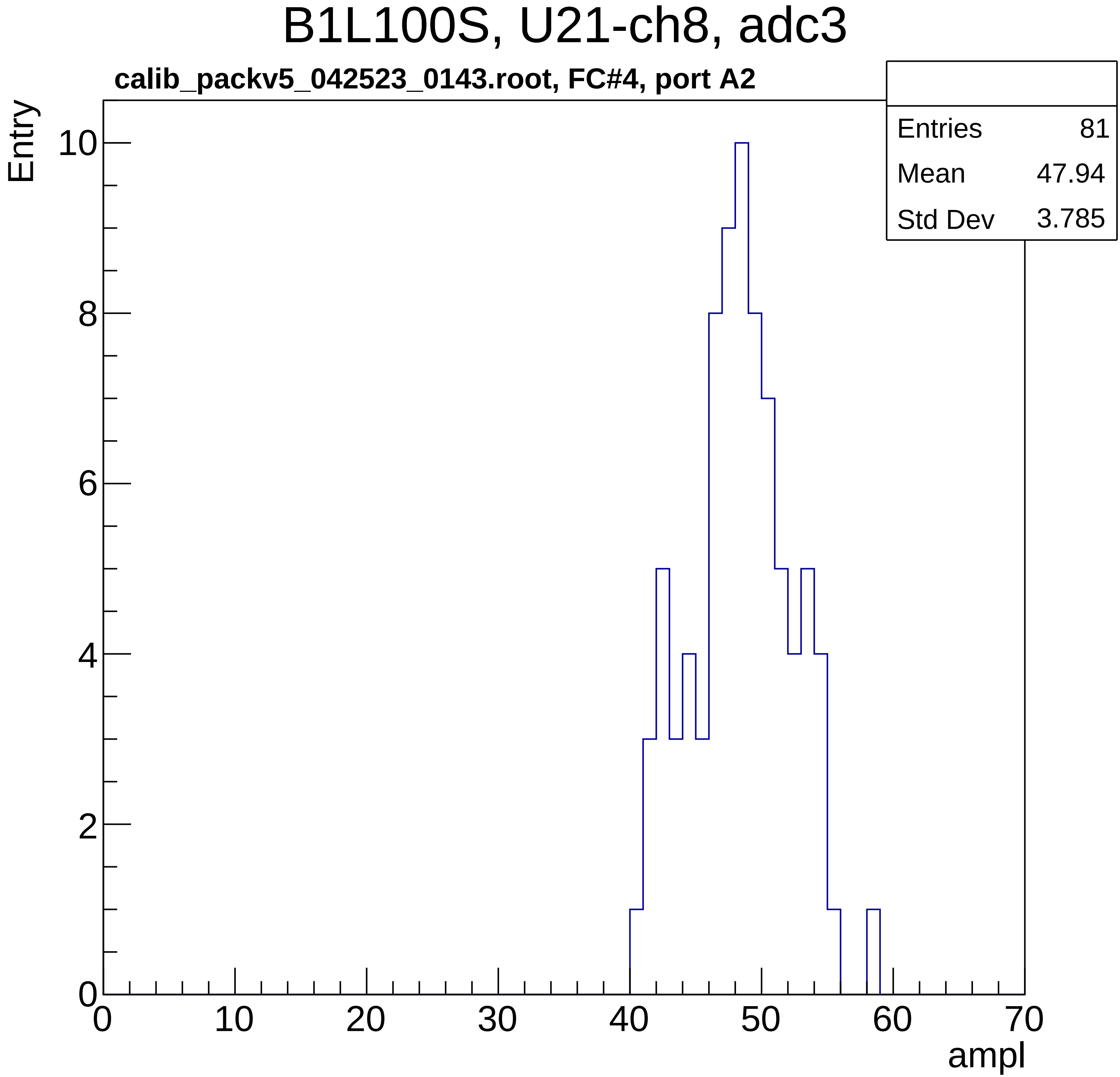
B1L100S, U21-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

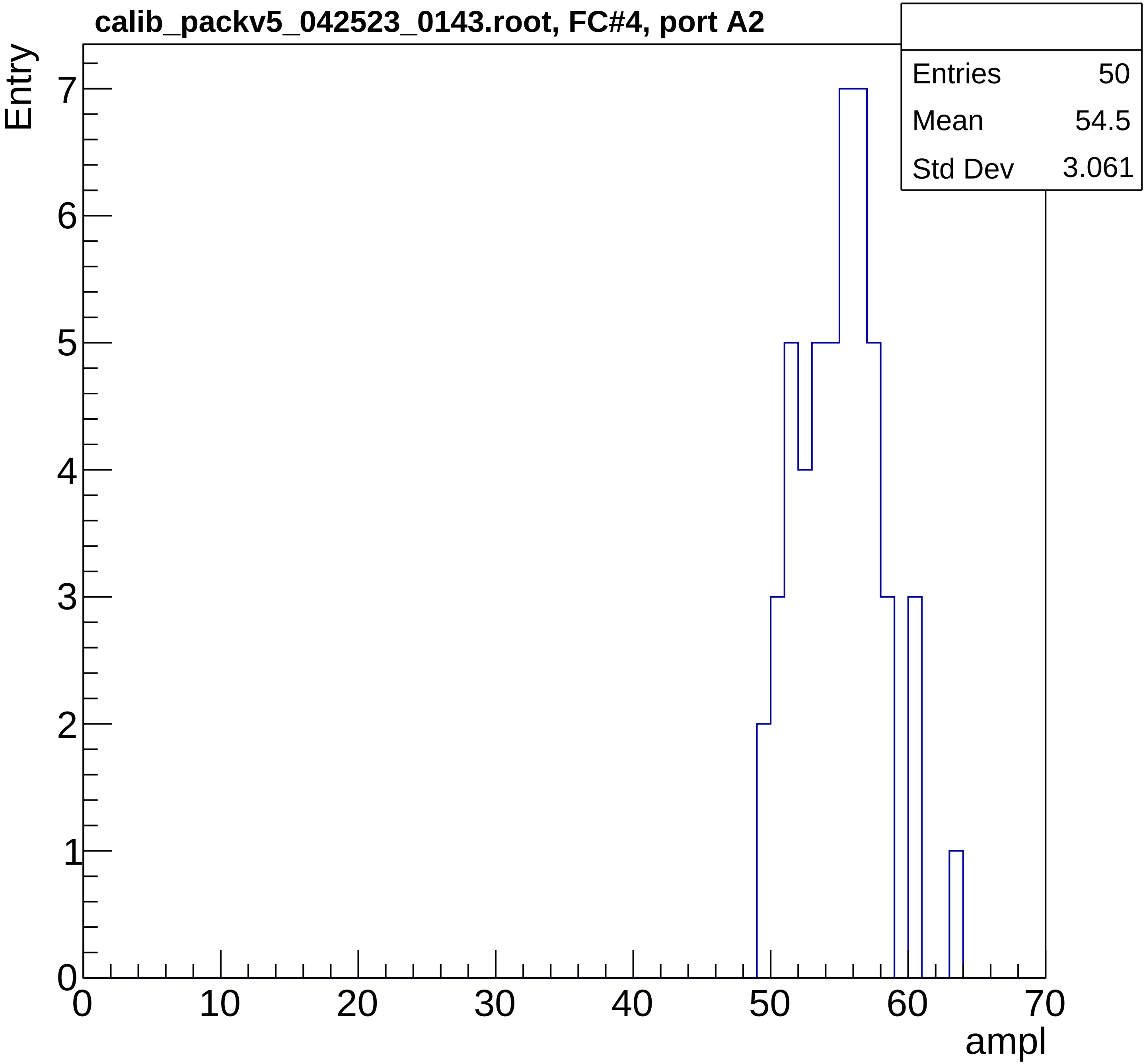

B1L100S, U21-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 28.48
Std Dev 4.636
Gaus mean : 29.0940
Gaus Width: 3.3465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 35.69
Std Dev 3.175
Gaus mean : 35.5260
Gaus Width: 2.7051
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

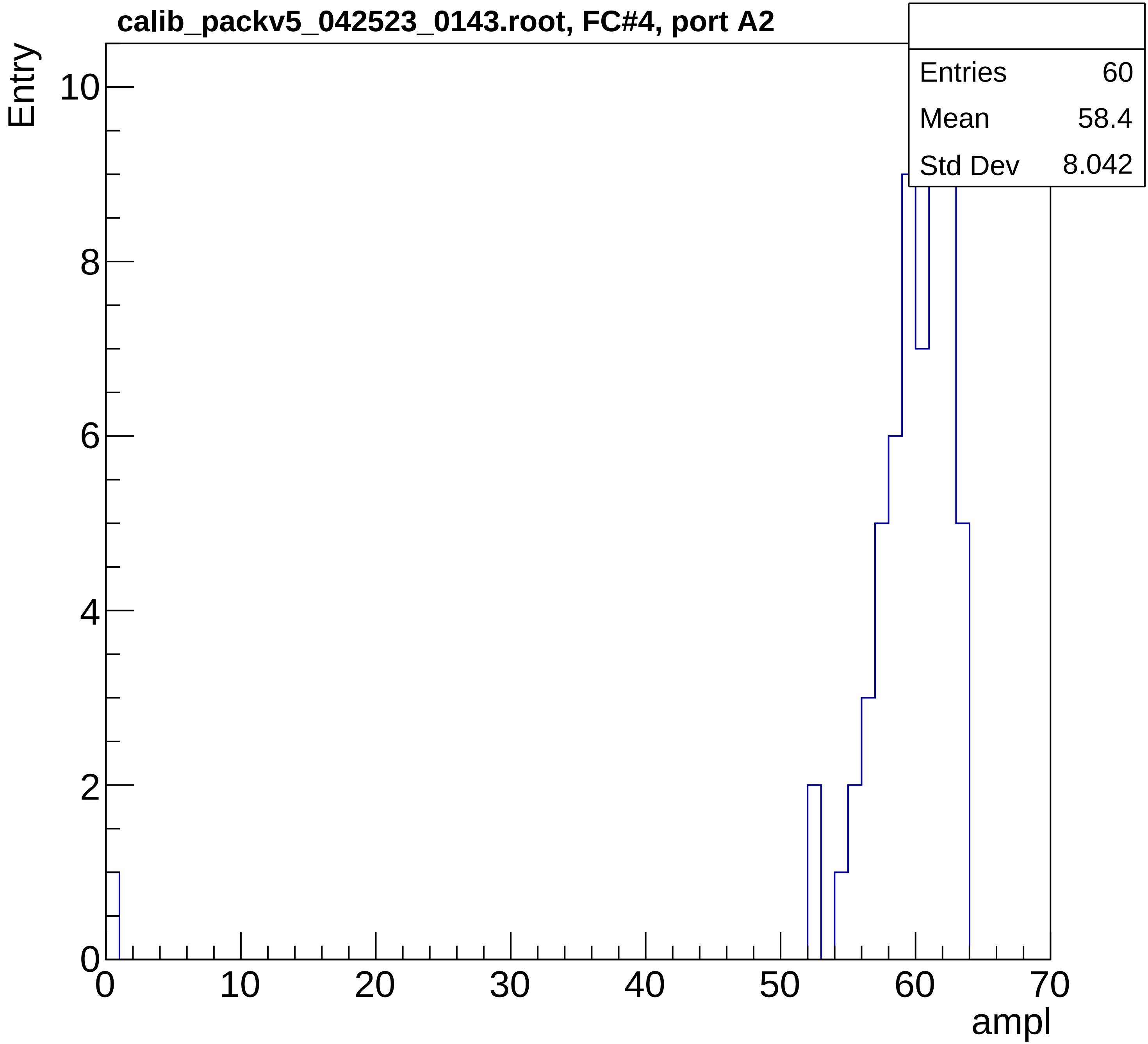

B1L100S, U21-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 40.65
Std Dev 3.128
Gaus mean : 40.6517
Gaus Width: 3.9278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 47.94
Std Dev 3.785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

B1L100S, U21-ch8, adc4

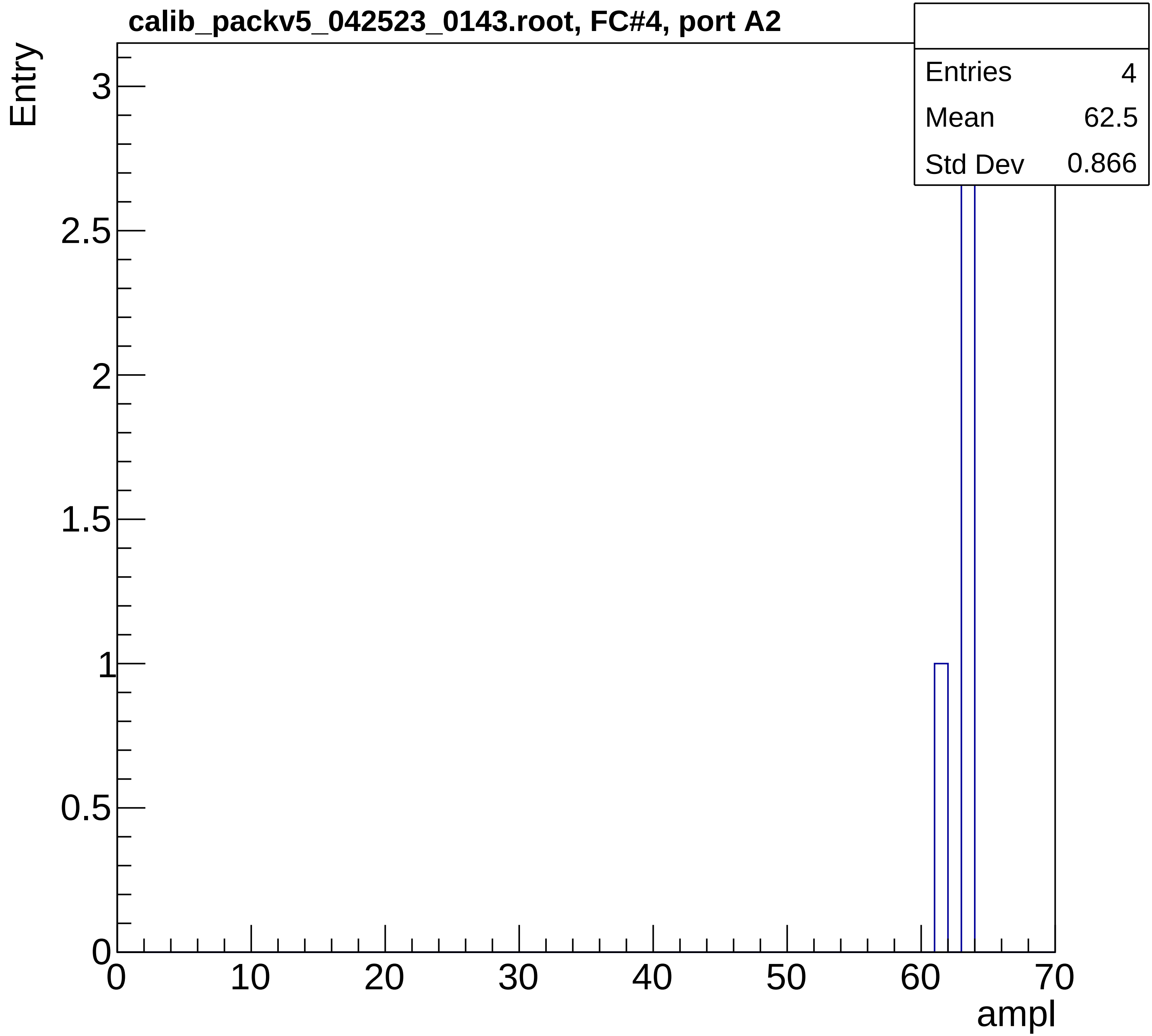


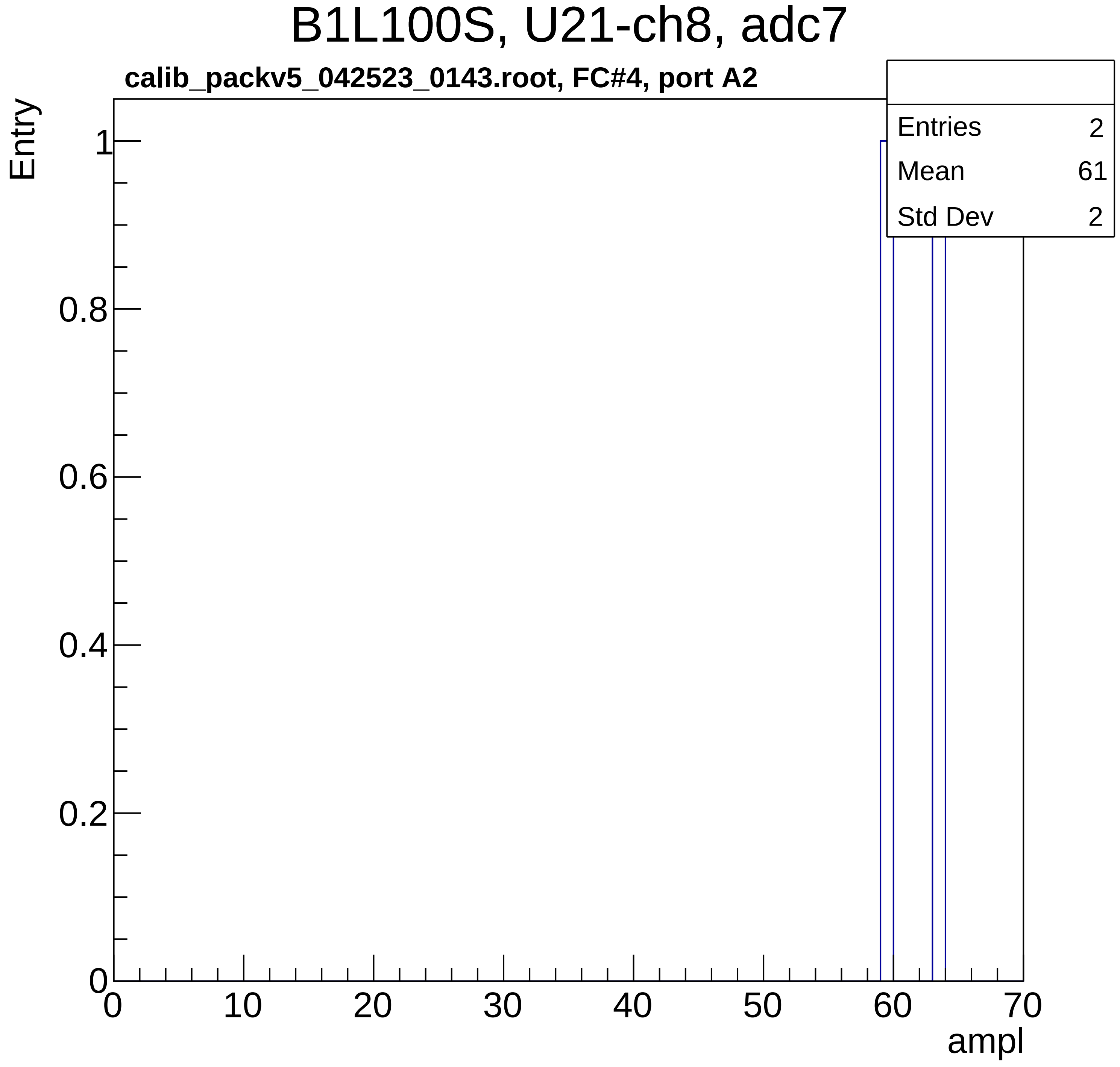
B1L100S, U21-ch8, adc5

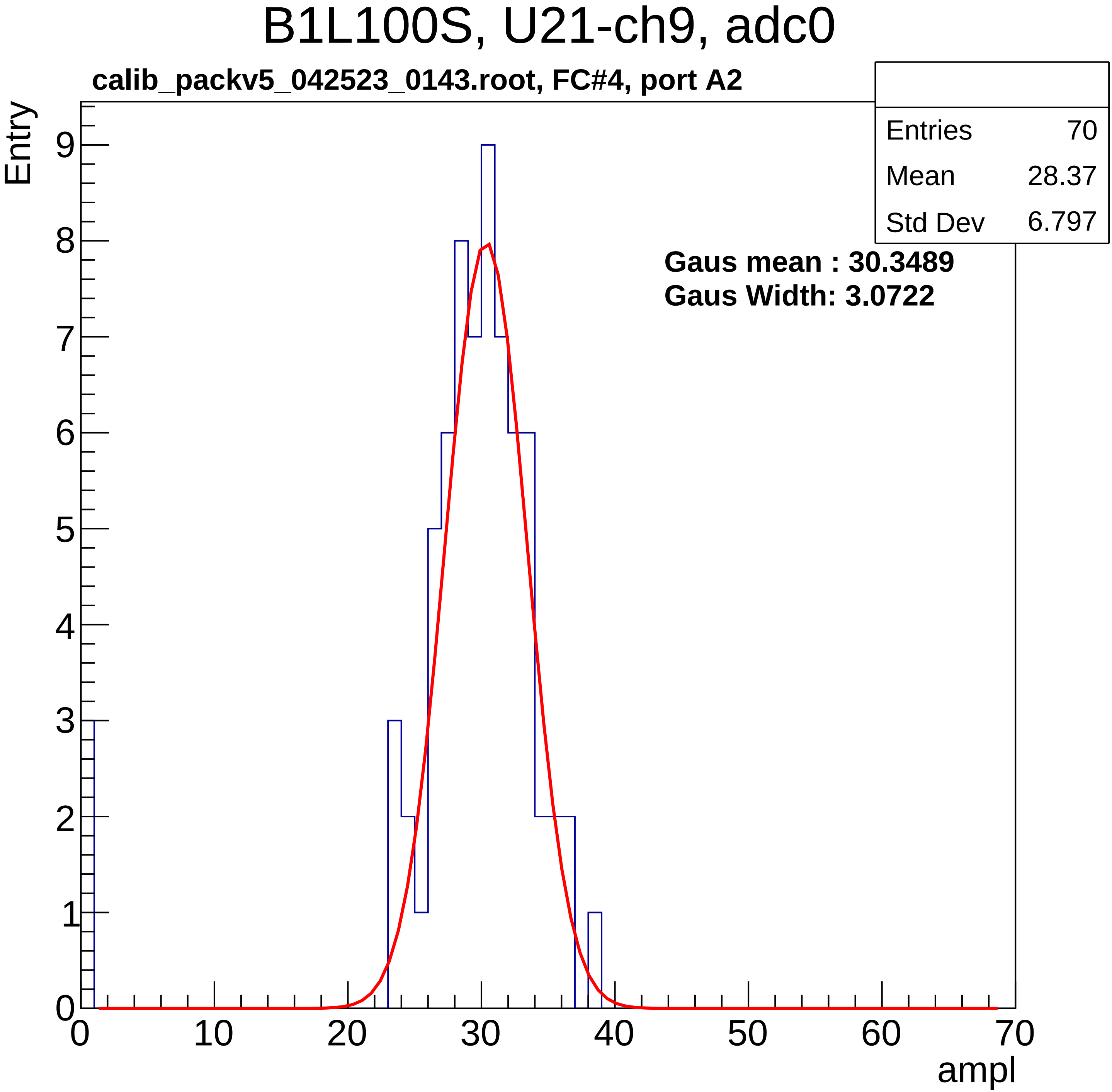


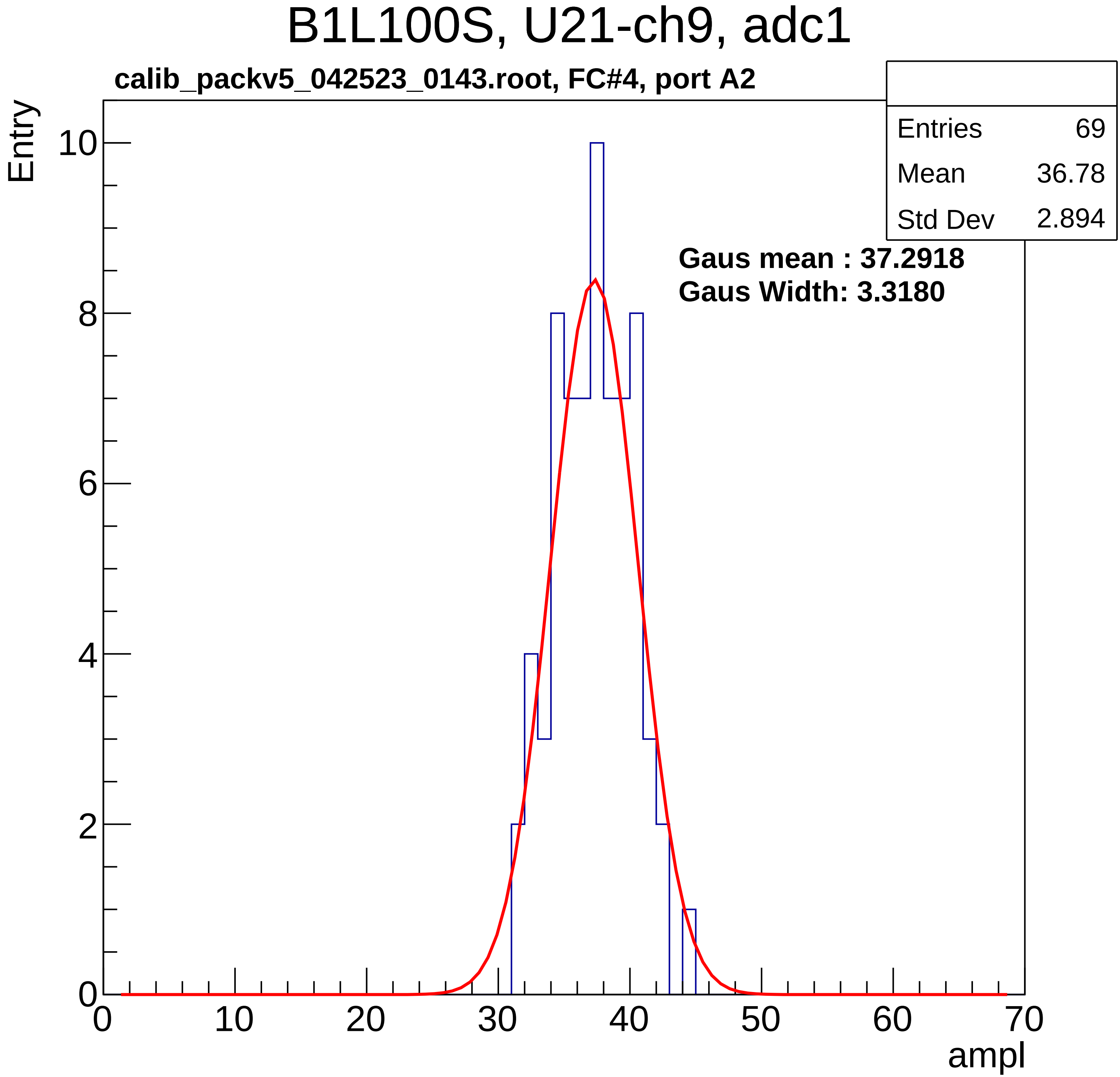
B1L100S, U21-ch8, adc6

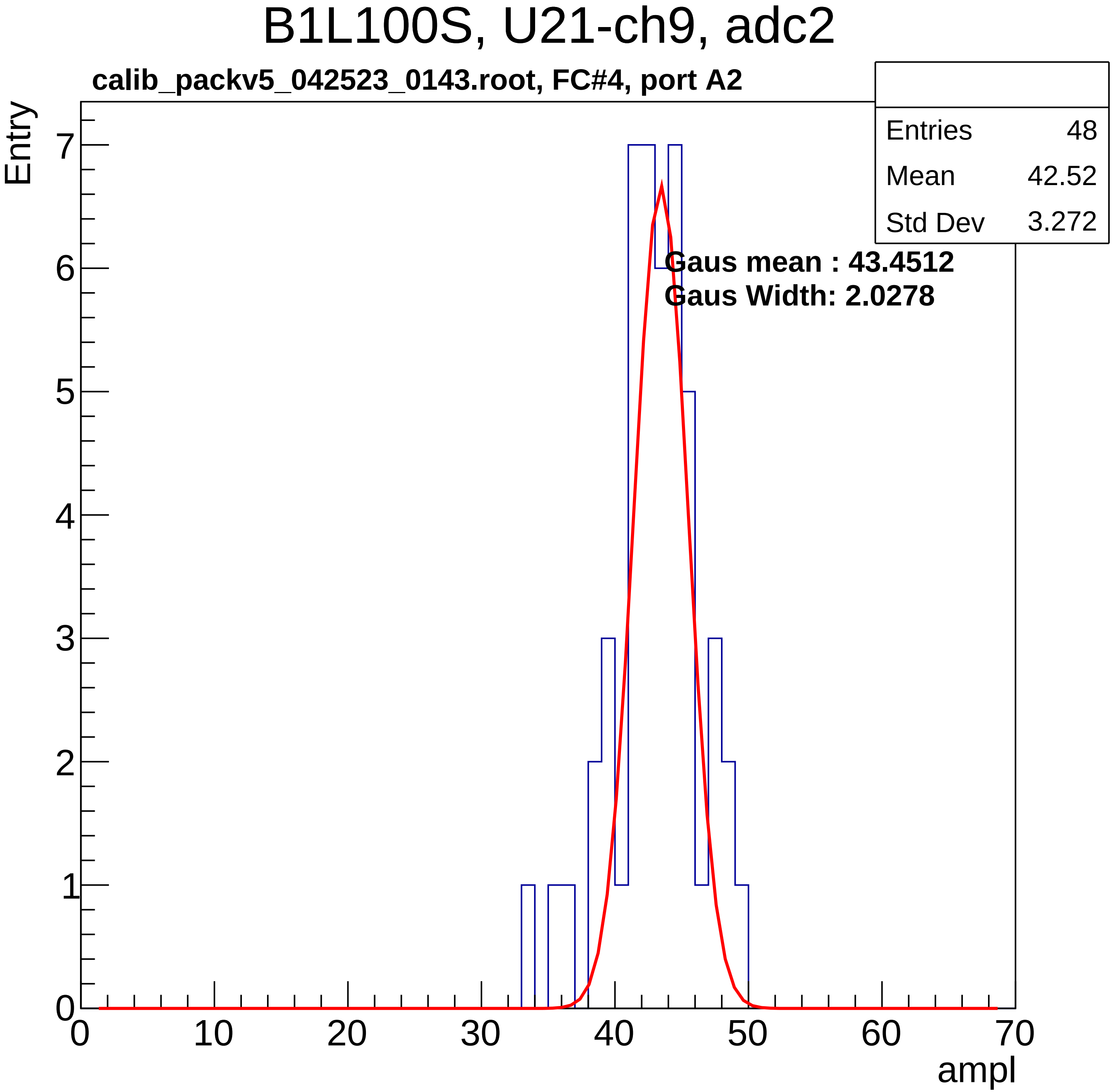
calib_packv5_042523_0143.root, FC#4, port A2




B1L100S, U21-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

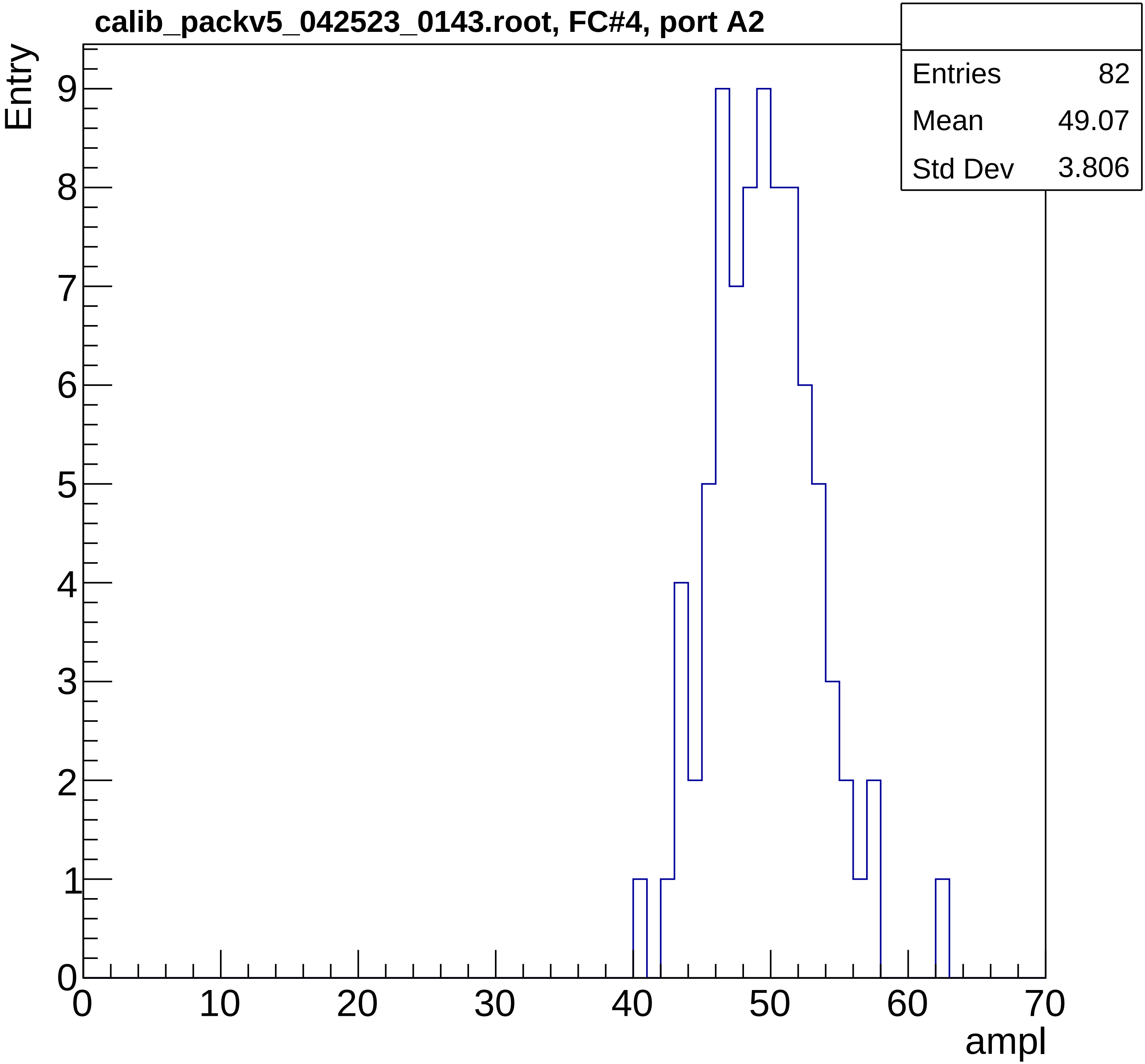

B1L100S, U21-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.37
Std Dev 6.797
Gaus mean : 30.3489
Gaus Width: 3.0722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

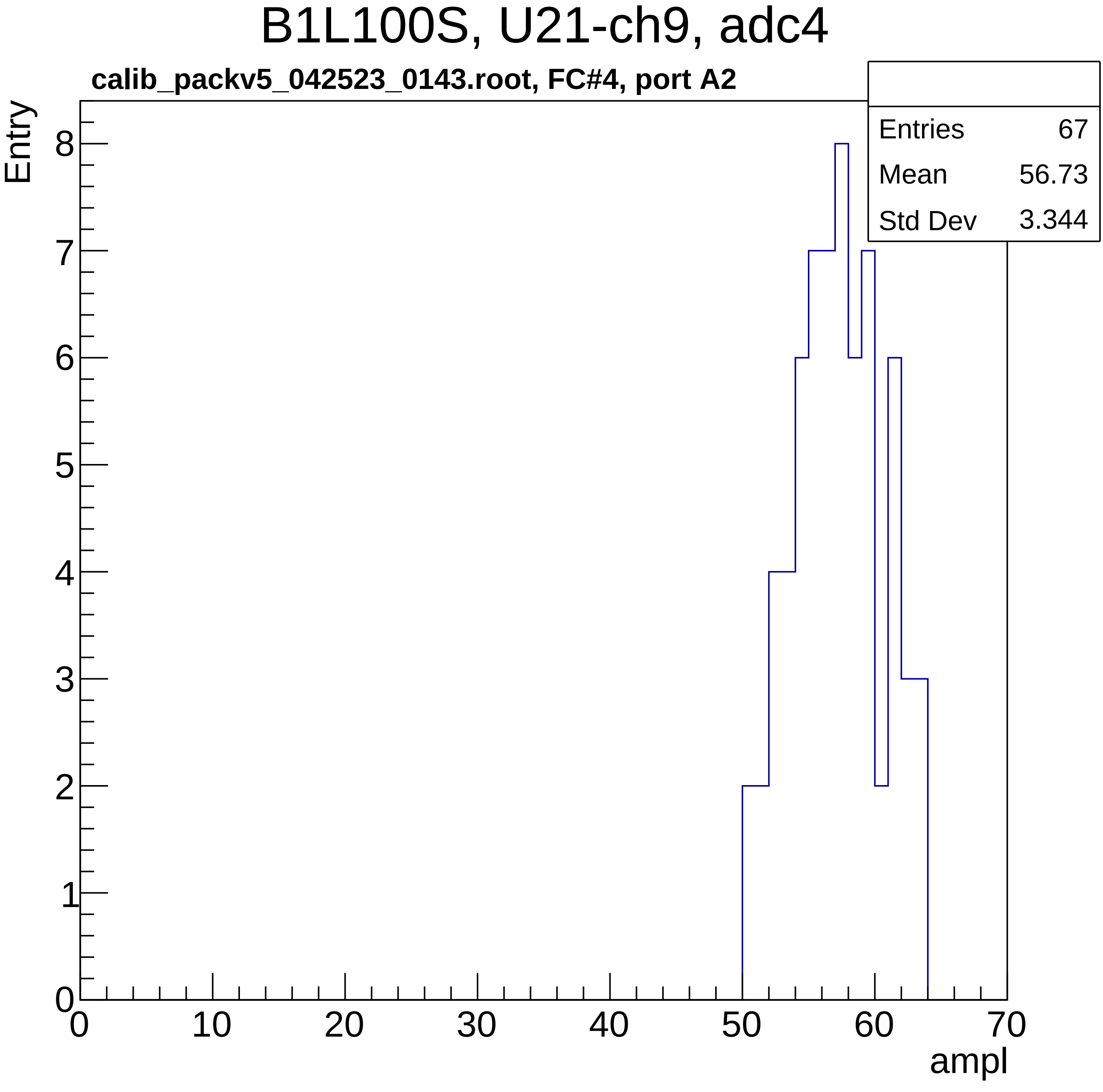

B1L100S, U21-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.78
Std Dev 2.894
Gaus mean : 37.2918
Gaus Width: 3.3180
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 48
Mean 42.52
Std Dev 3.272
Gaus mean : 43.4512
Gaus Width: 2.0278
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

B1L100S, U21-ch9, adc3

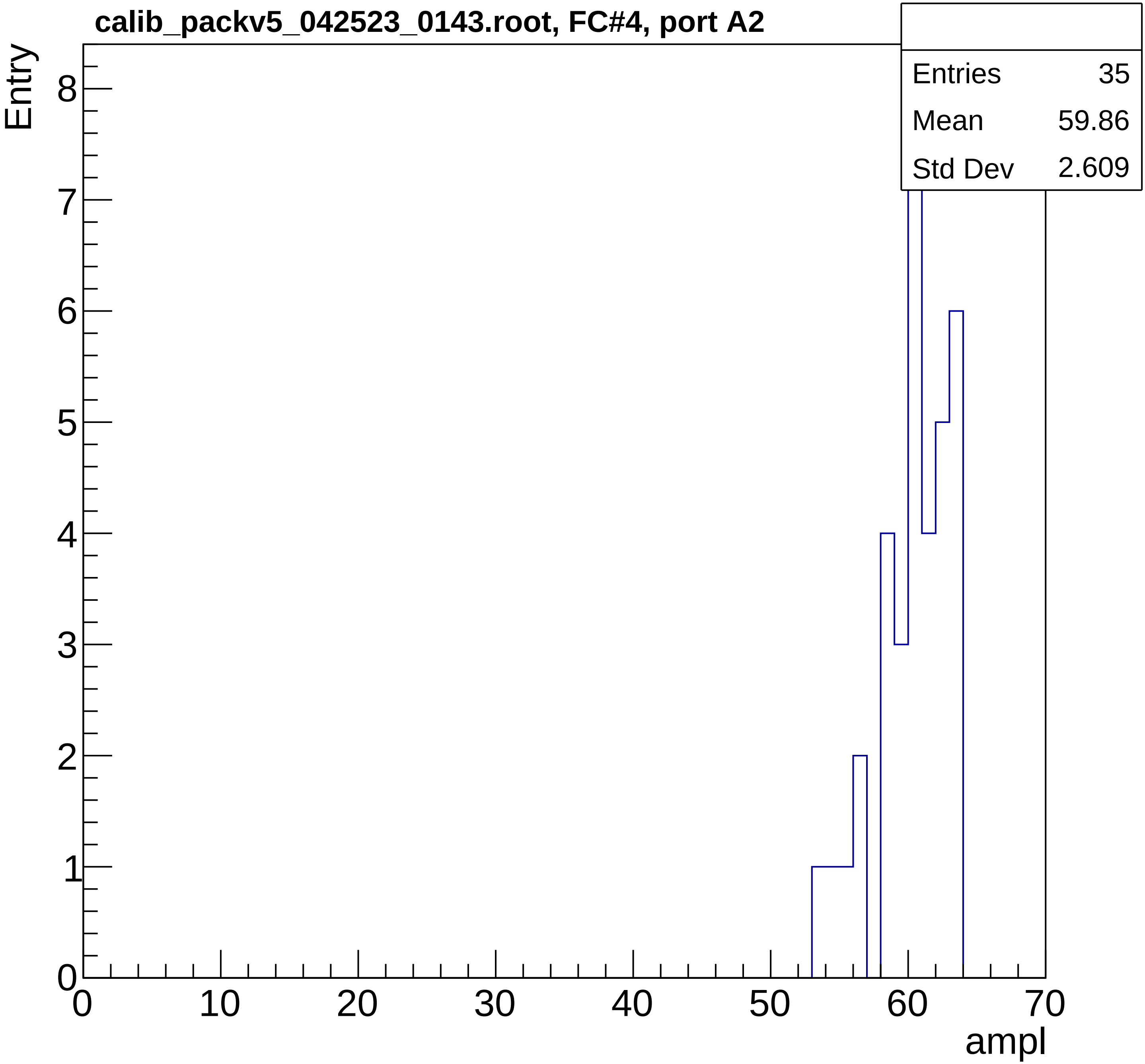
calib_packv5_042523_0143.root, FC#4, port A2



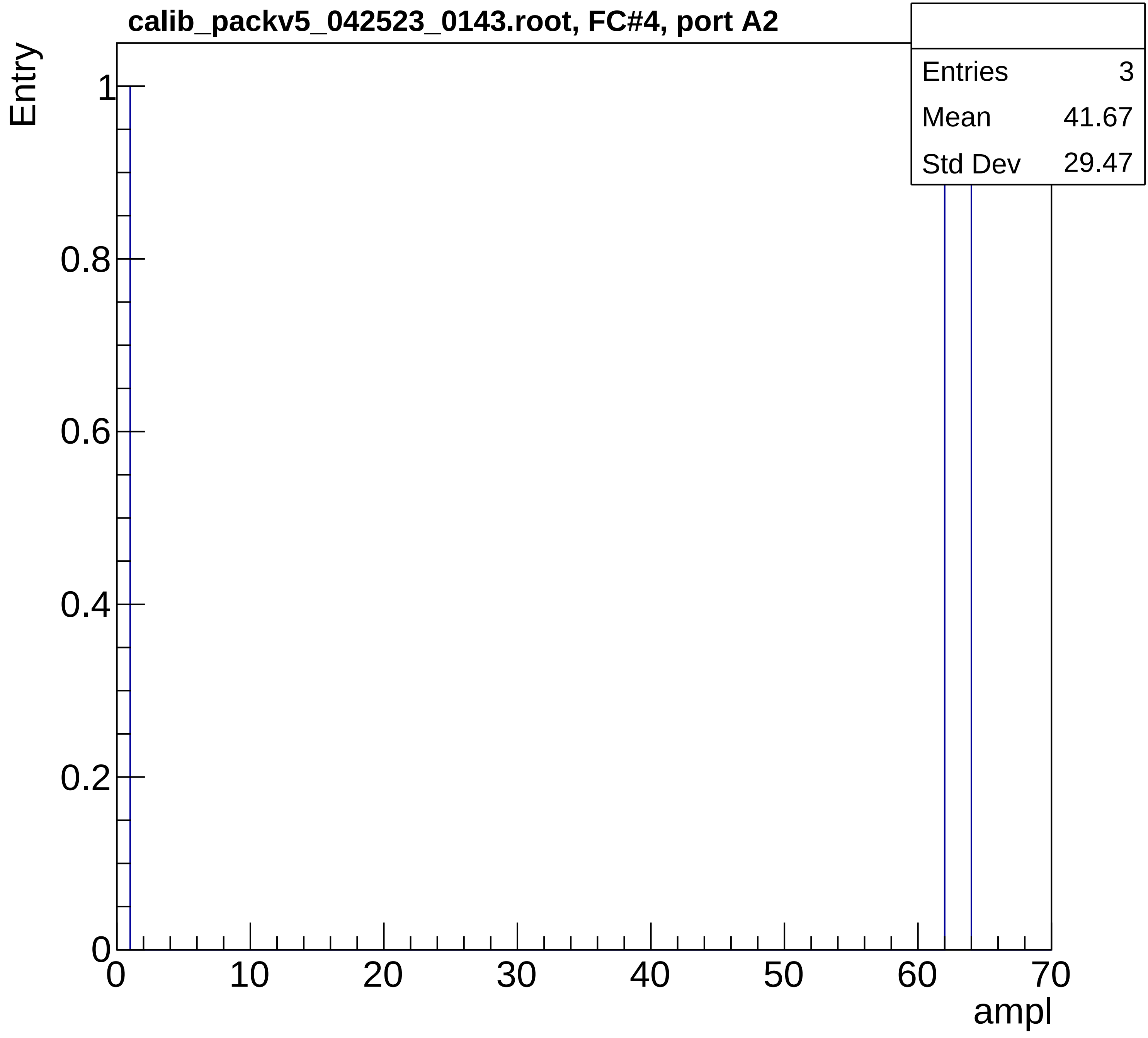

B1L100S, U21-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.73
Std Dev 3.344
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

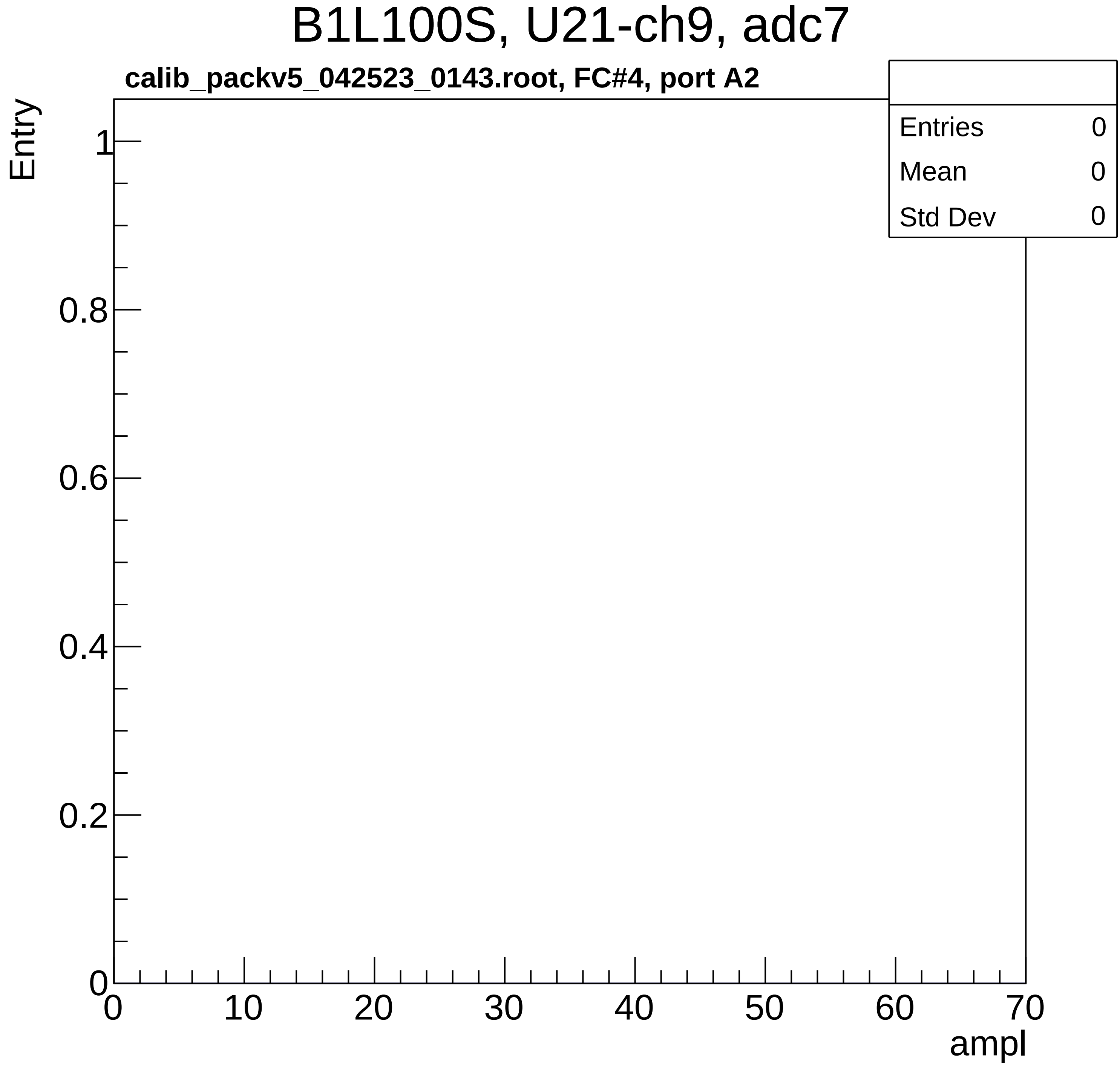
B1L100S, U21-ch9, adc5

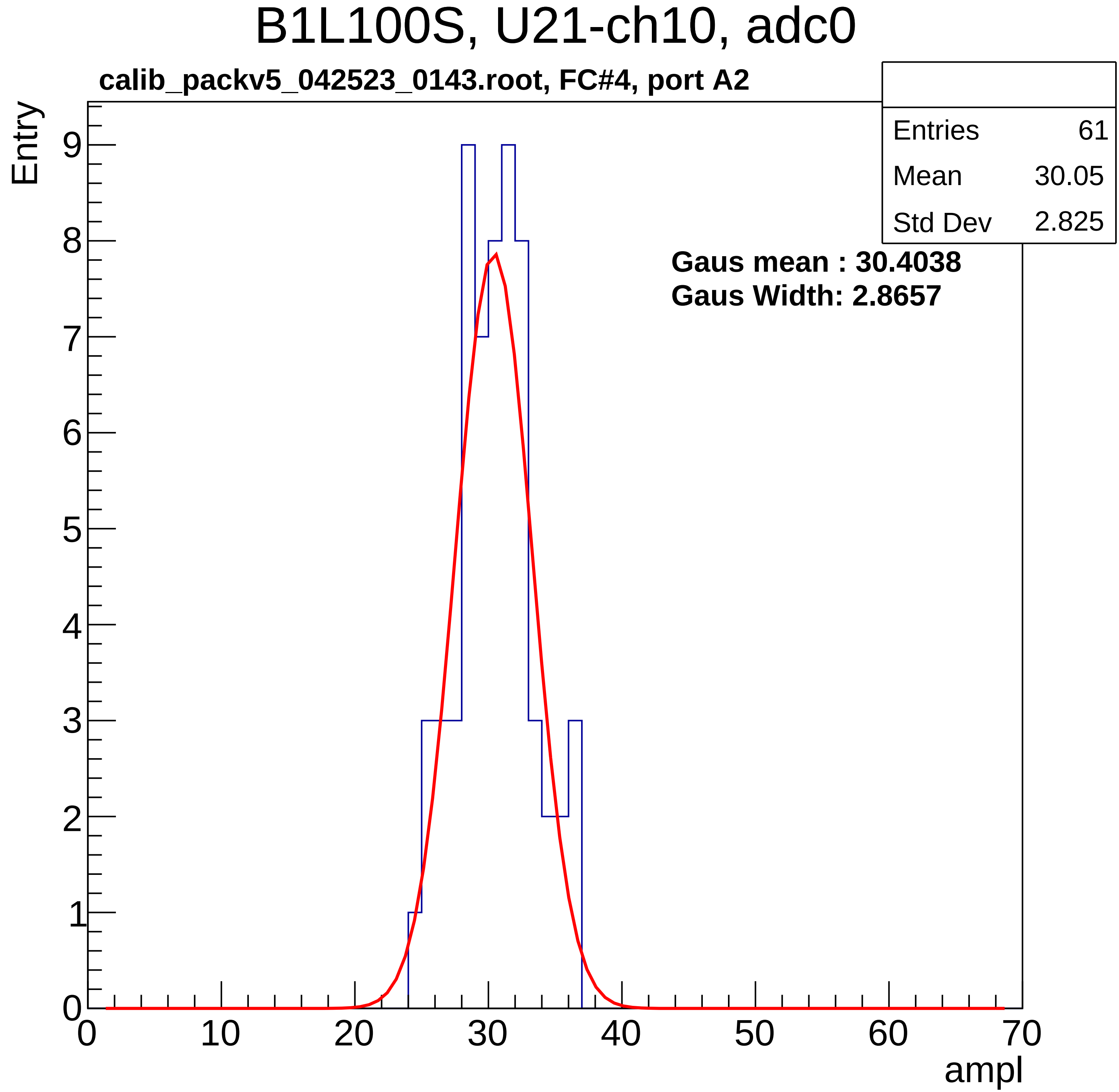
calib_packv5_042523_0143.root, FC#4, port A2

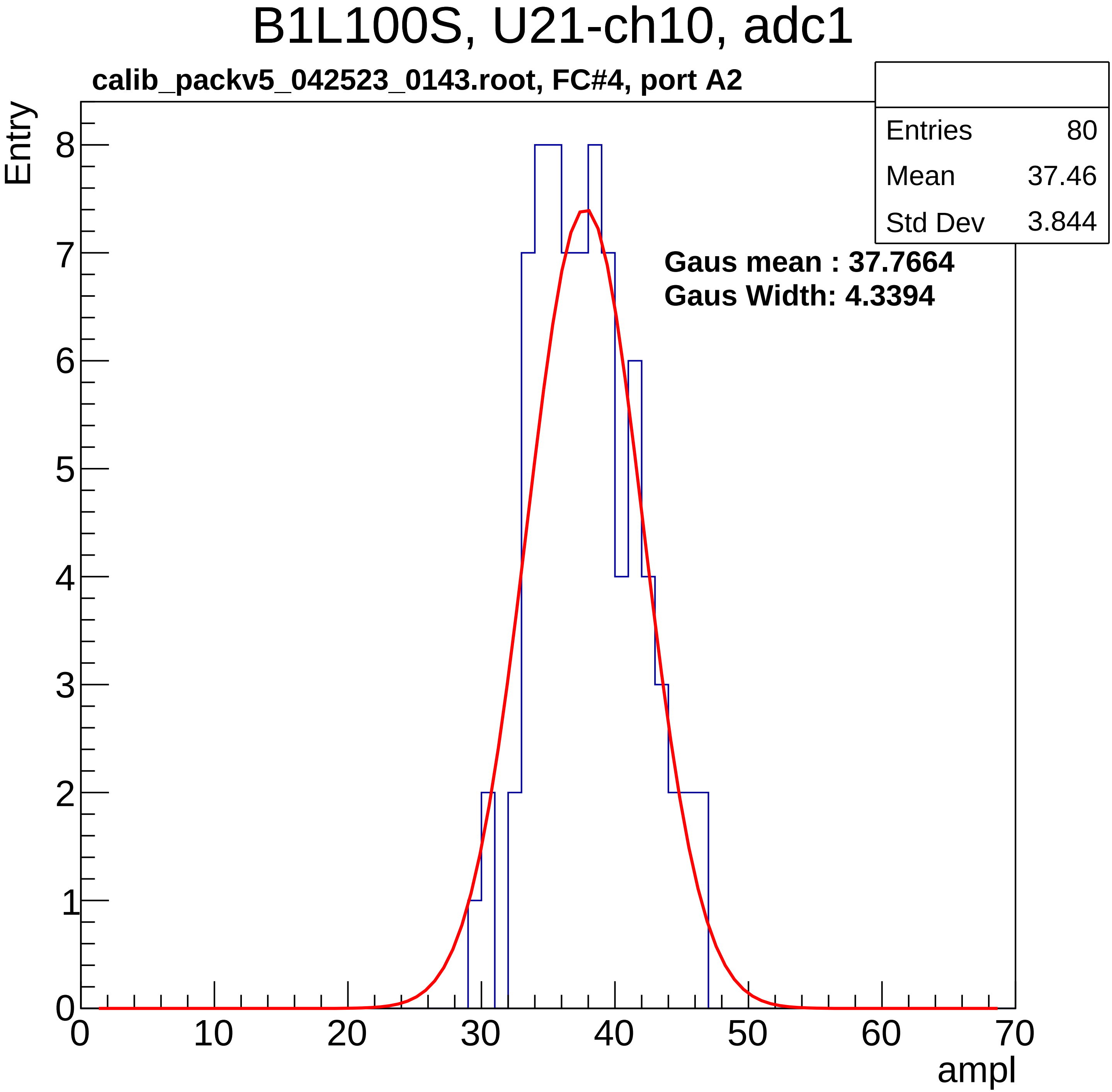


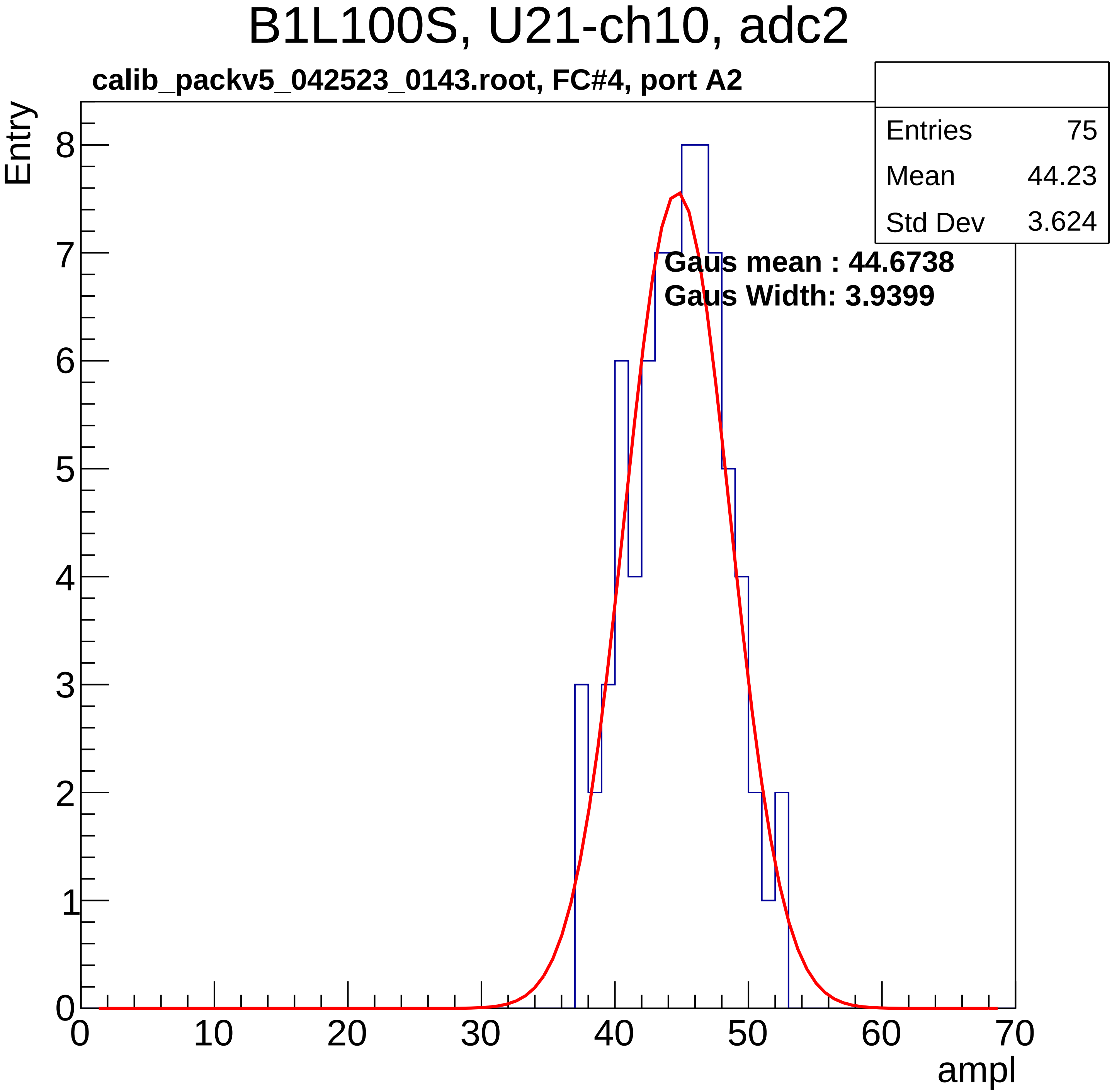
B1L100S, U21-ch9, adc6

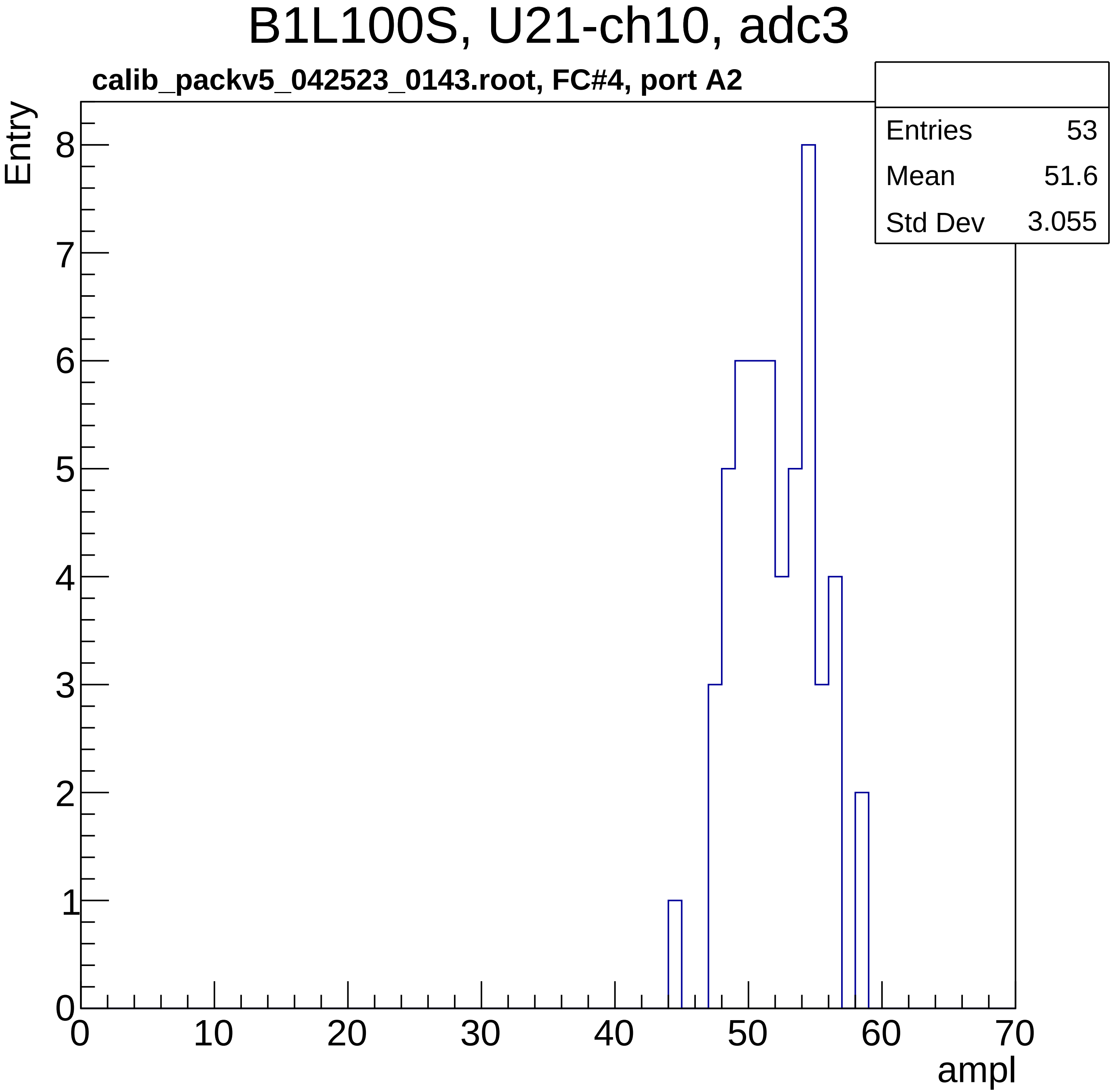


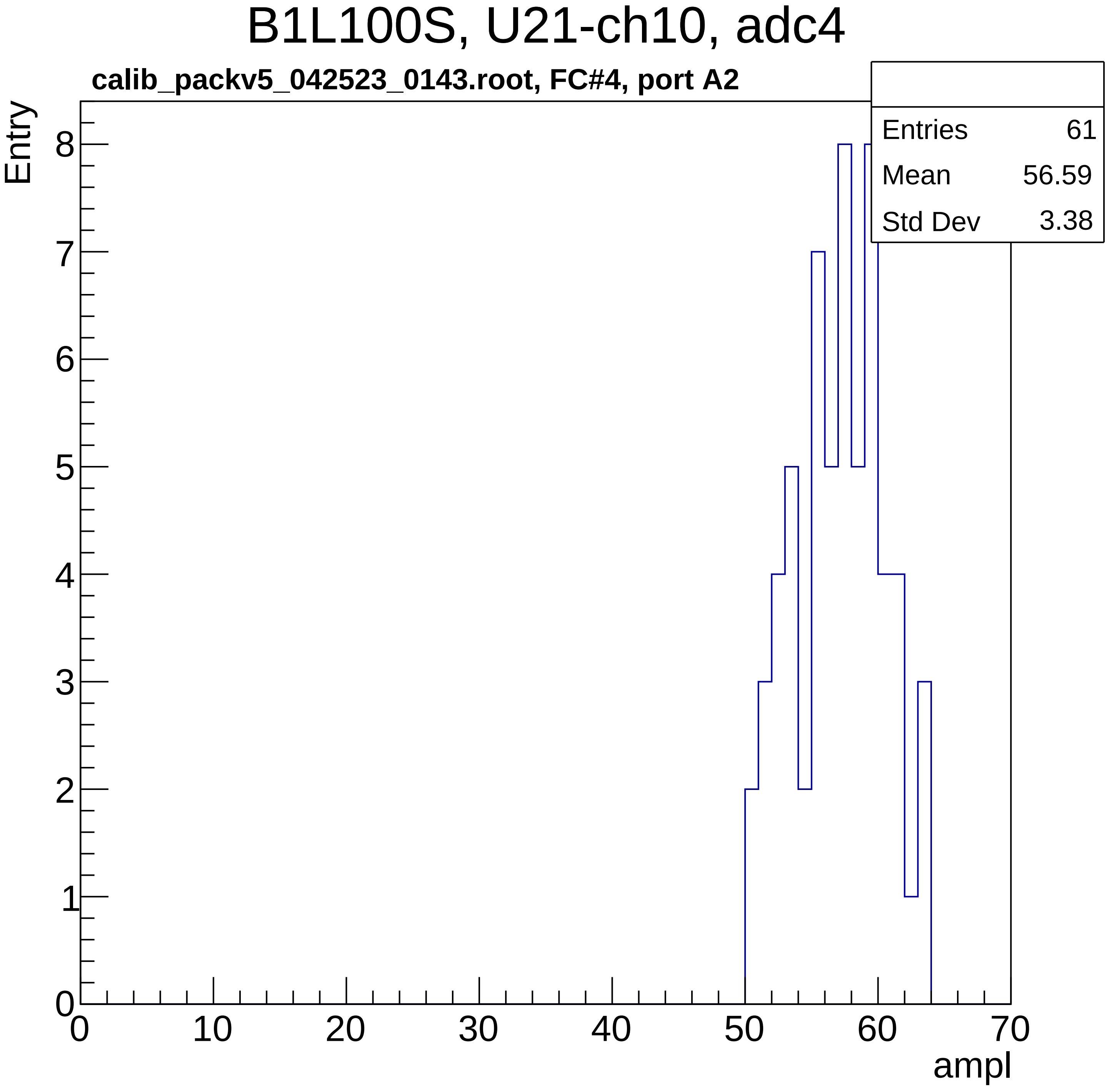

B1L100S, U21-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

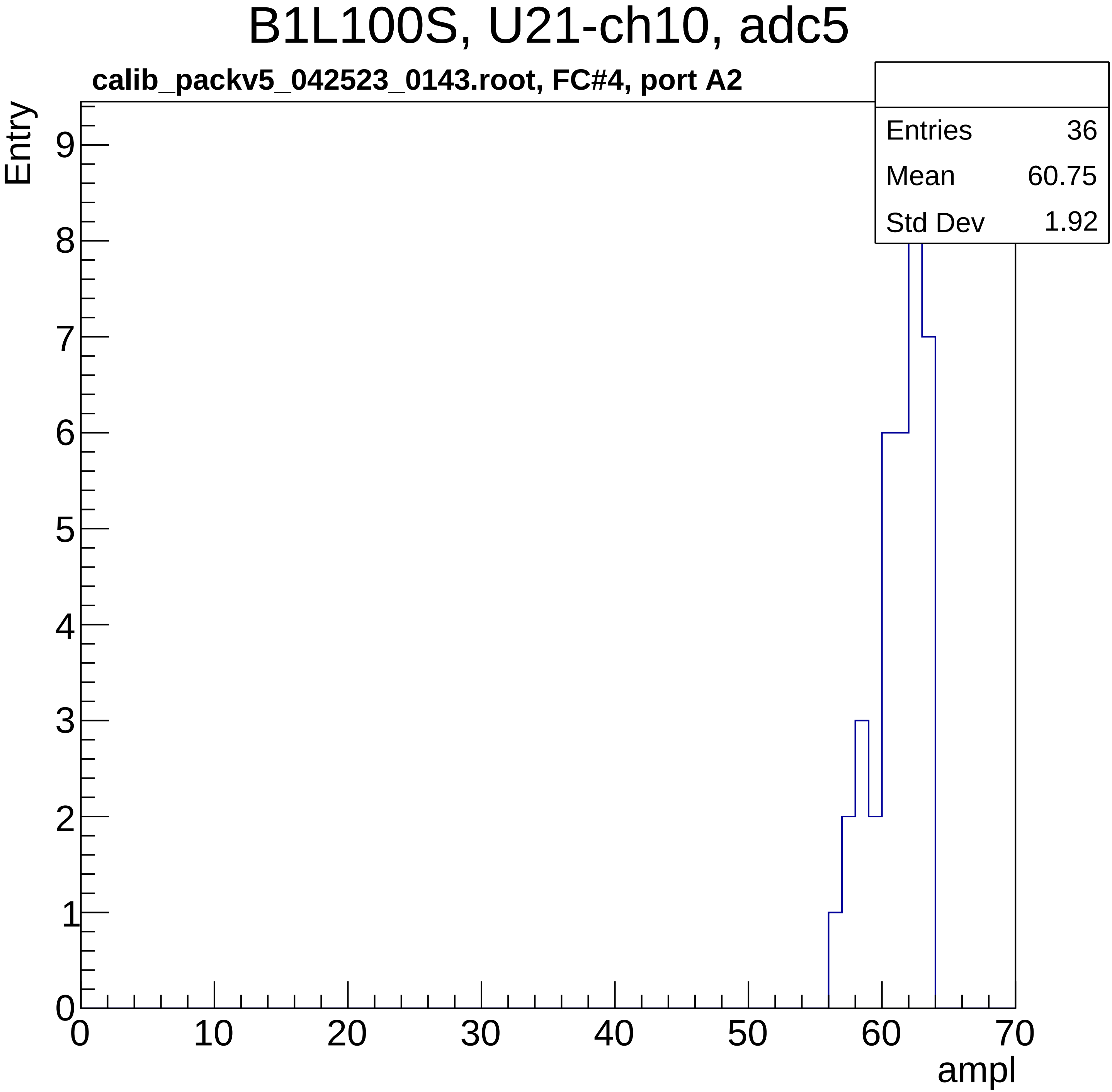

B1L100S, U21-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 61
Mean 30.05
Std Dev 2.825
Gaus mean : 30.4038
Gaus Width: 2.8657
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.46
Std Dev 3.844
Gaus mean : 37.7664
Gaus Width: 4.3394
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

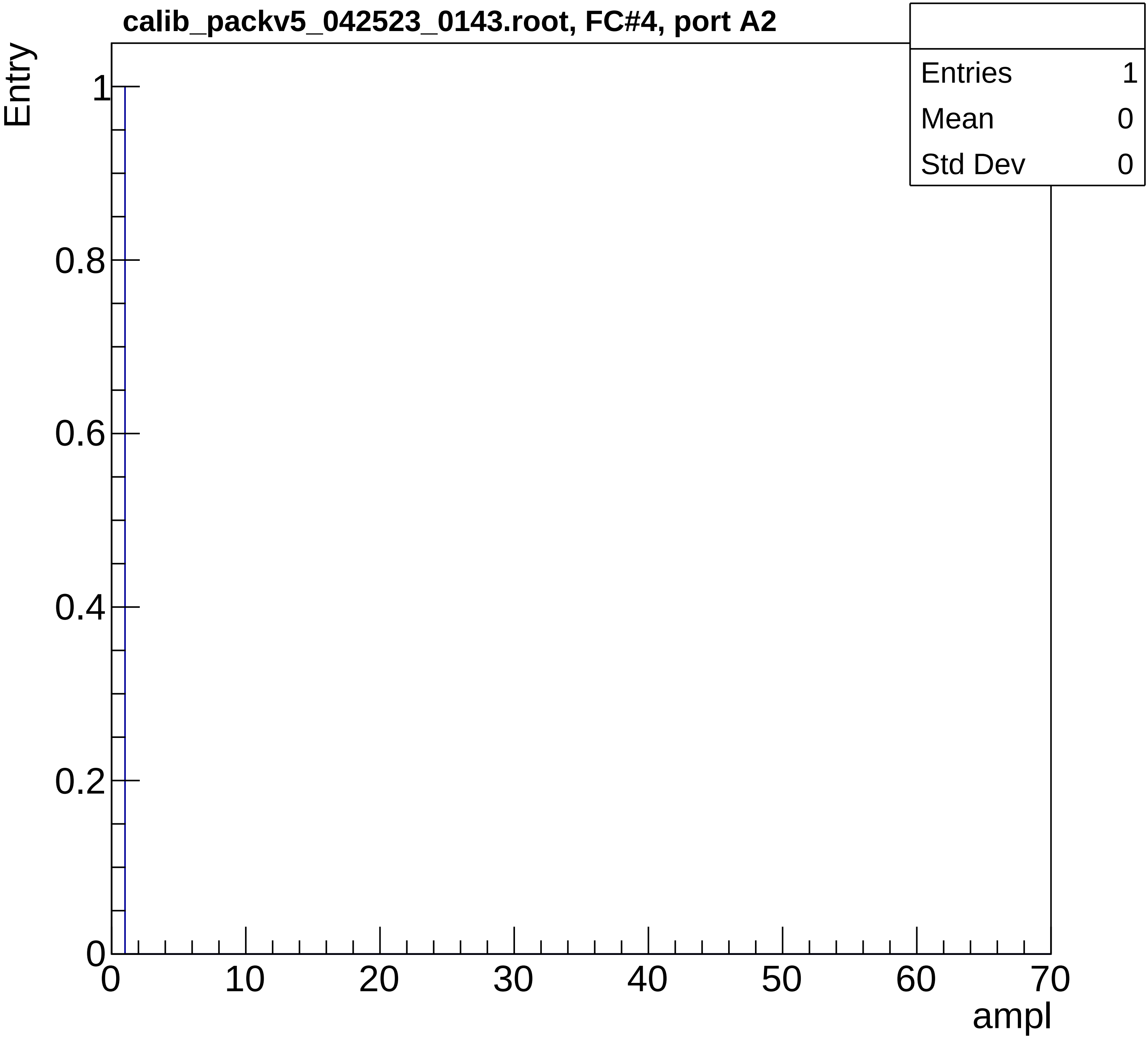

B1L100S, U21-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.23
Std Dev 3.624
Gaus mean : 44.6738
Gaus Width: 3.9399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

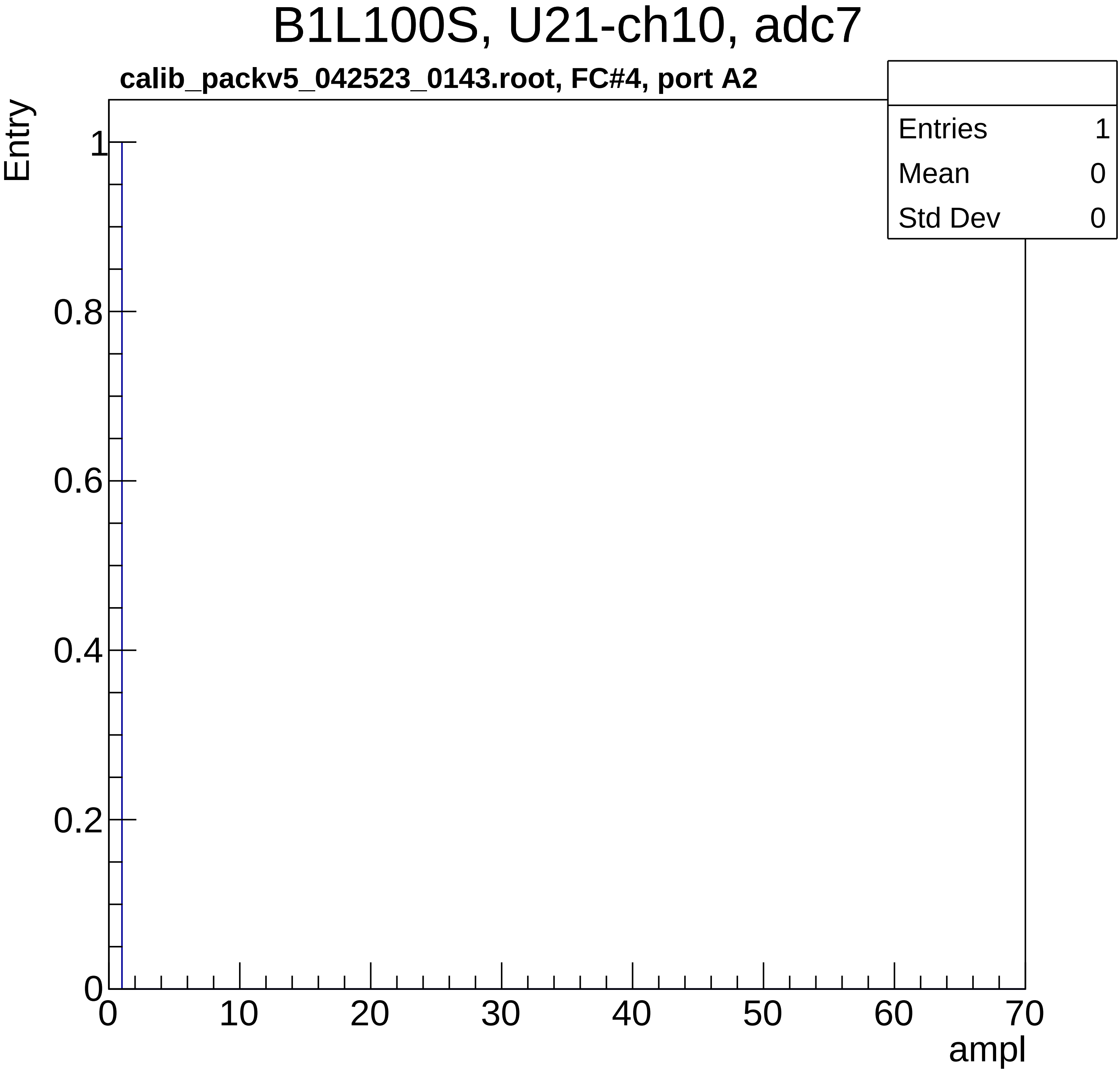

B1L100S, U21-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 51.6
Std Dev 3.055
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

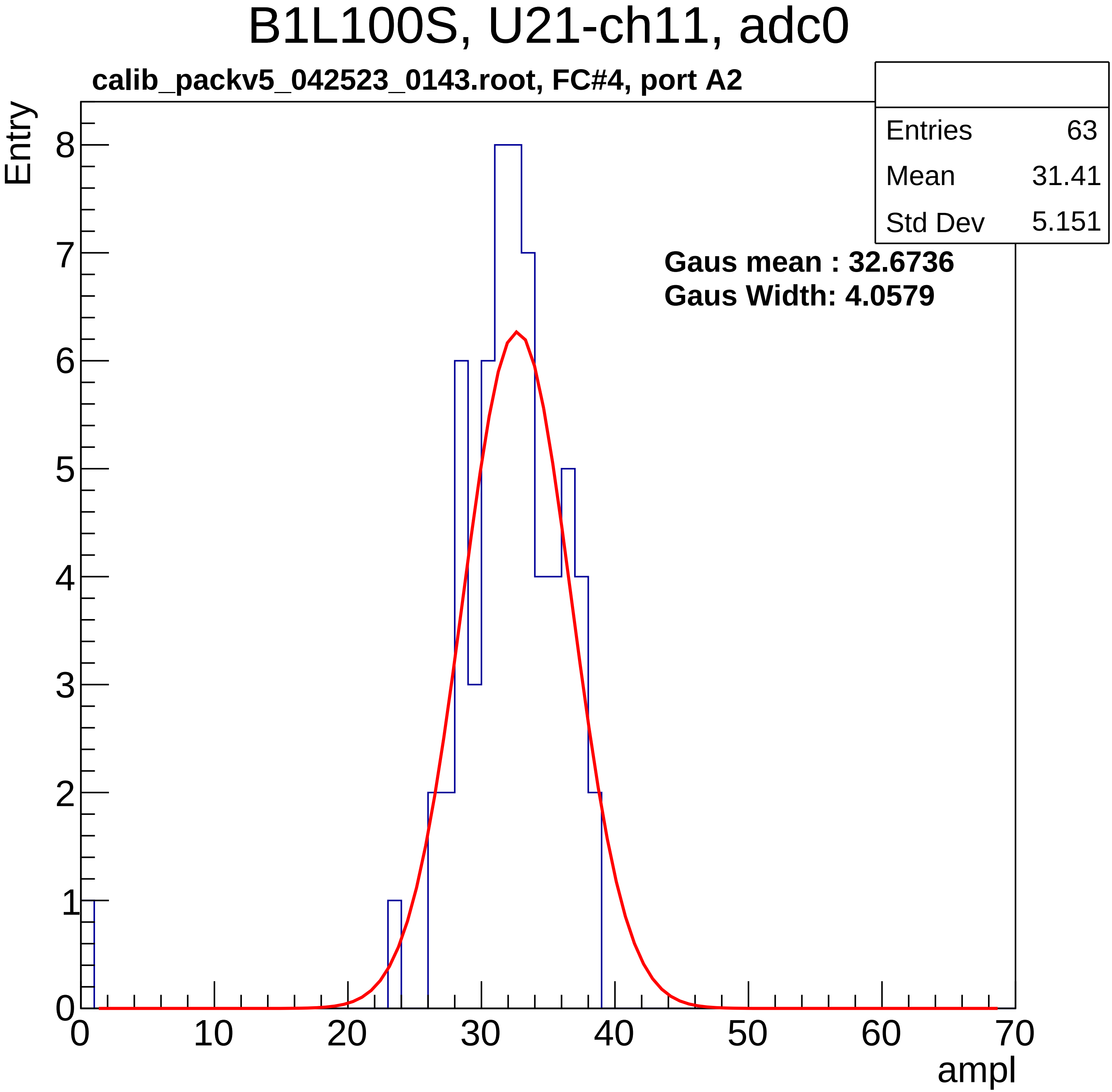

B1L100S, U21-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 56.59
Std Dev 3.38
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

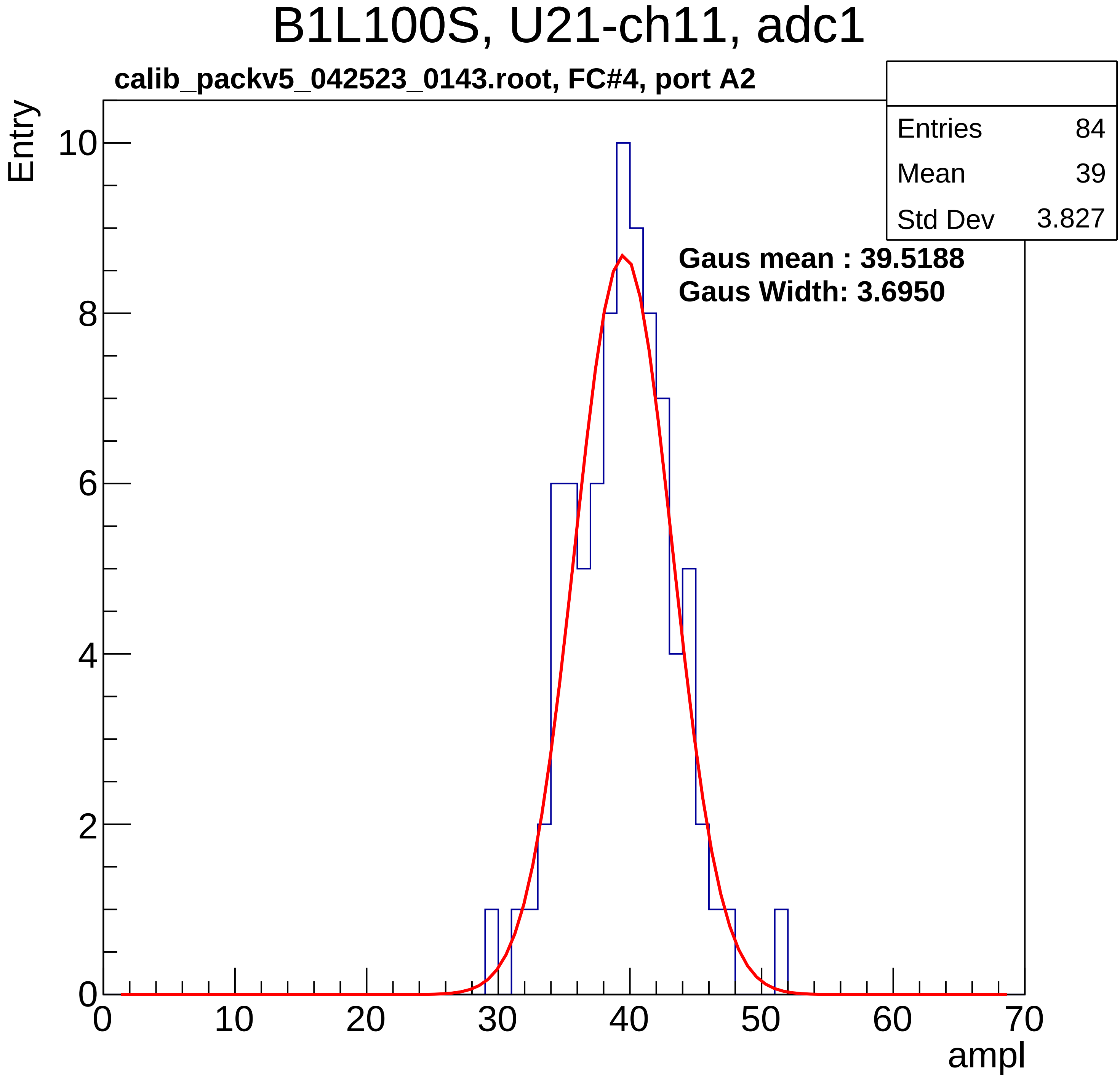

B1L100S, U21-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
Entries 36
Mean 60.75
Std Dev 1.92

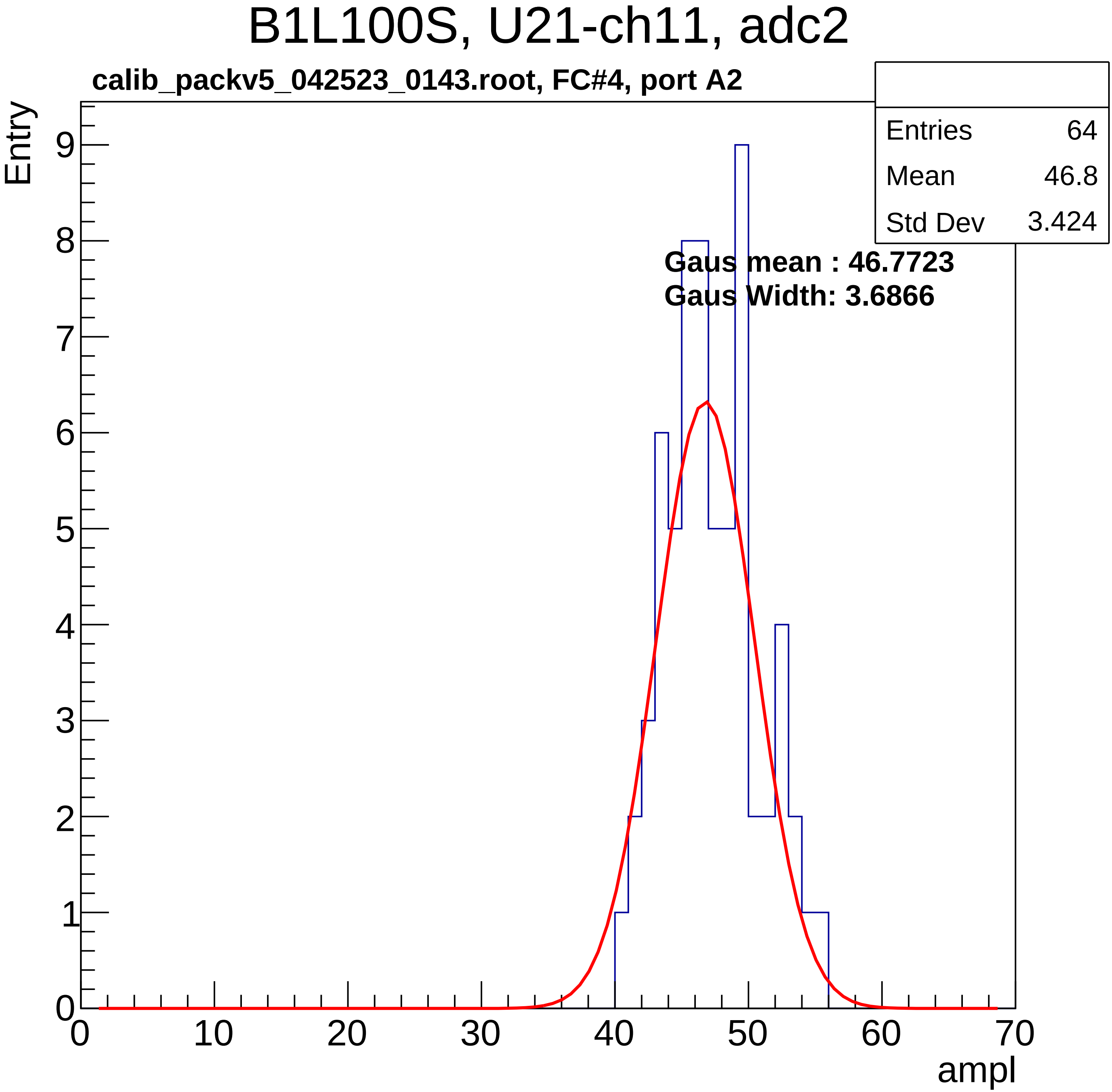
B1L100S, U21-ch10, adc6




B1L100S, U21-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

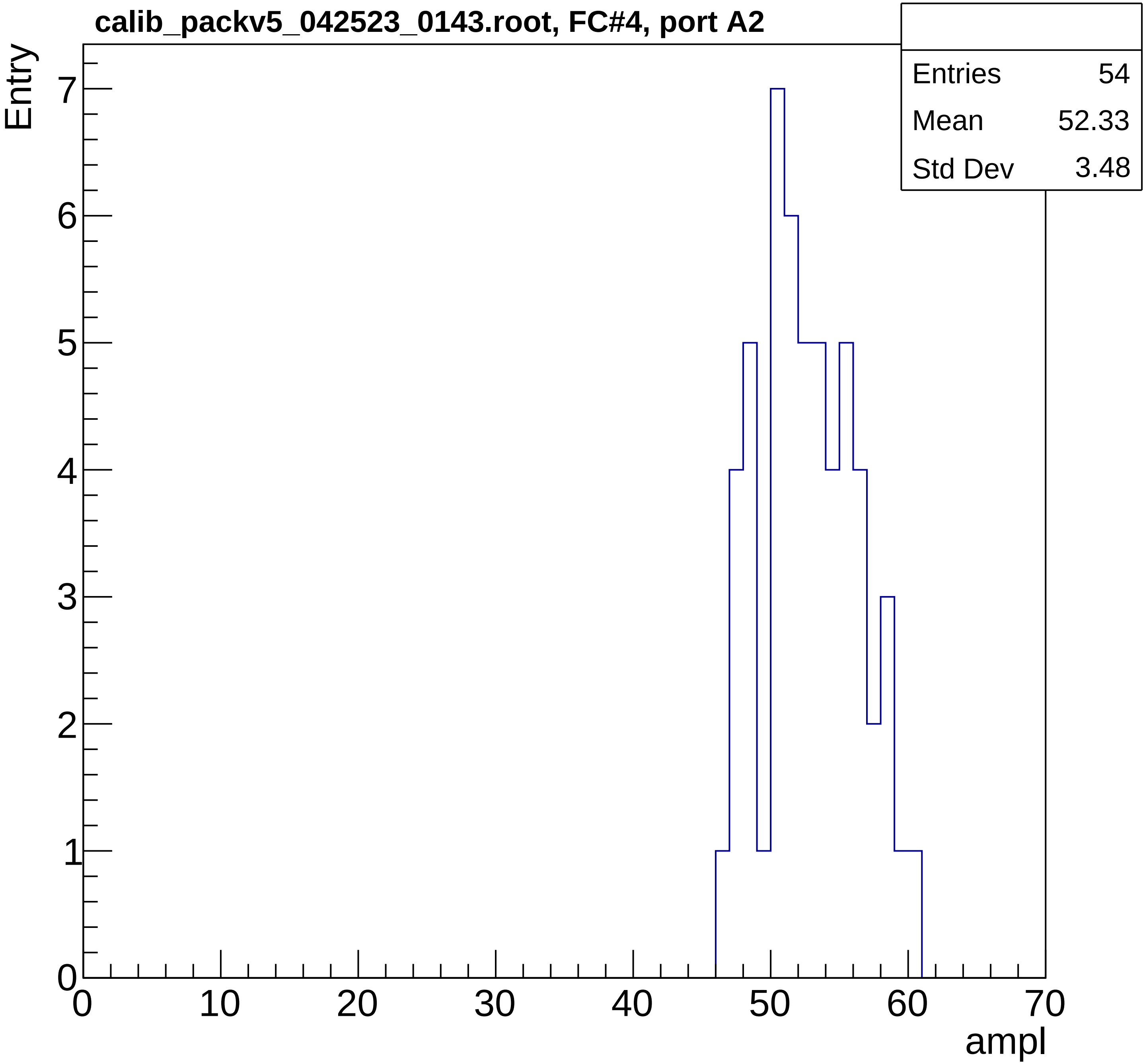

B1L100S, U21-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.41
Std Dev 5.151
Gaus mean : 32.6736
Gaus Width: 4.0579
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 39
Std Dev 3.827
Gaus mean : 39.5188
Gaus Width: 3.6950
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 46.8
Std Dev 3.424
Gaus mean : 46.7723
Gaus Width: 3.6866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

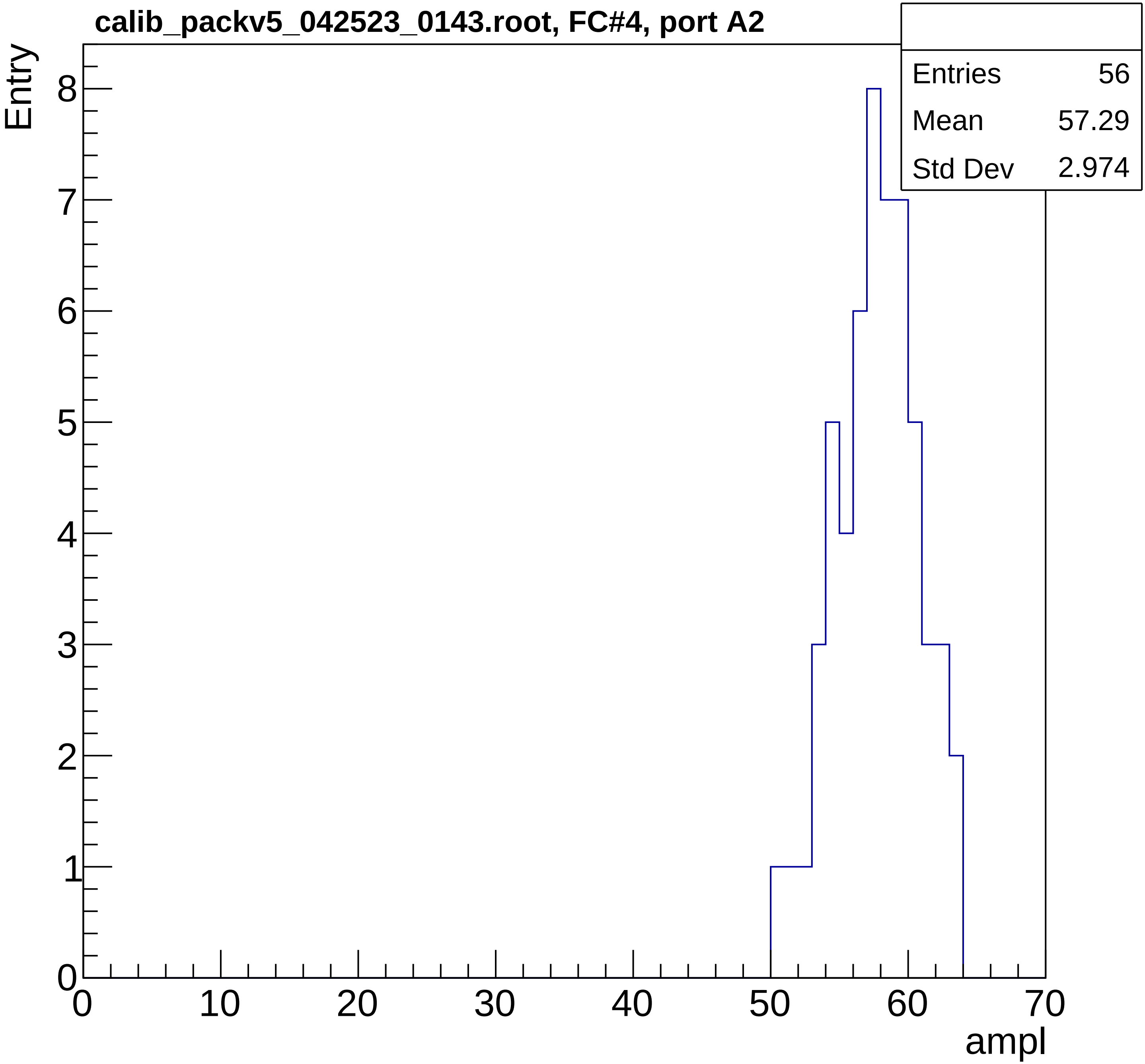
B1L100S, U21-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2



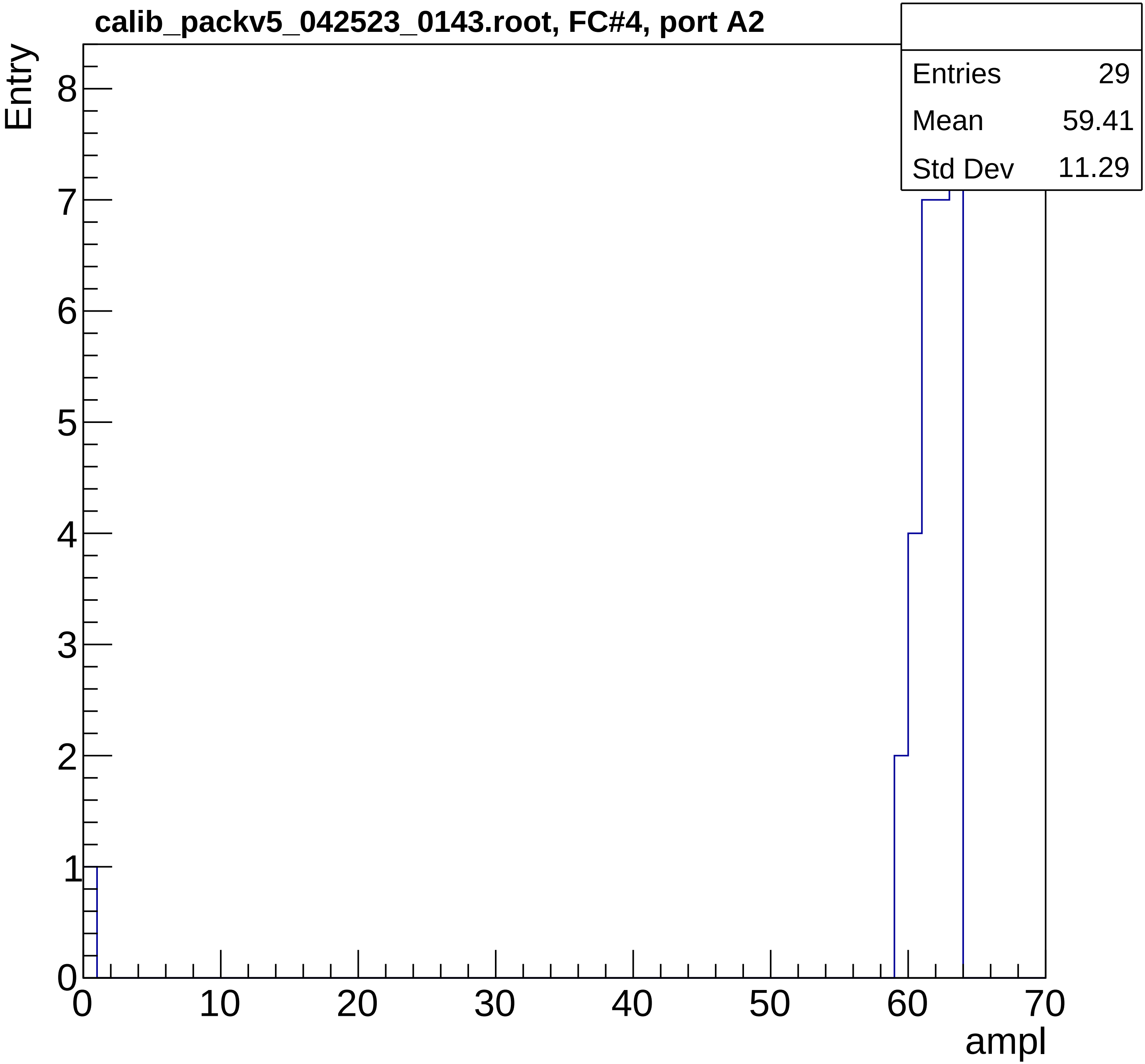
B1L100S, U21-ch11, adc4

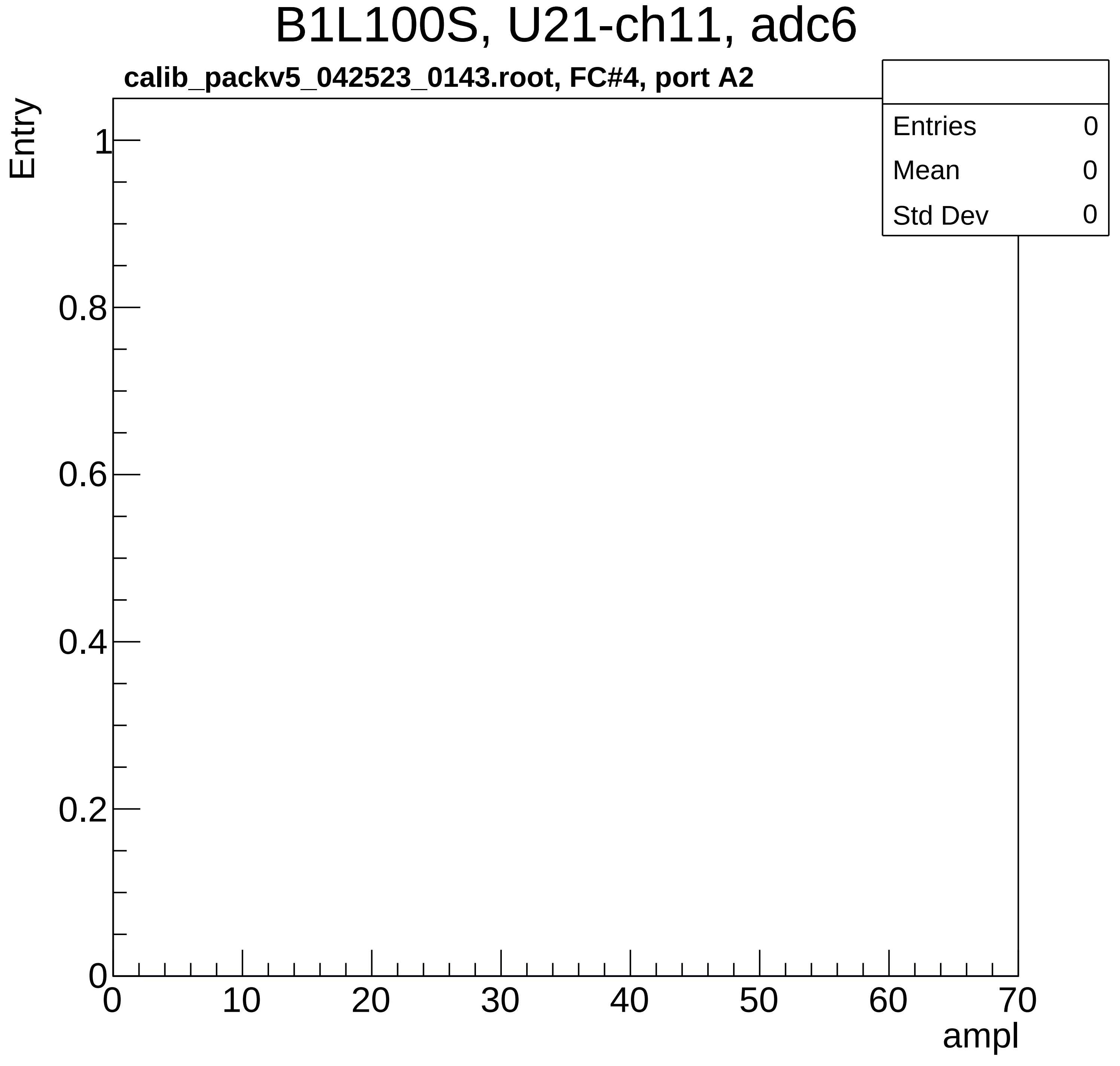
calib_packv5_042523_0143.root, FC#4, port A2

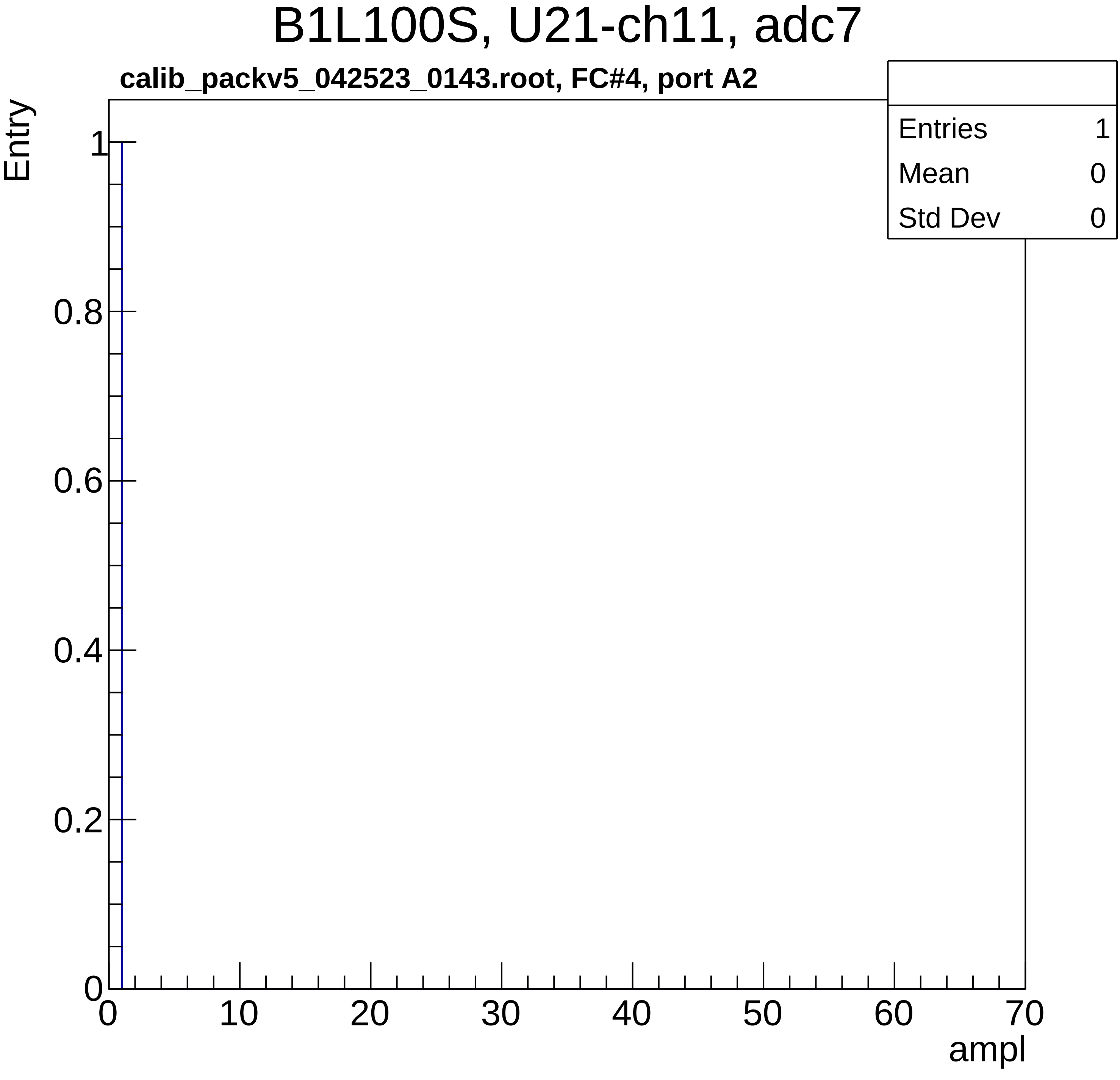


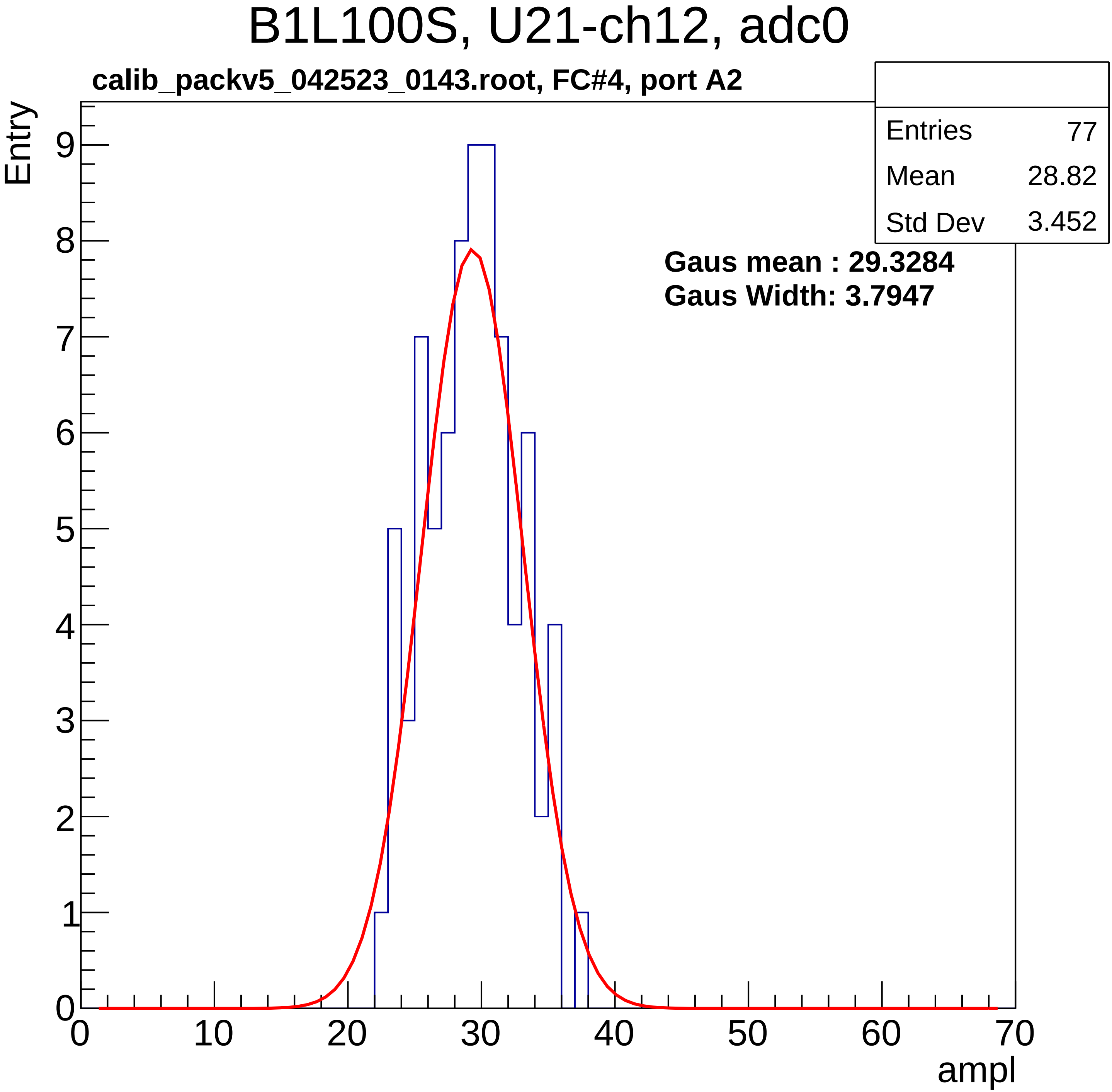
B1L100S, U21-ch11, adc5

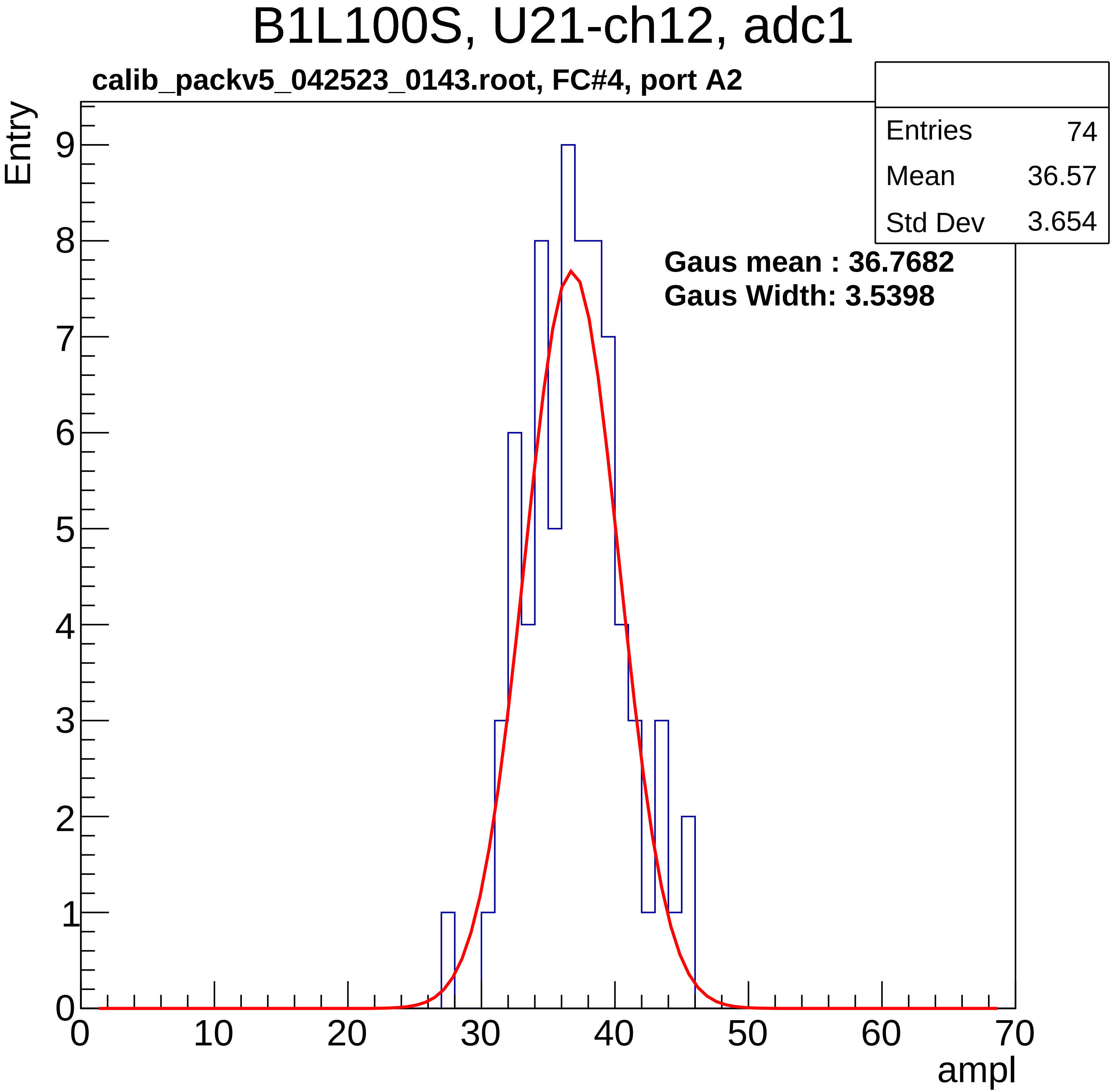
calib_packv5_042523_0143.root, FC#4, port A2

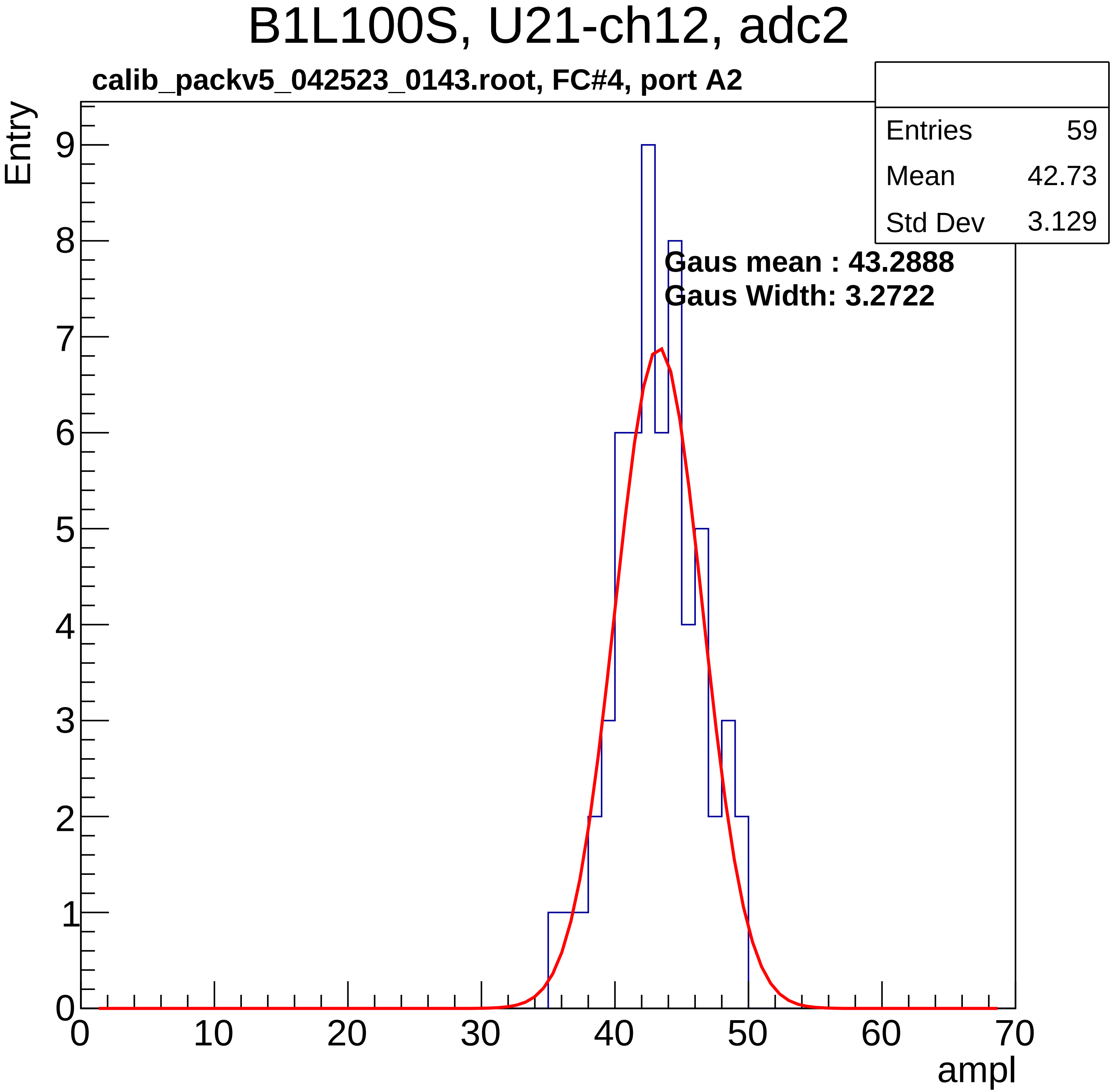


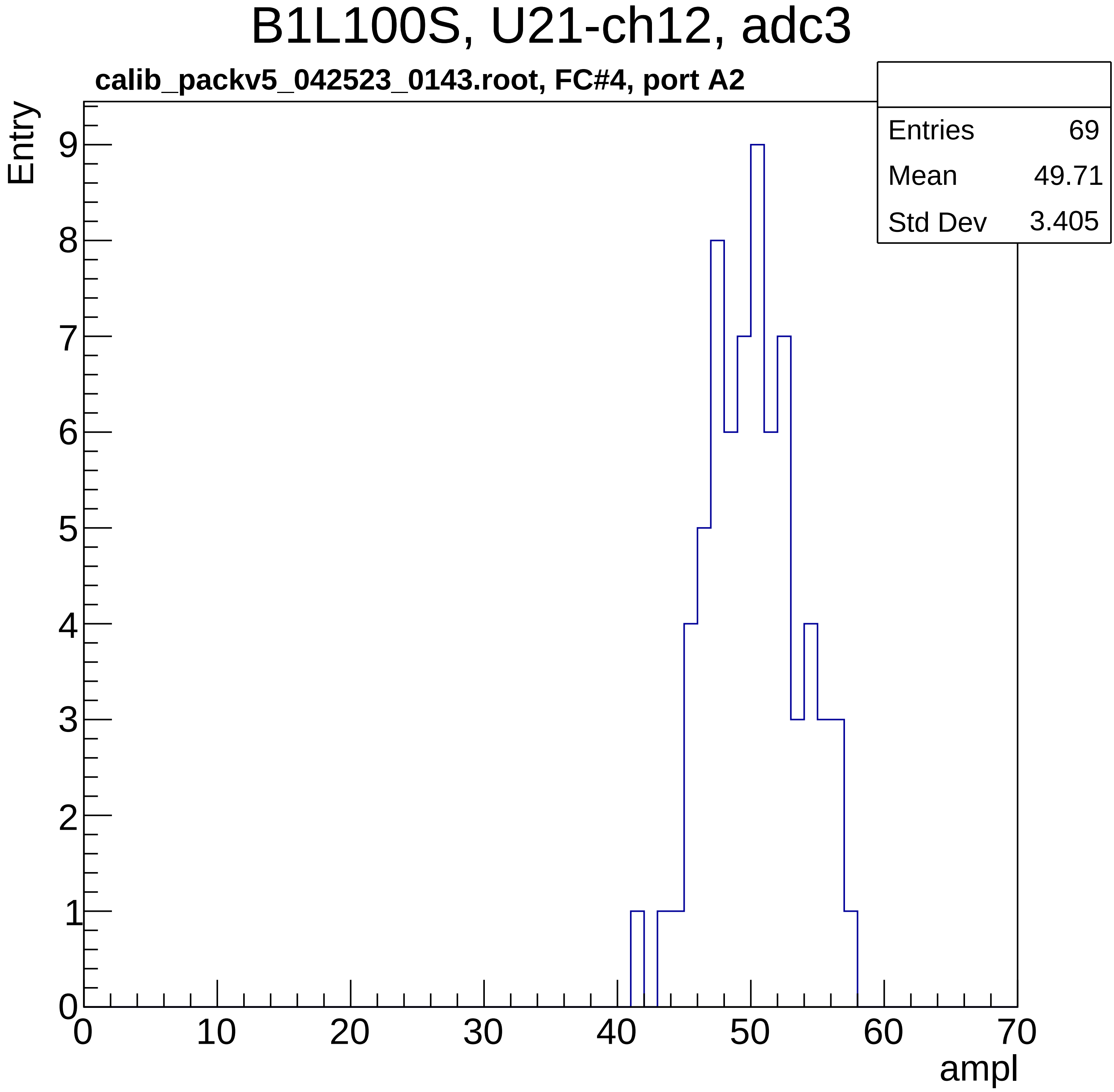

B1L100S, U21-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

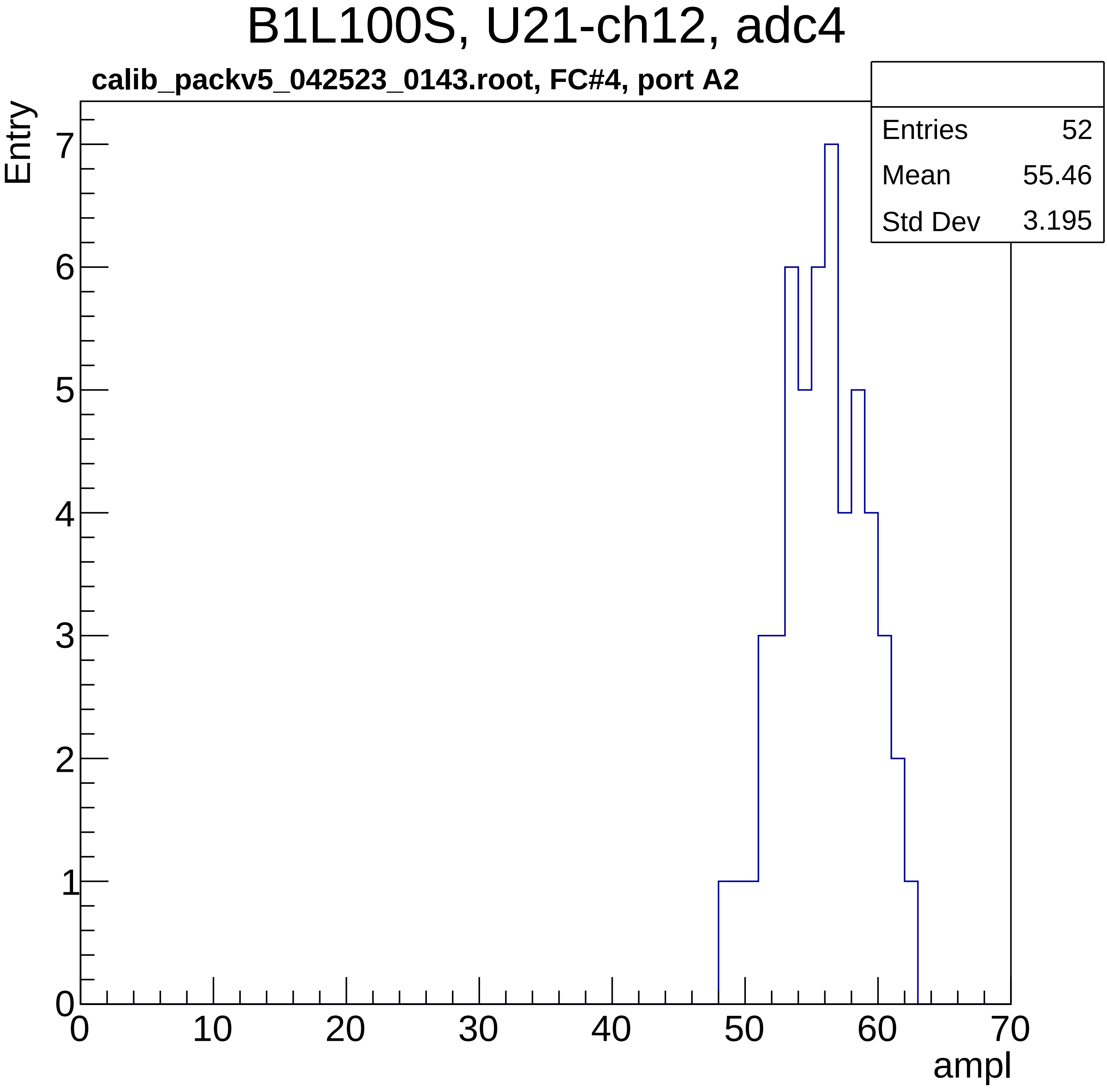

B1L100S, U21-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

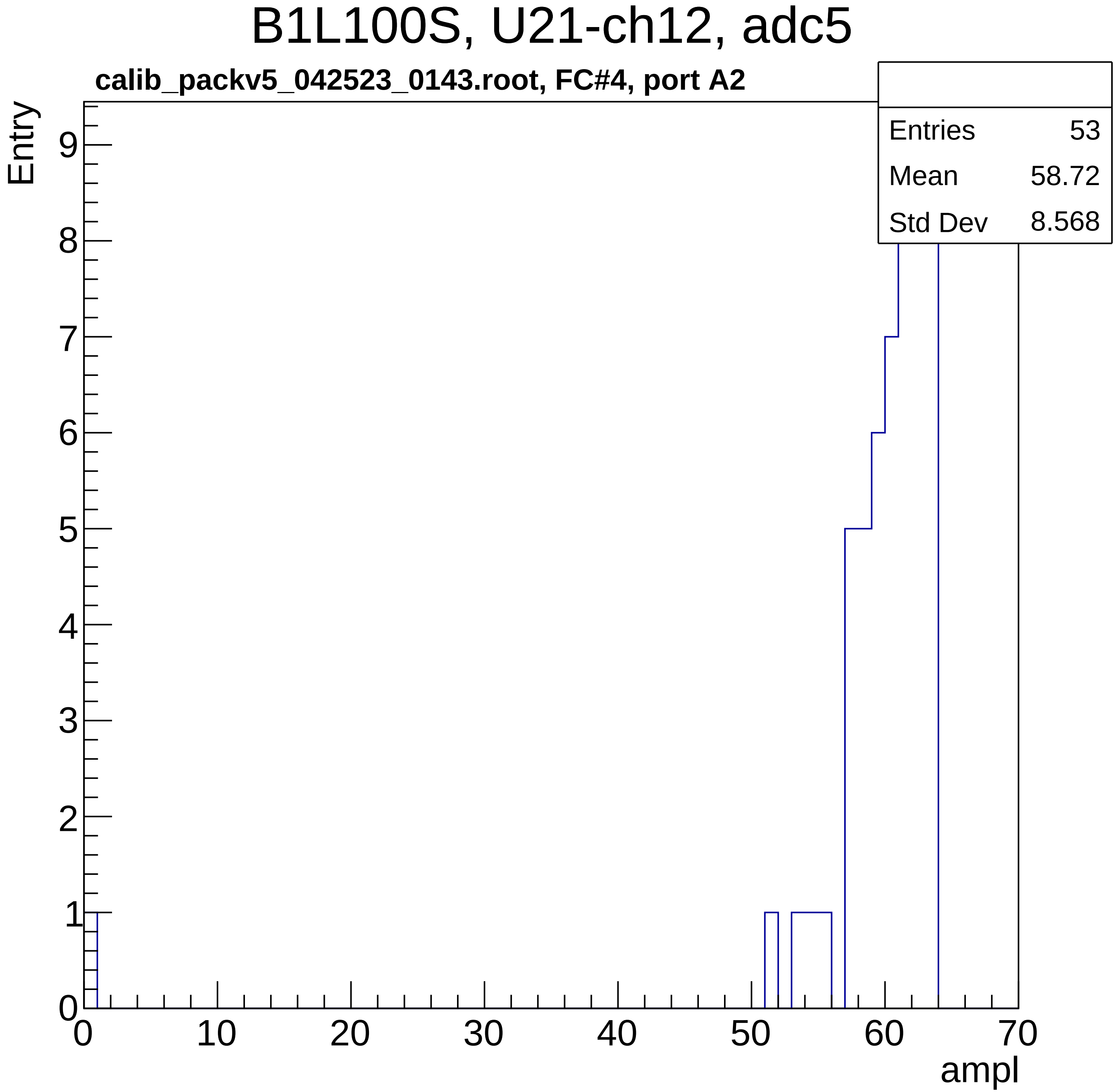

B1L100S, U21-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.82
Std Dev 3.452
Gaus mean : 29.3284
Gaus Width: 3.7947
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 74
Mean 36.57
Std Dev 3.654
Gaus mean : 36.7682
Gaus Width: 3.5398
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

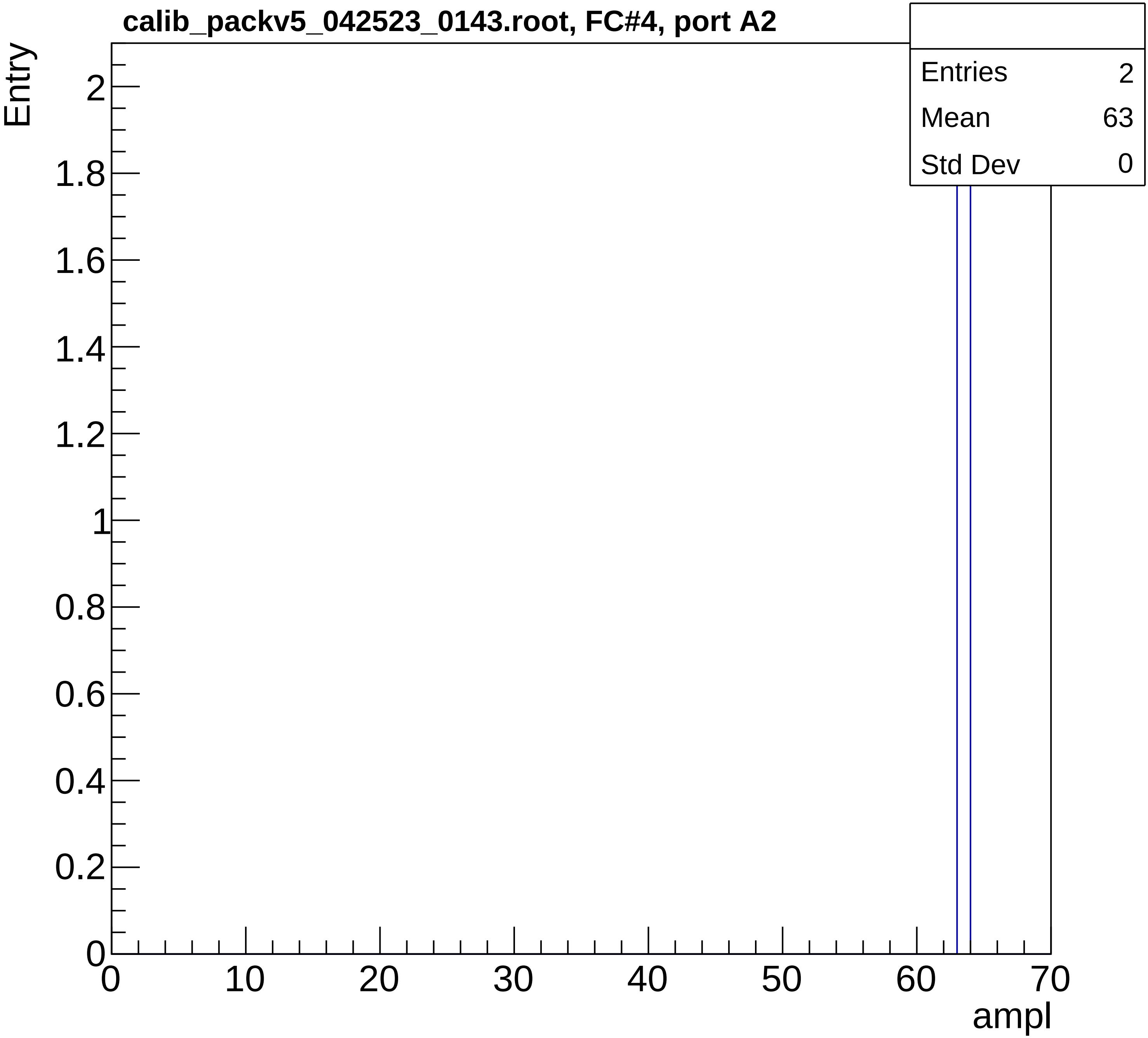

B1L100S, U21-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.73
Std Dev 3.129
Gaus mean : 43.2888
Gaus Width: 3.2722
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
Entries 69
Mean 49.71
Std Dev 3.405

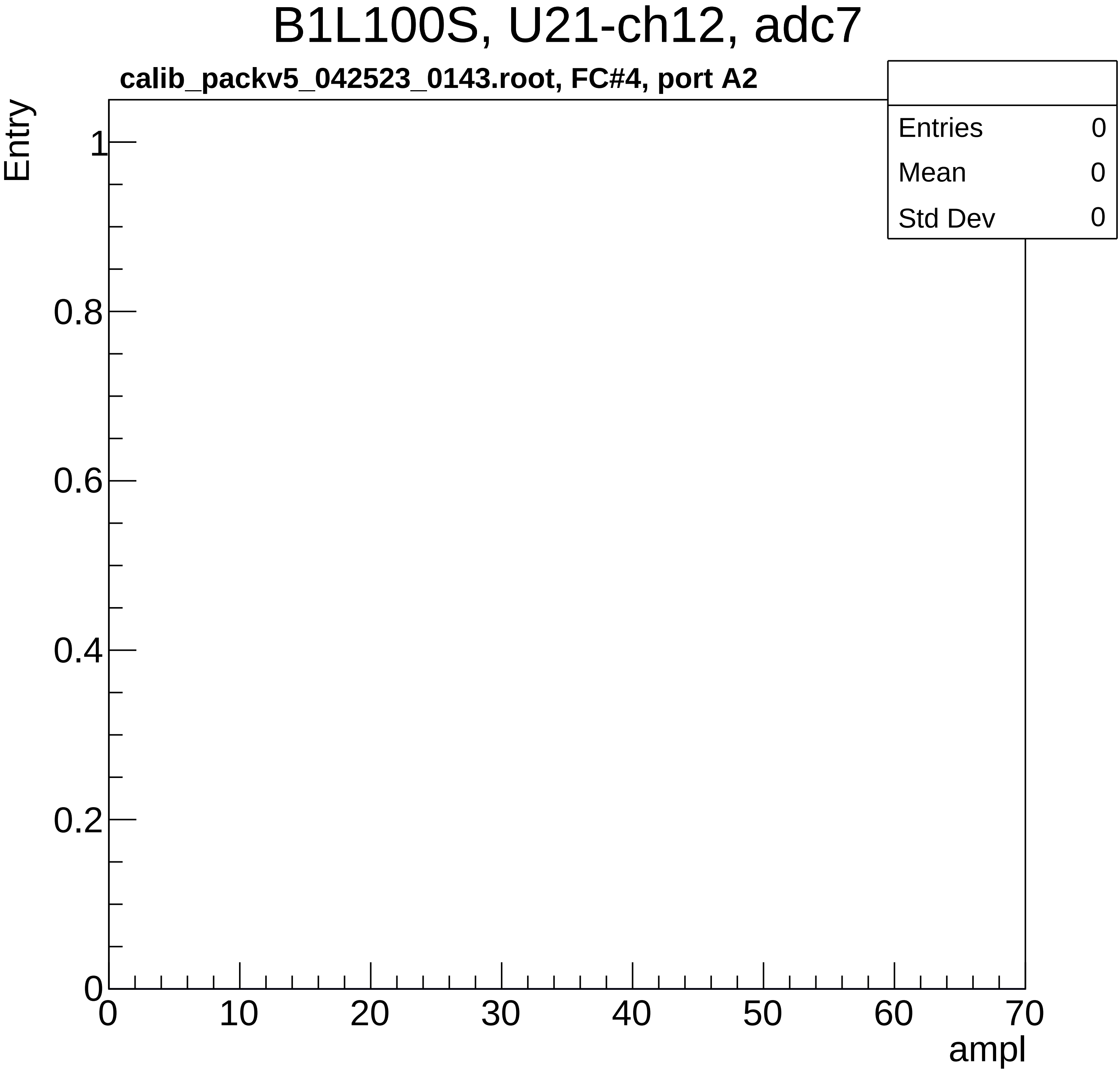

B1L100S, U21-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.46
Std Dev 3.195
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

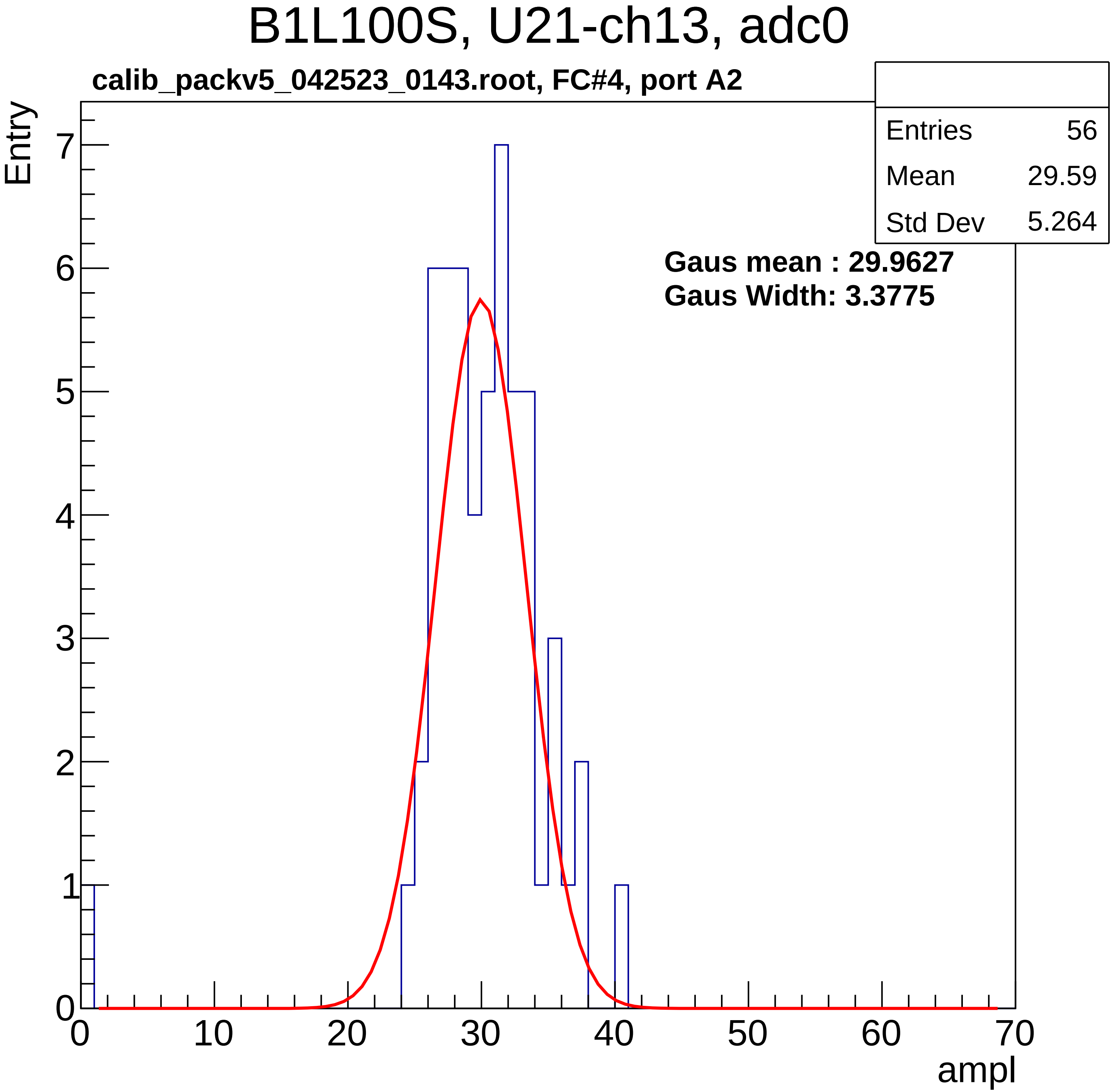

B1L100S, U21-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.72
Std Dev 8.568
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

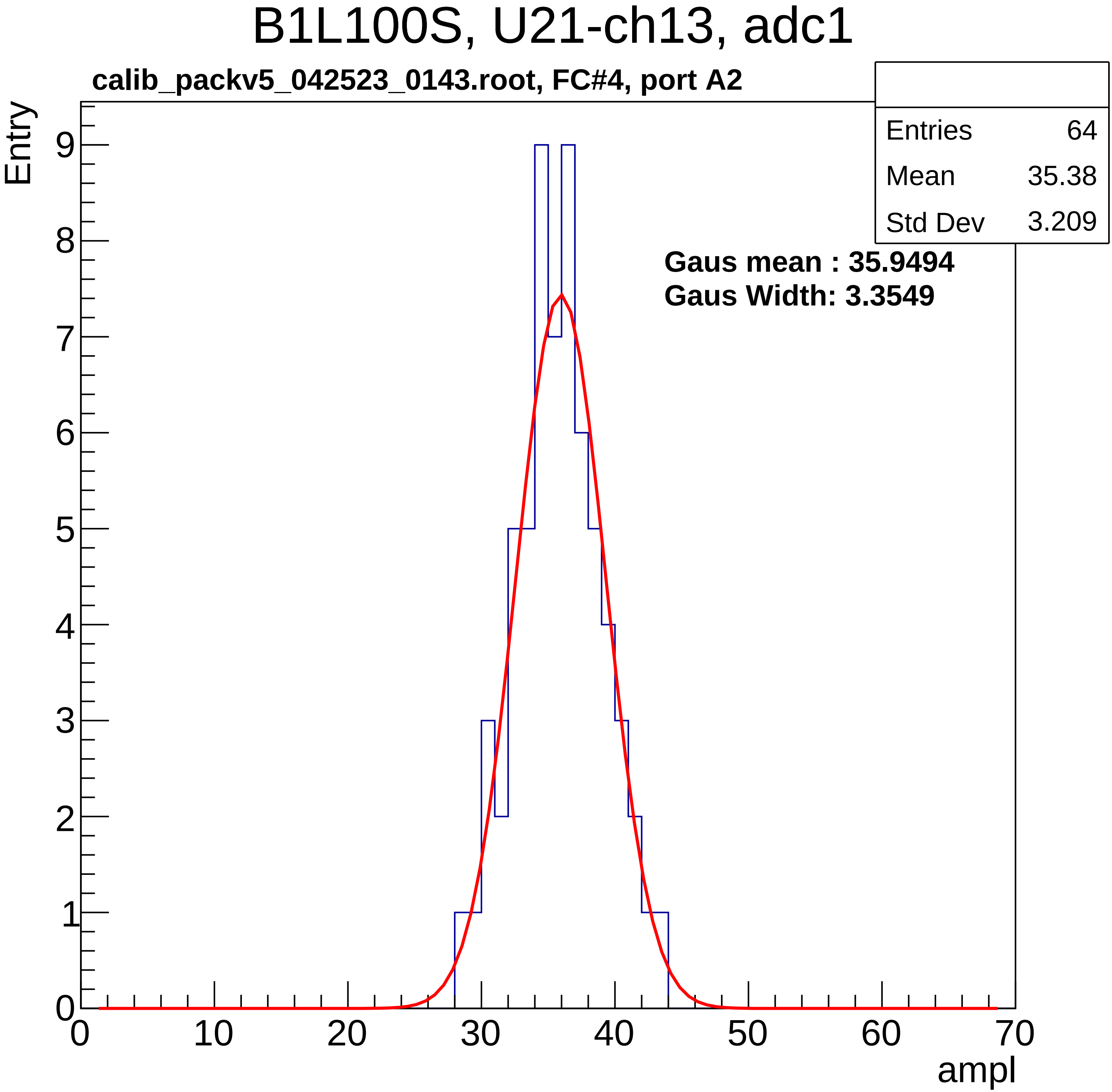
B1L100S, U21-ch12, adc6

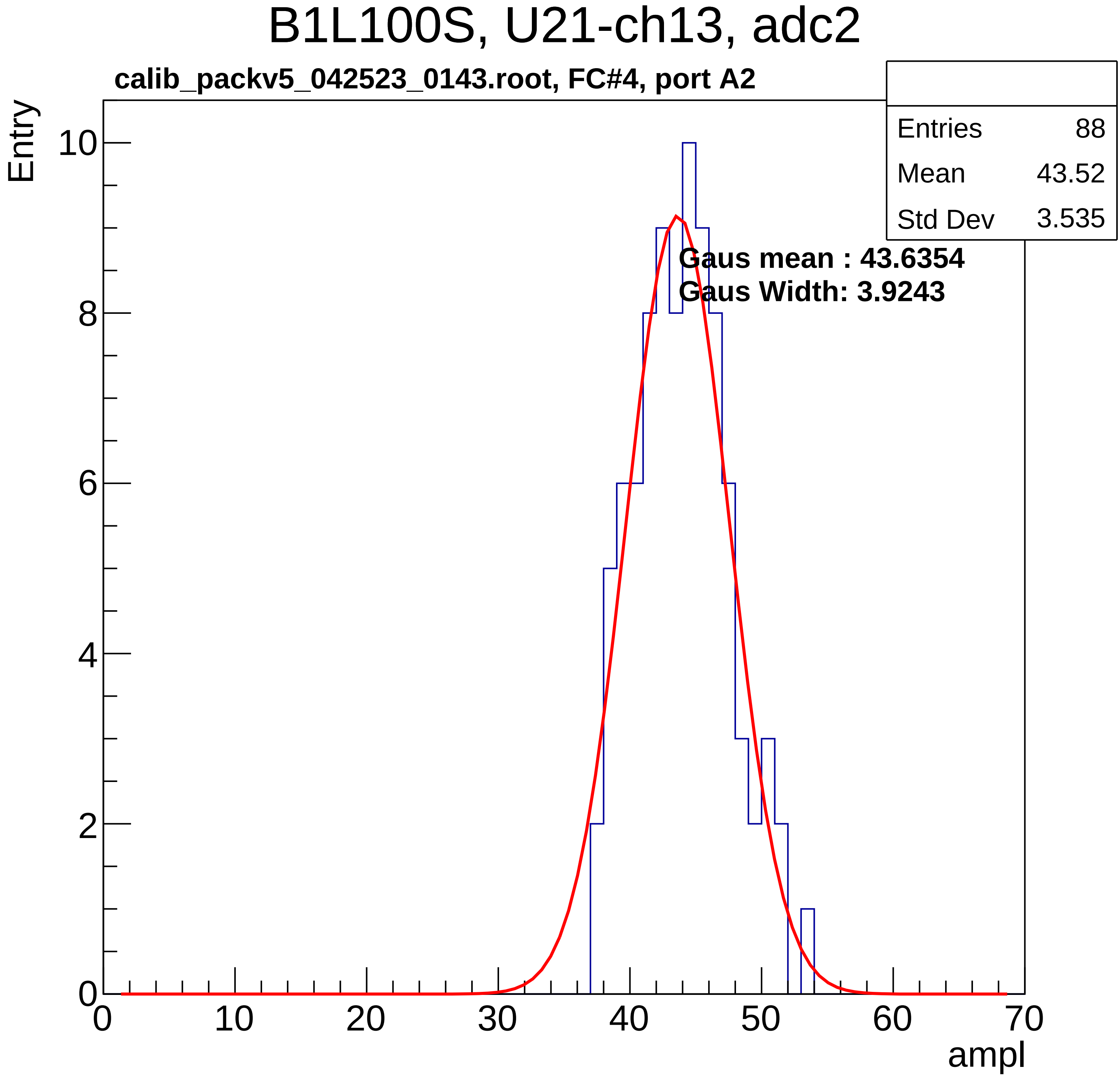


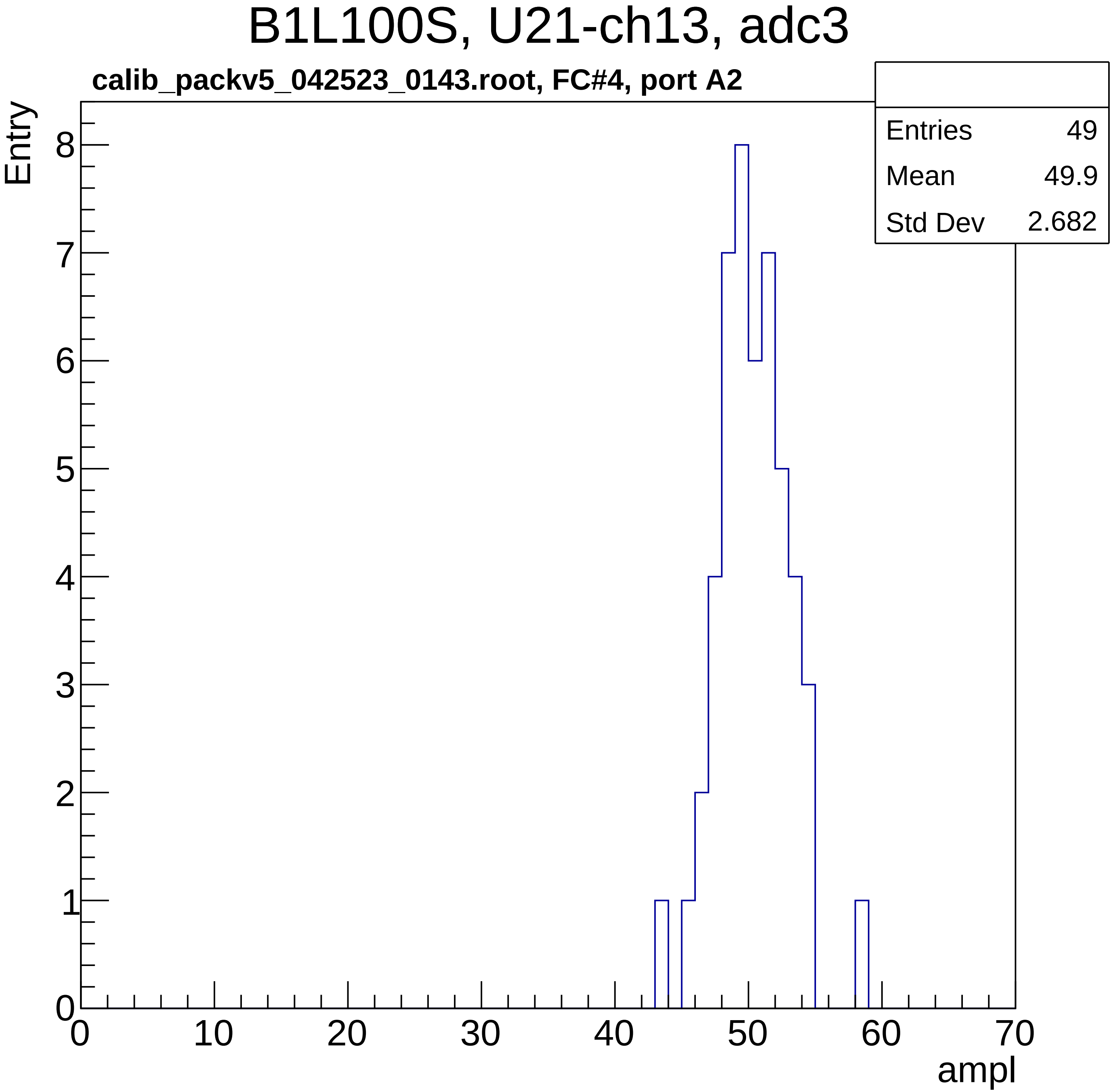
calib_packv5_042523_0143.root, FC#4, port A2

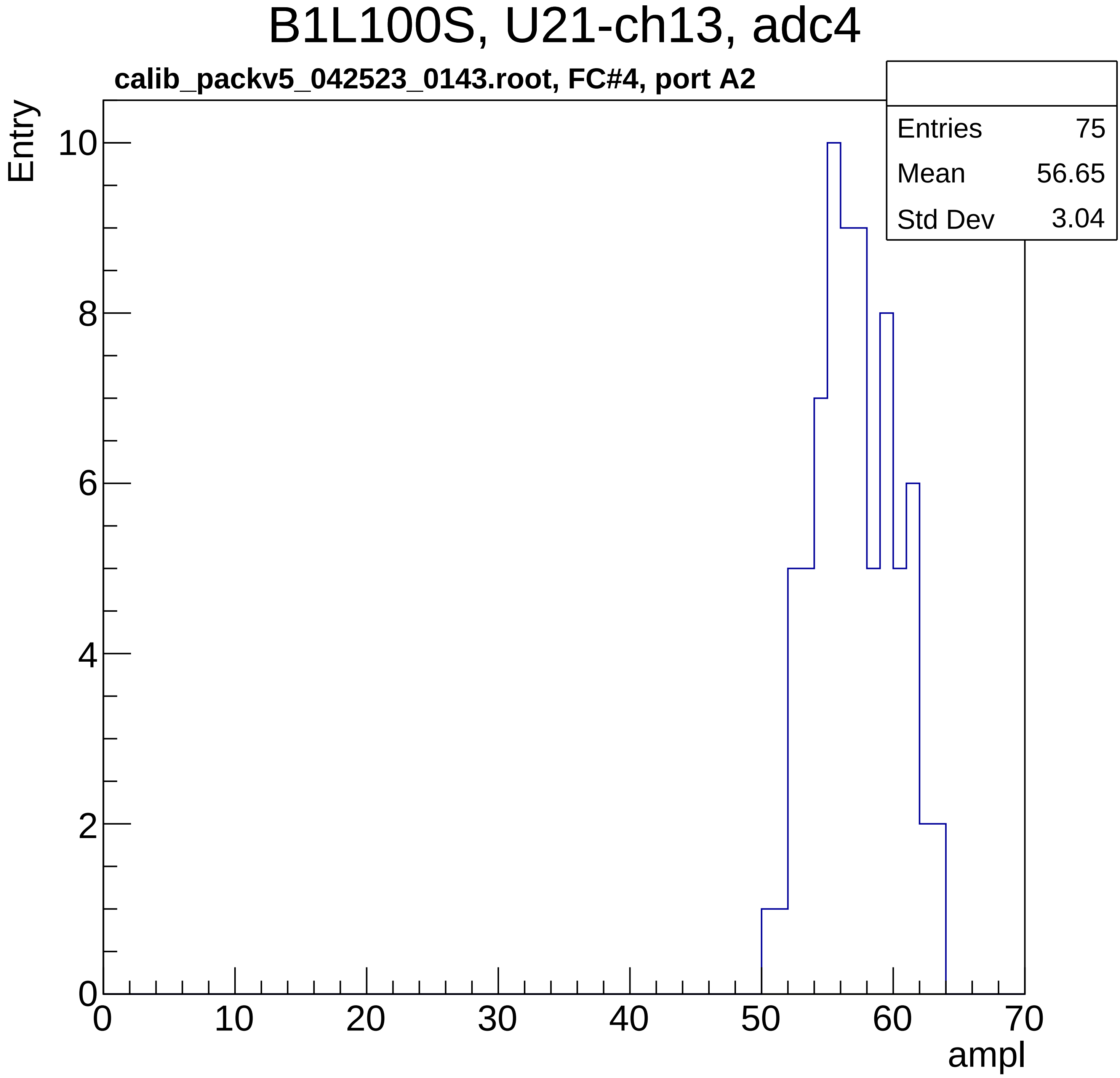

B1L100S, U21-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

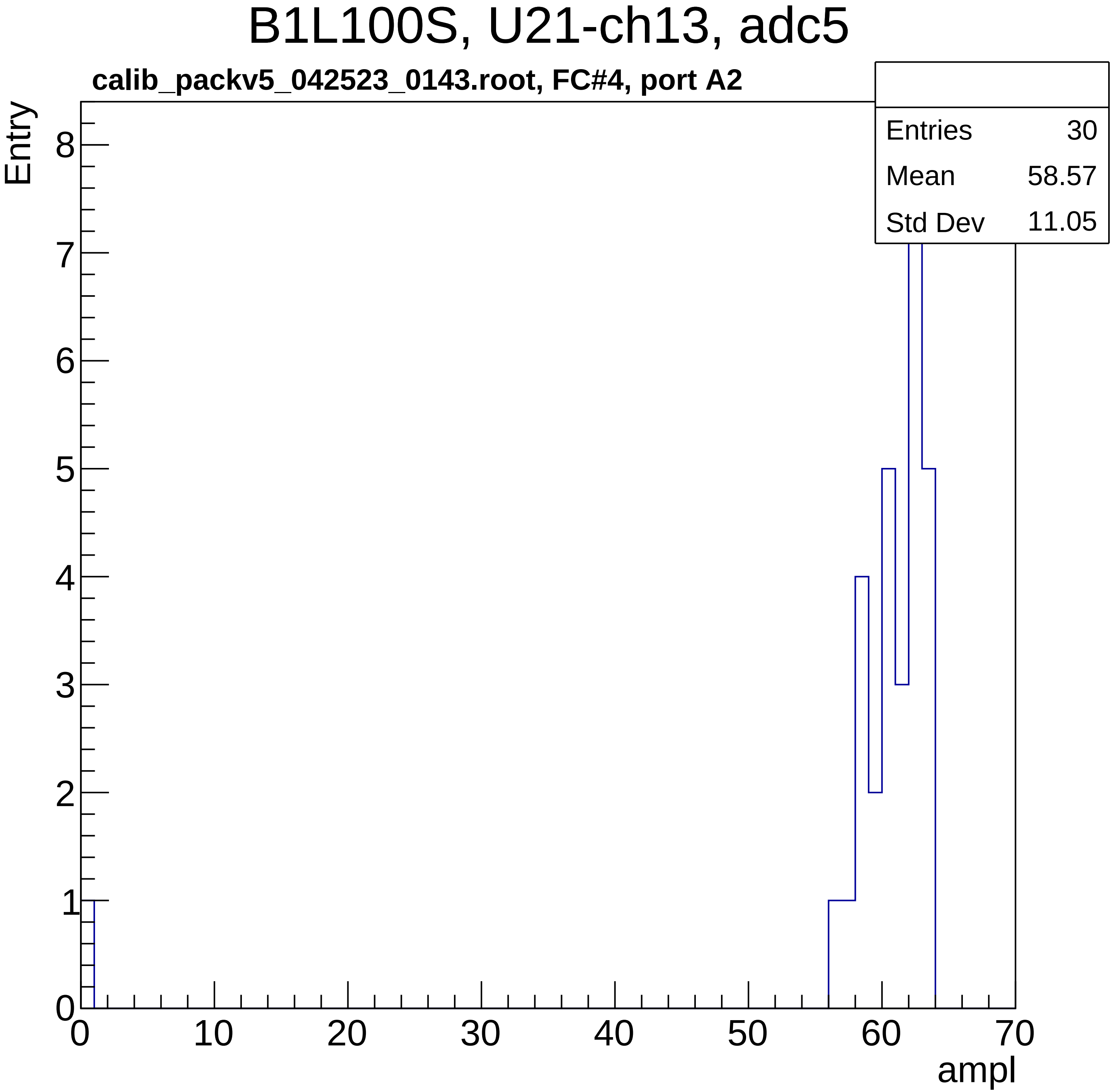

B1L100S, U21-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 29.59
Std Dev 5.264
Gaus mean : 29.9627
Gaus Width: 3.3775
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

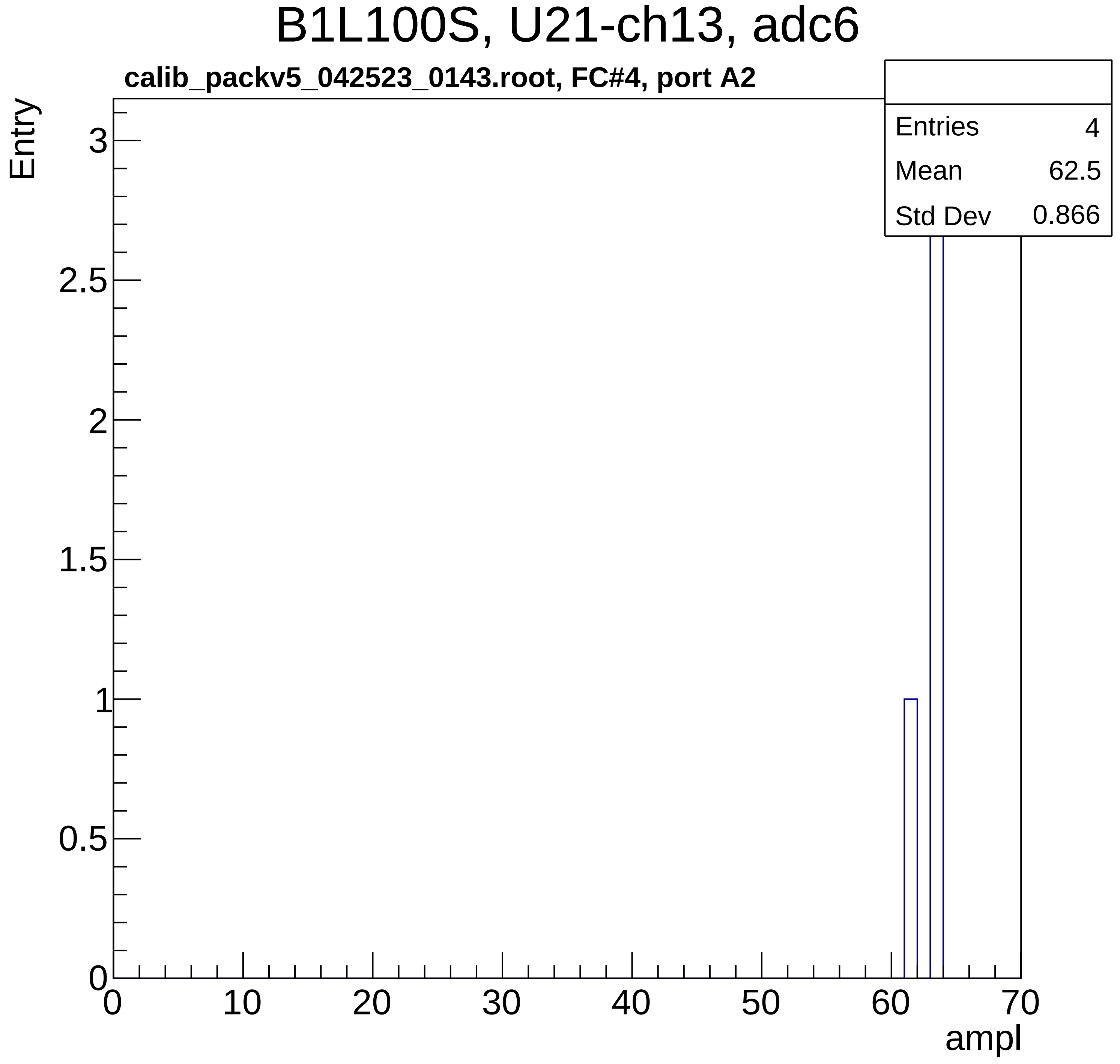

B1L100S, U21-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 35.38
Std Dev 3.209
Gaus mean : 35.9494
Gaus Width: 3.3549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

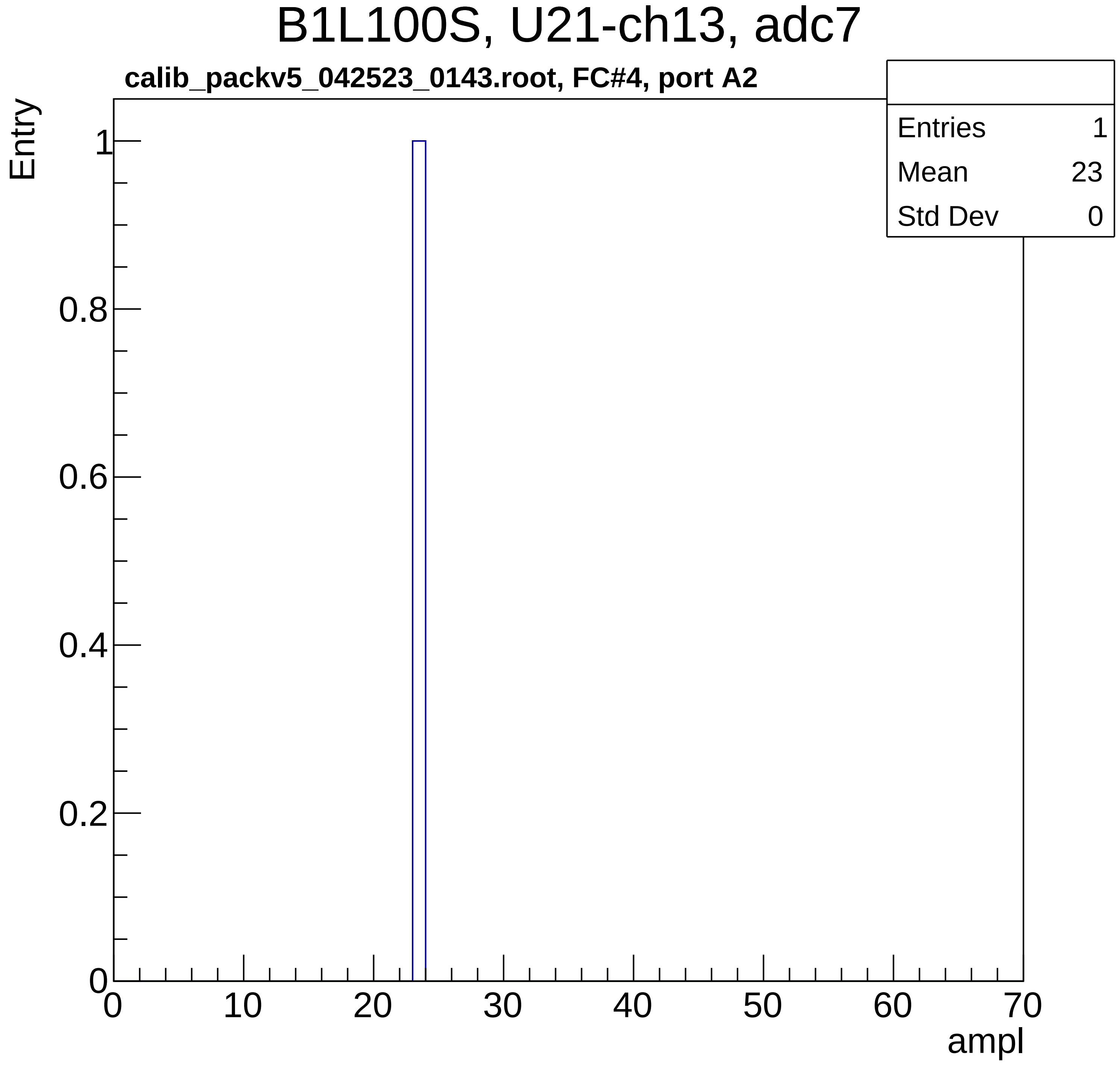

B1L100S, U21-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 43.52
Std Dev 3.535
Gaus mean : 43.6354
Gaus Width: 3.9243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

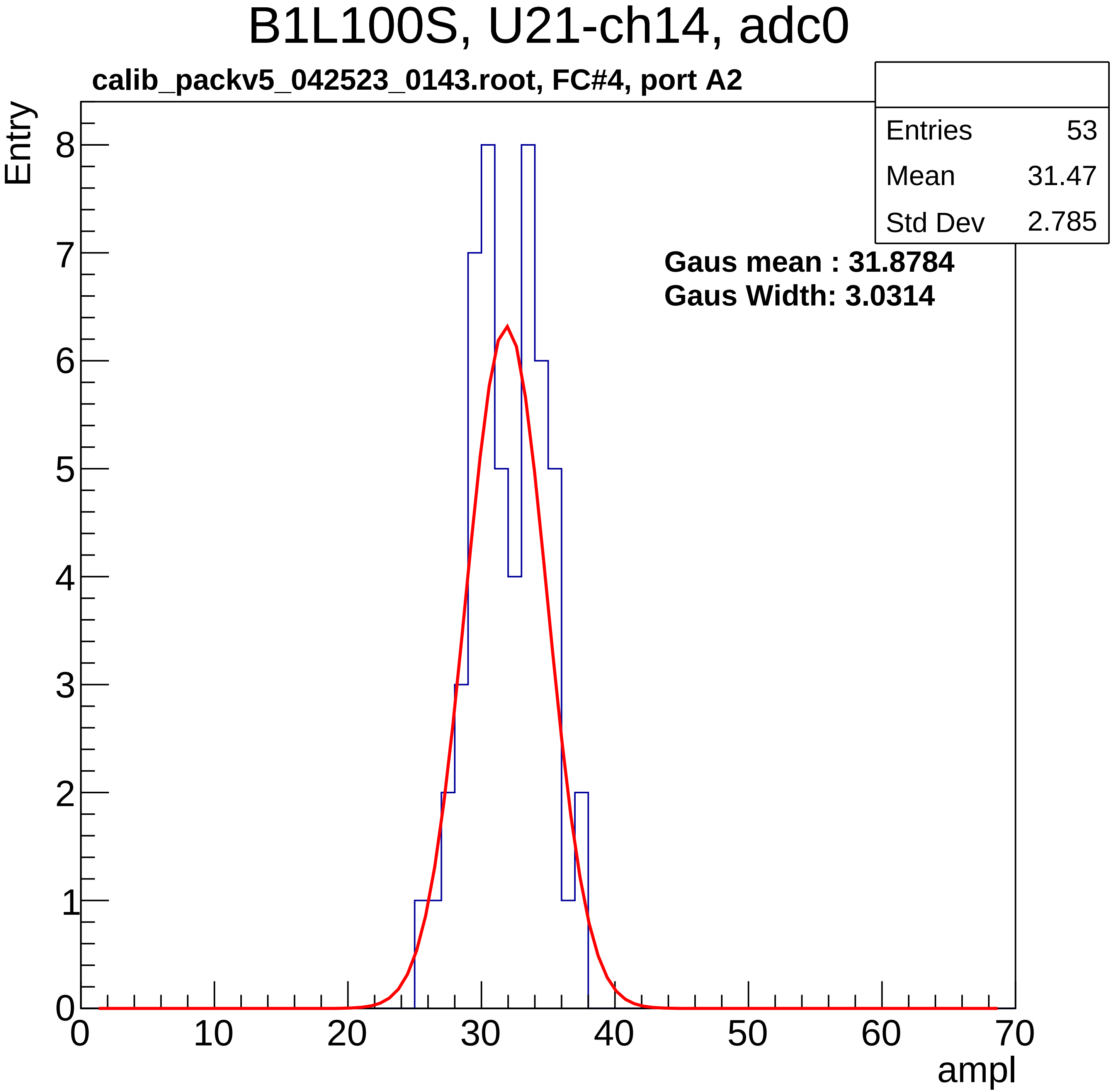

B1L100S, U21-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 49.9
Std Dev 2.682
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

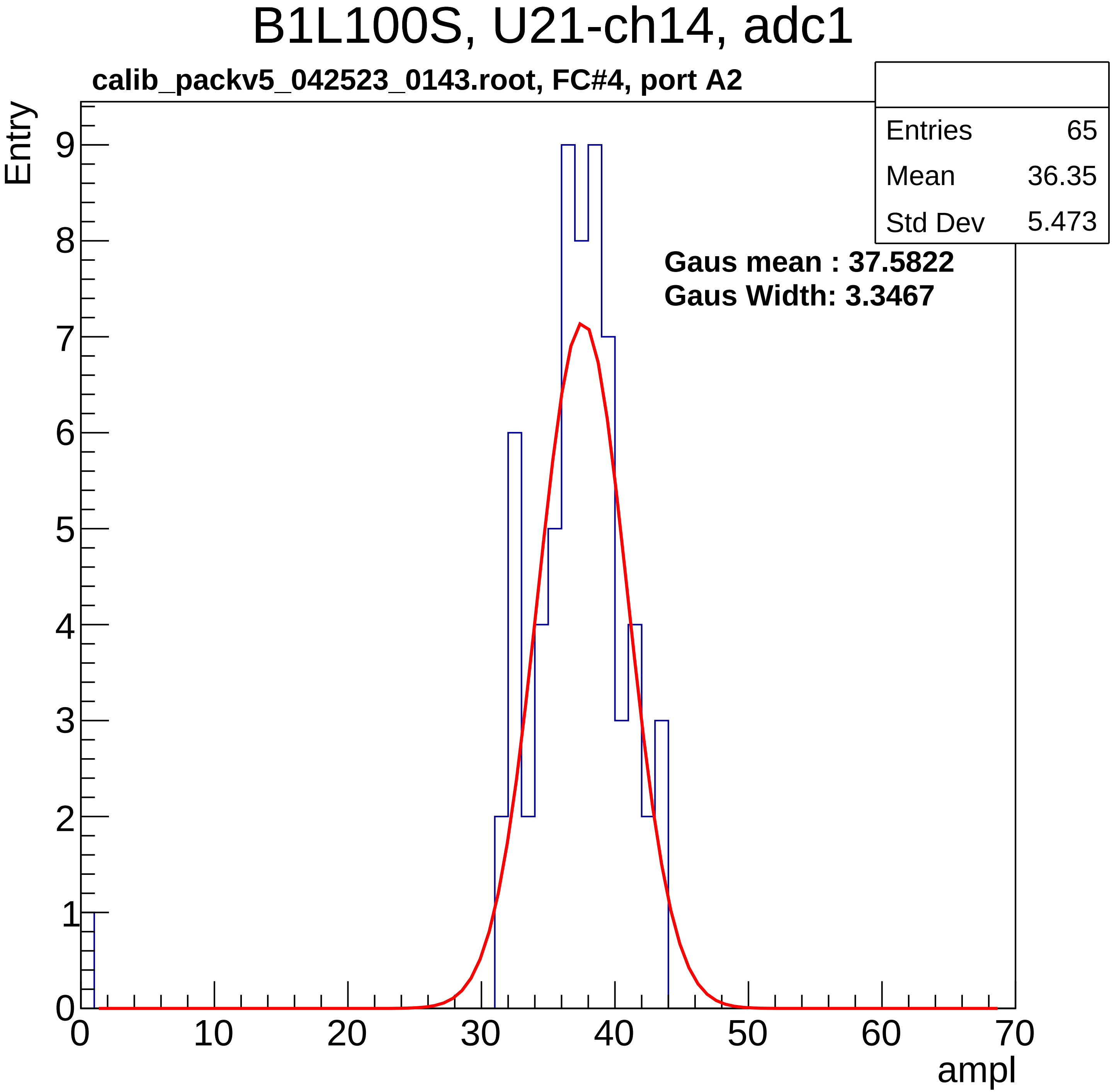

B1L100S, U21-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 56.65
Std Dev 3.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

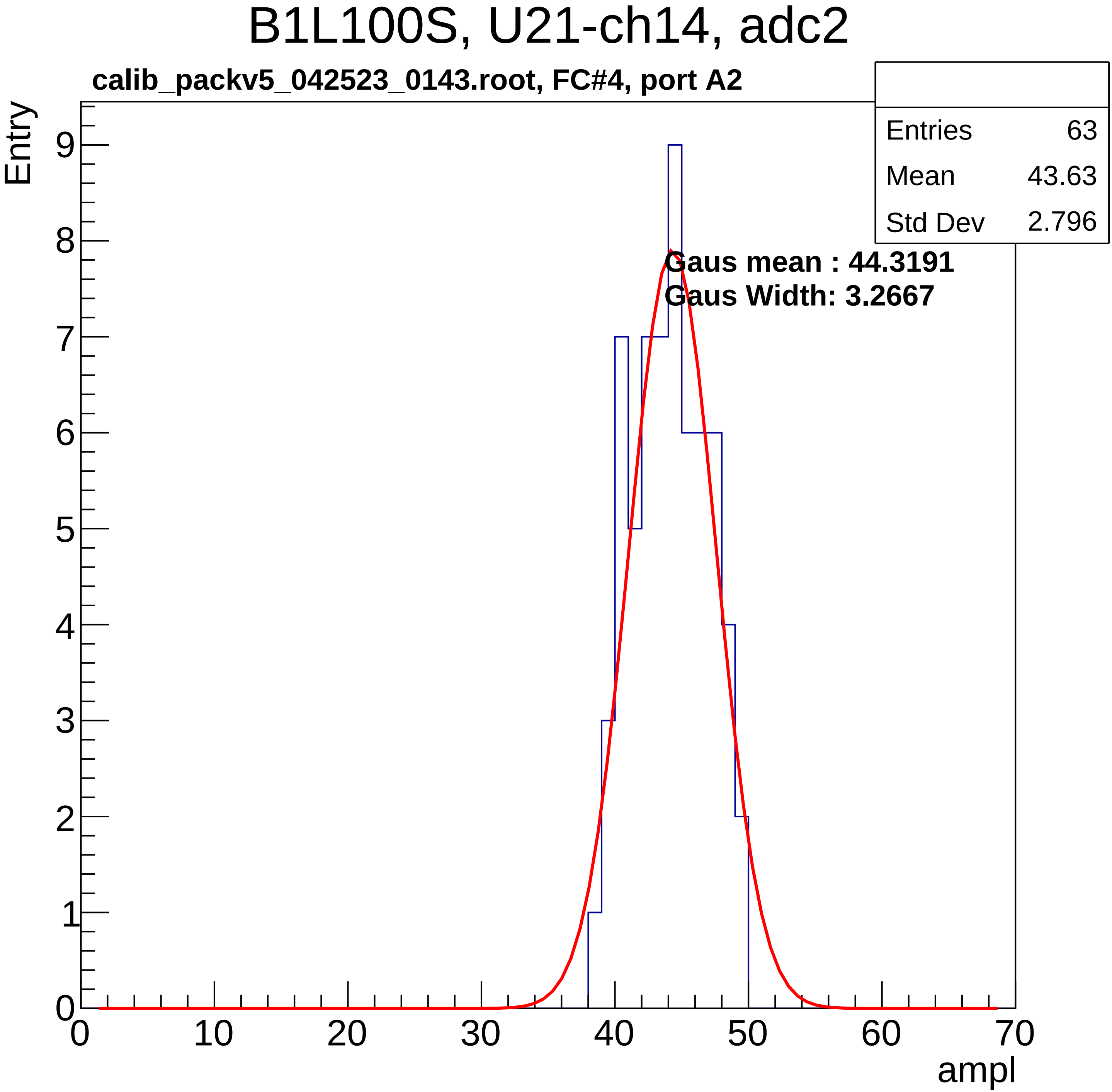

B1L100S, U21-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.57
Std Dev 11.05
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

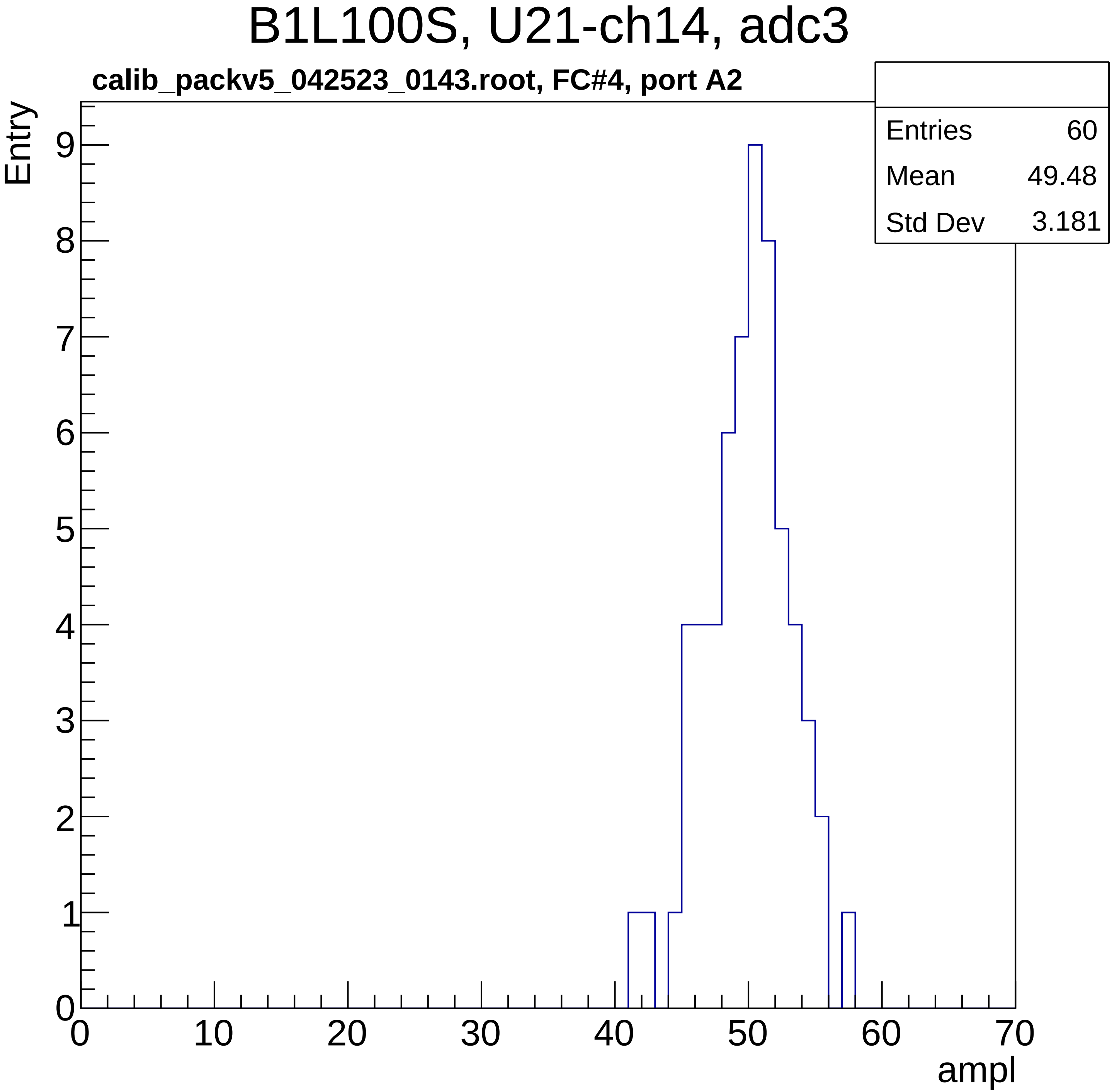

B1L100S, U21-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

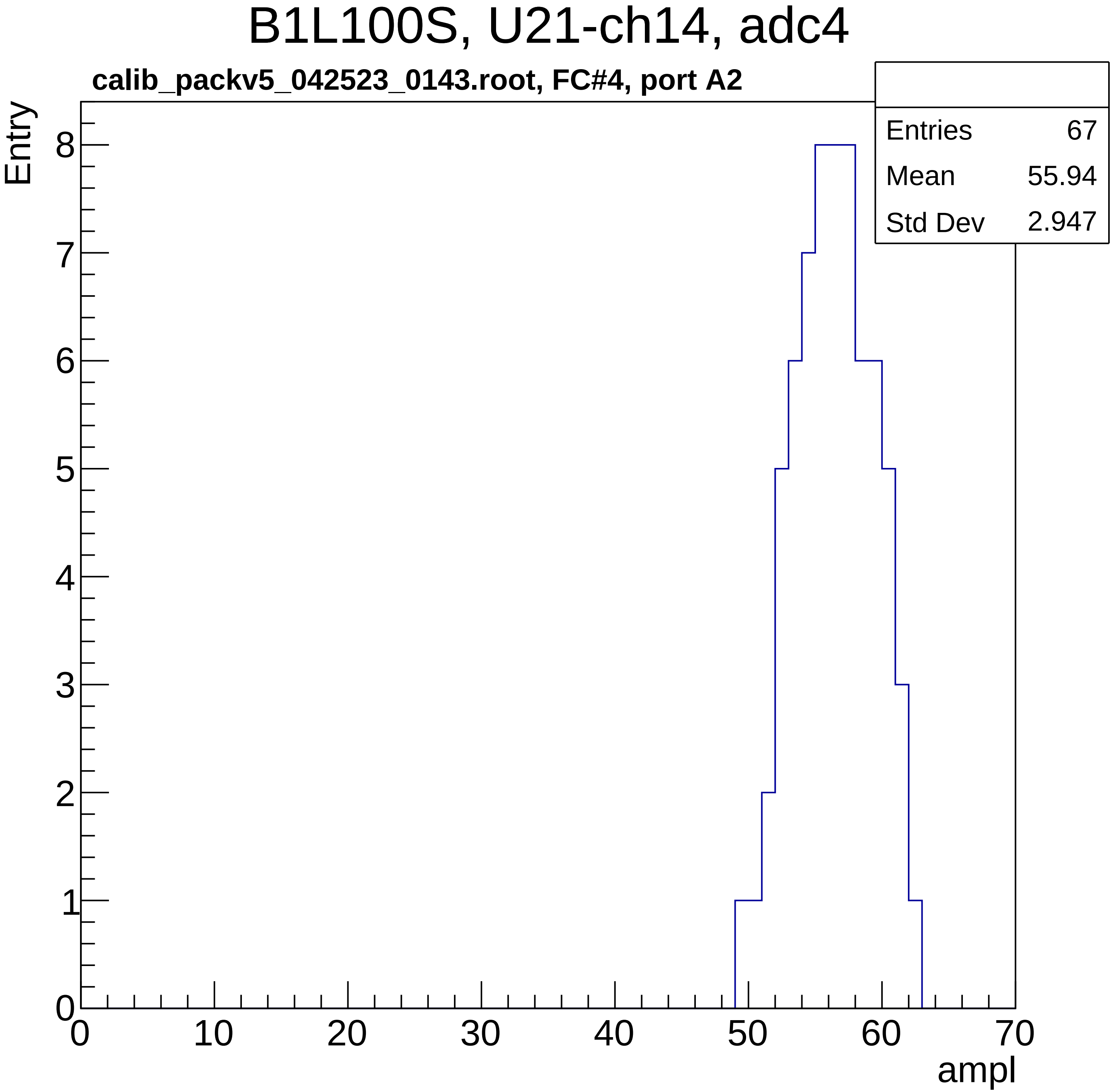

B1L100S, U21-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 31.47
Std Dev 2.785
Gaus mean : 31.8784
Gaus Width: 3.0314
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.35
Std Dev 5.473
Gaus mean : 37.5822
Gaus Width: 3.3467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70



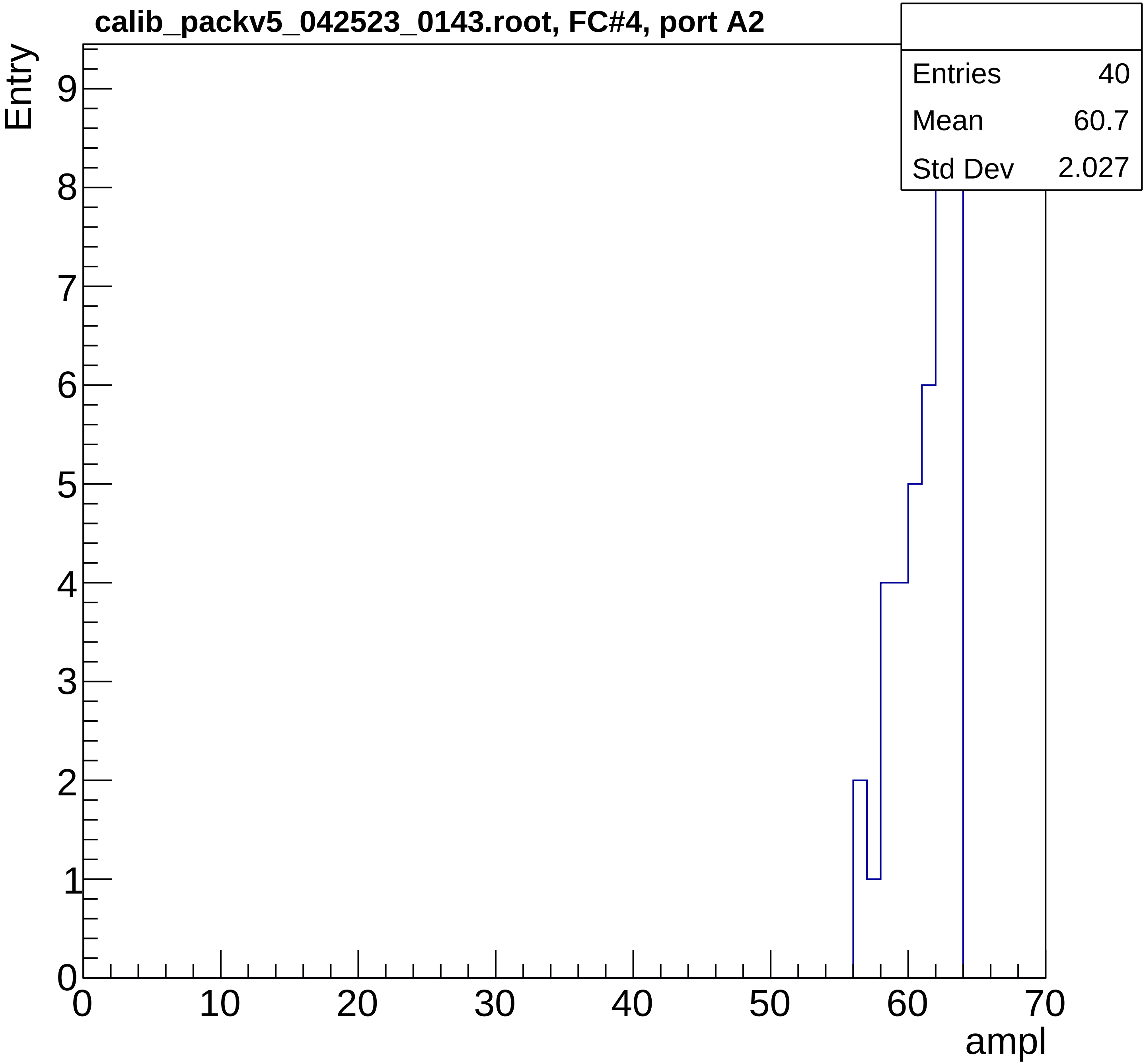
B1L100S, U21-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.63
Std Dev 2.796
Gaus mean : 44.3191
Gaus Width: 3.2667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

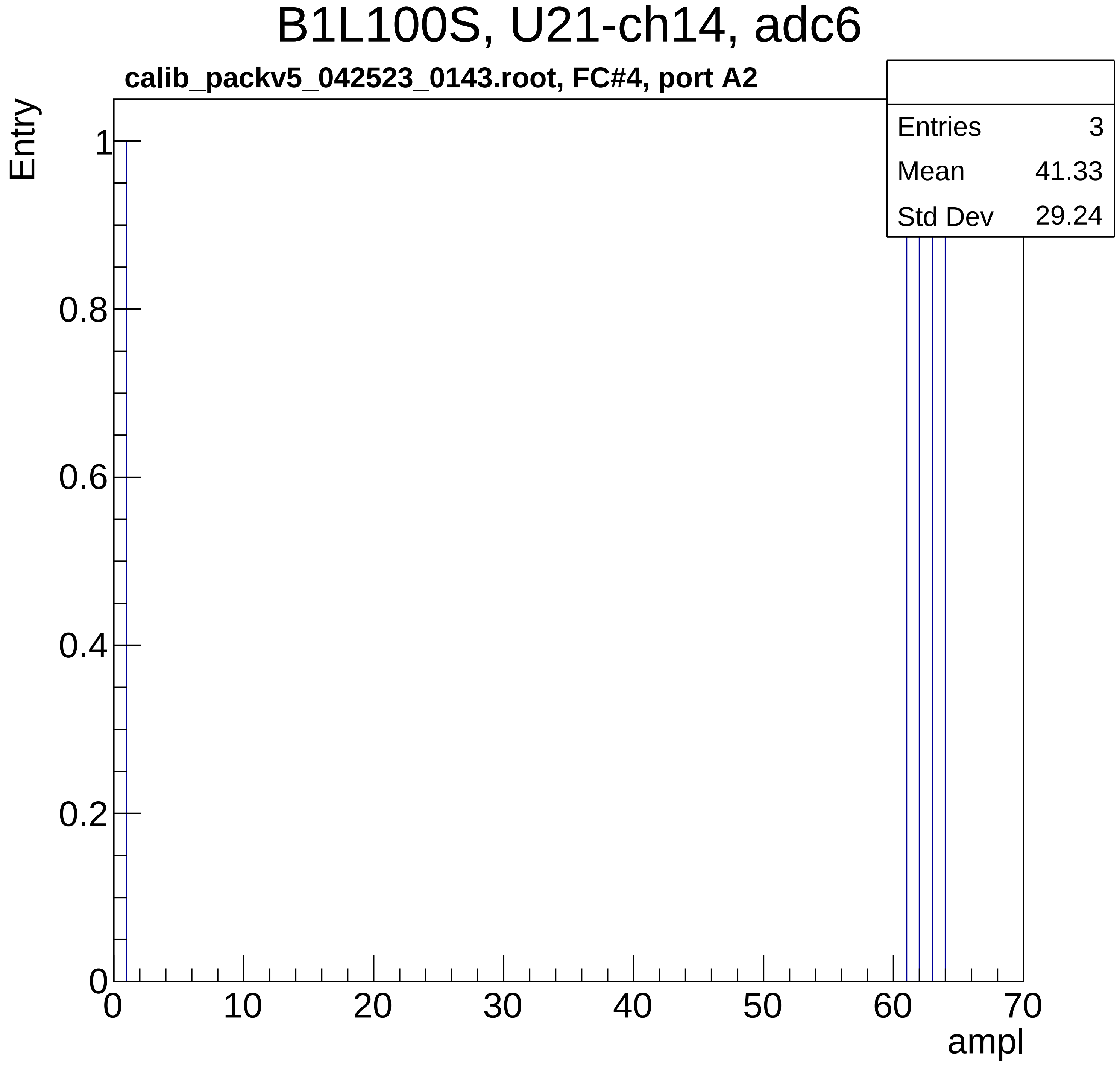

B1L100S, U21-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.48
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.94
Std Dev 2.947
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch14, adc5

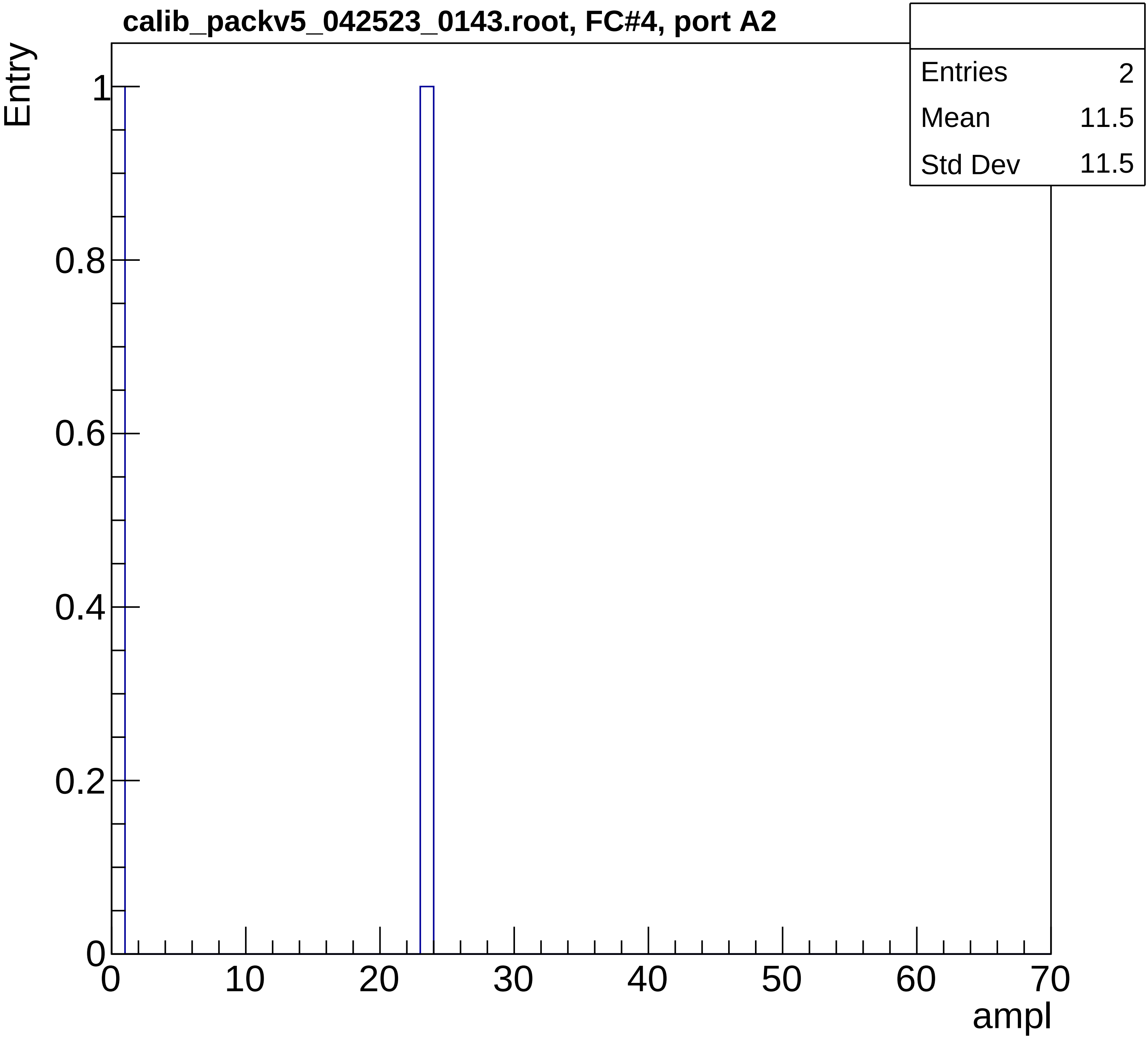
calib_packv5_042523_0143.root, FC#4, port A2

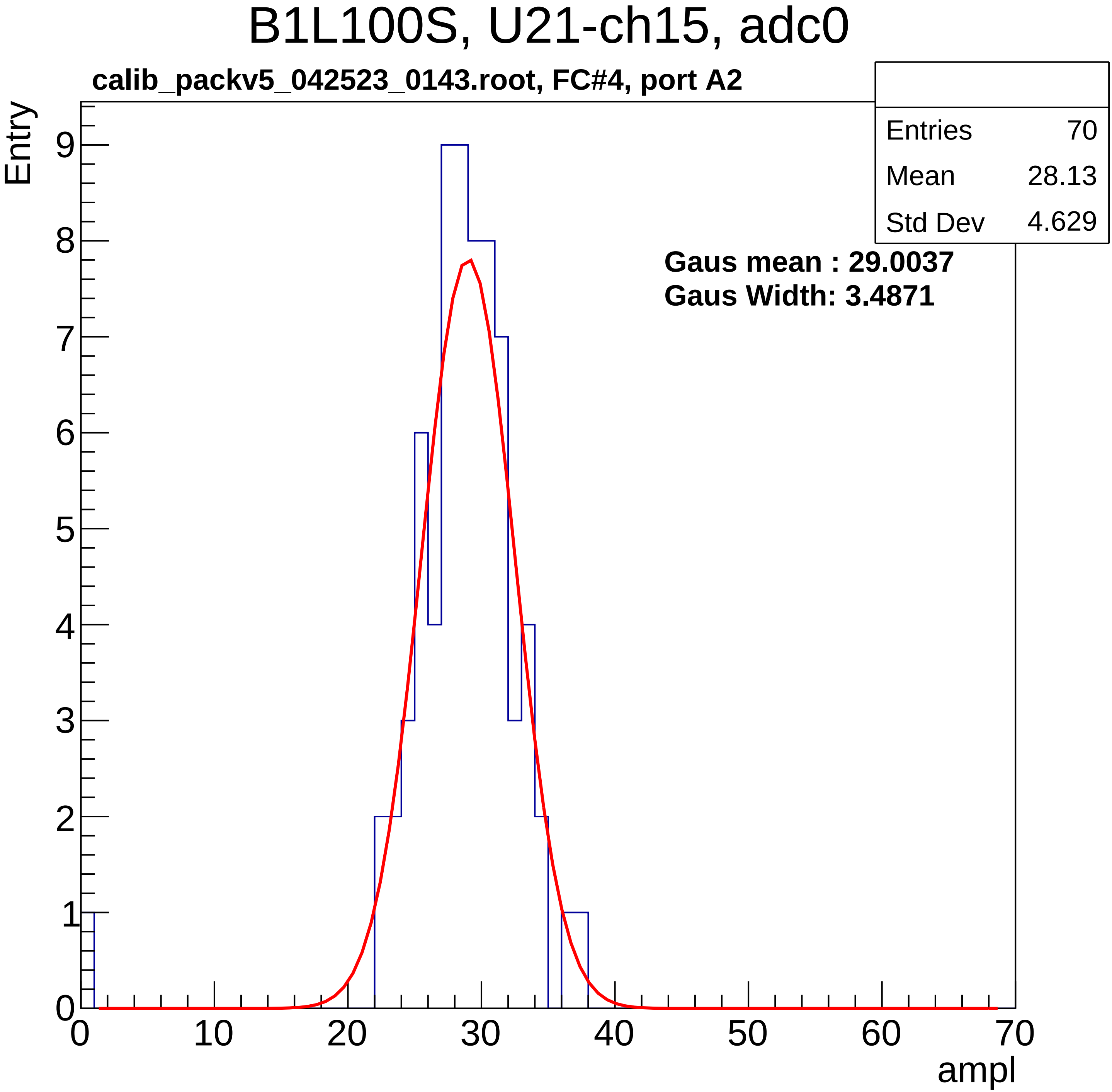


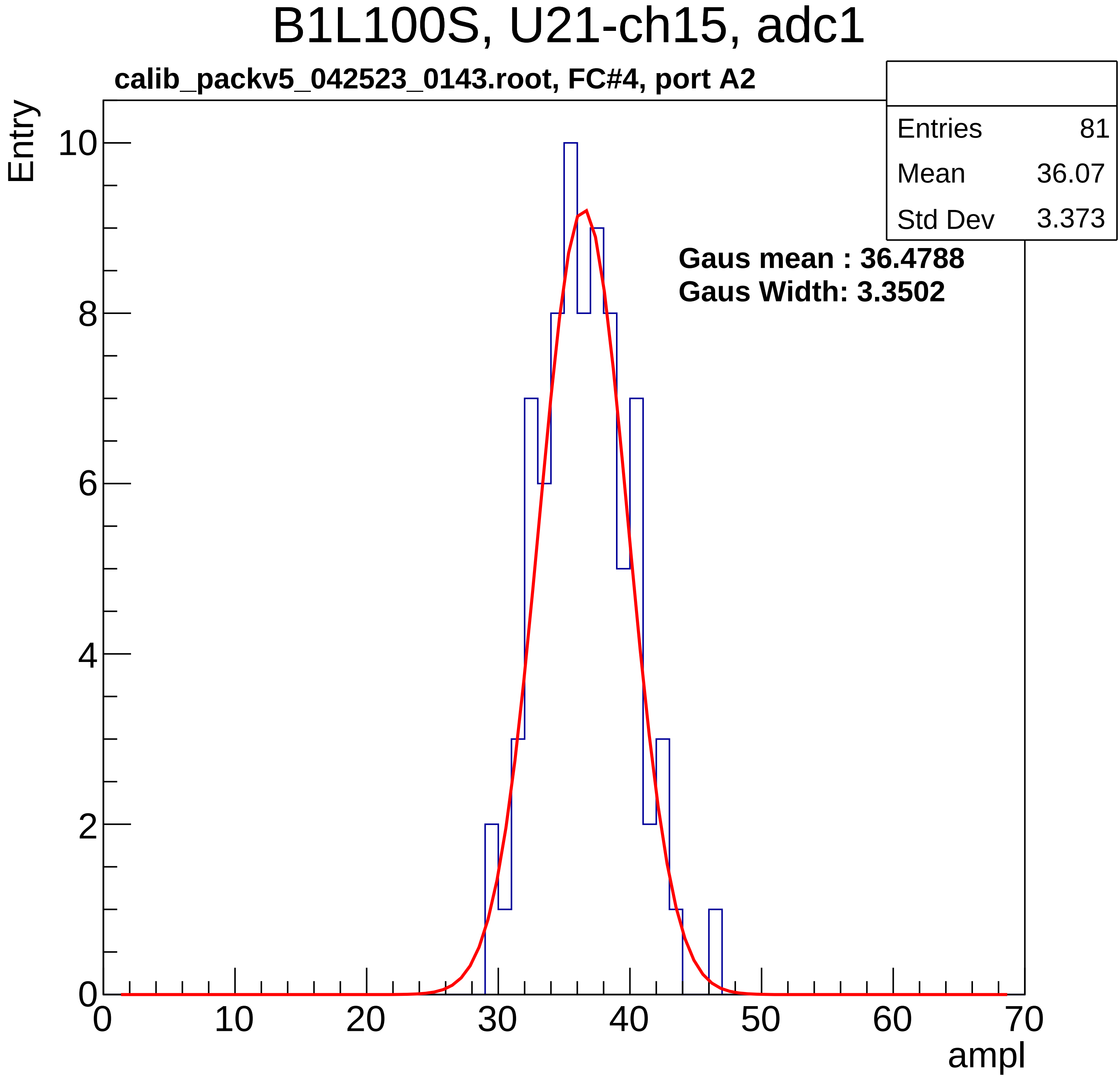

B1L100S, U21-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch14, adc7

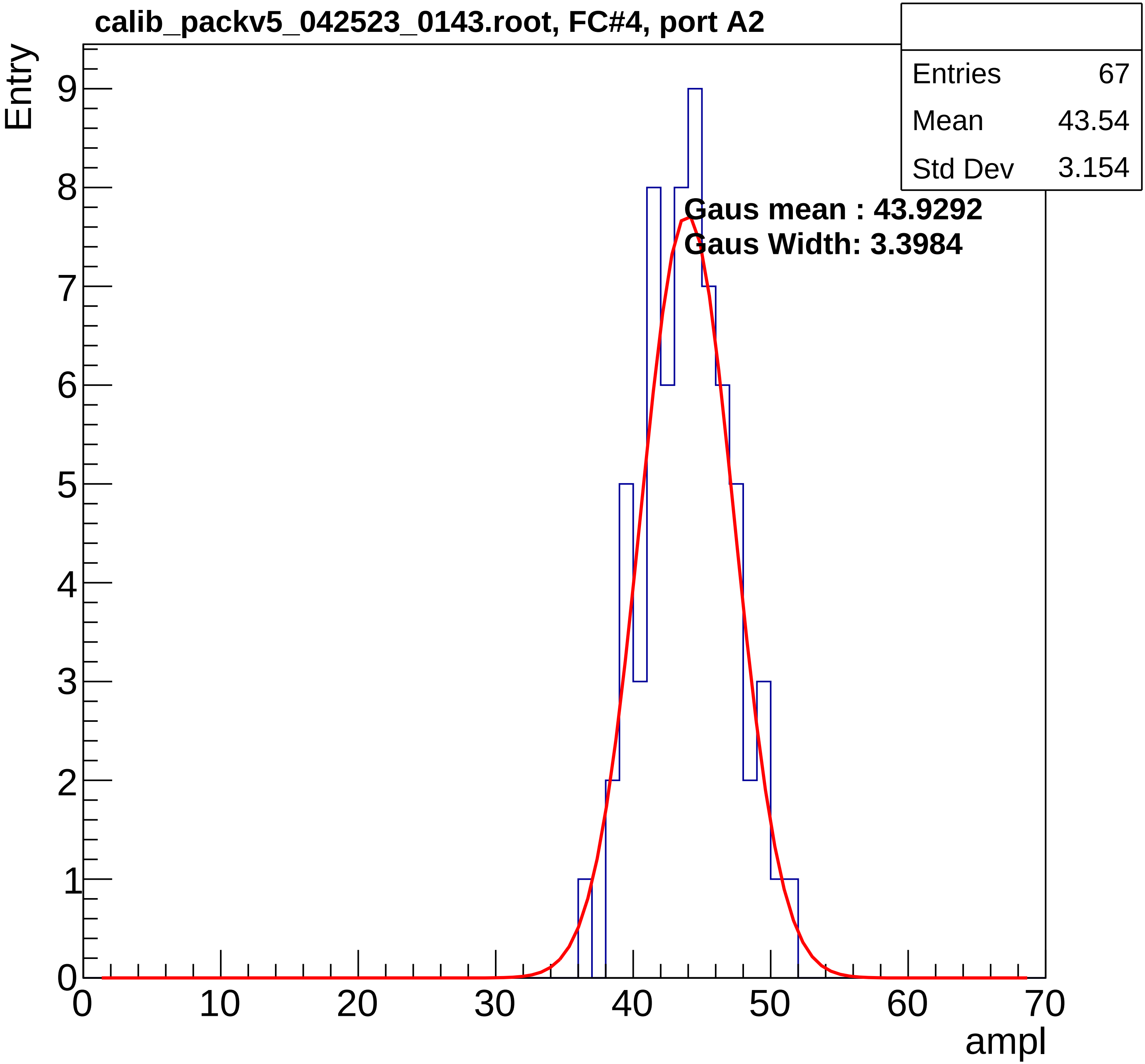
calib_packv5_042523_0143.root, FC#4, port A2

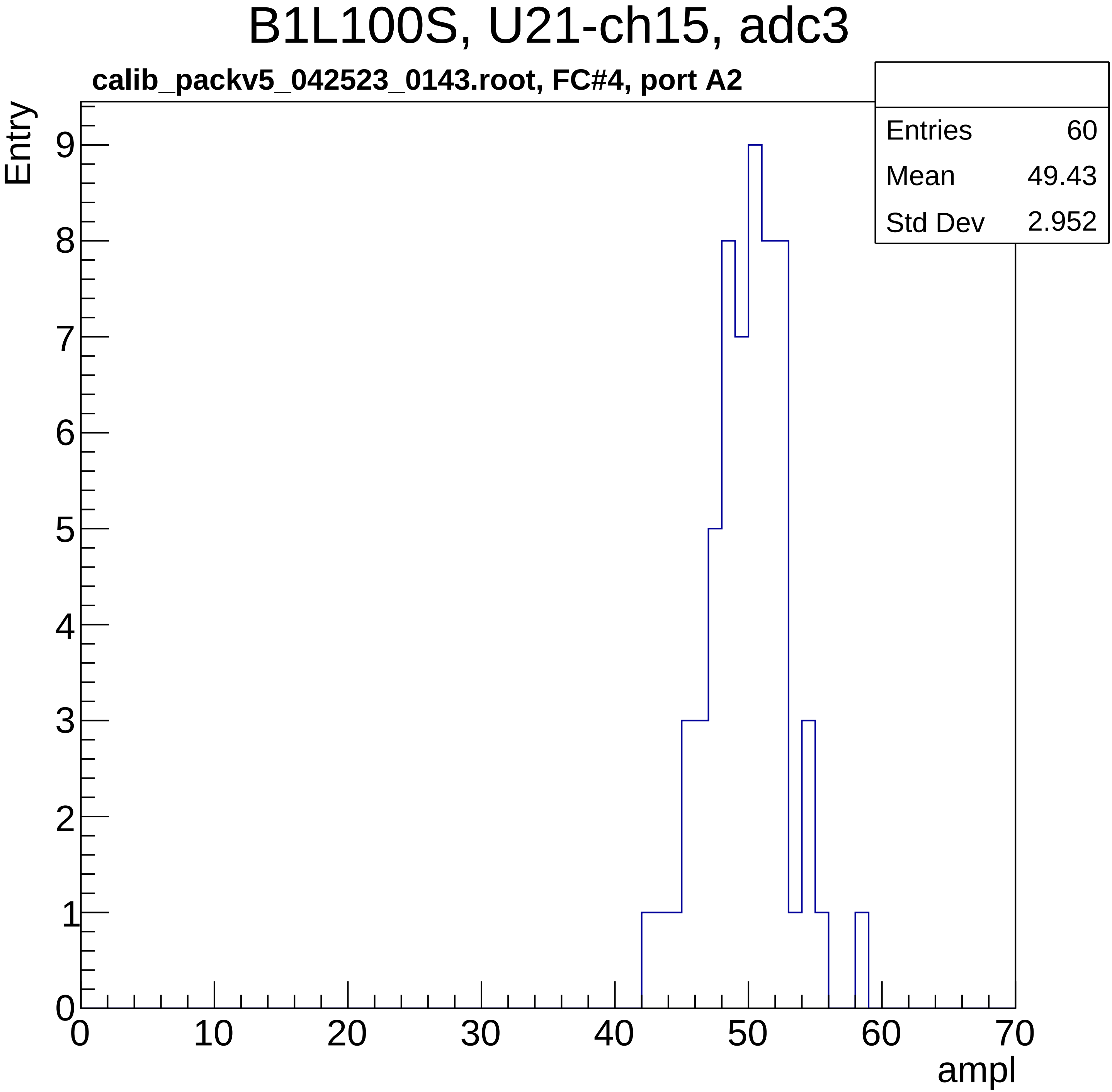


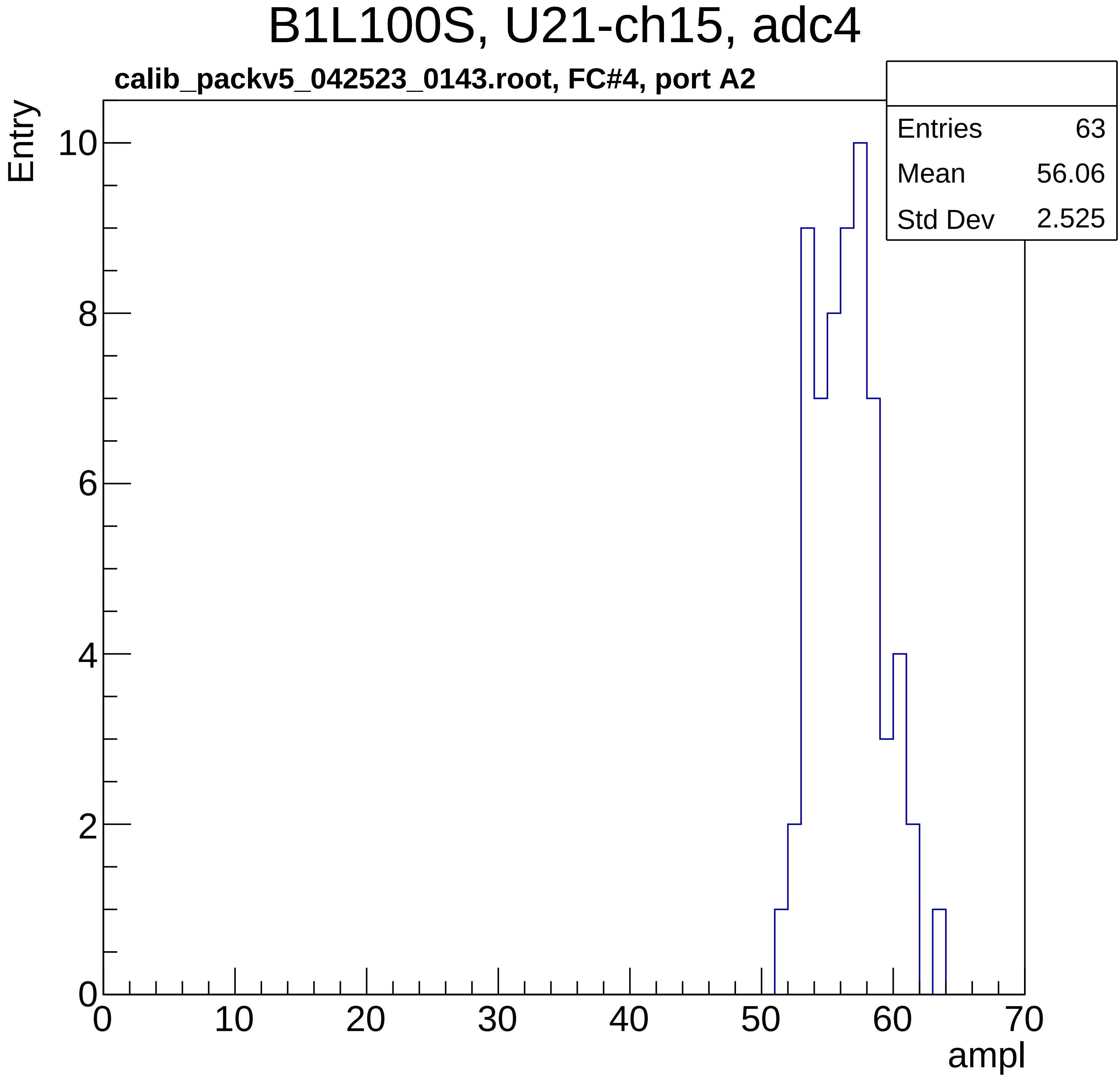

B1L100S, U21-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.13
Std Dev 4.629
Gaus mean : 29.0037
Gaus Width: 3.4871
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

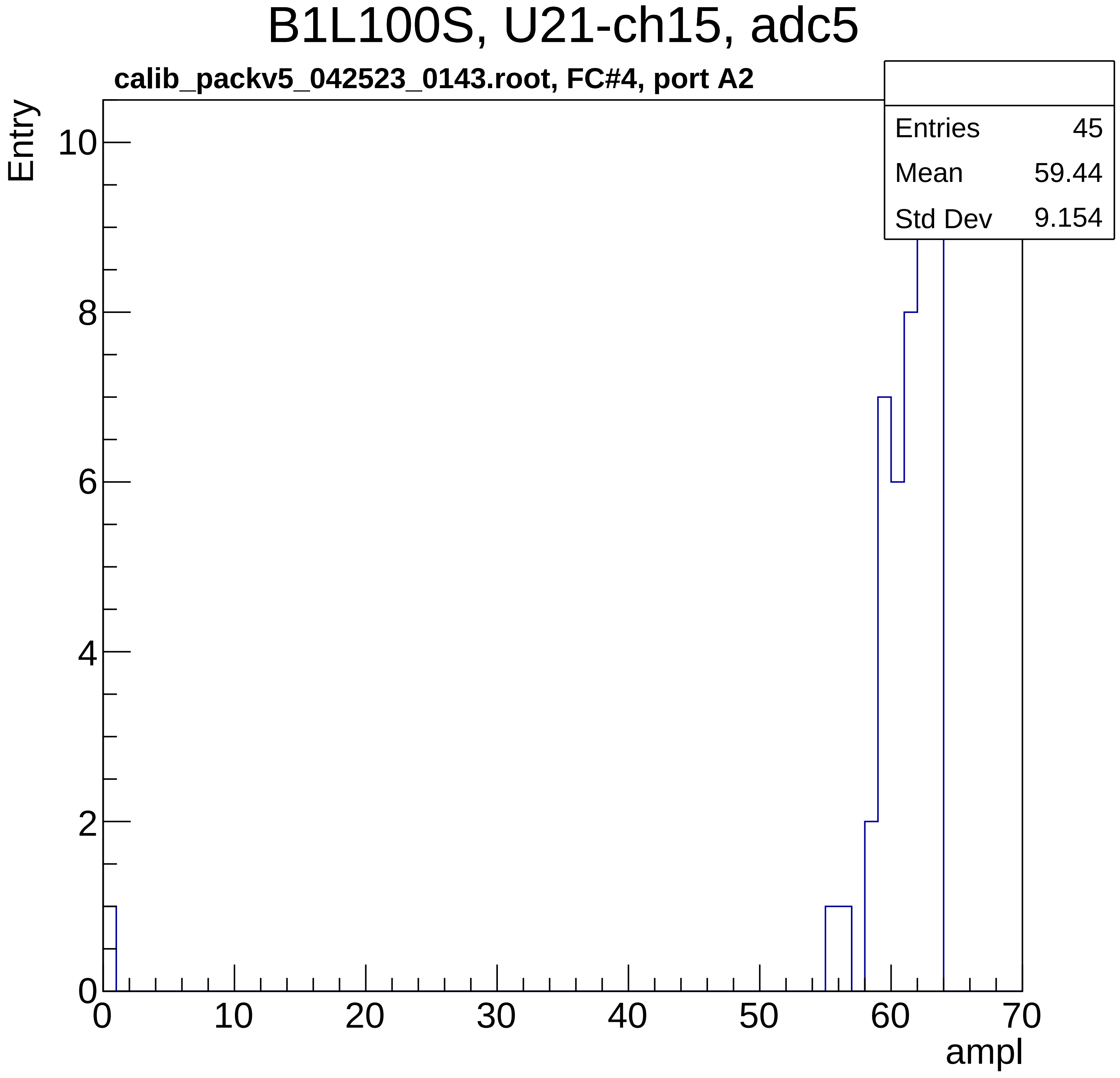

B1L100S, U21-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 36.07
Std Dev 3.373
Gaus mean : 36.4788
Gaus Width: 3.3502
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

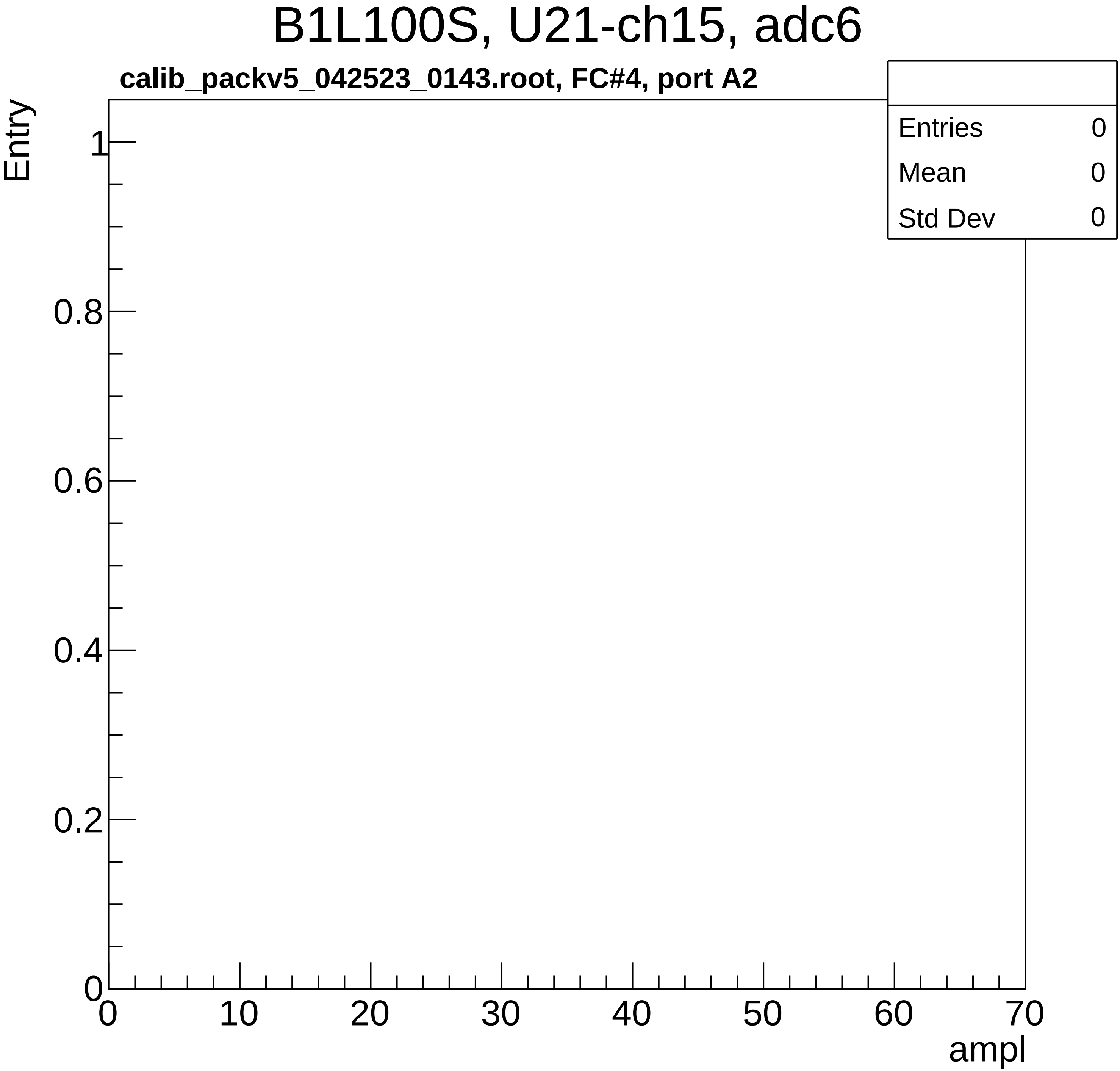
B1L100S, U21-ch15, adc2



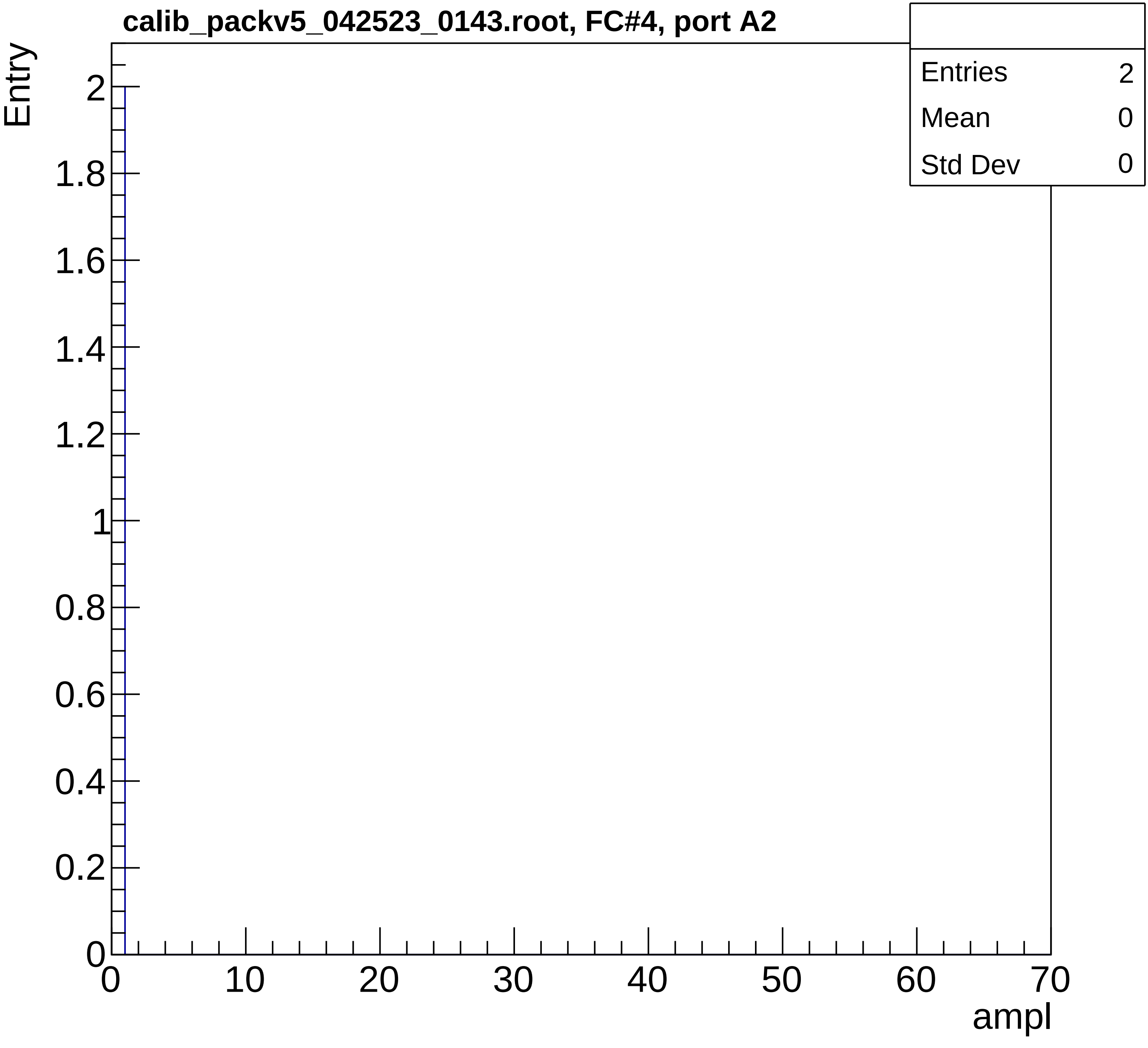

B1L100S, U21-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.43
Std Dev 2.952
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.06
Std Dev 2.525
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.44
Std Dev 9.154
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

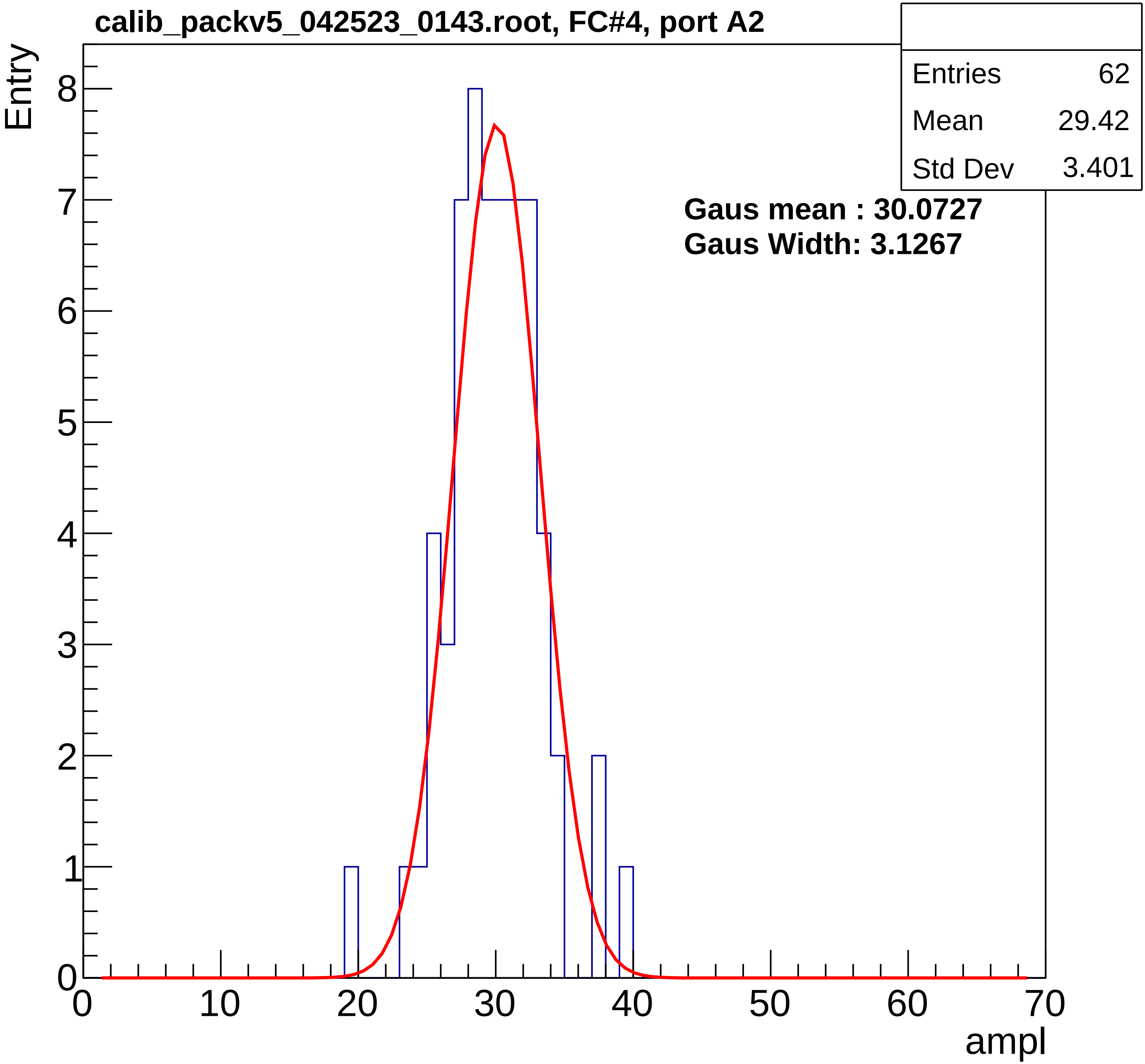

B1L100S, U21-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch15, adc7



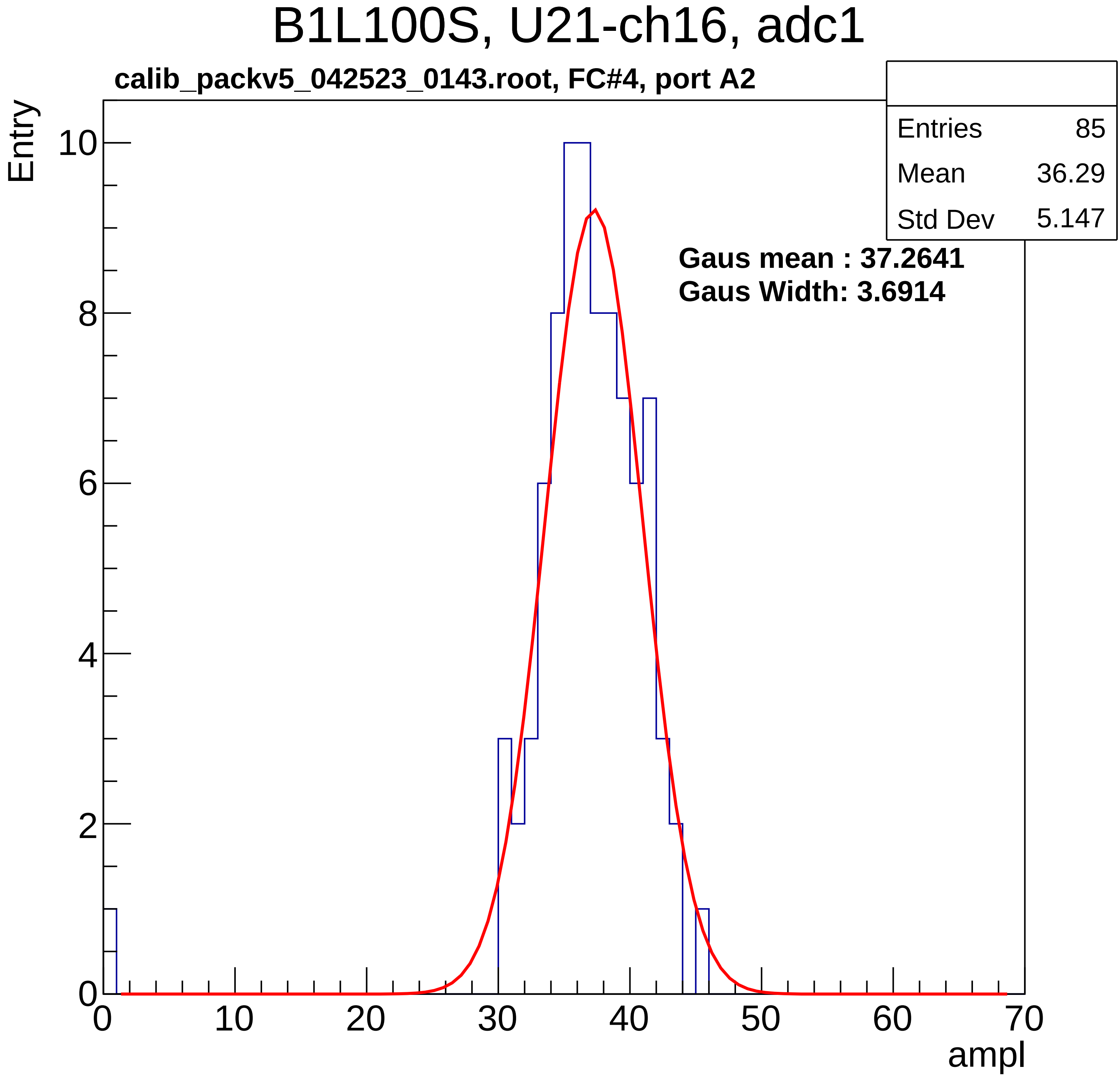
B1L100S, U21-ch16, adc0

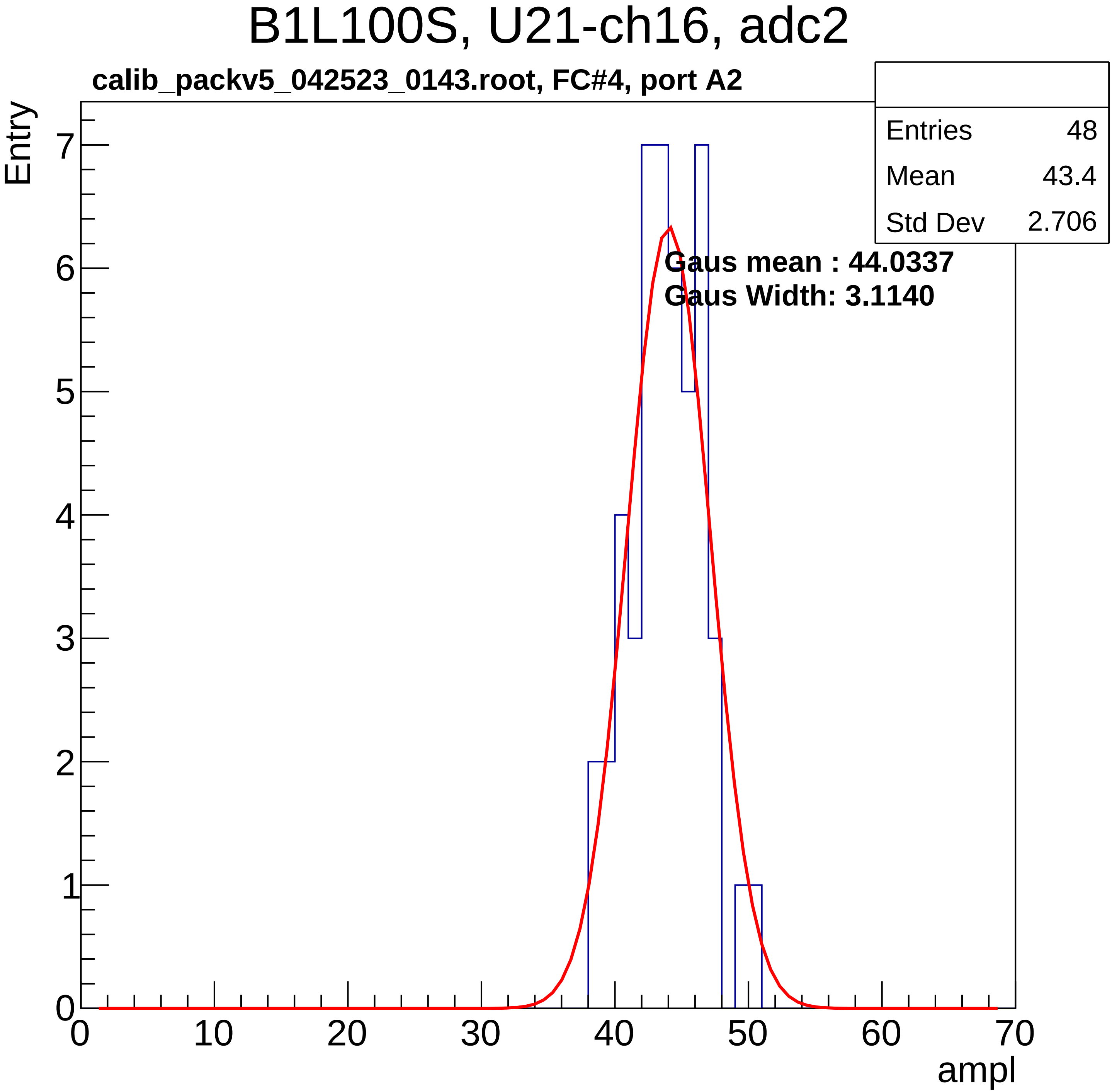
calib_packv5_042523_0143.root, FC#4, port A2



Entries	62
Mean	29.42
Std Dev	3.401

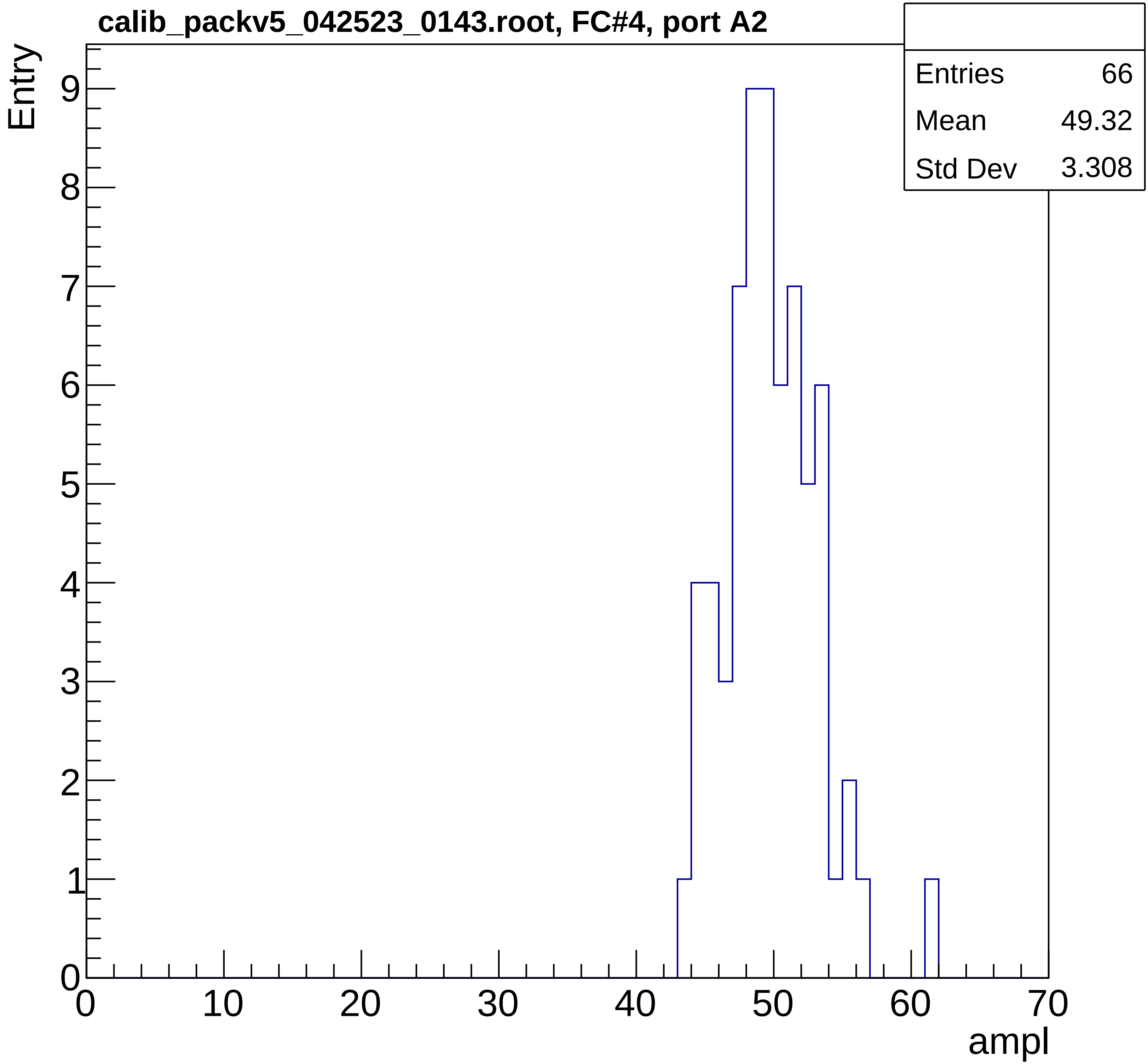
Gaus mean : 30.0727
Gaus Width: 3.1267


B1L100S, U21-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 36.29
Std Dev 5.147
Gaus mean : 37.2641
Gaus Width: 3.6914
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

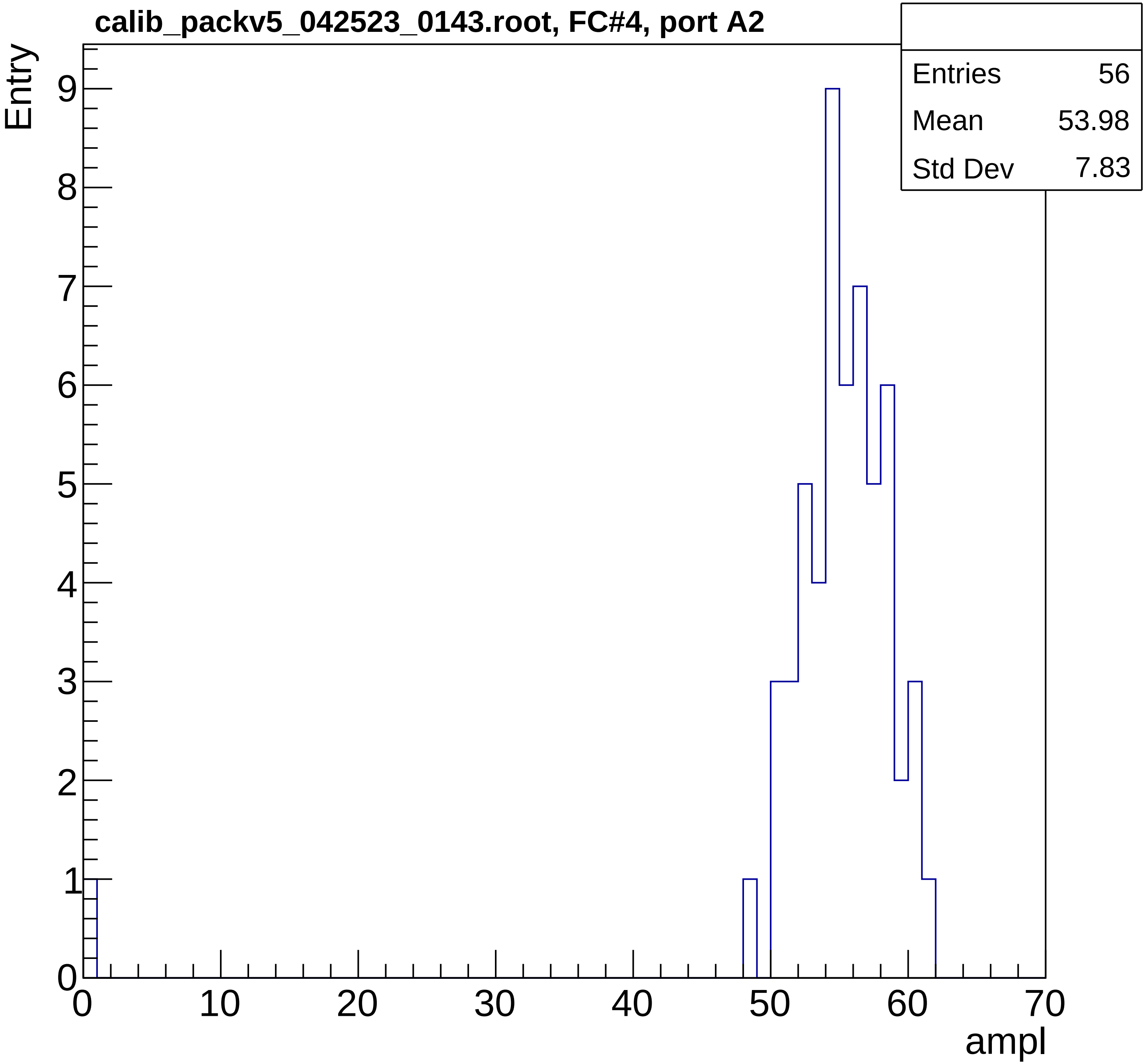

B1L100S, U21-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 43.4
Std Dev 2.706
Gaus mean : 44.0337
Gaus Width: 3.1140
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

B1L100S, U21-ch16, adc3

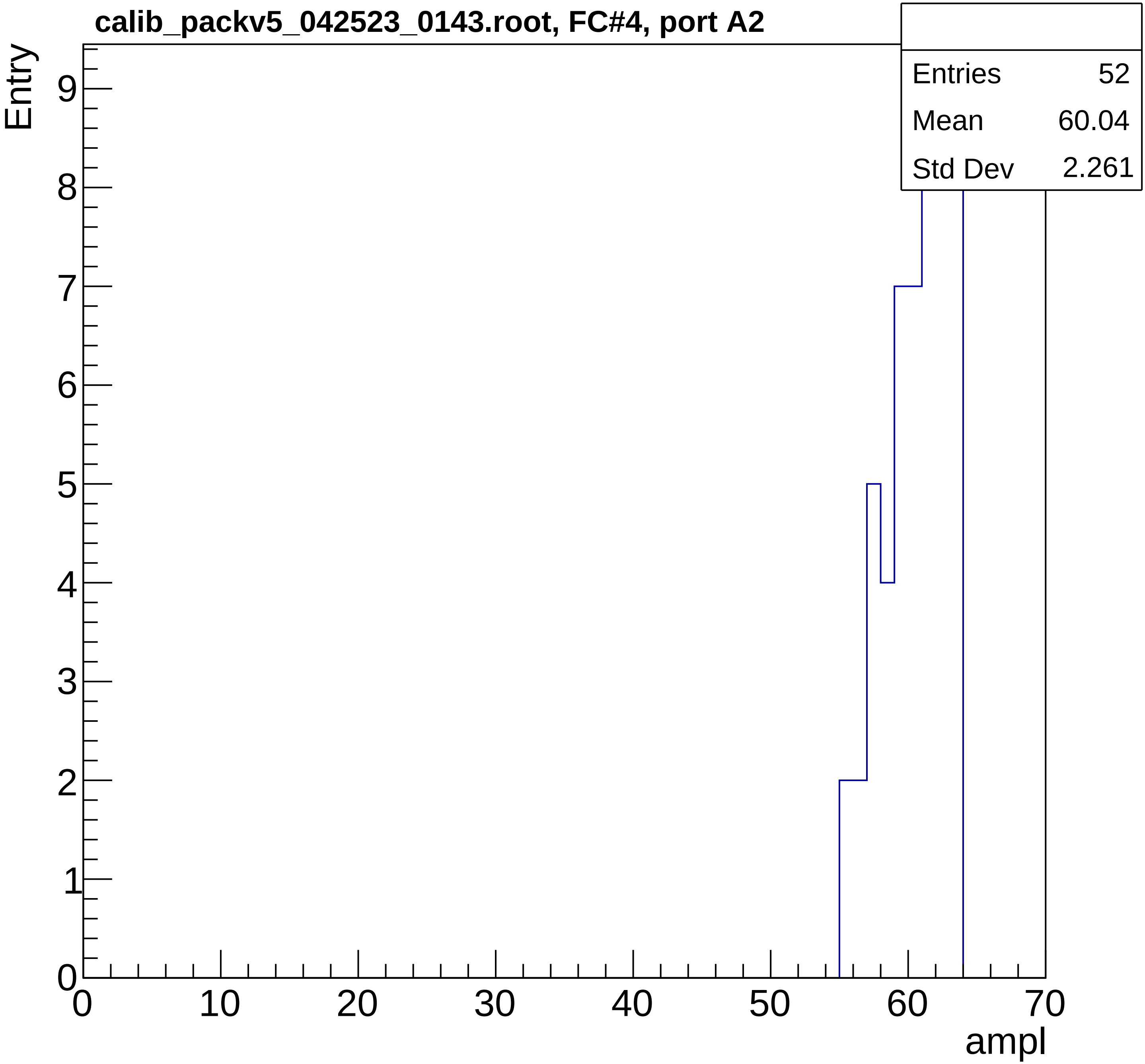
calib_packv5_042523_0143.root, FC#4, port A2



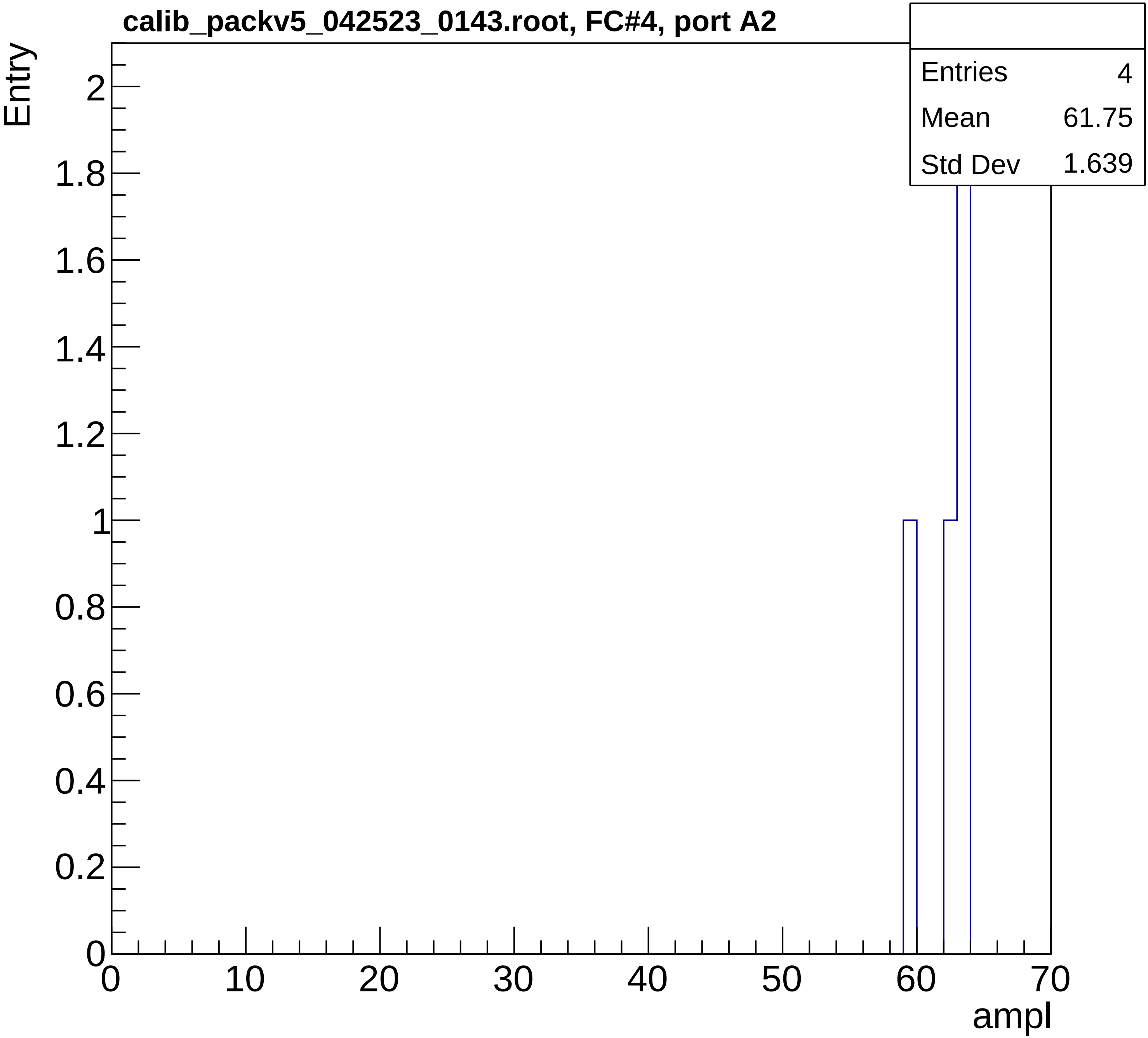
B1L100S, U21-ch16, adc4

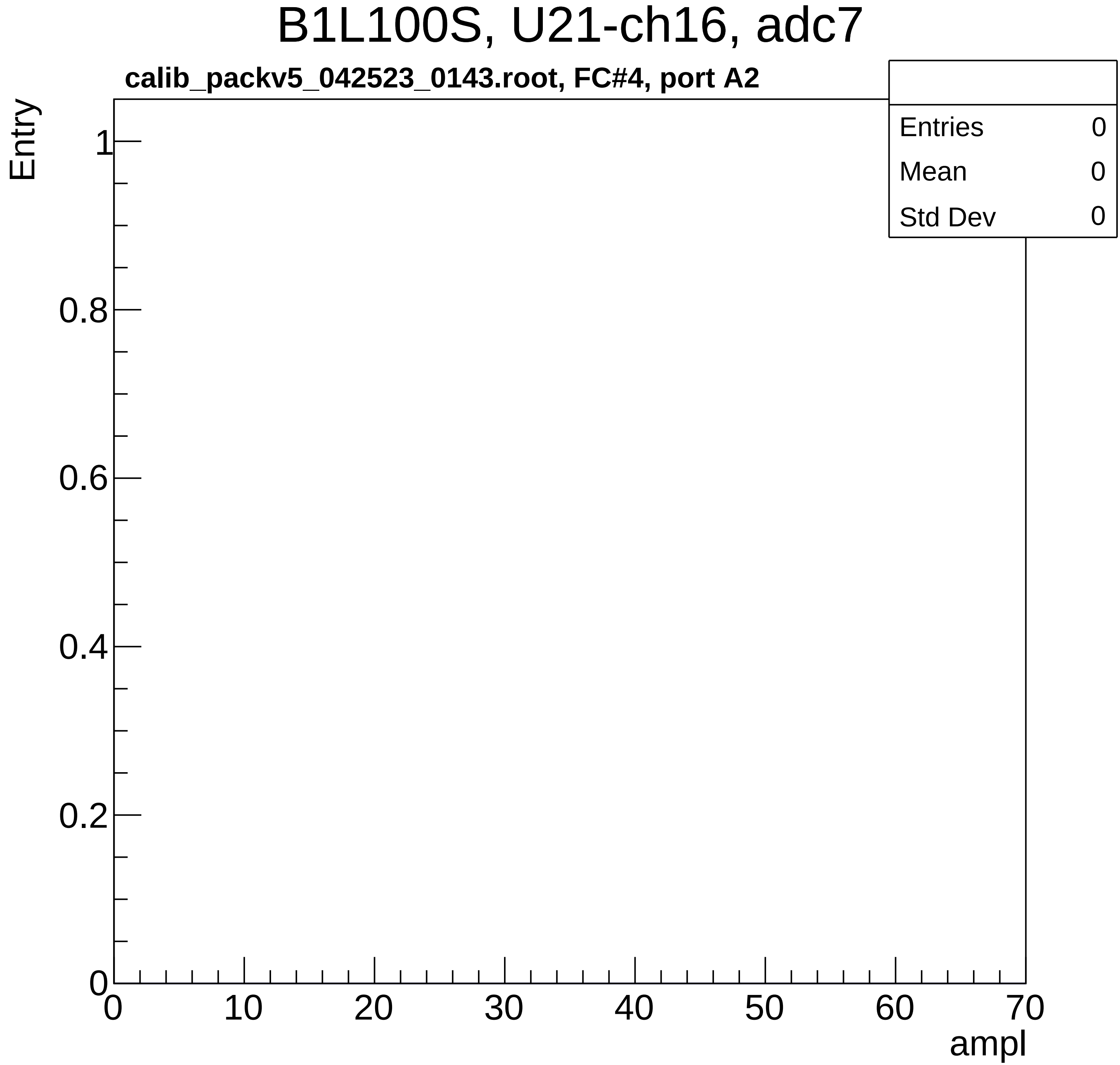


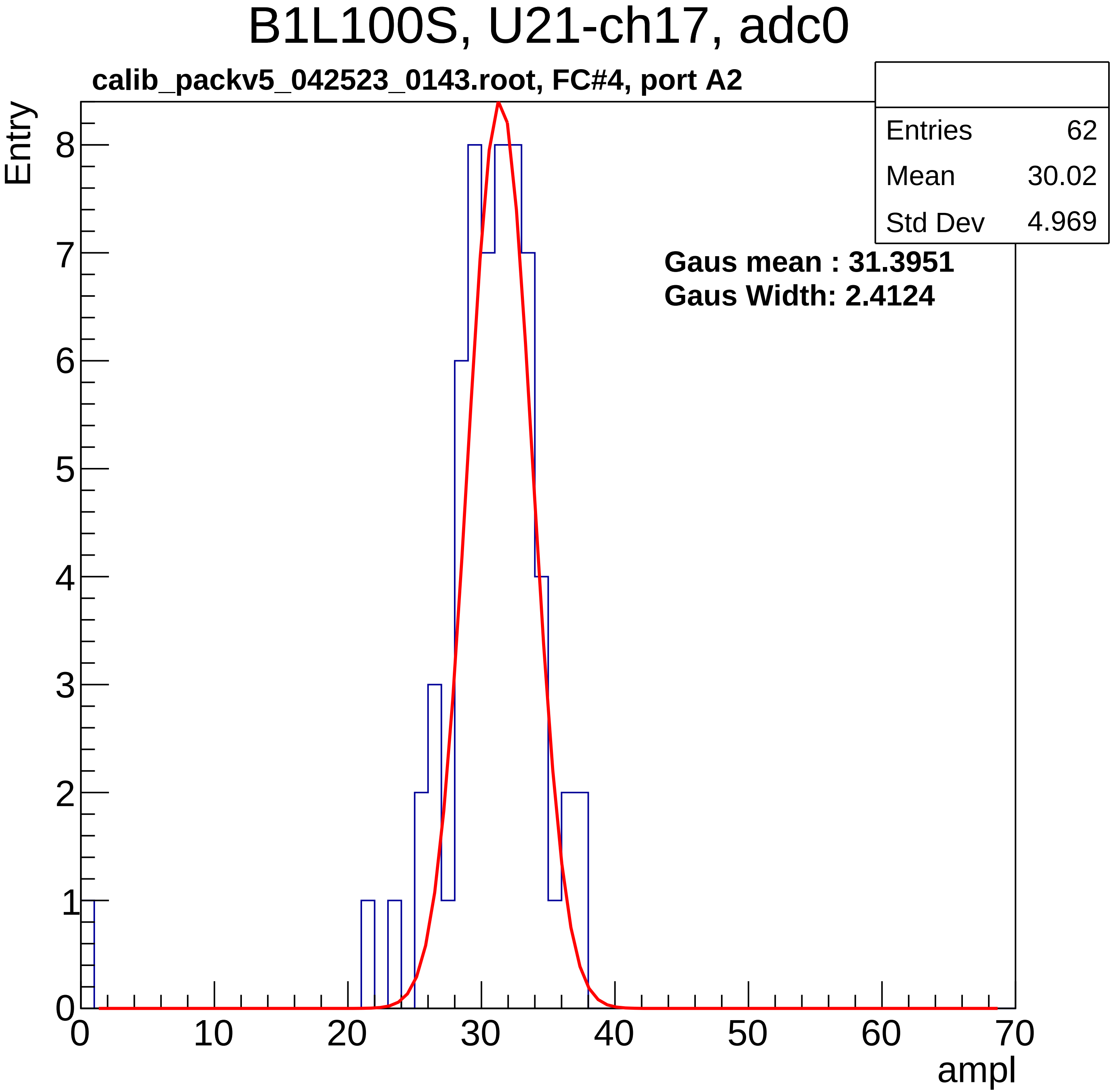
B1L100S, U21-ch16, adc5

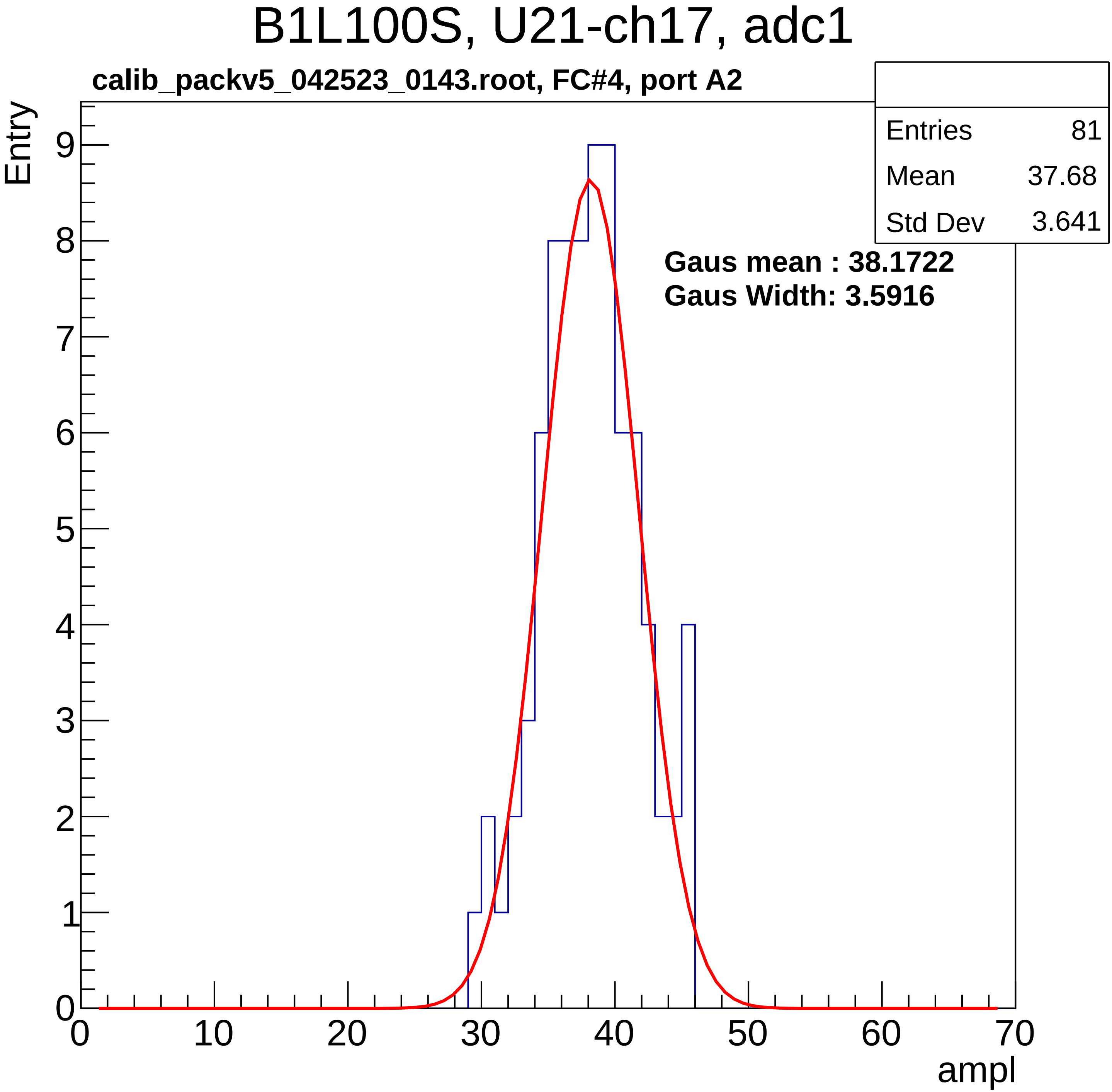


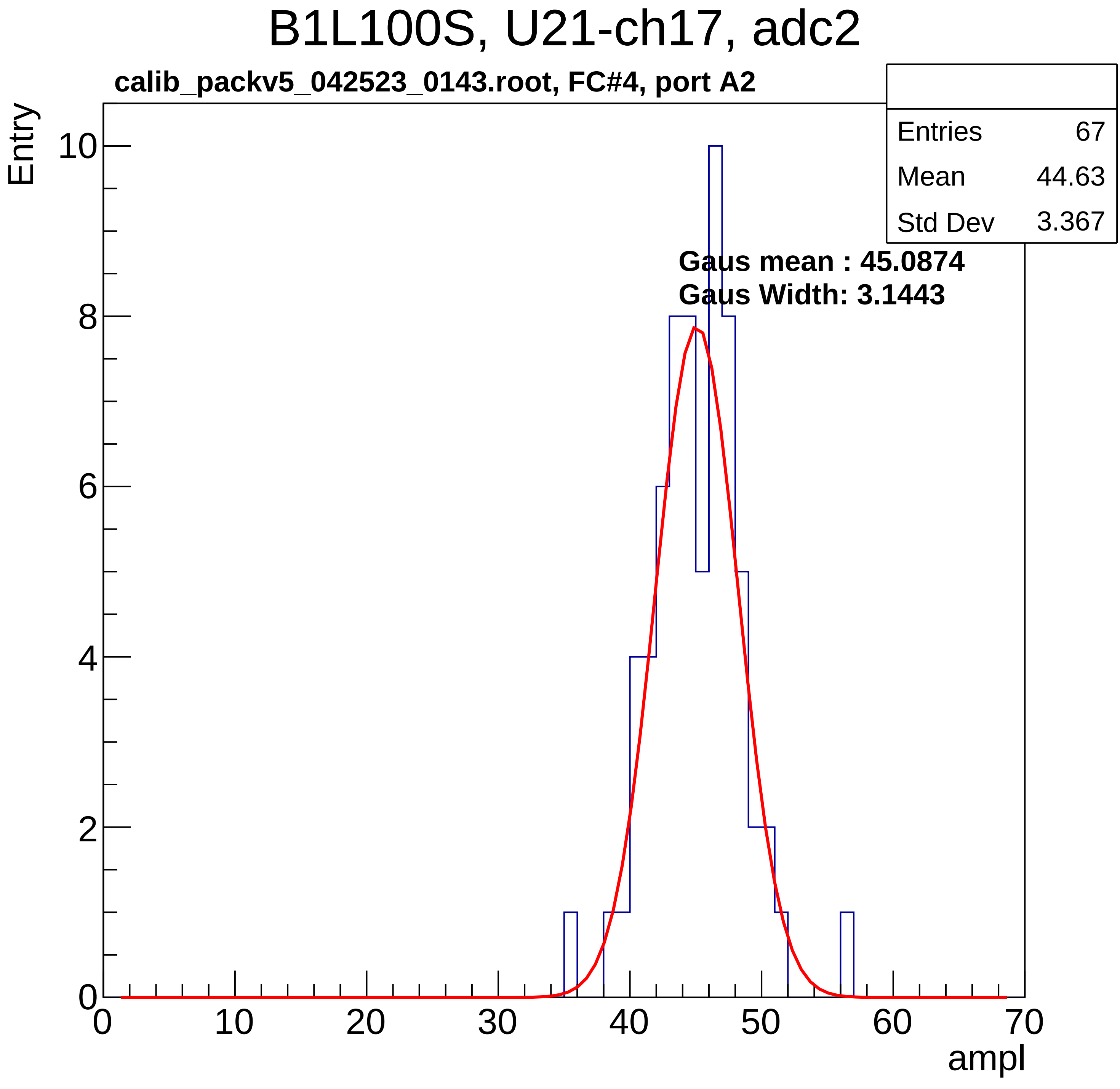
B1L100S, U21-ch16, adc6



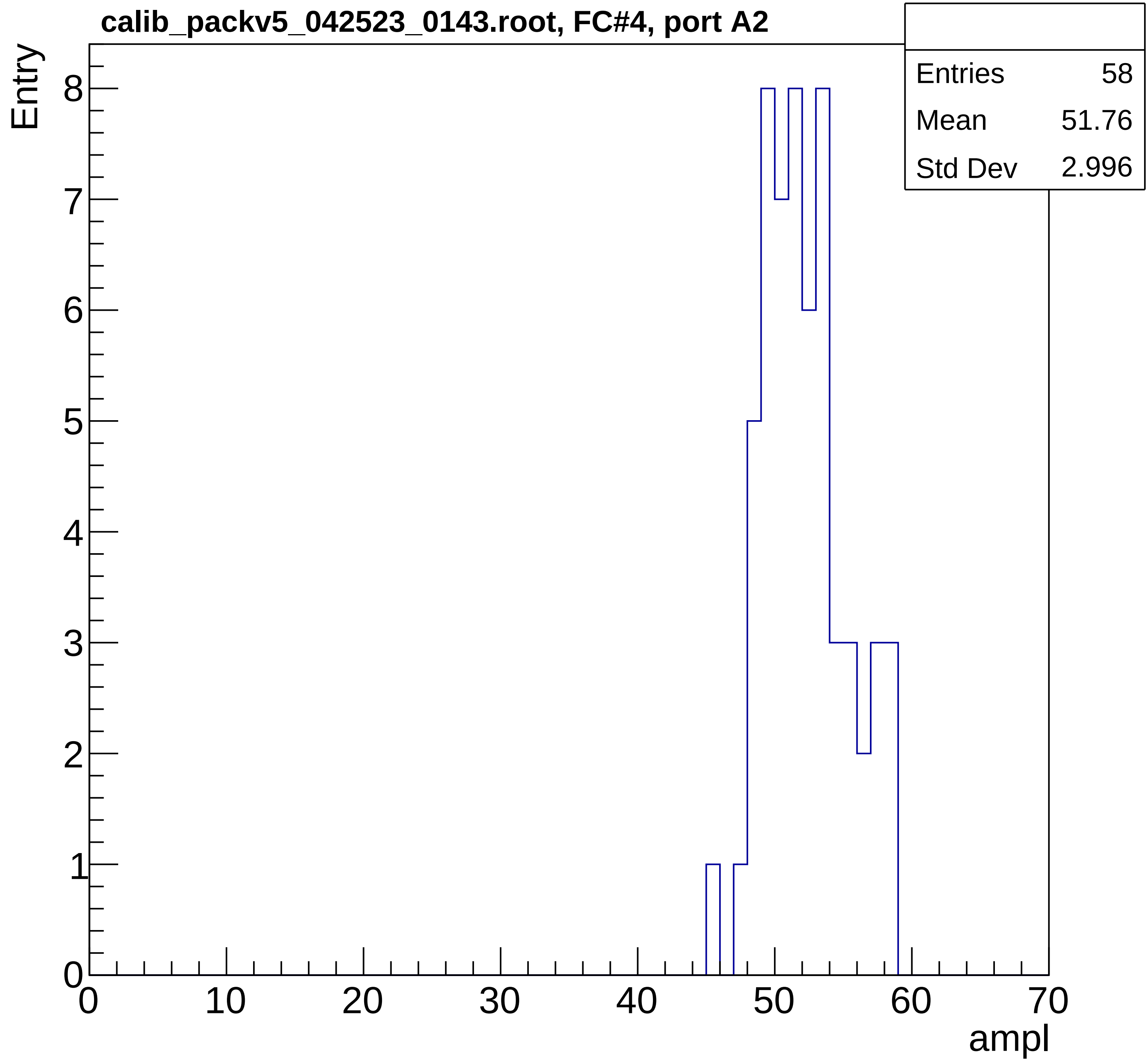

B1L100S, U21-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

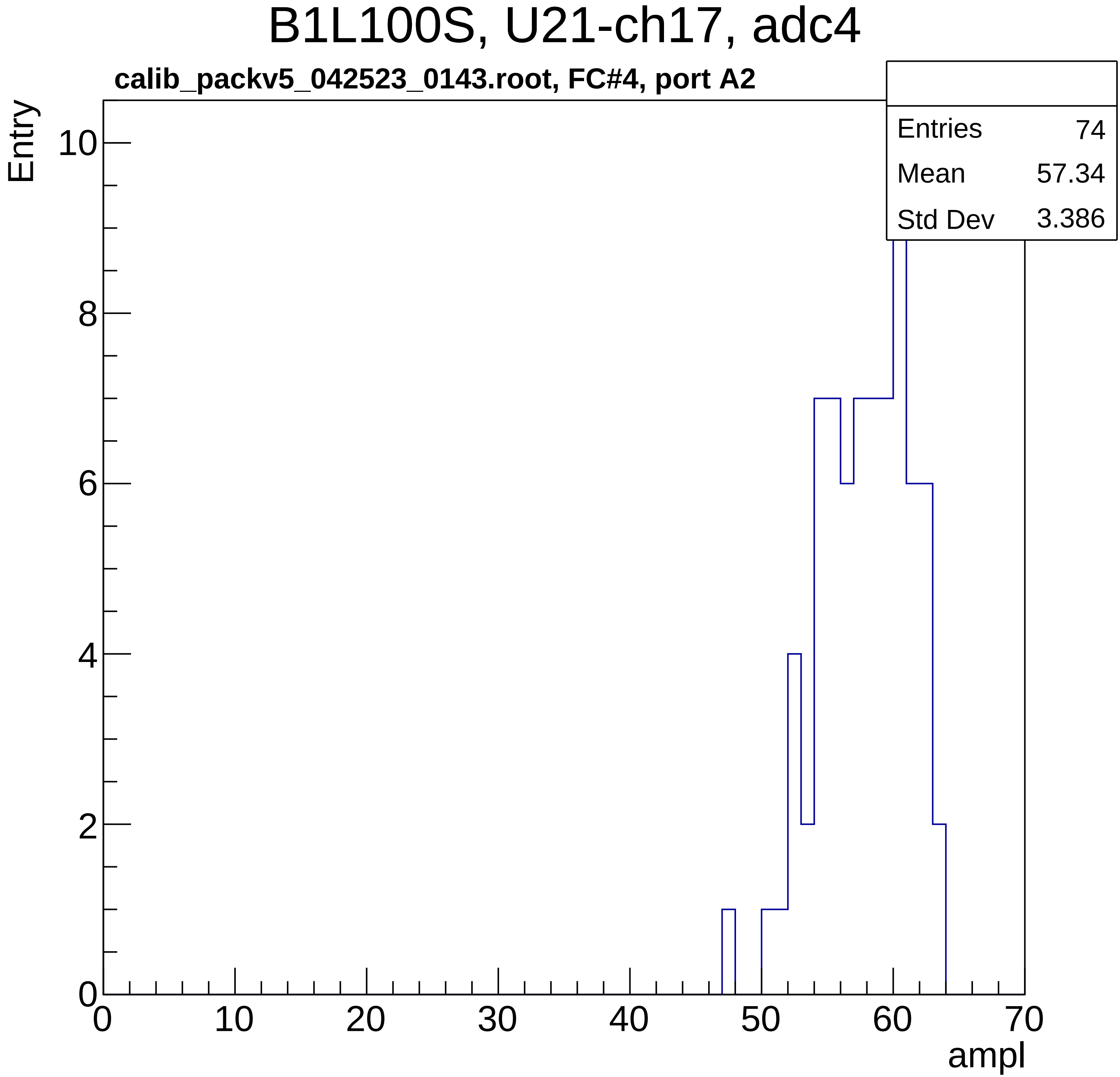

B1L100S, U21-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30.02
Std Dev 4.969
Gaus mean : 31.3951
Gaus Width: 2.4124
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 37.68
Std Dev 3.641
Gaus mean : 38.1722
Gaus Width: 3.5916
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

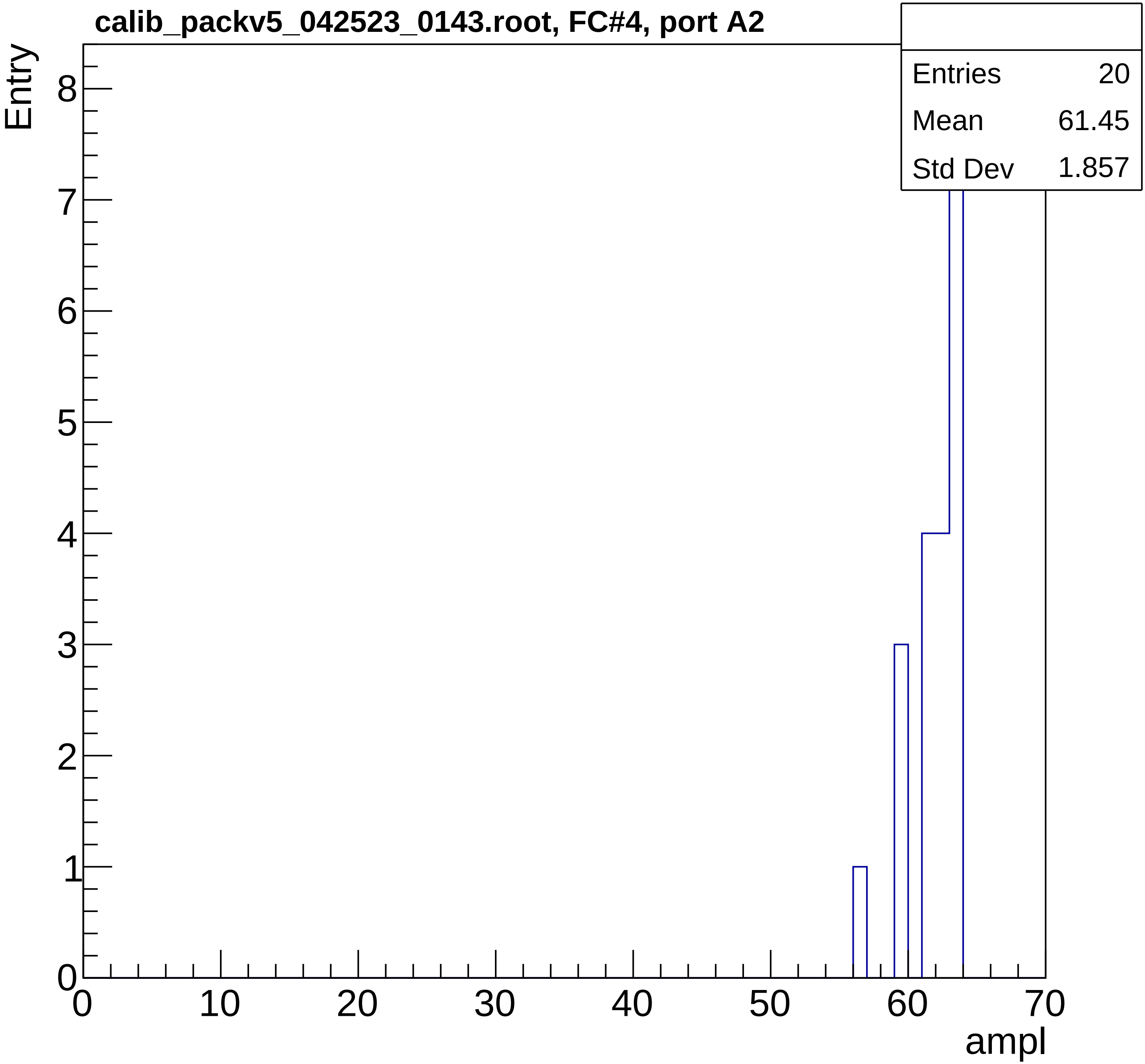

B1L100S, U21-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 67
Mean 44.63
Std Dev 3.367
Gaus mean : 45.0874
Gaus Width: 3.1443
0
2
4
6
8
10
0
10
20
30
40
50
60
70

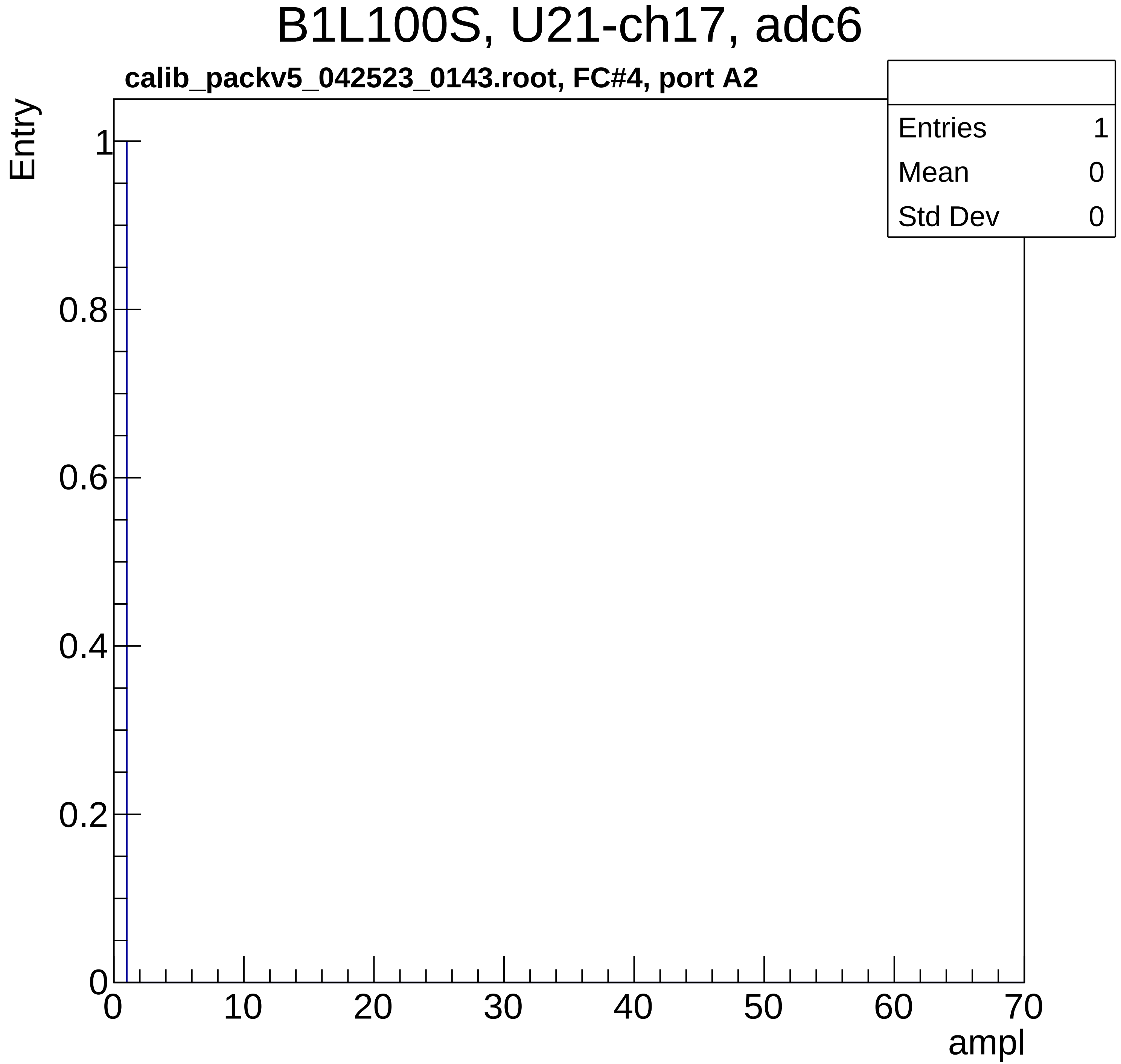
B1L100S, U21-ch17, adc3

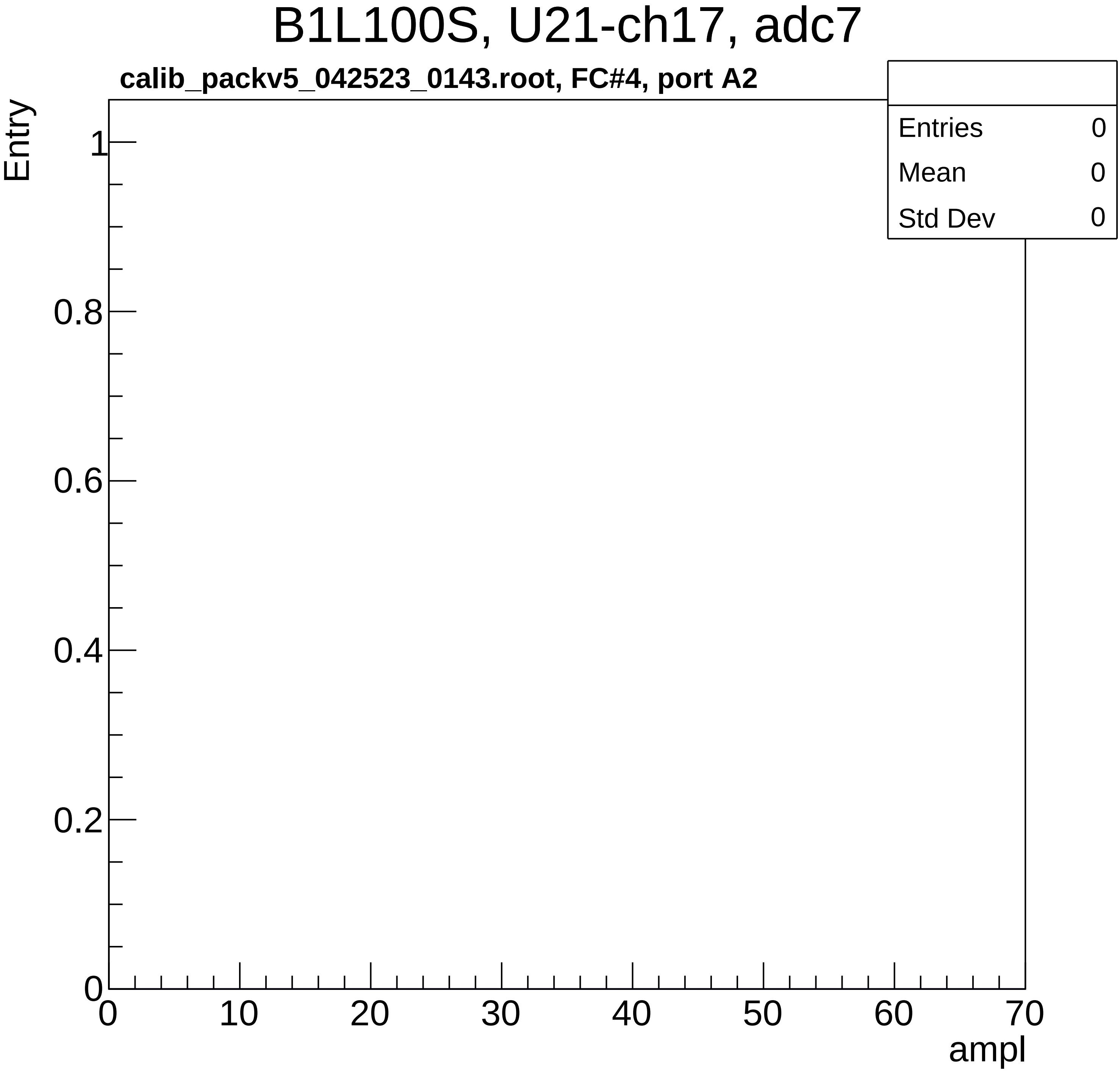


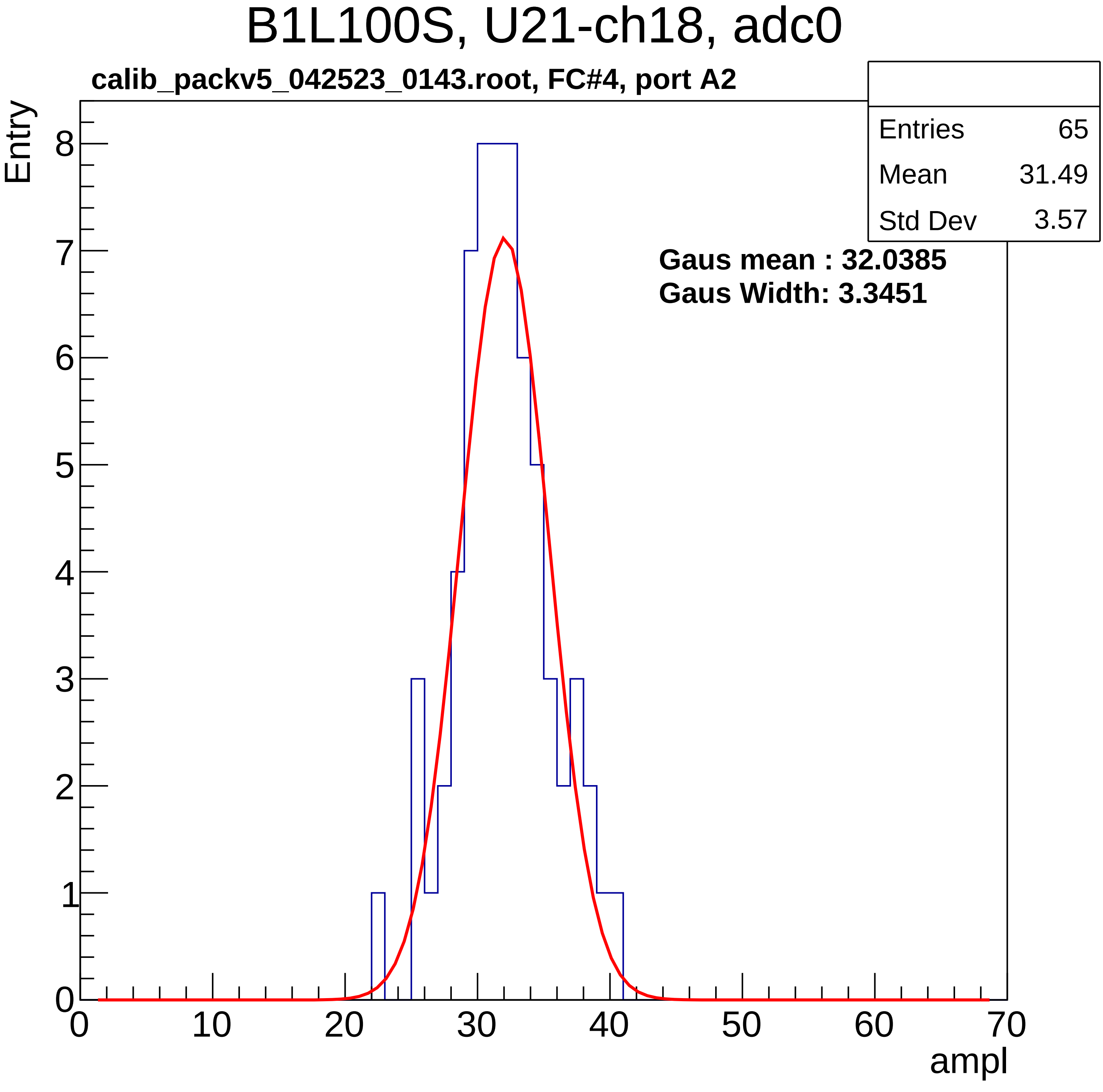

B1L100S, U21-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 57.34
Std Dev 3.386
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

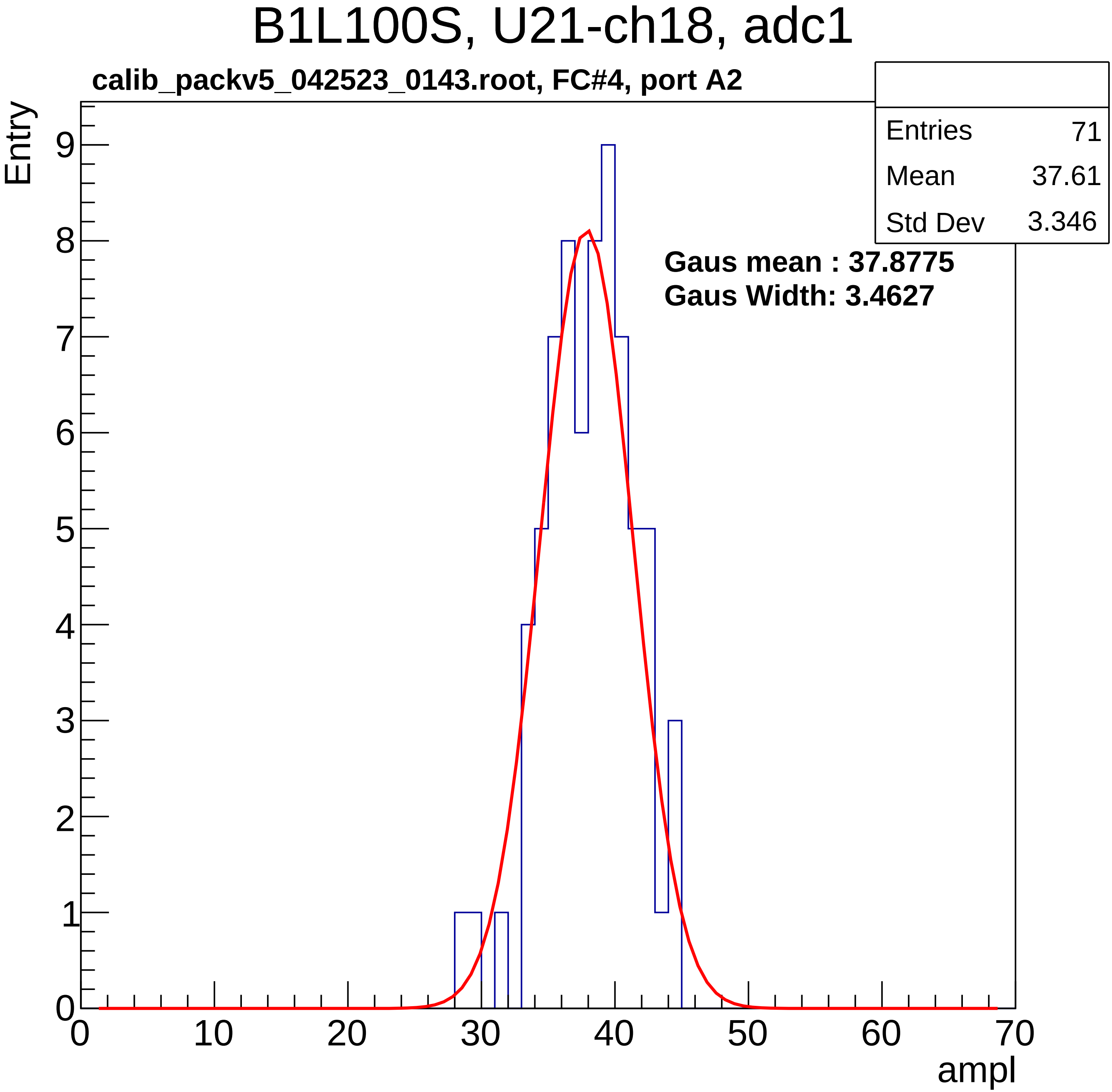
B1L100S, U21-ch17, adc5

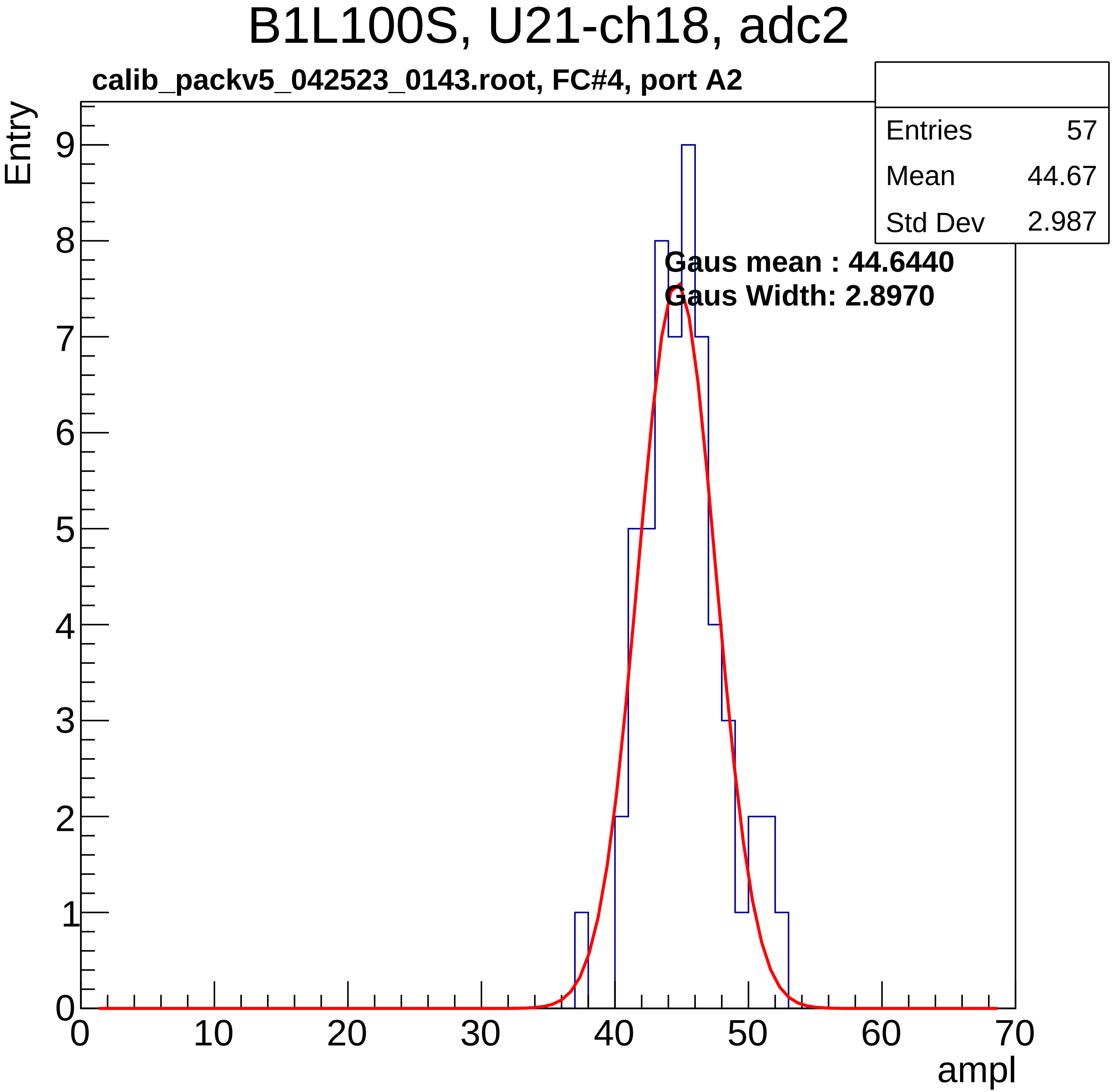


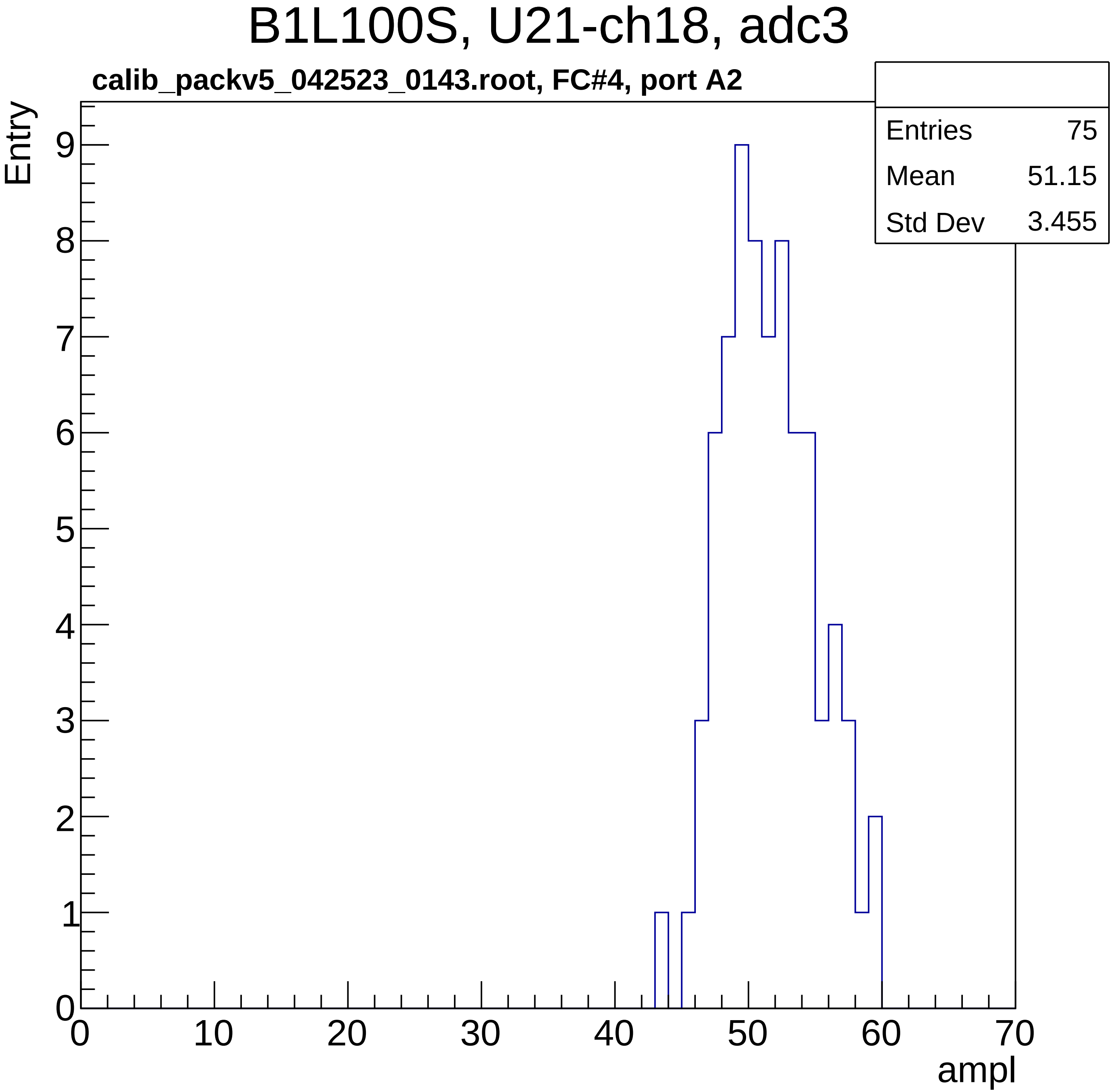

B1L100S, U21-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 1
Mean 0
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

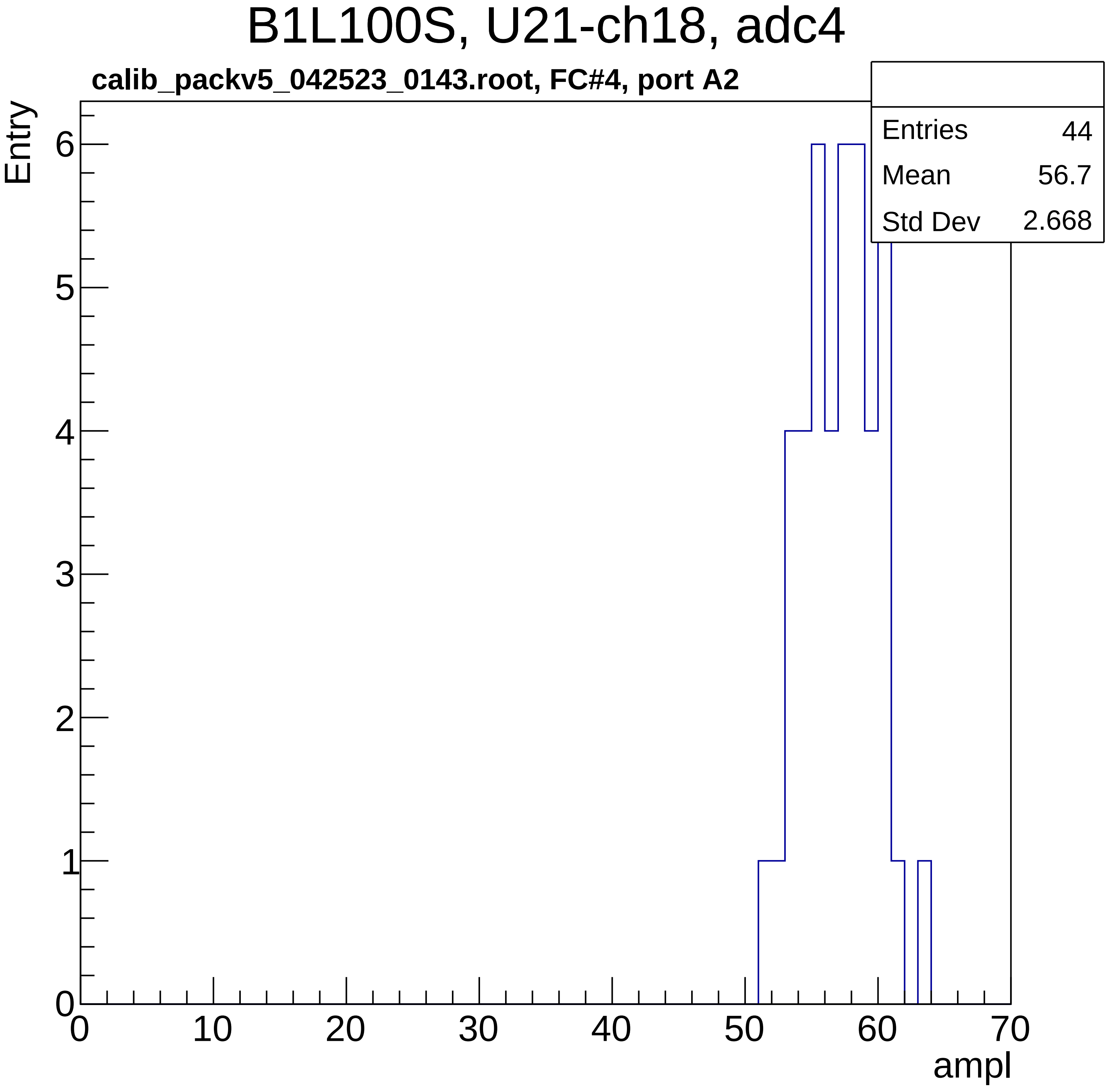

B1L100S, U21-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

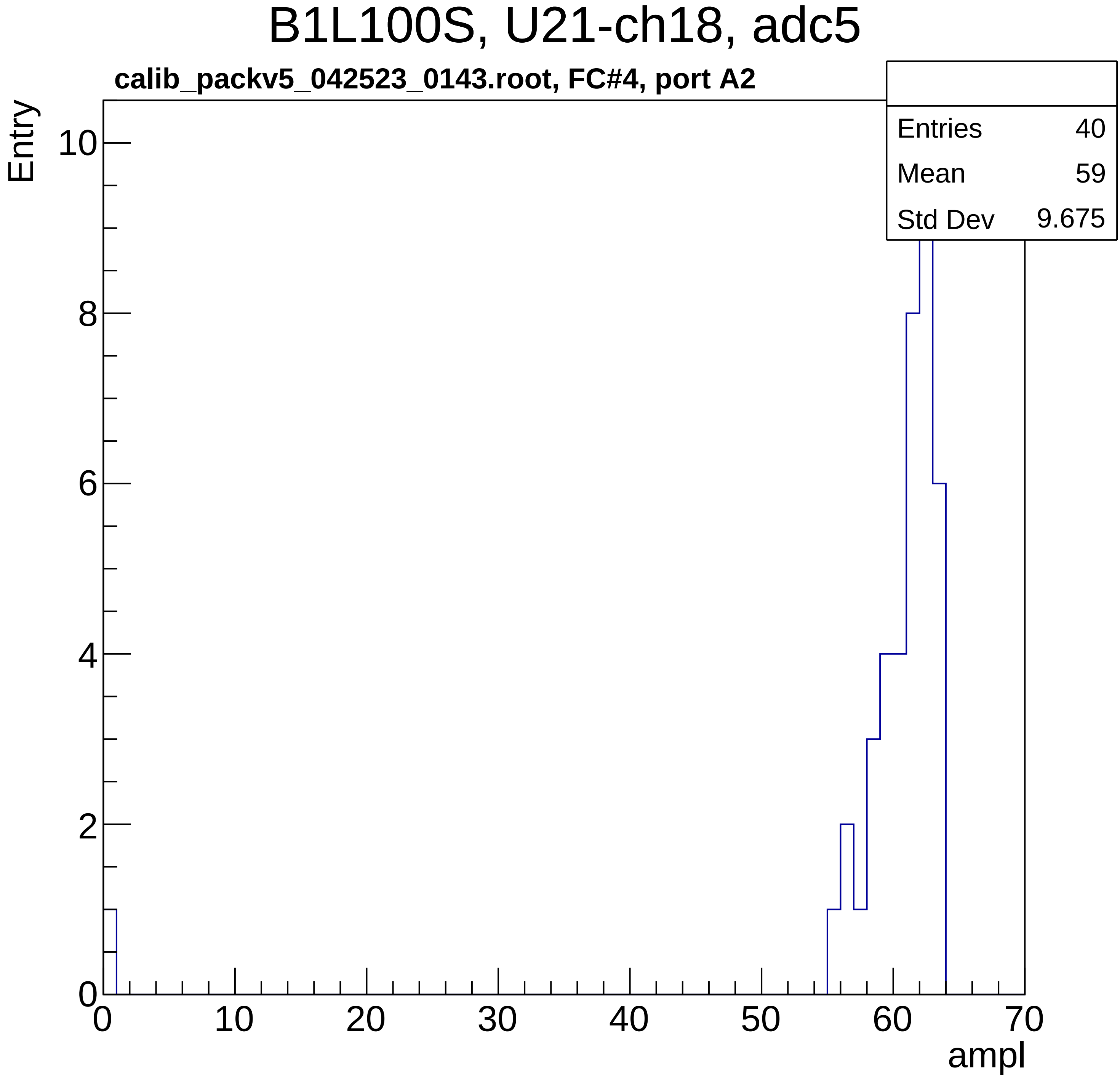

B1L100S, U21-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 31.49
Std Dev 3.57
Gaus mean : 32.0385
Gaus Width: 3.3451
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

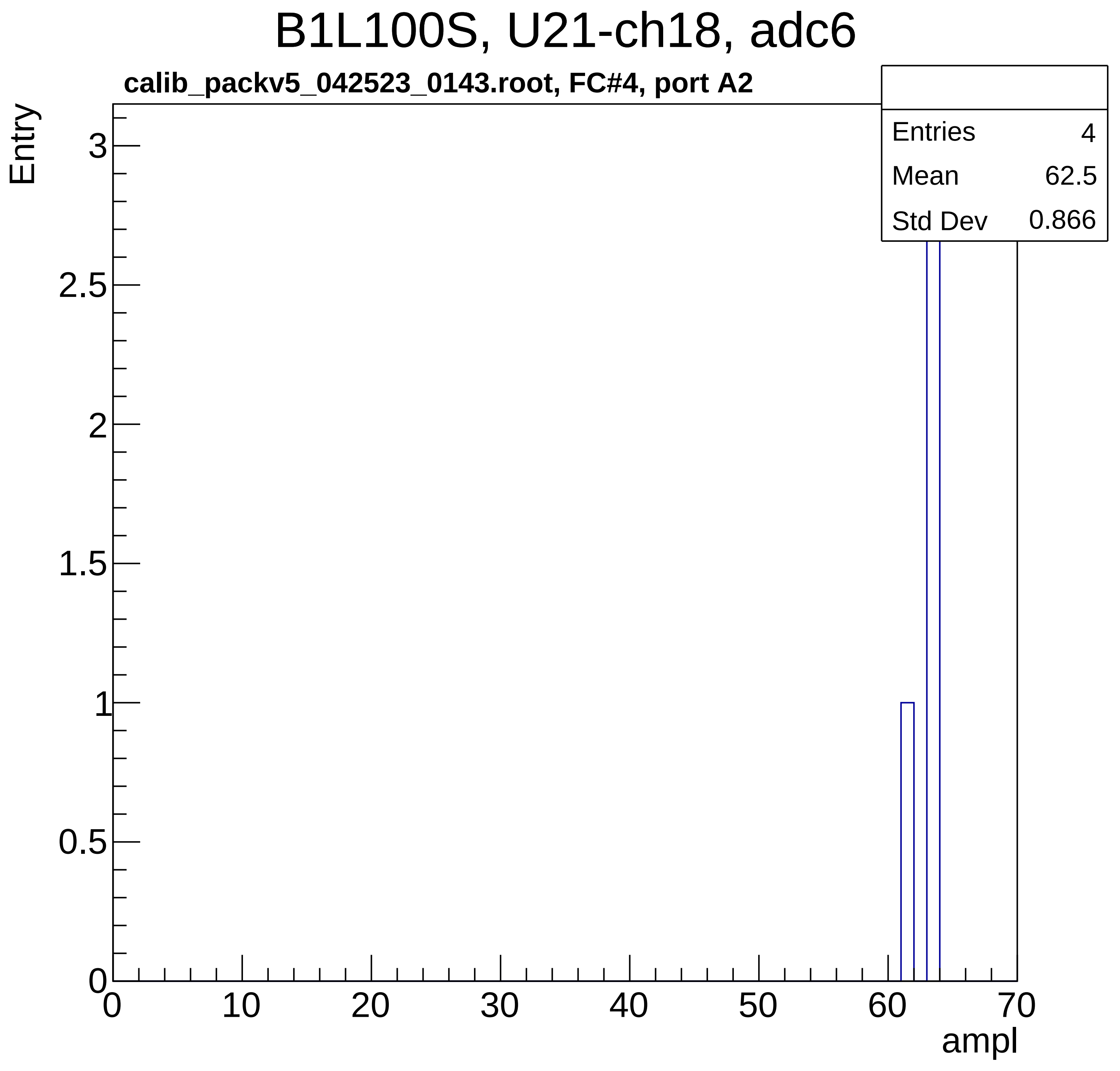

B1L100S, U21-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.61
Std Dev 3.346
Gaus mean : 37.8775
Gaus Width: 3.4627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

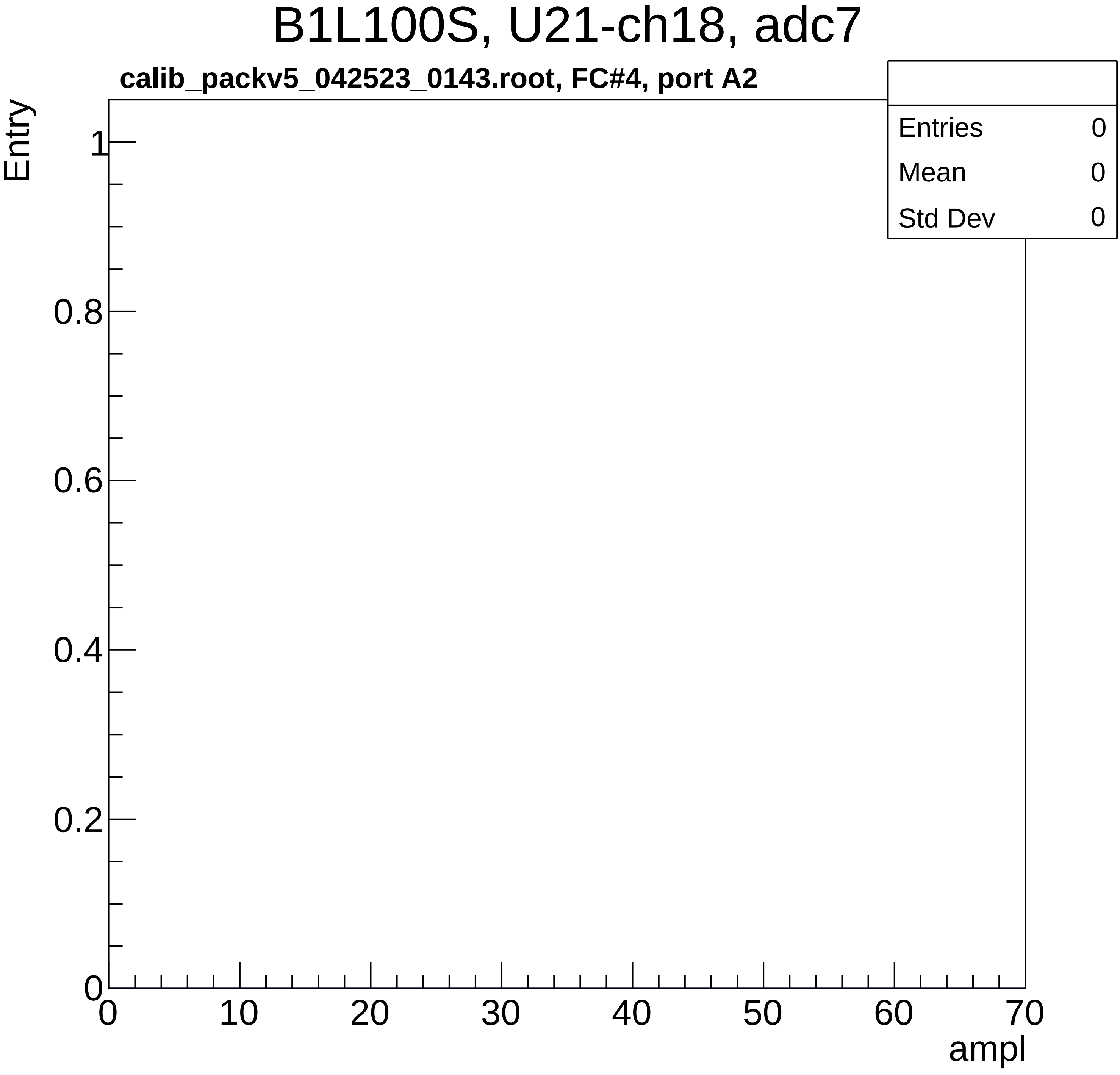

B1L100S, U21-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 57
Mean 44.67
Std Dev 2.987
Gaus mean : 44.6440
Gaus Width: 2.8970
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

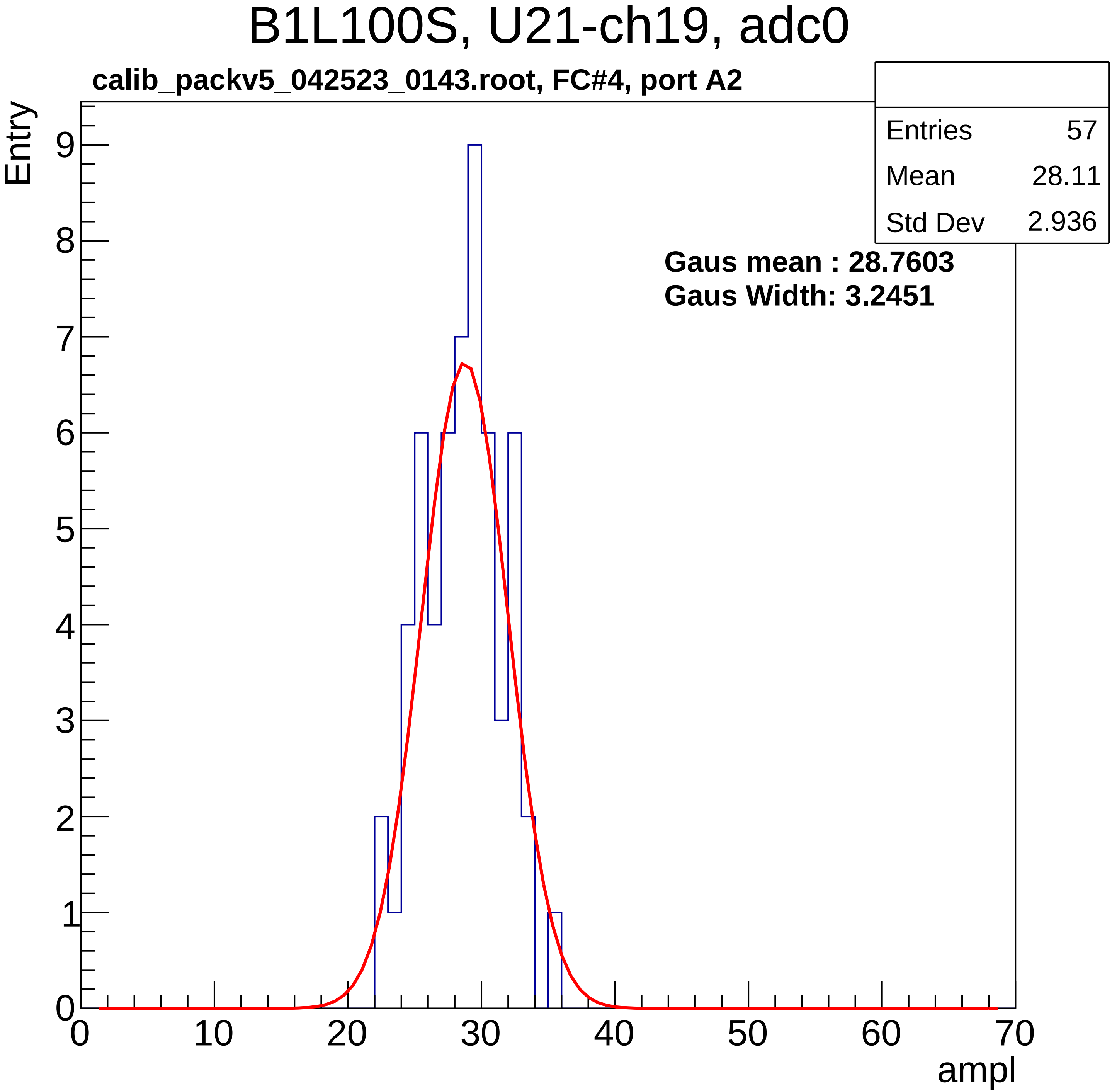

B1L100S, U21-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 51.15
Std Dev 3.455
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 56.7
Std Dev 2.668
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

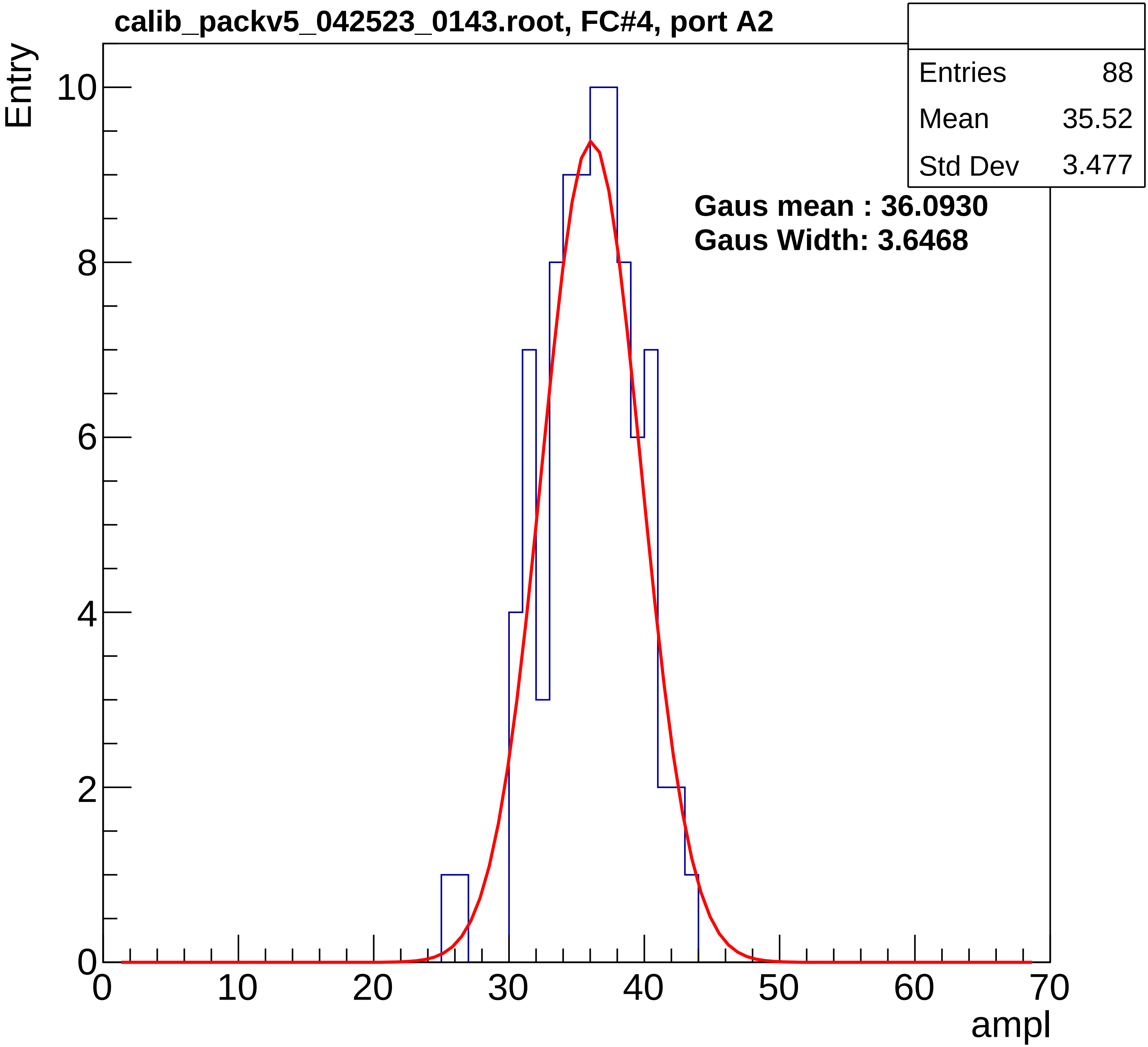

B1L100S, U21-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 40
Mean 59
Std Dev 9.675
0
2
4
6
8
10
0
10
20
30
40
50
60
70

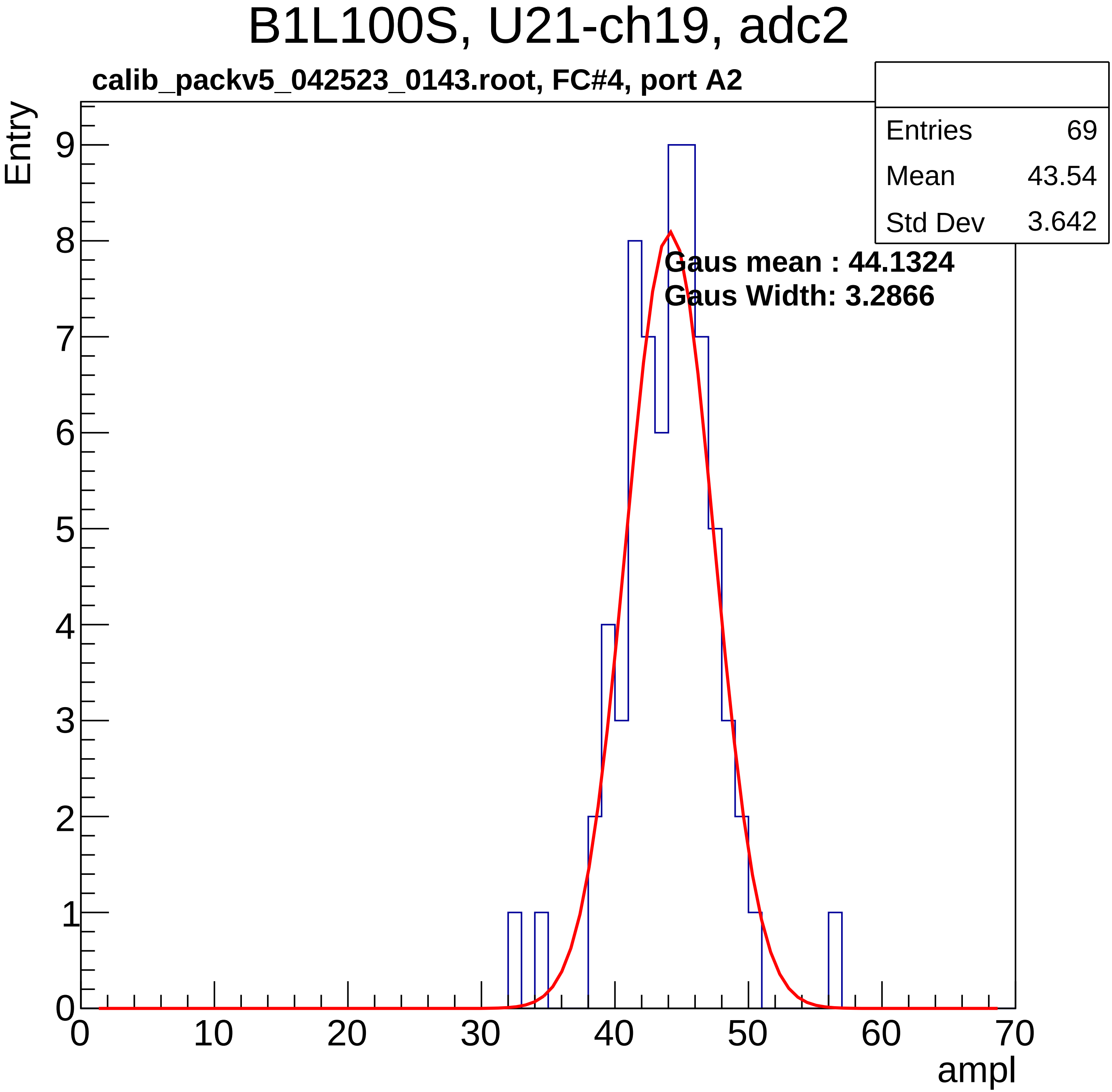

B1L100S, U21-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

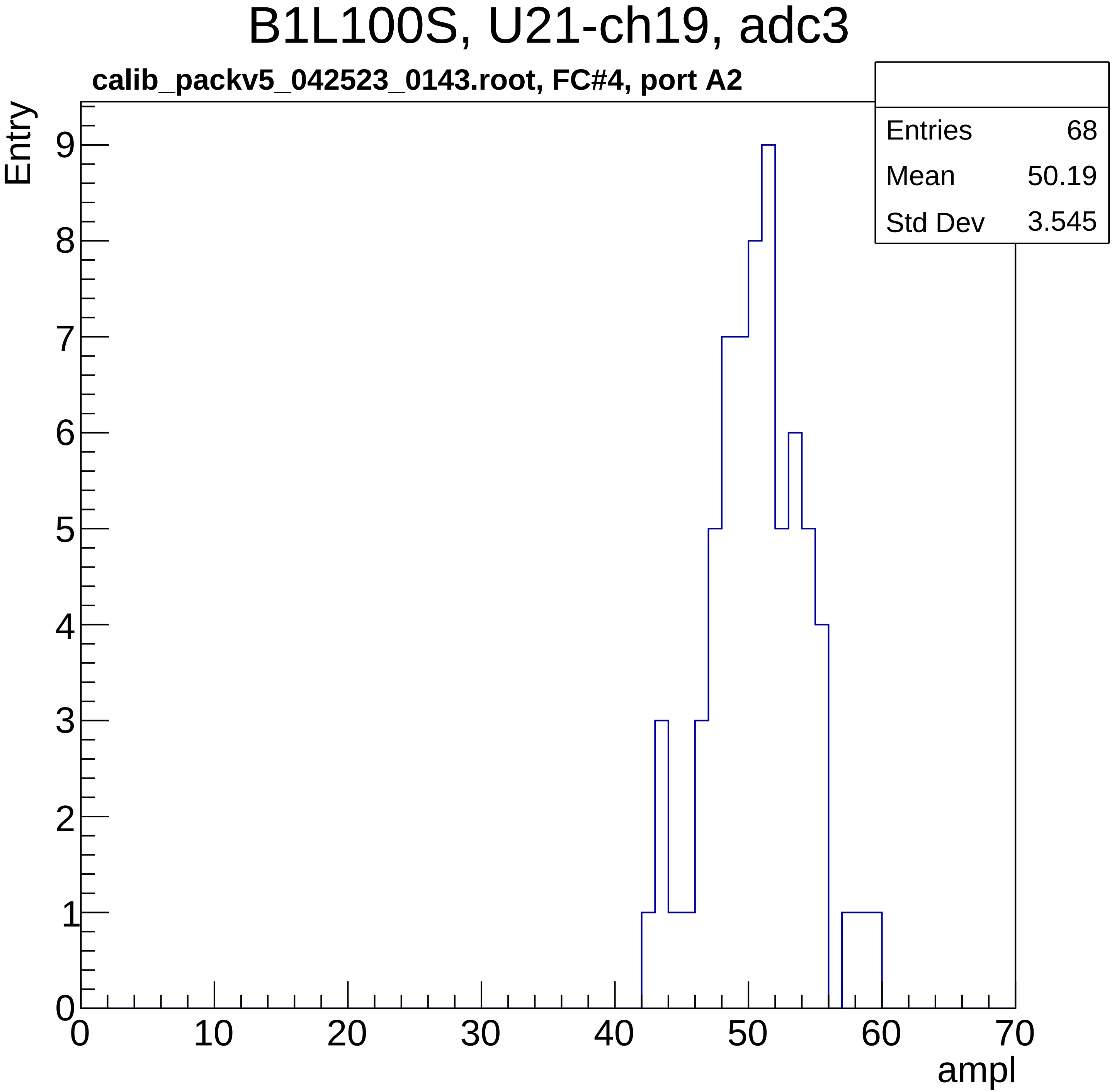

B1L100S, U21-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

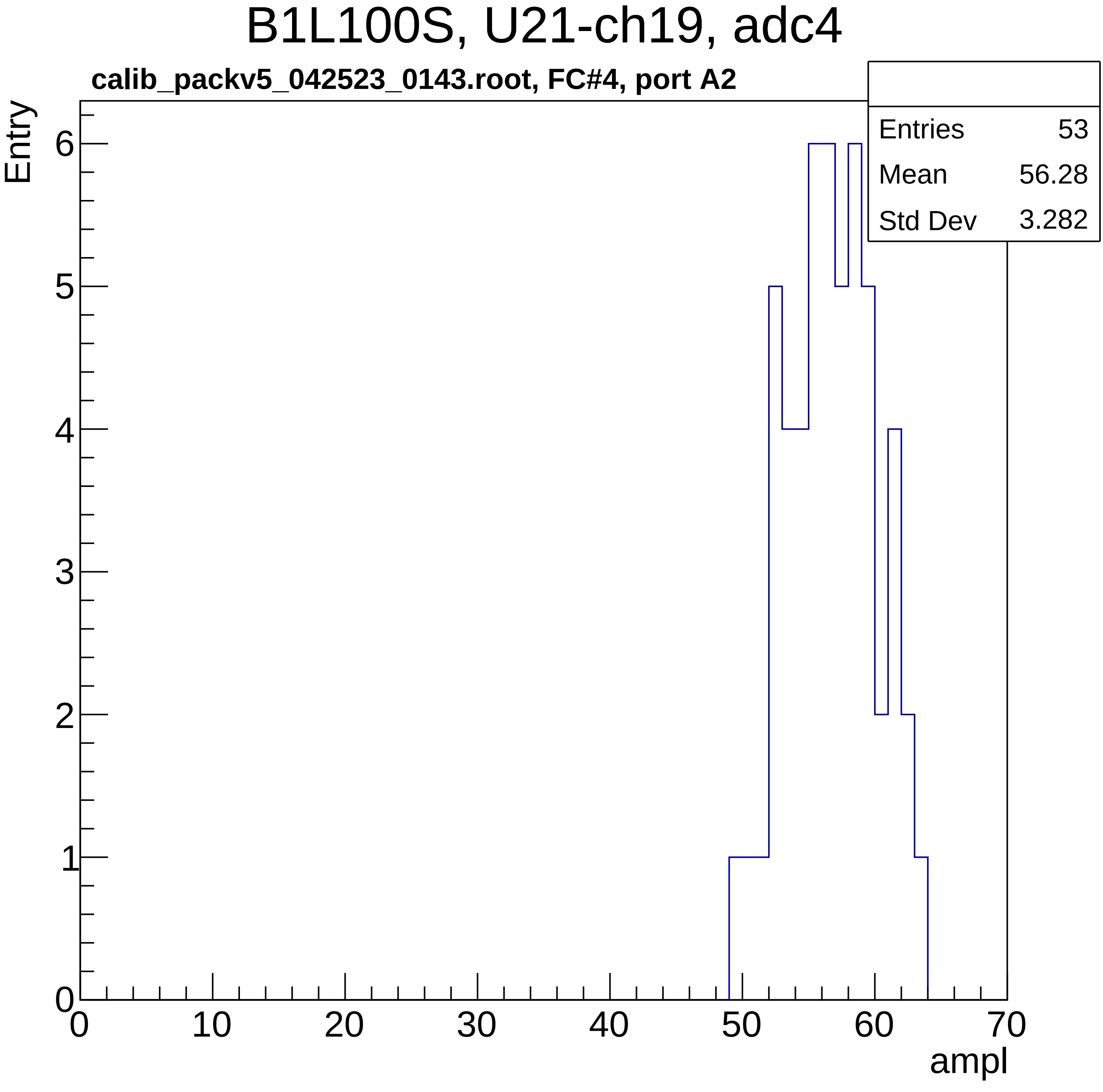

B1L100S, U21-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 28.11
Std Dev 2.936
Gaus mean : 28.7603
Gaus Width: 3.2451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch19, adc1

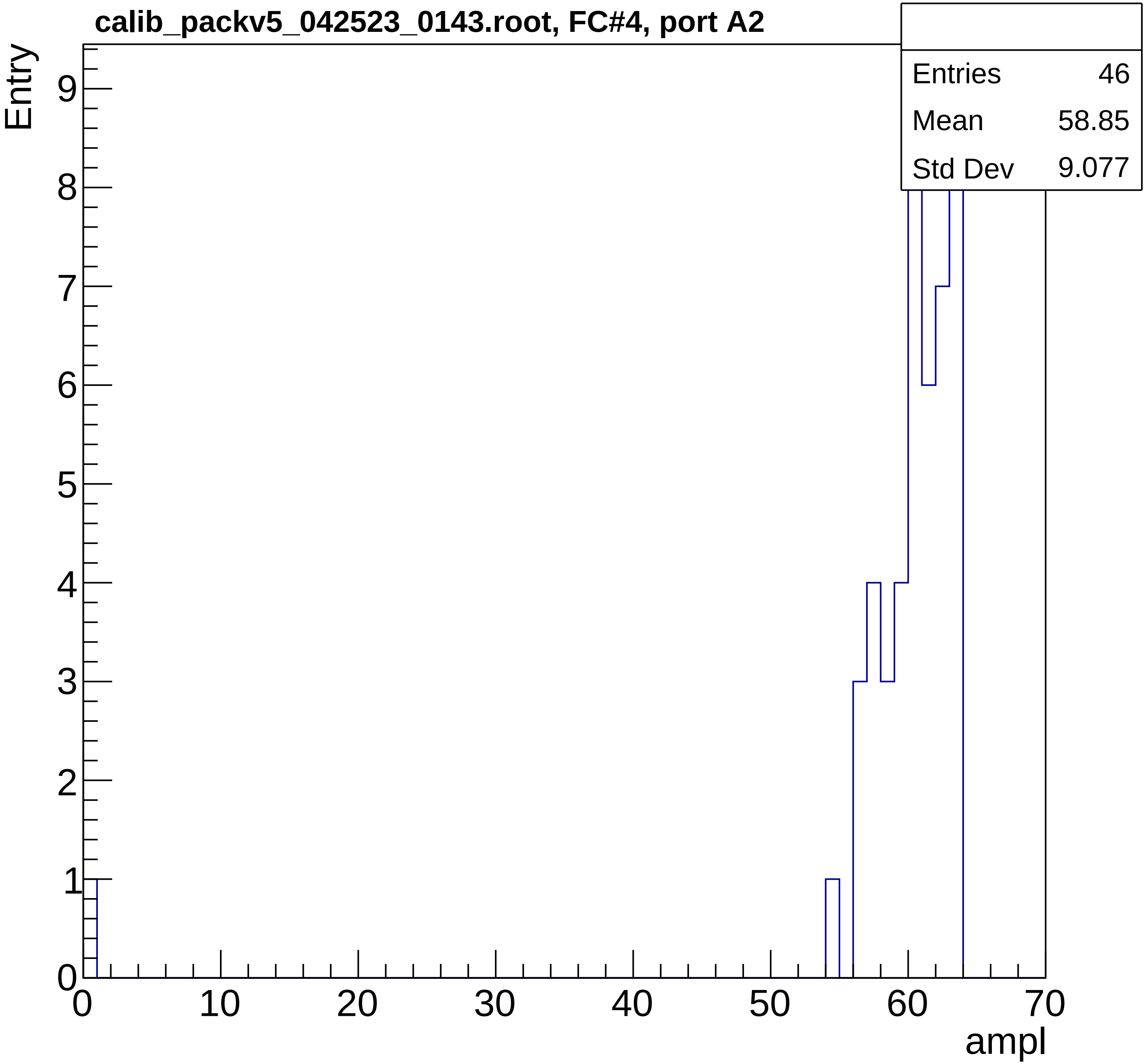


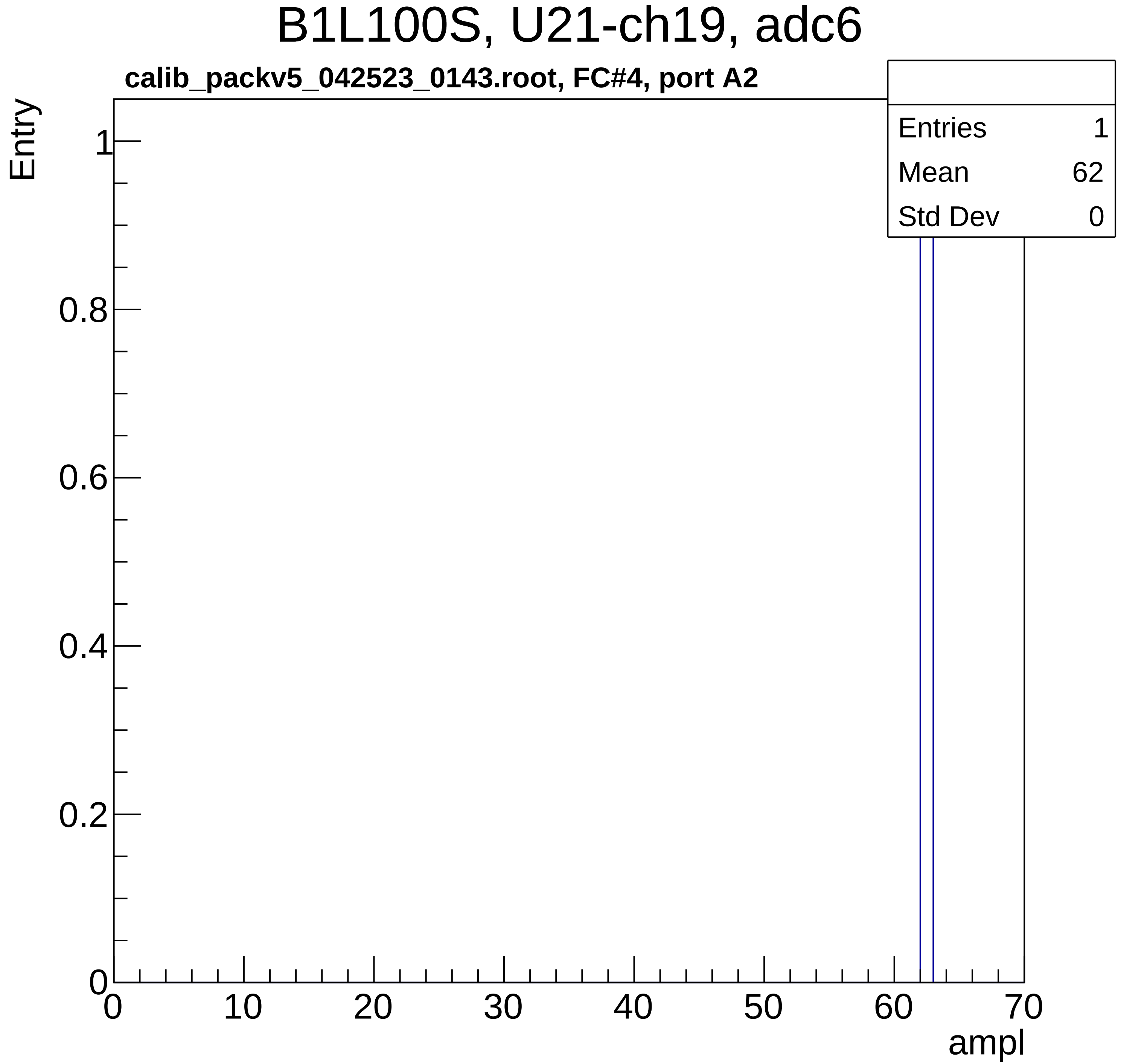

B1L100S, U21-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.54
Std Dev 3.642
Gaus mean : 44.1324
Gaus Width: 3.2866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

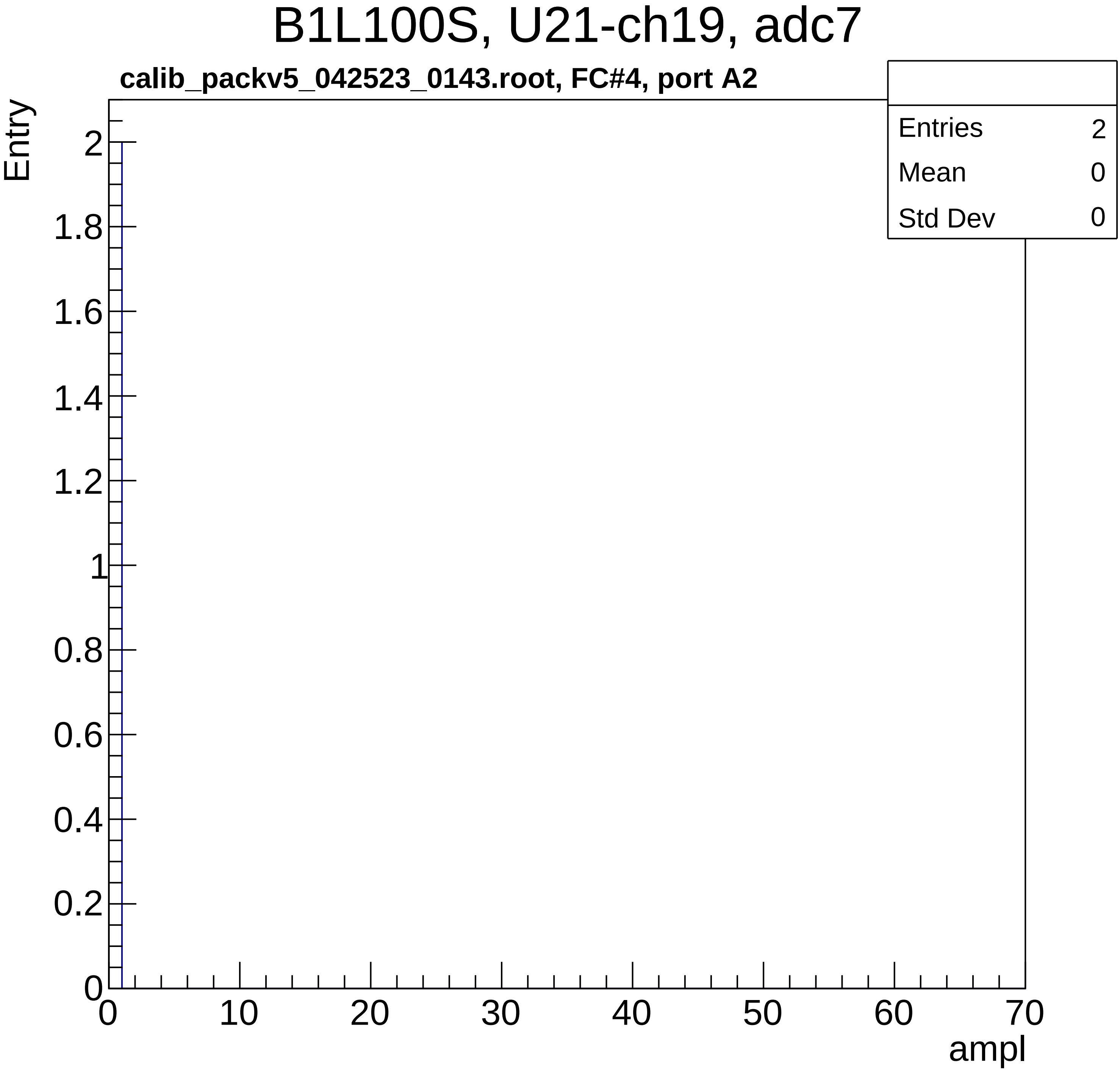

B1L100S, U21-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.19
Std Dev 3.545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

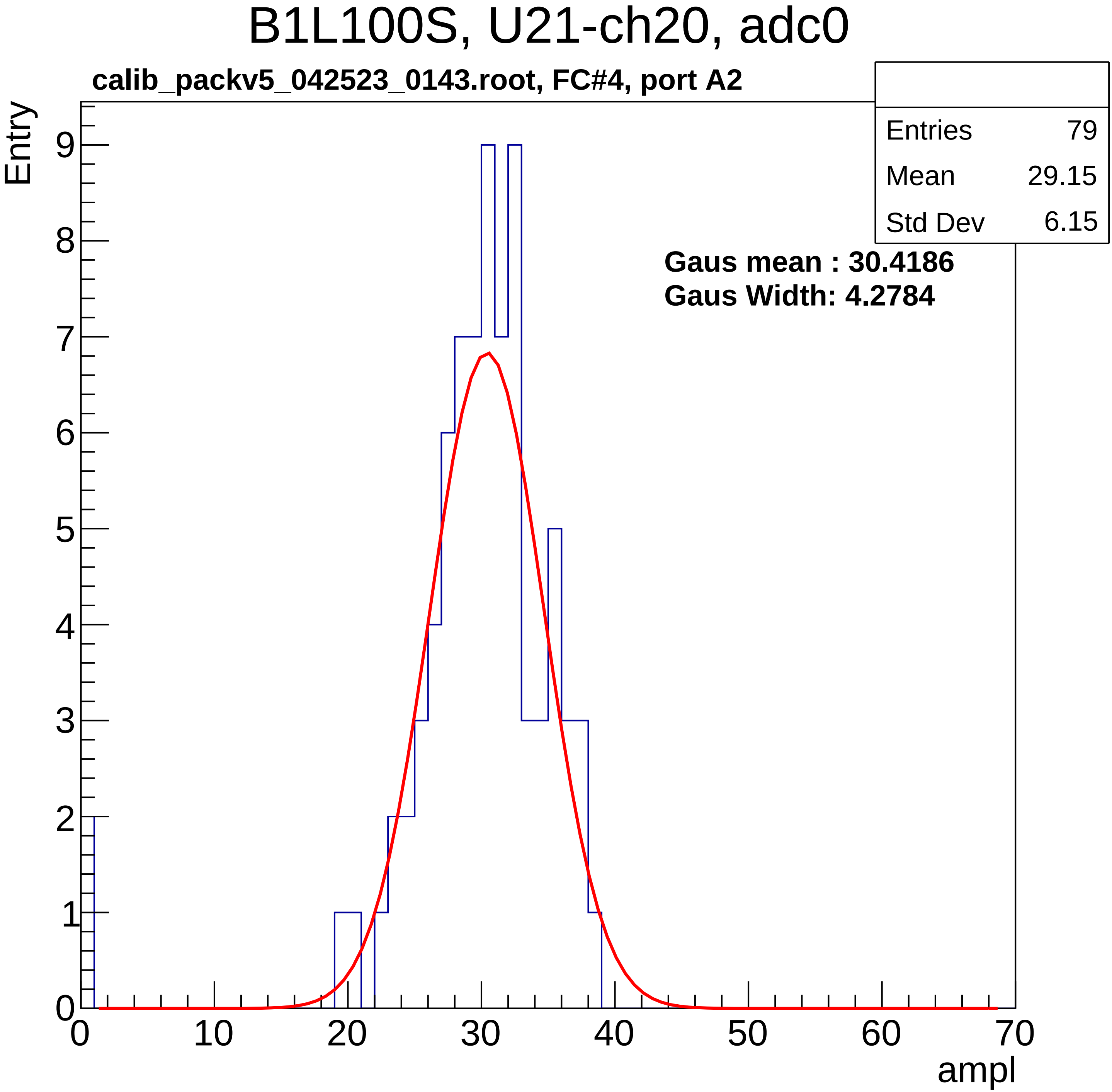

B1L100S, U21-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.28
Std Dev 3.282
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

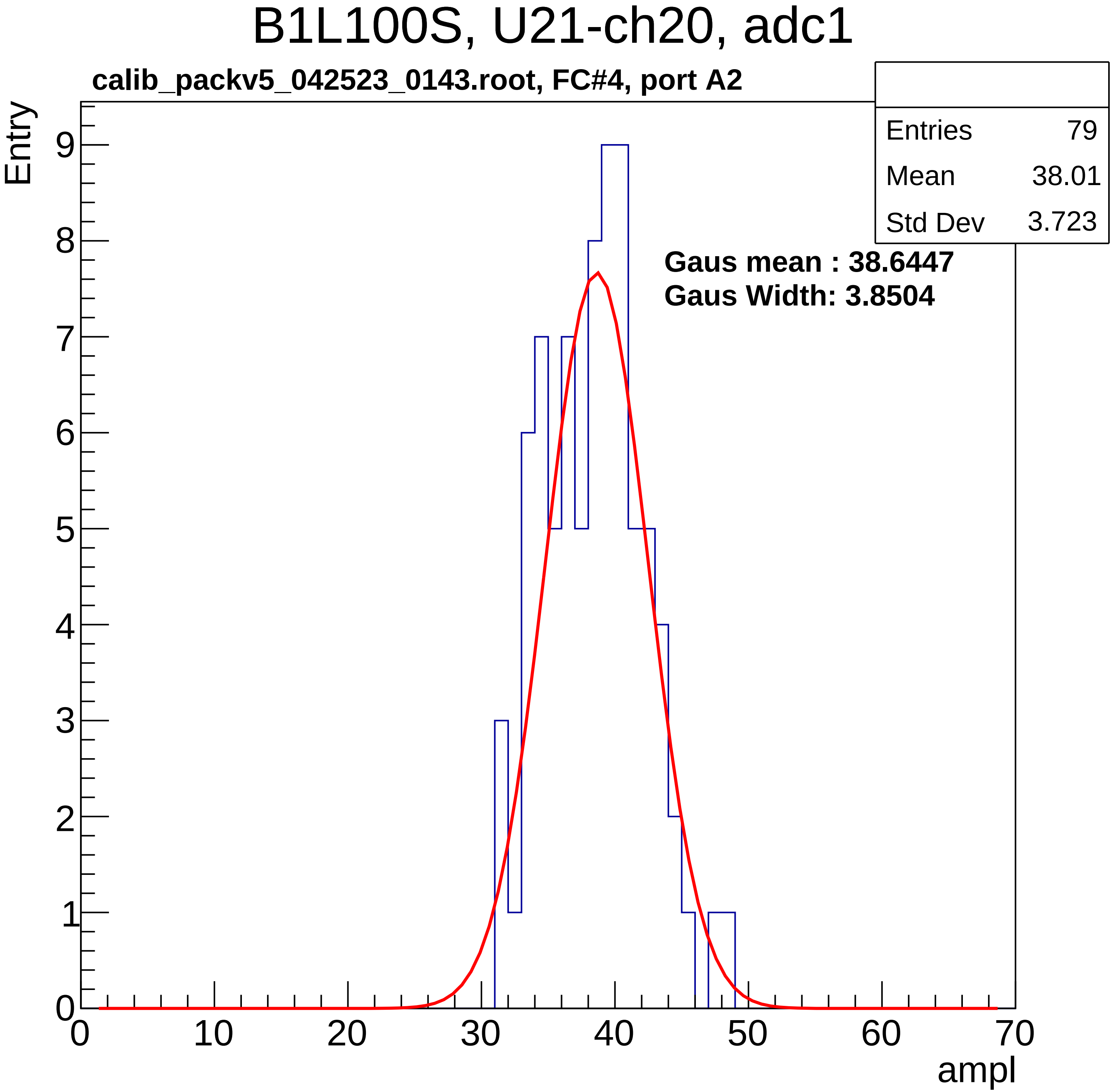
B1L100S, U21-ch19, adc5

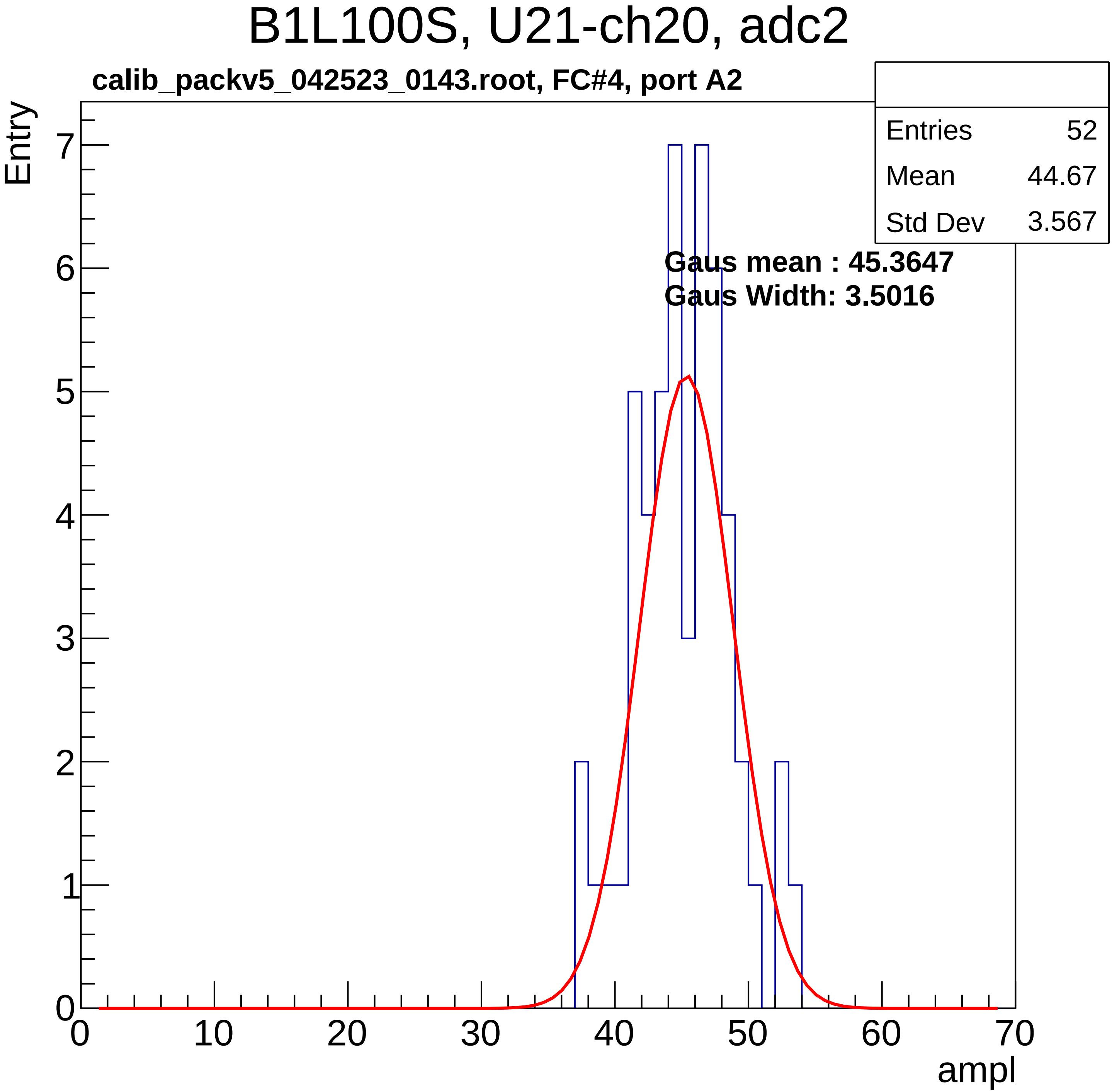


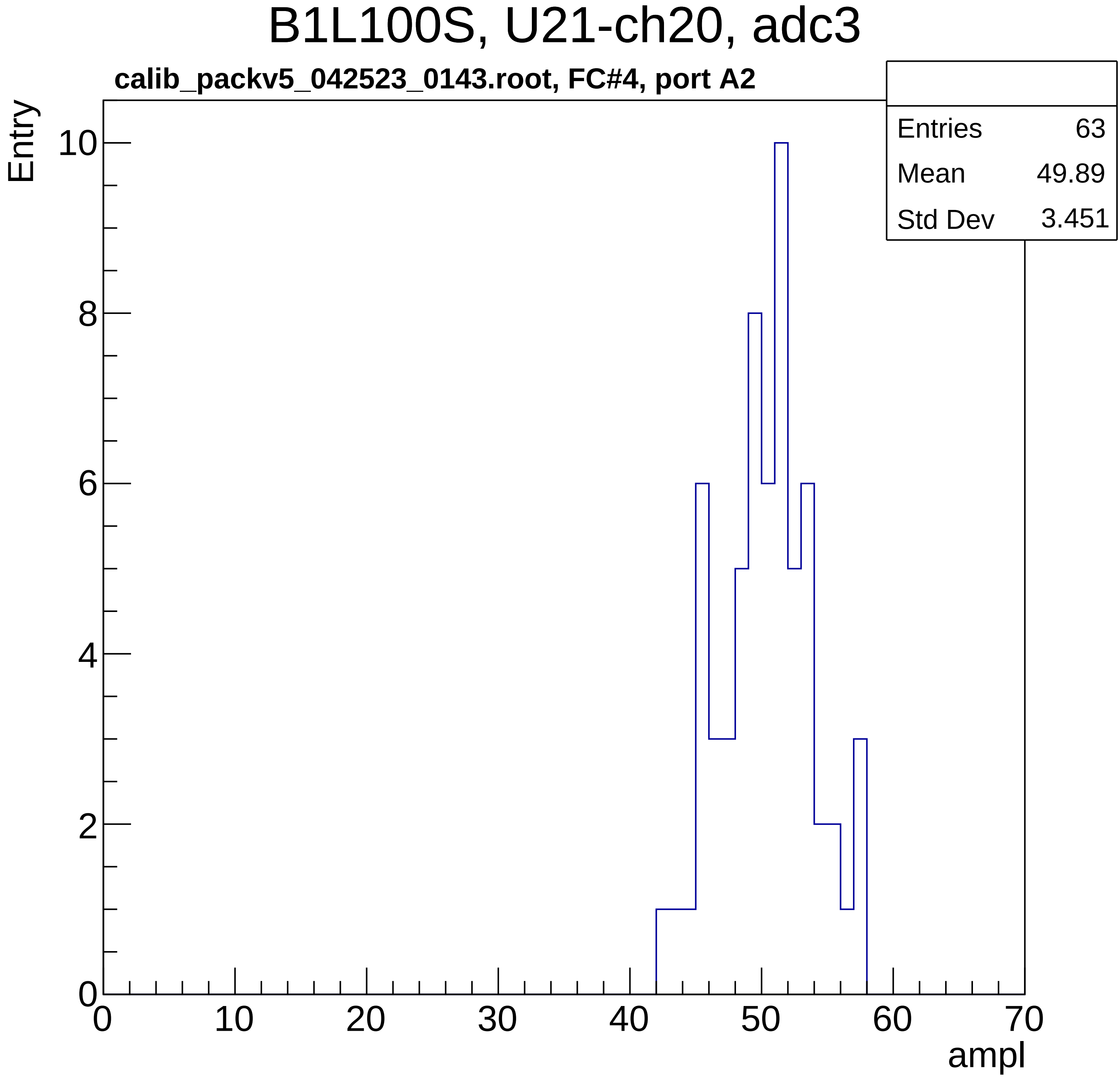

B1L100S, U21-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

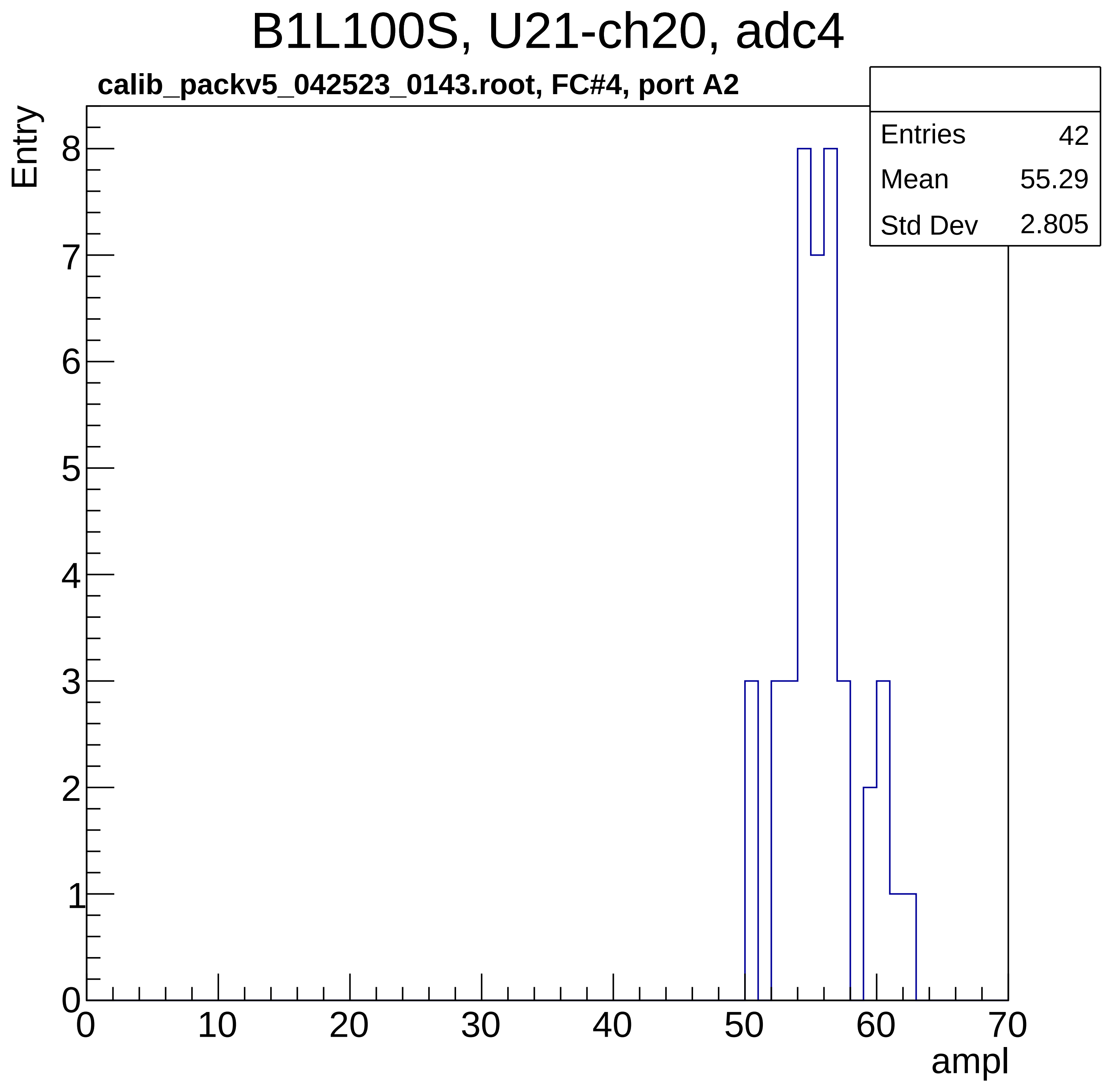

B1L100S, U21-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.15
Std Dev 6.15
Gaus mean : 30.4186
Gaus Width: 4.2784
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

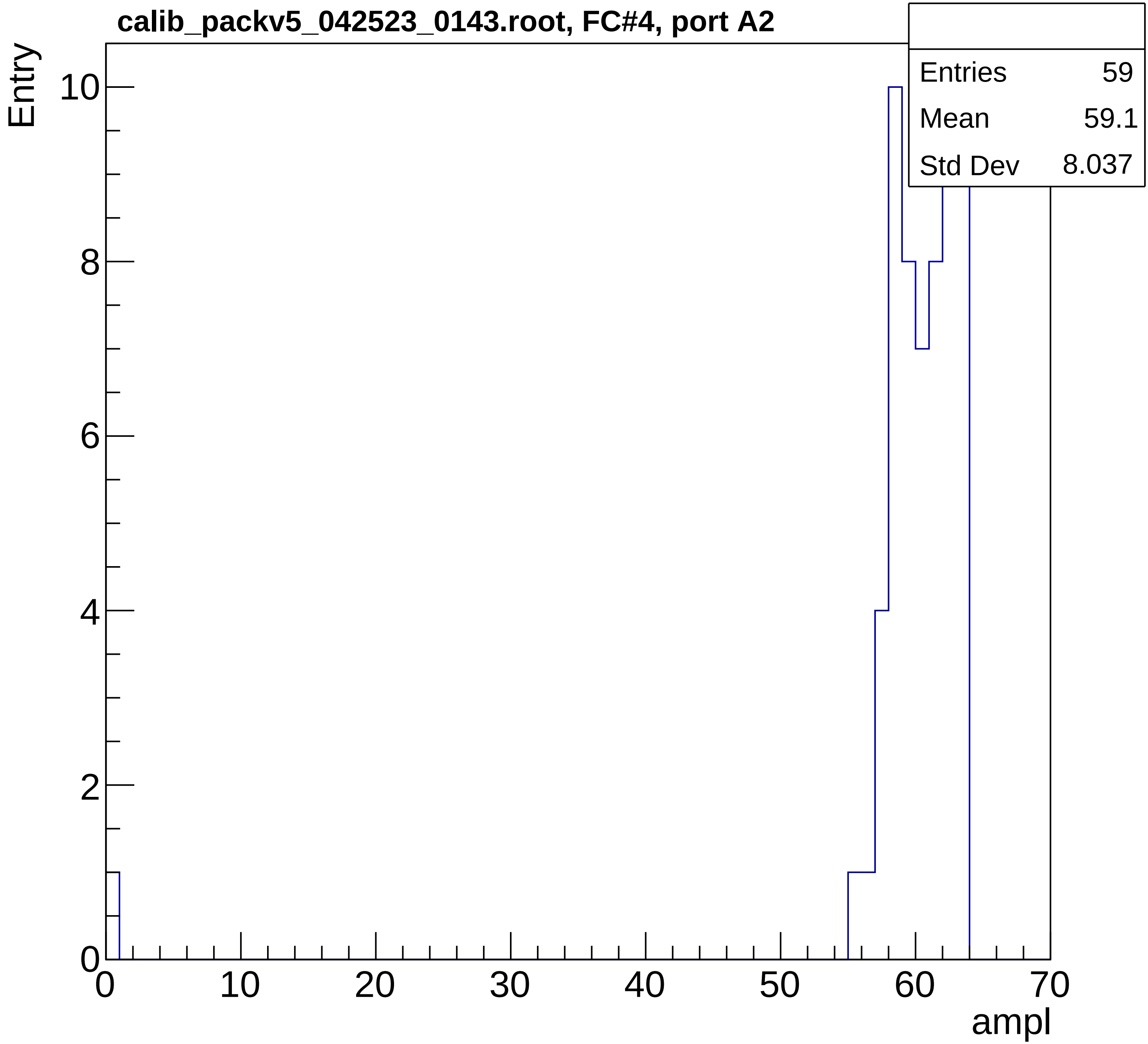

B1L100S, U21-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 38.01
Std Dev 3.723
Gaus mean : 38.6447
Gaus Width: 3.8504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

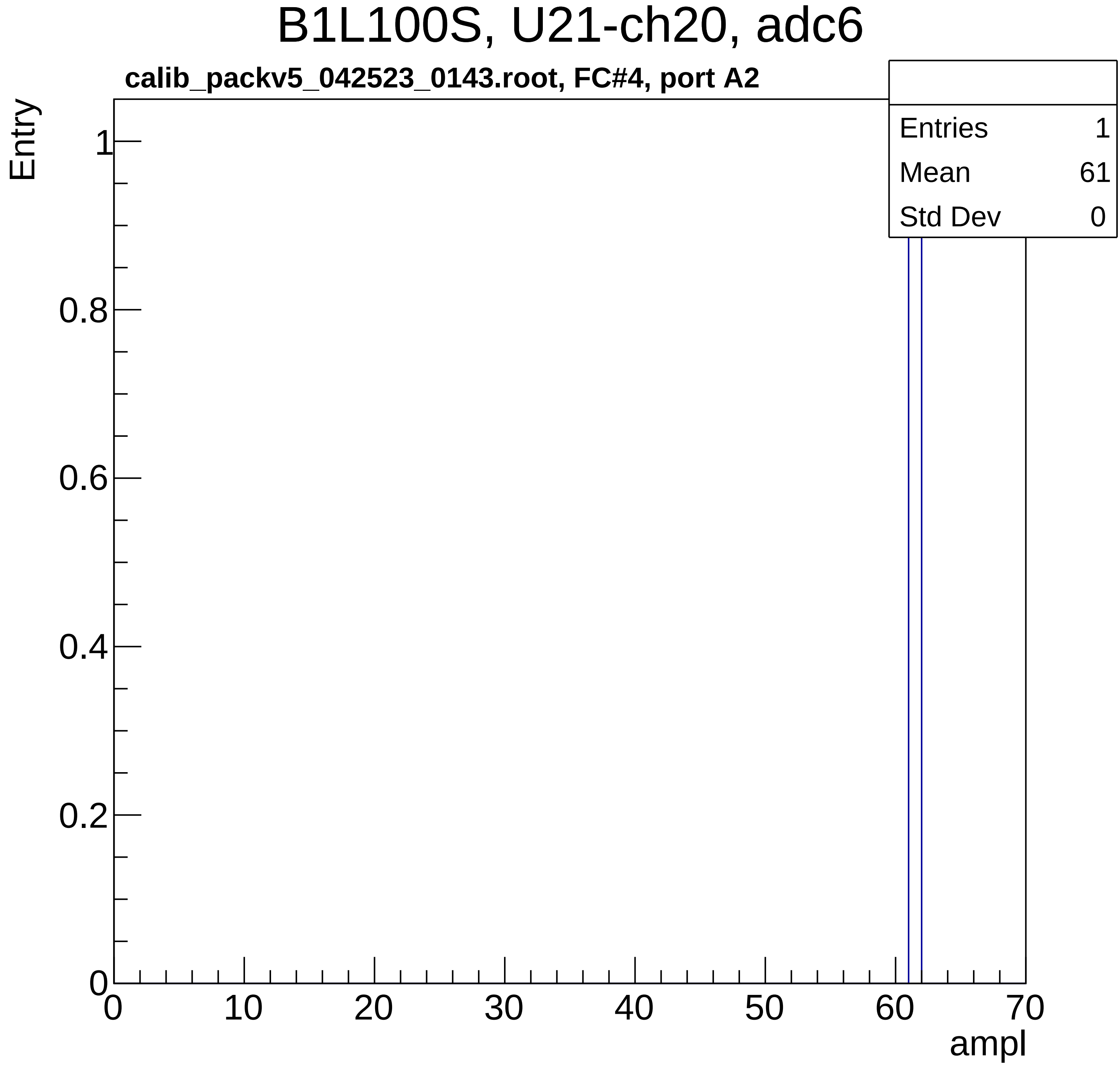

B1L100S, U21-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 44.67
Std Dev 3.567
Gaus mean : 45.3647
Gaus Width: 3.5016
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 63
Mean 49.89
Std Dev 3.451
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 55.29
Std Dev 2.805
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

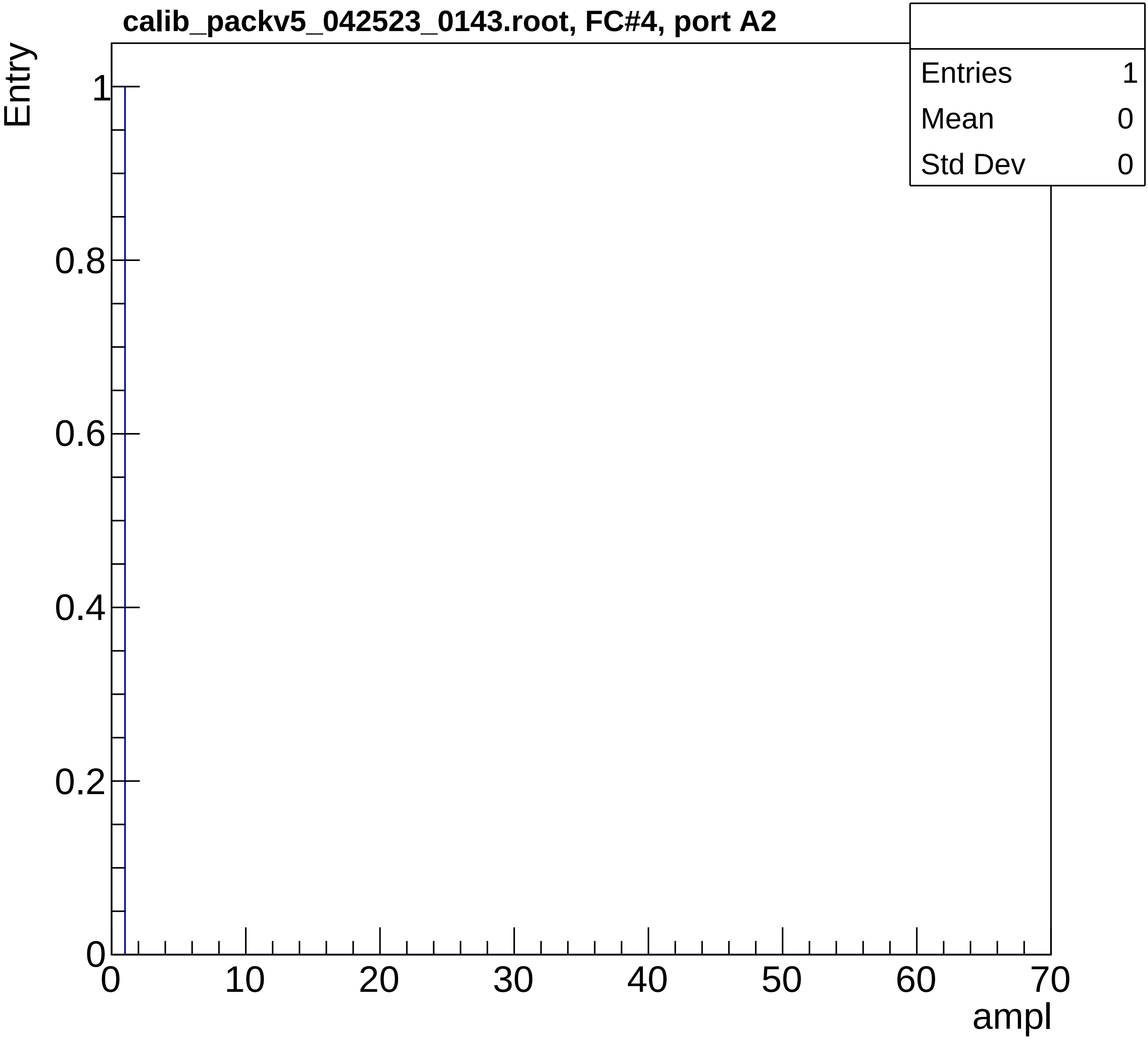
B1L100S, U21-ch20, adc5

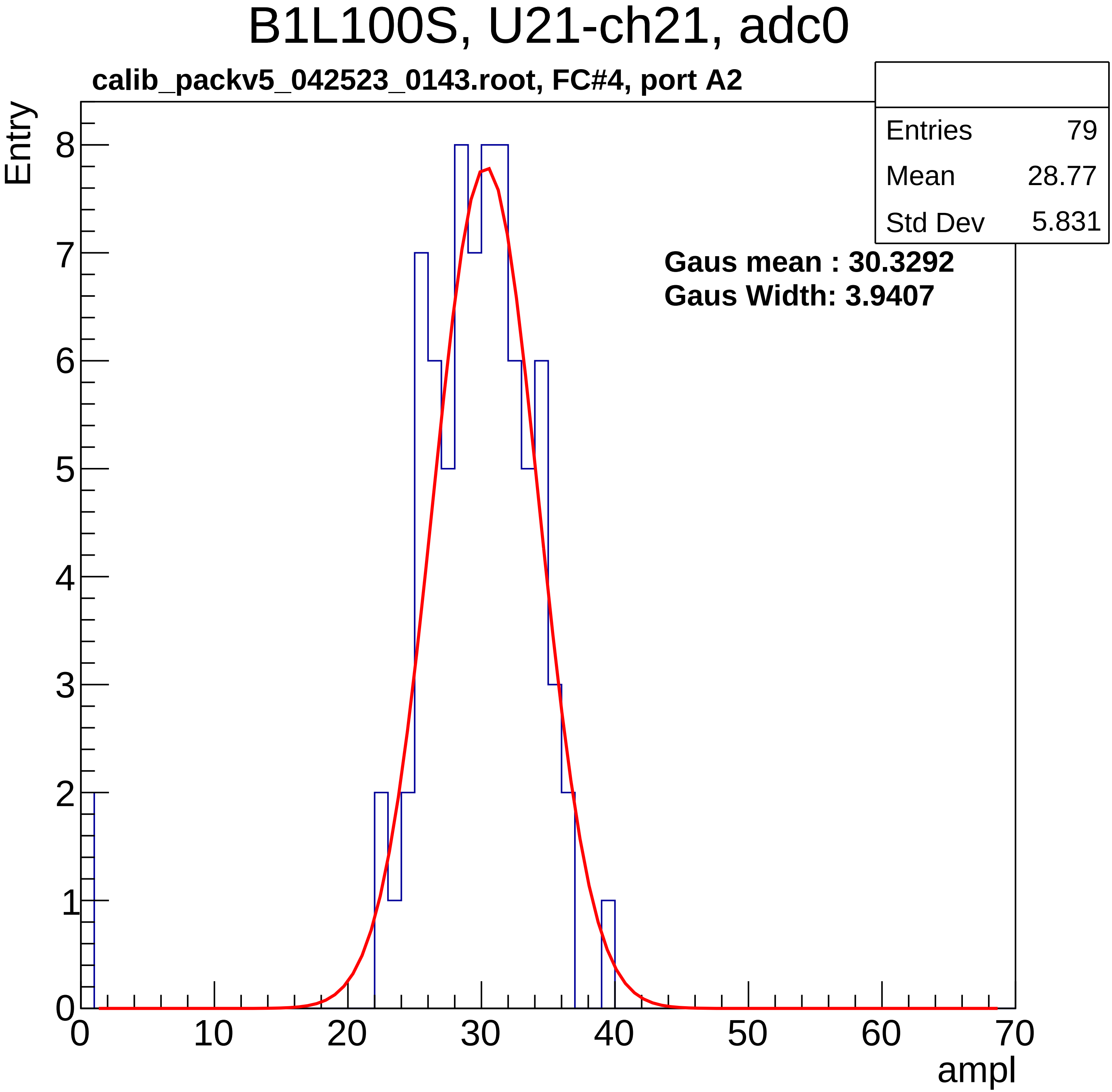


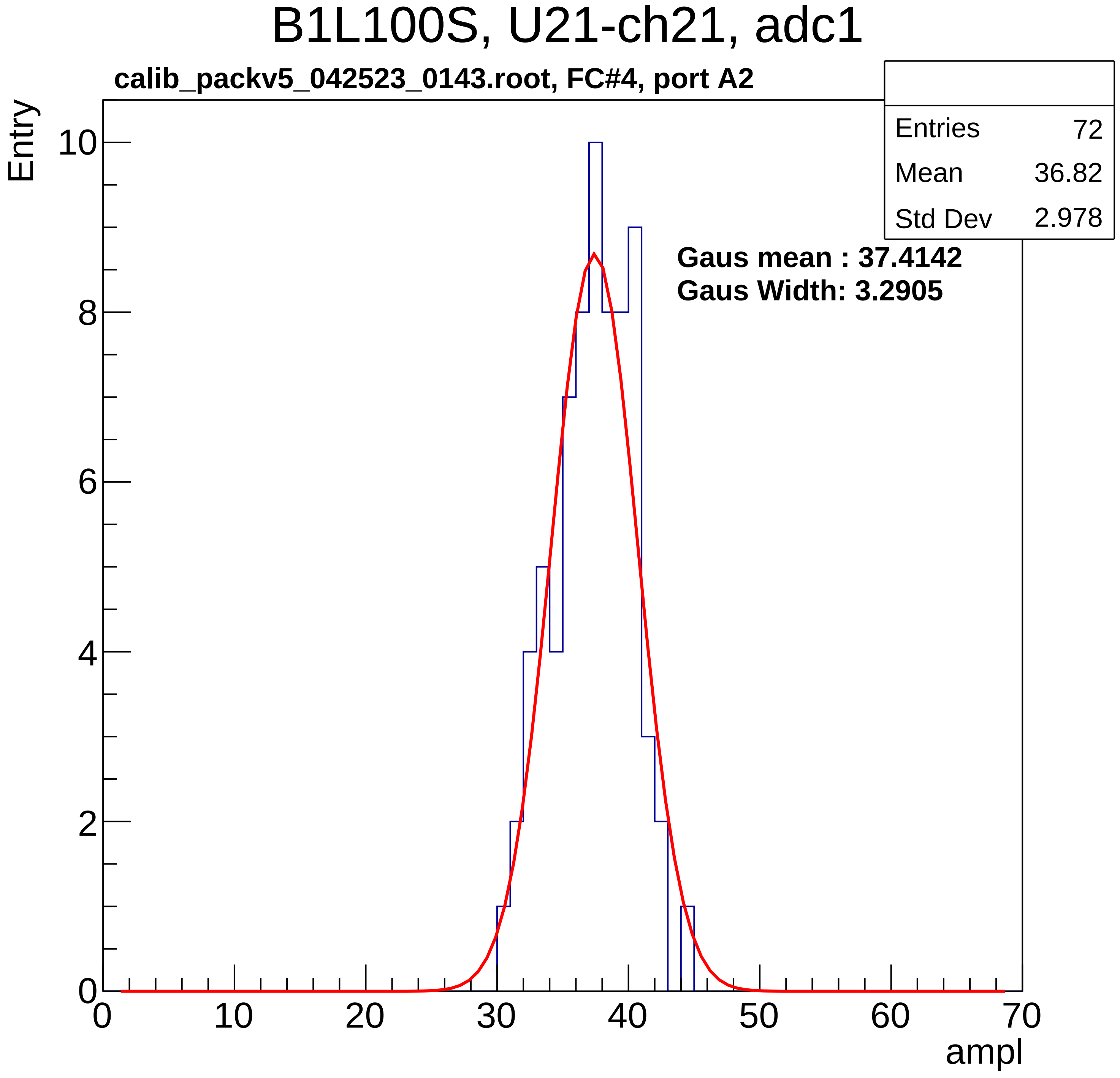

B1L100S, U21-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

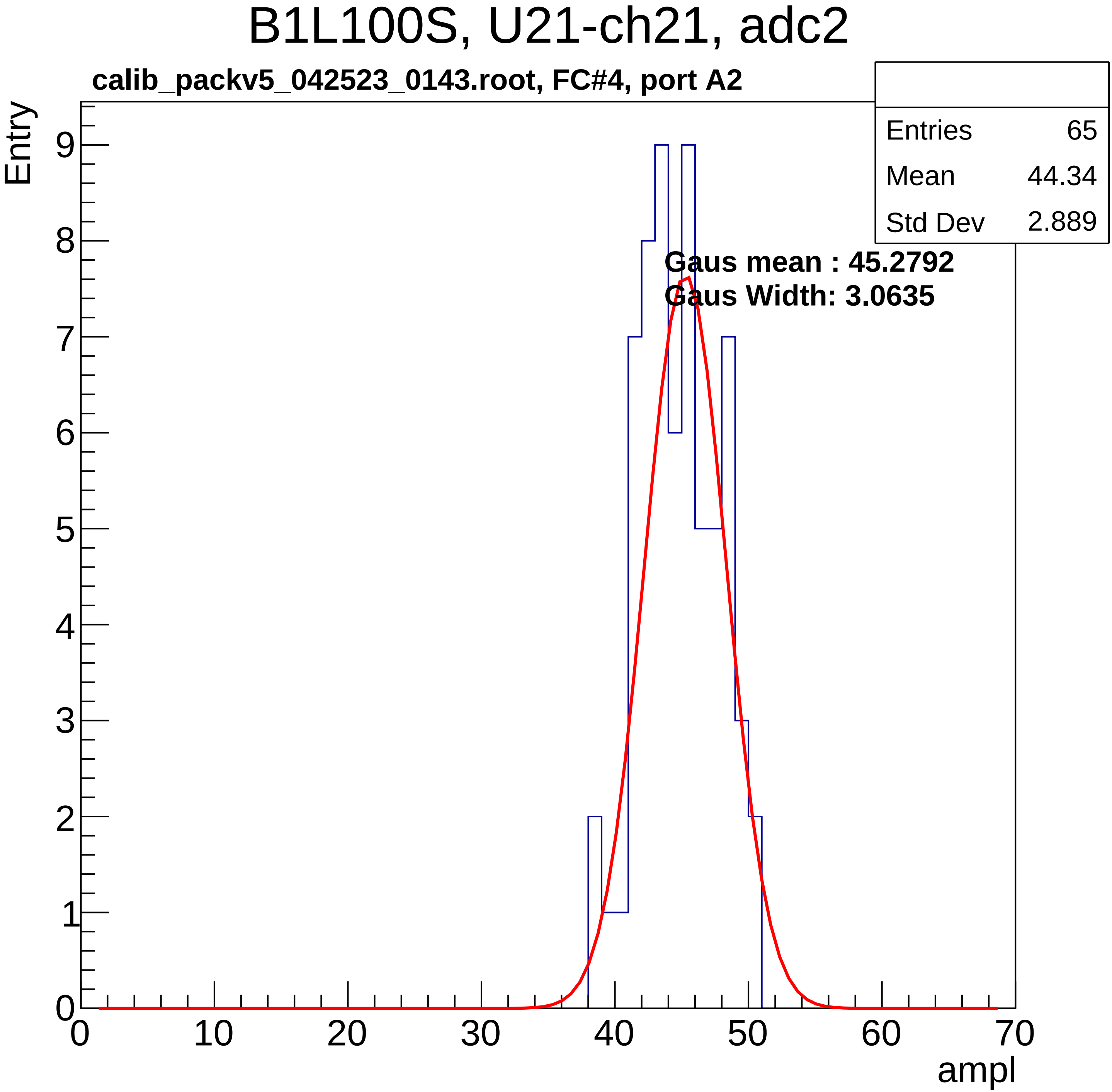
B1L100S, U21-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2



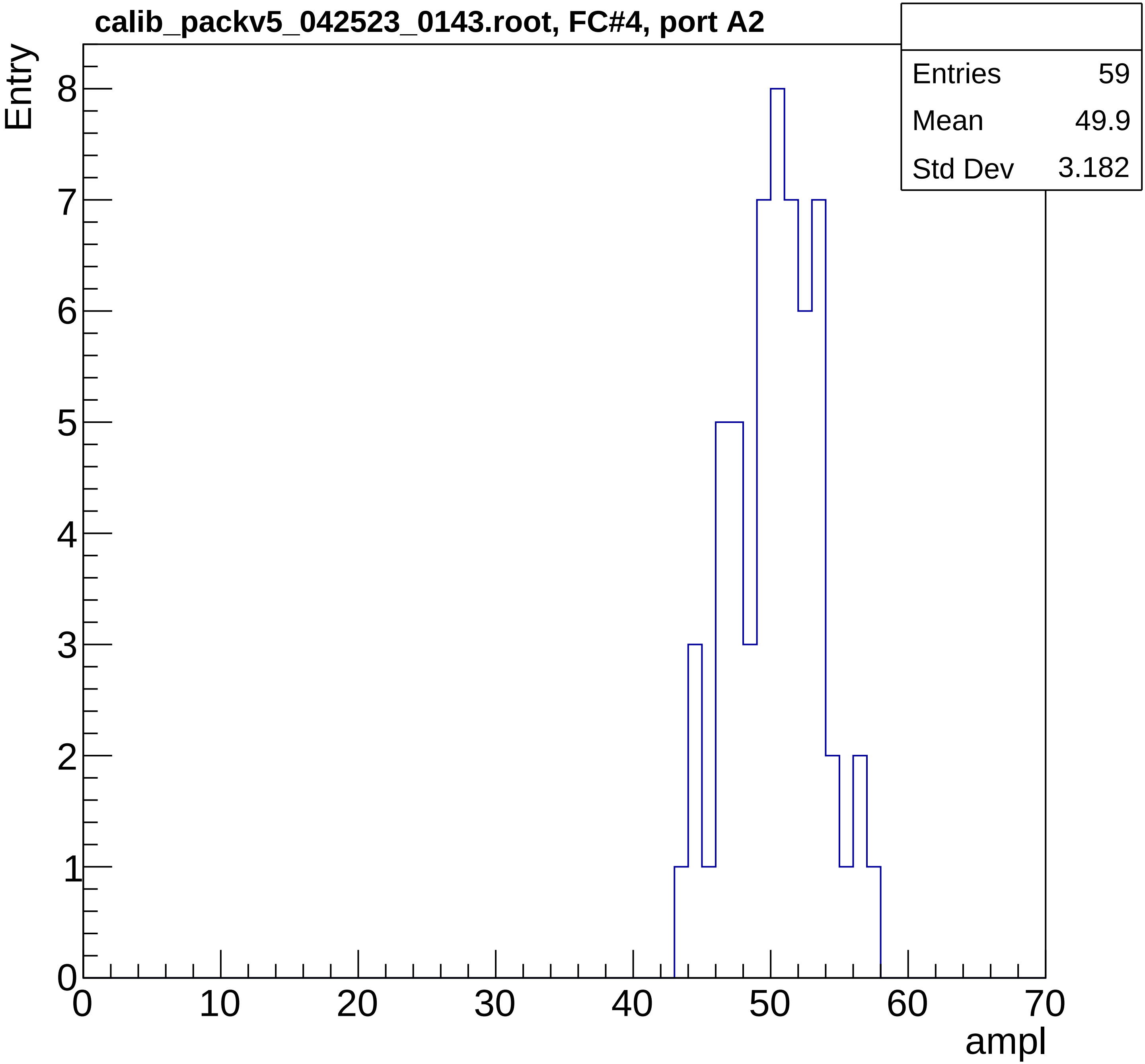

B1L100S, U21-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.77
Std Dev 5.831
Gaus mean : 30.3292
Gaus Width: 3.9407
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 72
Mean 36.82
Std Dev 2.978
Gaus mean : 37.4142
Gaus Width: 3.2905
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.34
Std Dev 2.889
Gaus mean : 45.2792
Gaus Width: 3.0635
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

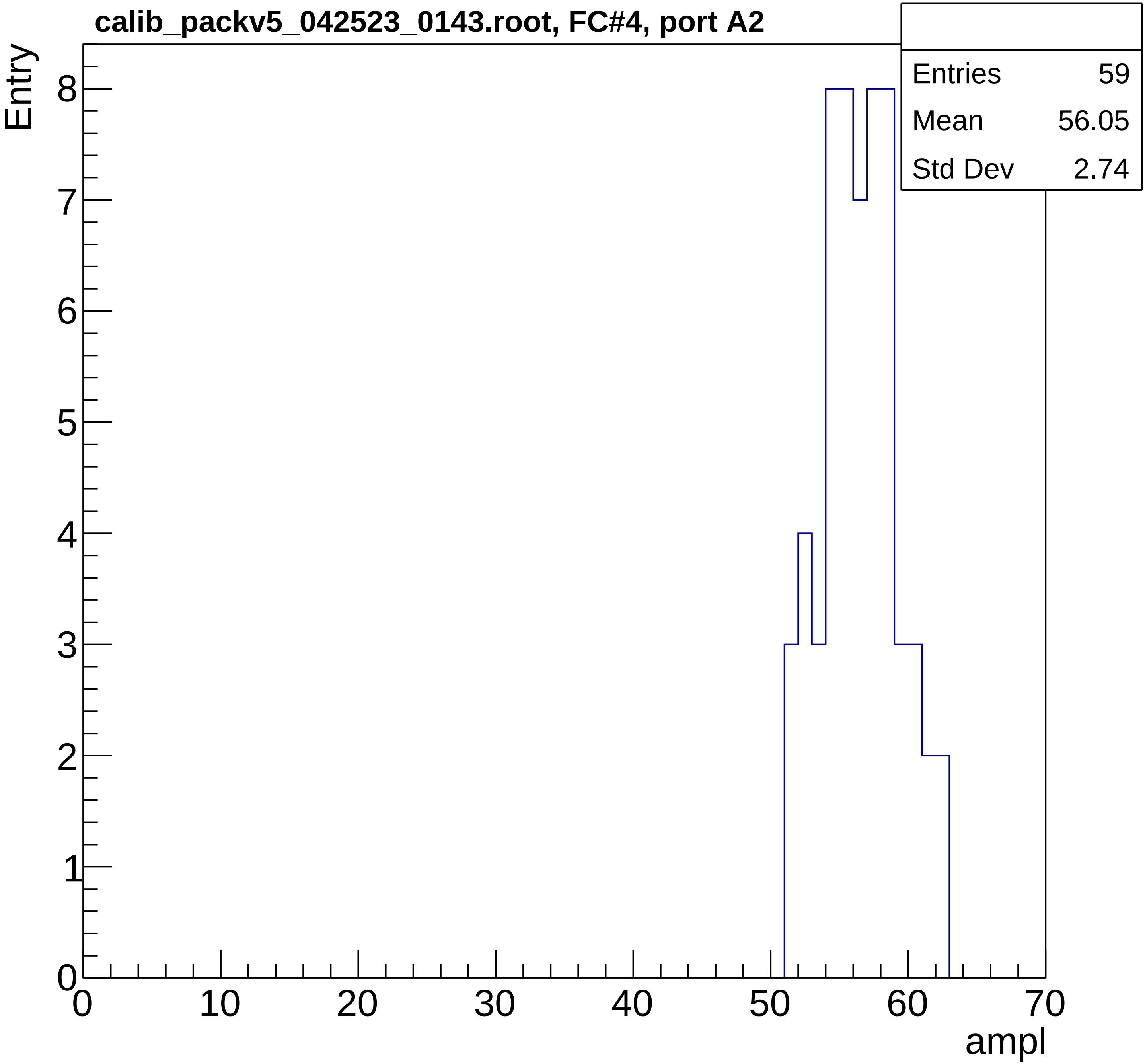
B1L100S, U21-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2



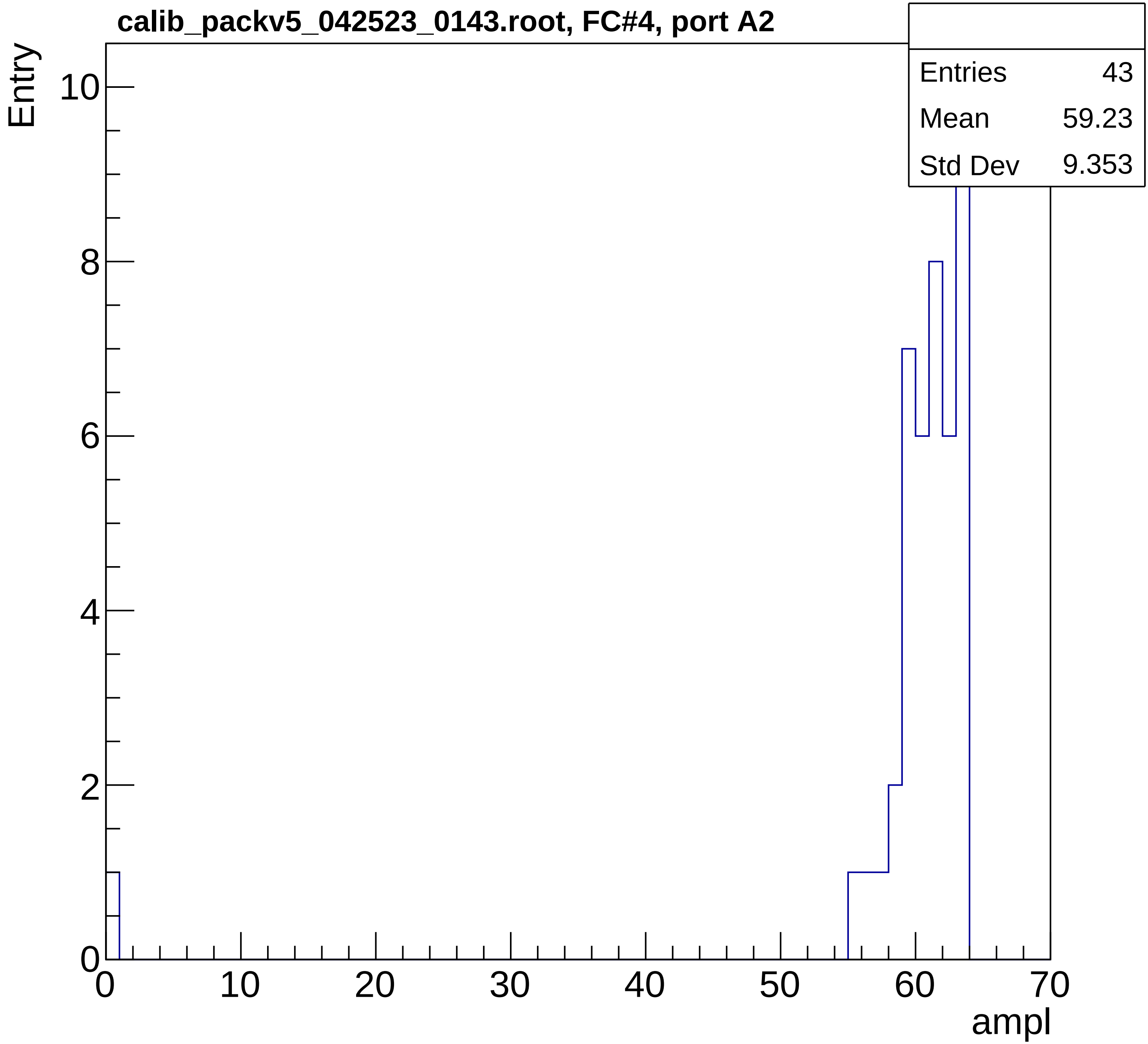
B1L100S, U21-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2



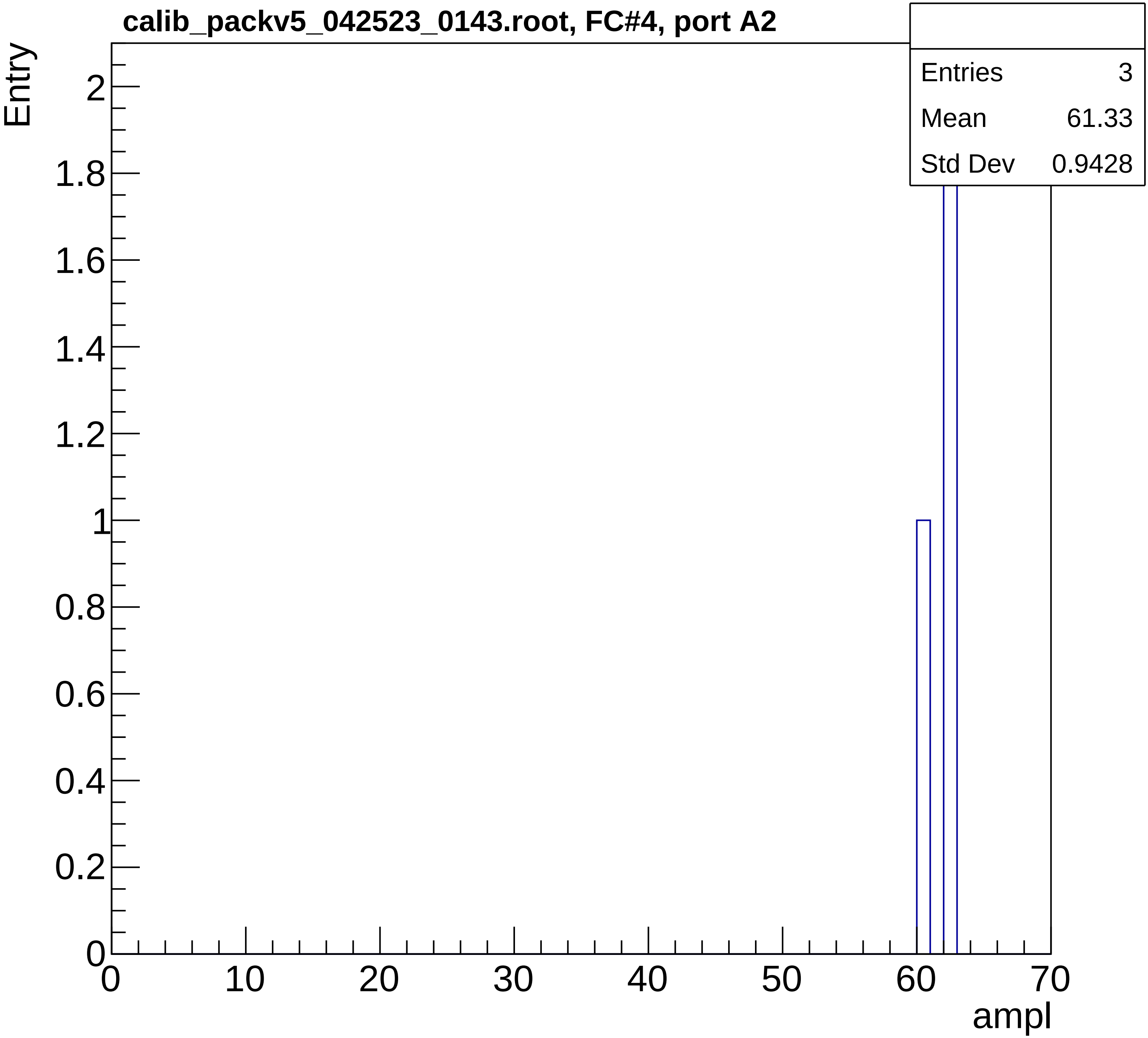
B1L100S, U21-ch21, adc5

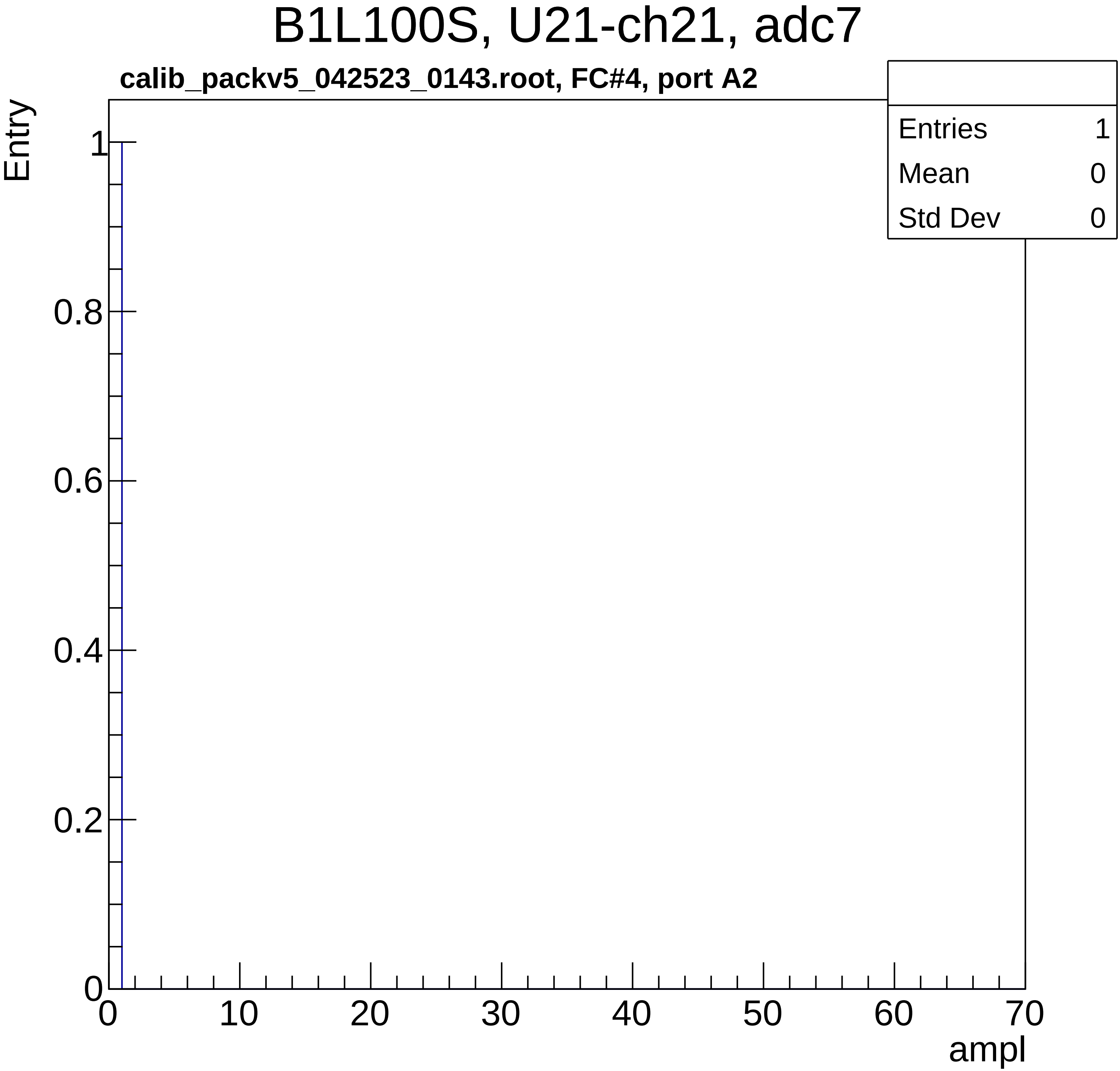
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U21-ch21, adc6

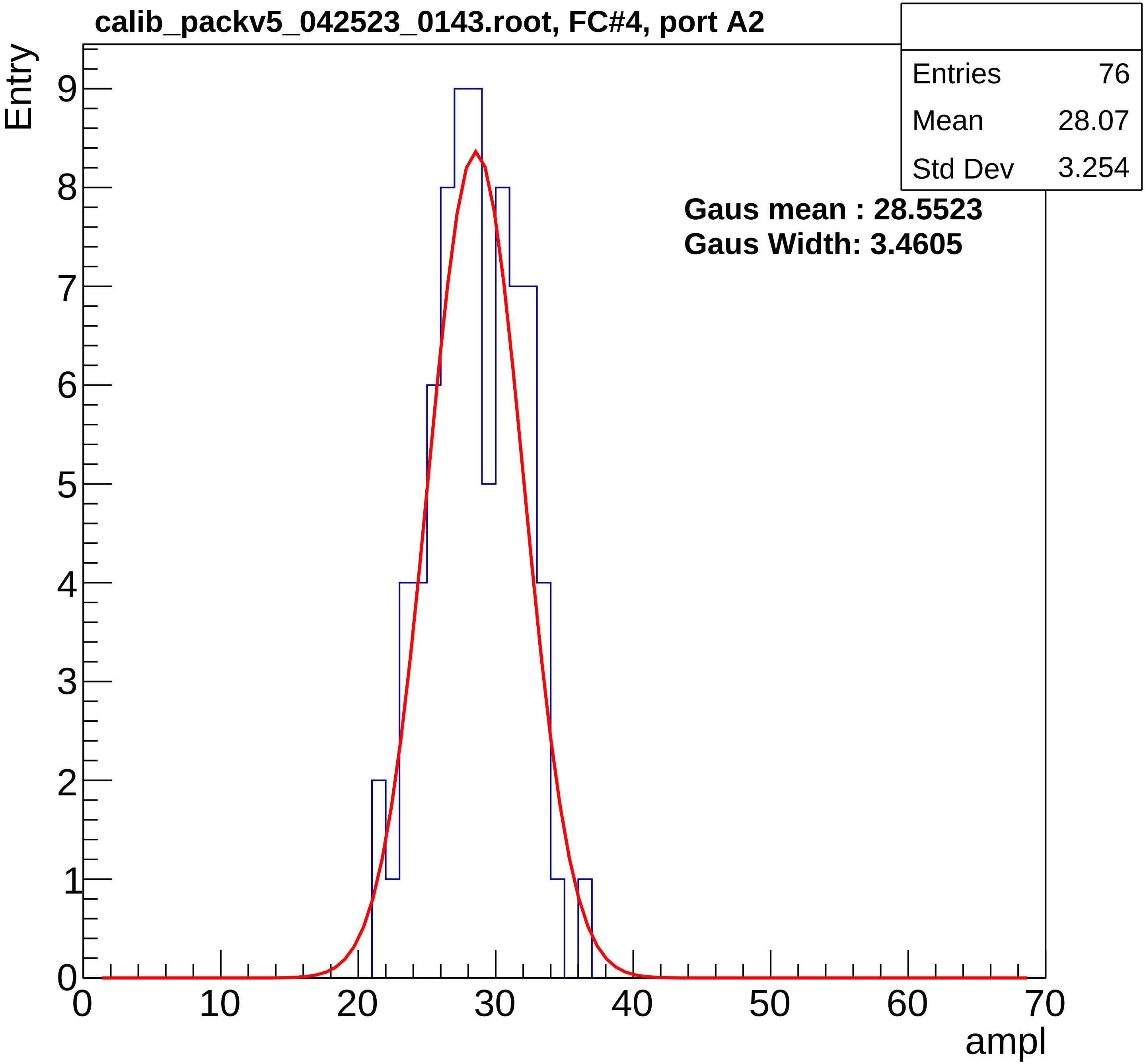
calib_packv5_042523_0143.root, FC#4, port A2



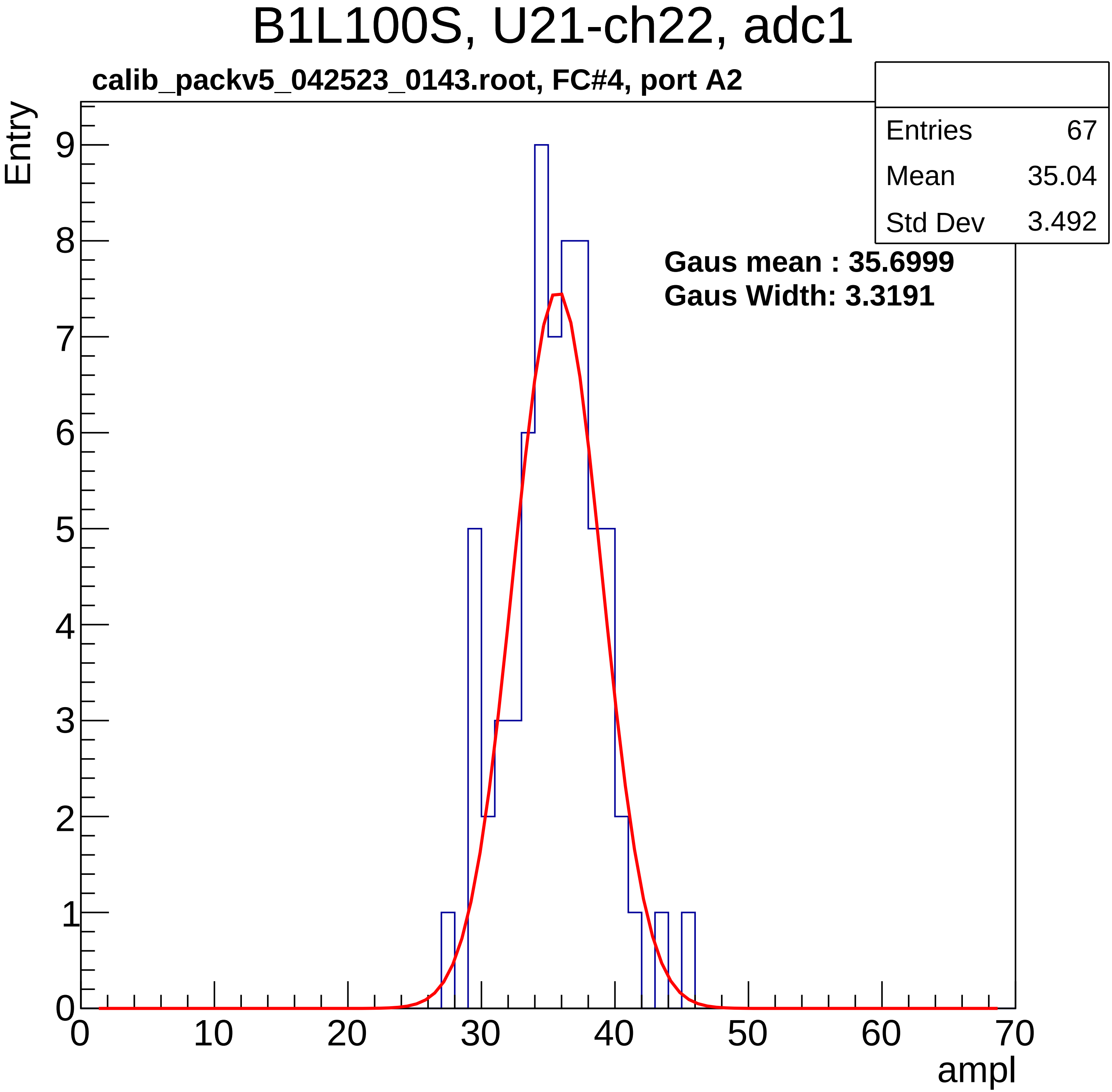

B1L100S, U21-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

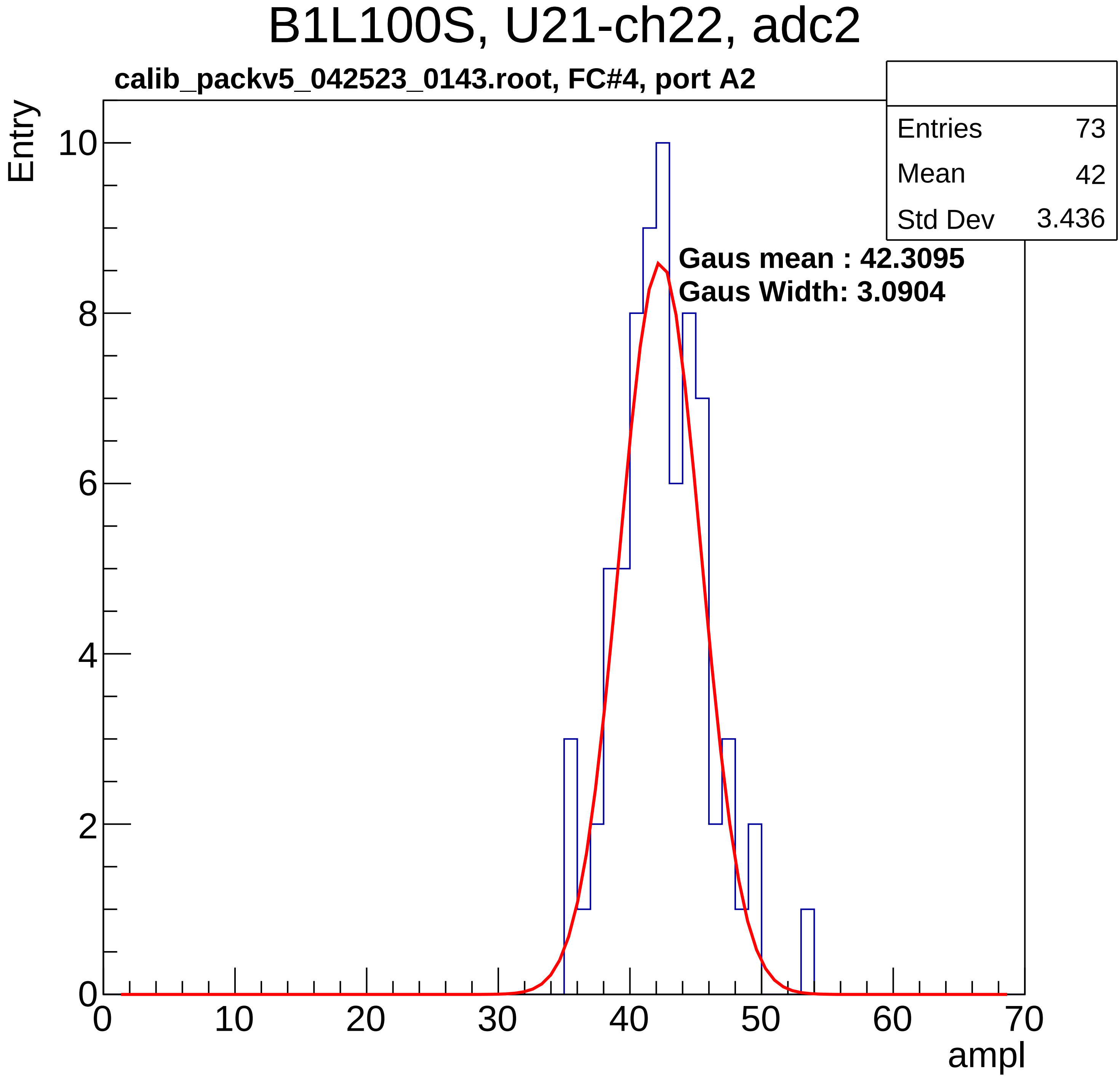
B1L100S, U21-ch22, adc0

calib_packv5_042523_0143.root, FC#4, port A2



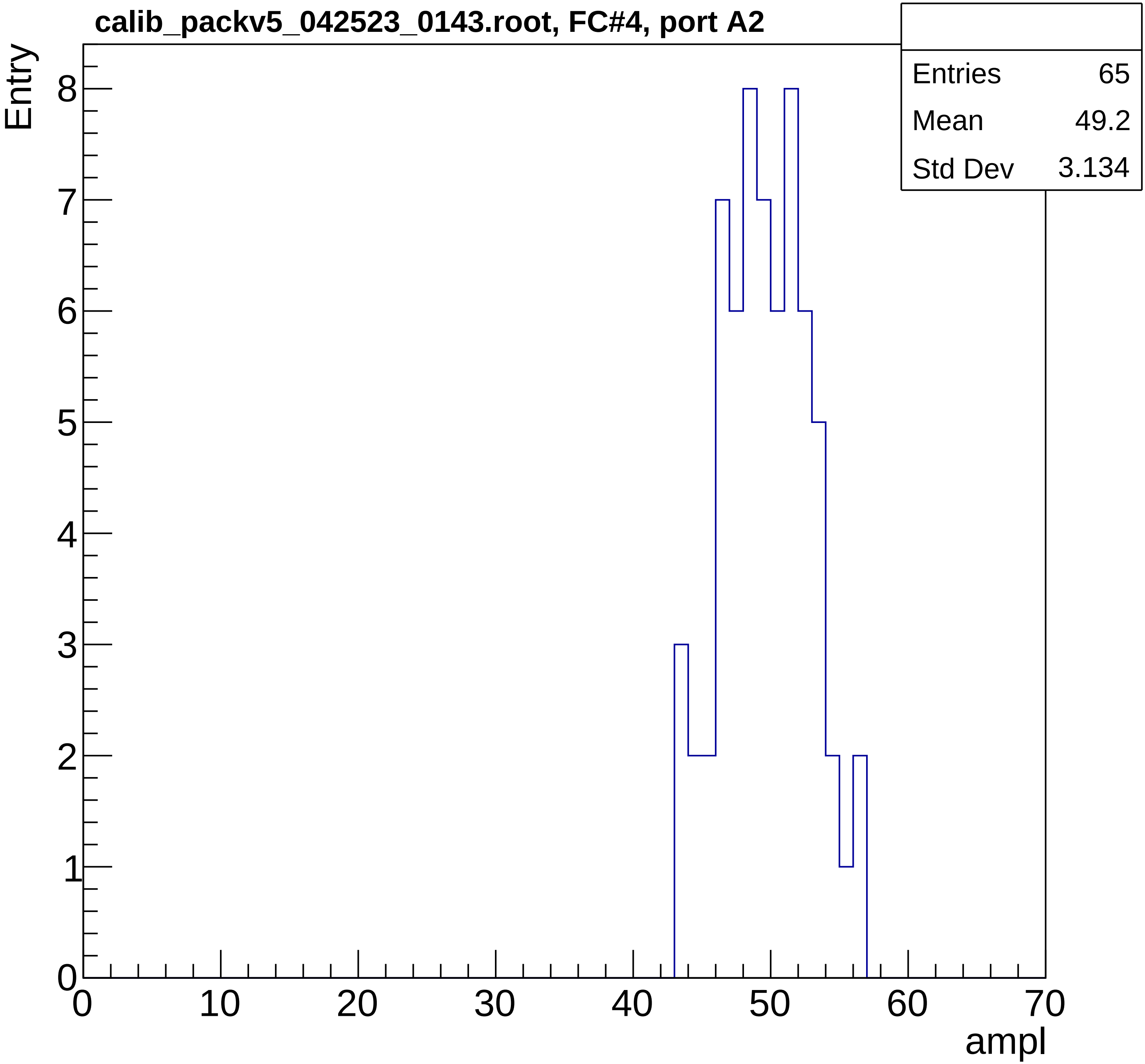
Gaus mean : 28.5523
Gaus Width: 3.4605


B1L100S, U21-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 67
Mean 35.04
Std Dev 3.492
Gaus mean : 35.6999
Gaus Width: 3.3191
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 73
Mean 42
Std Dev 3.436
Gaus mean : 42.3095
Gaus Width: 3.0904
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

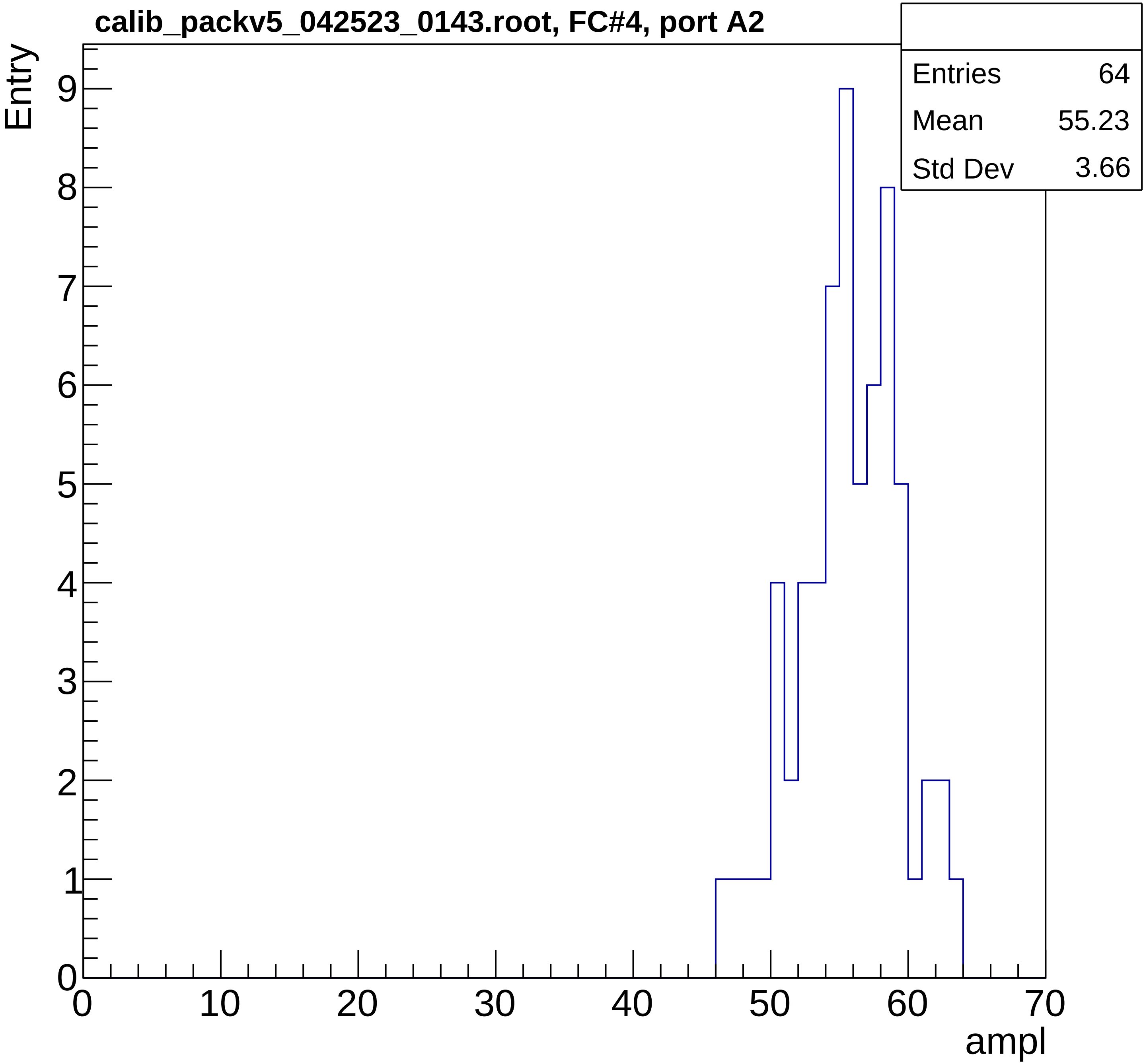
B1L100S, U21-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2



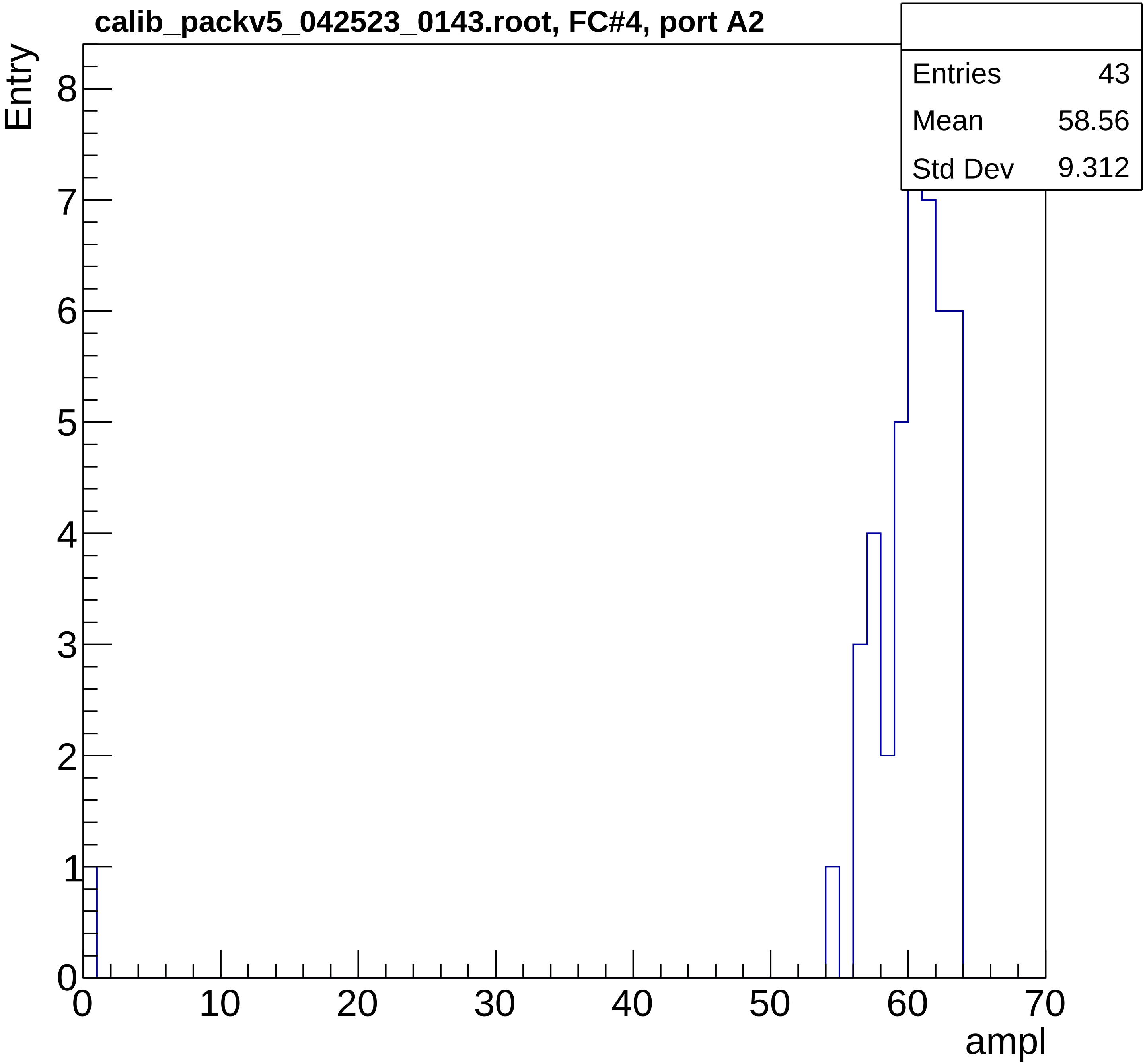
B1L100S, U21-ch22, adc4

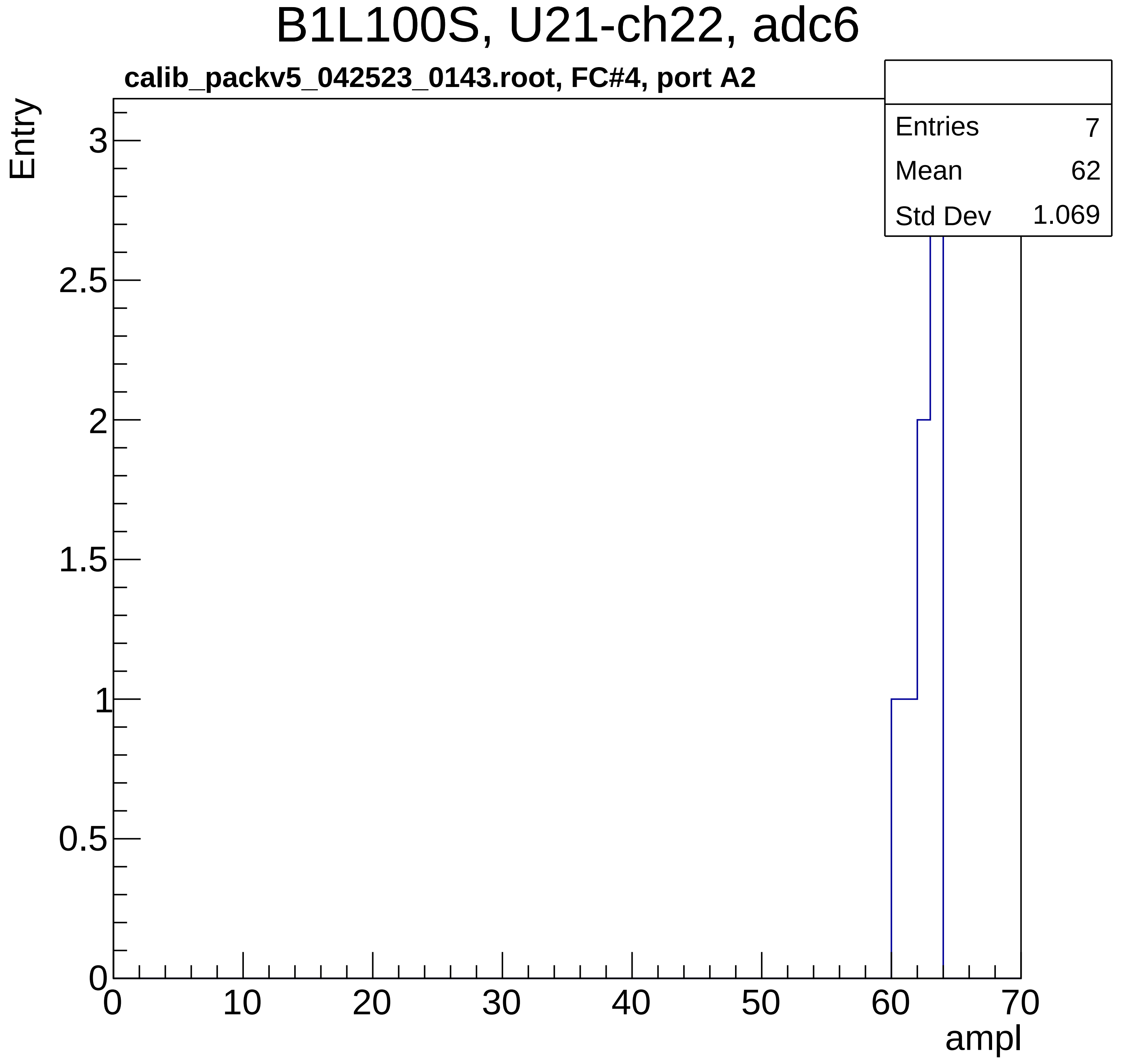
calib_packv5_042523_0143.root, FC#4, port A2



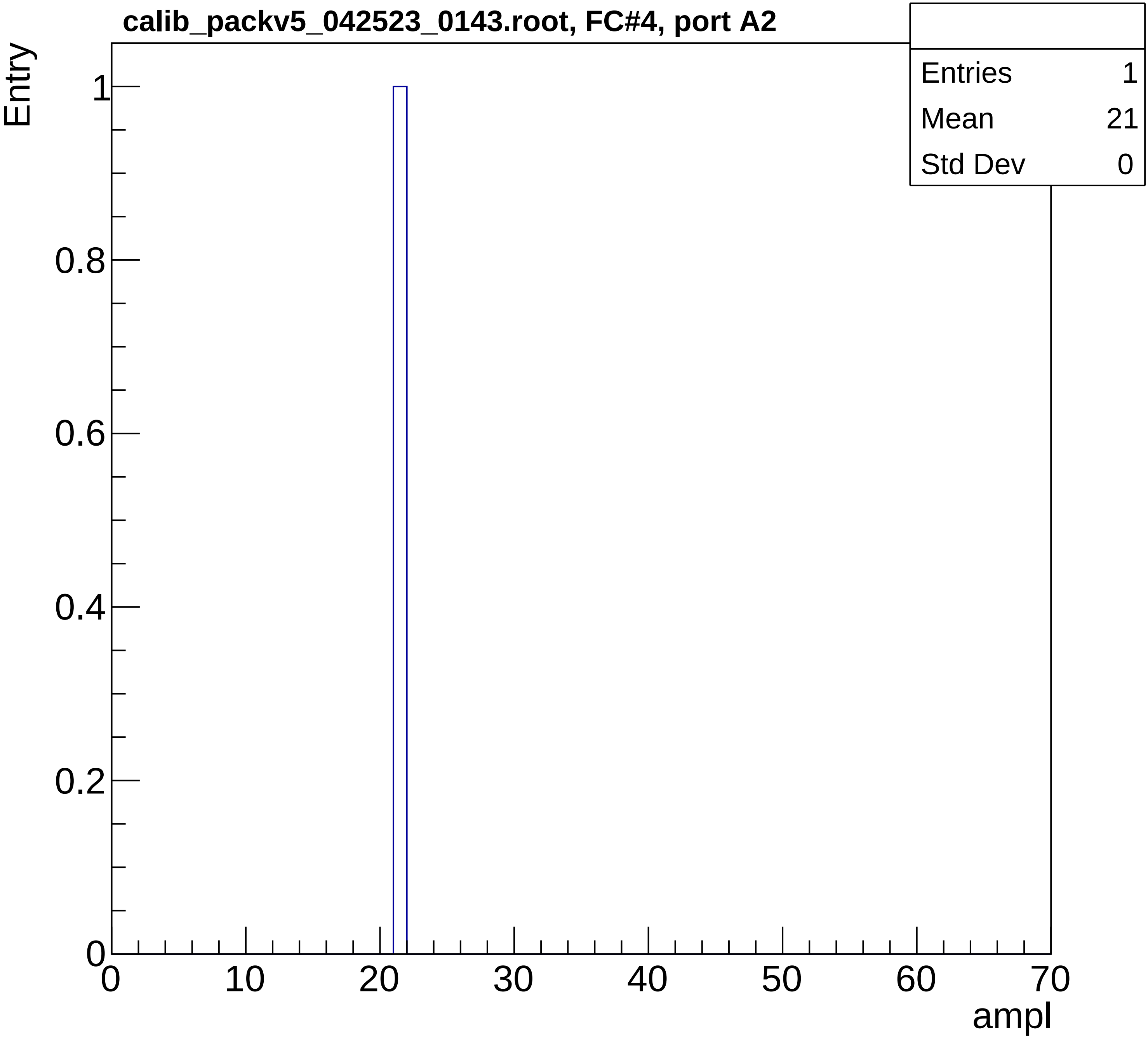
B1L100S, U21-ch22, adc5

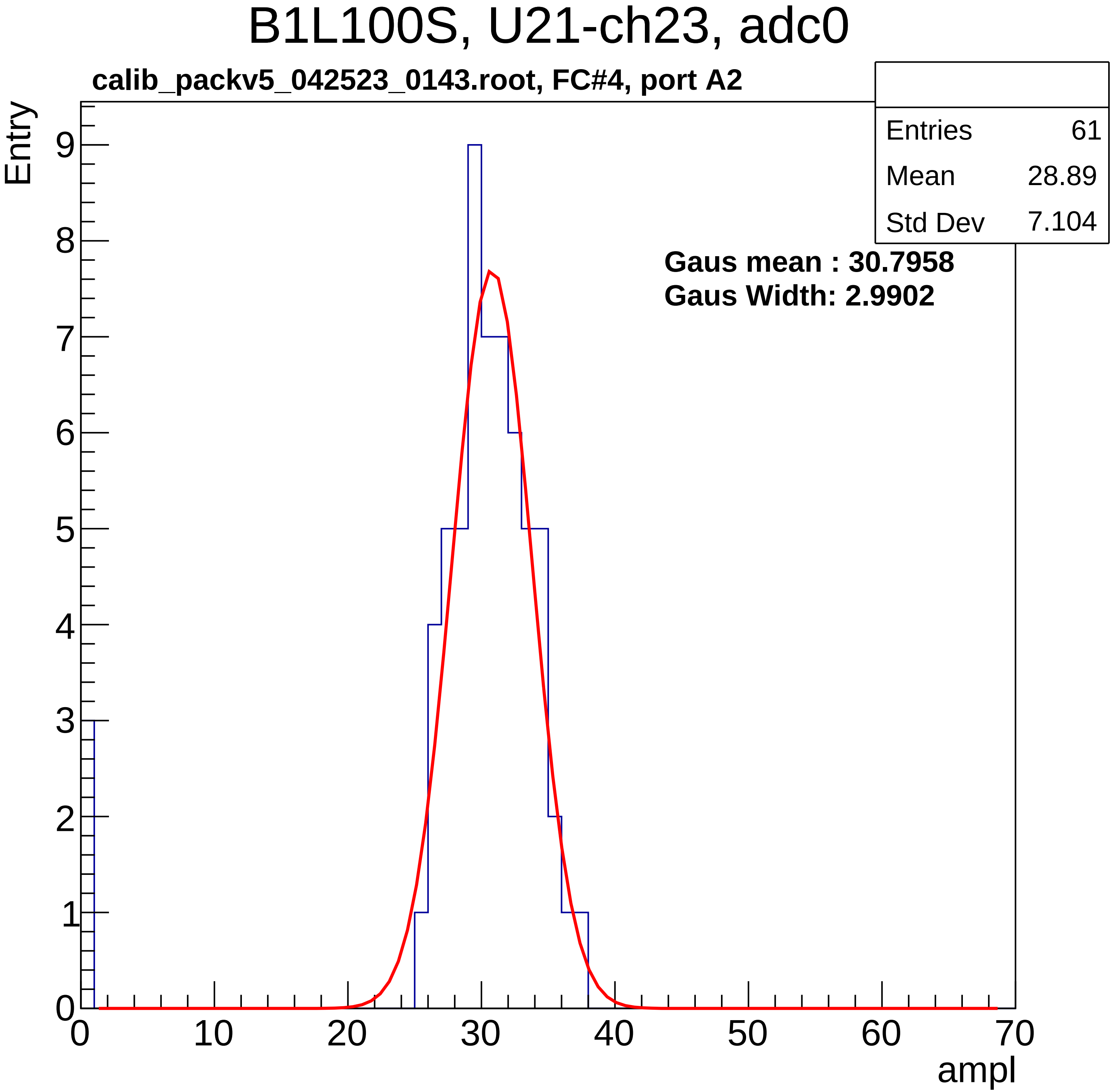
calib_packv5_042523_0143.root, FC#4, port A2

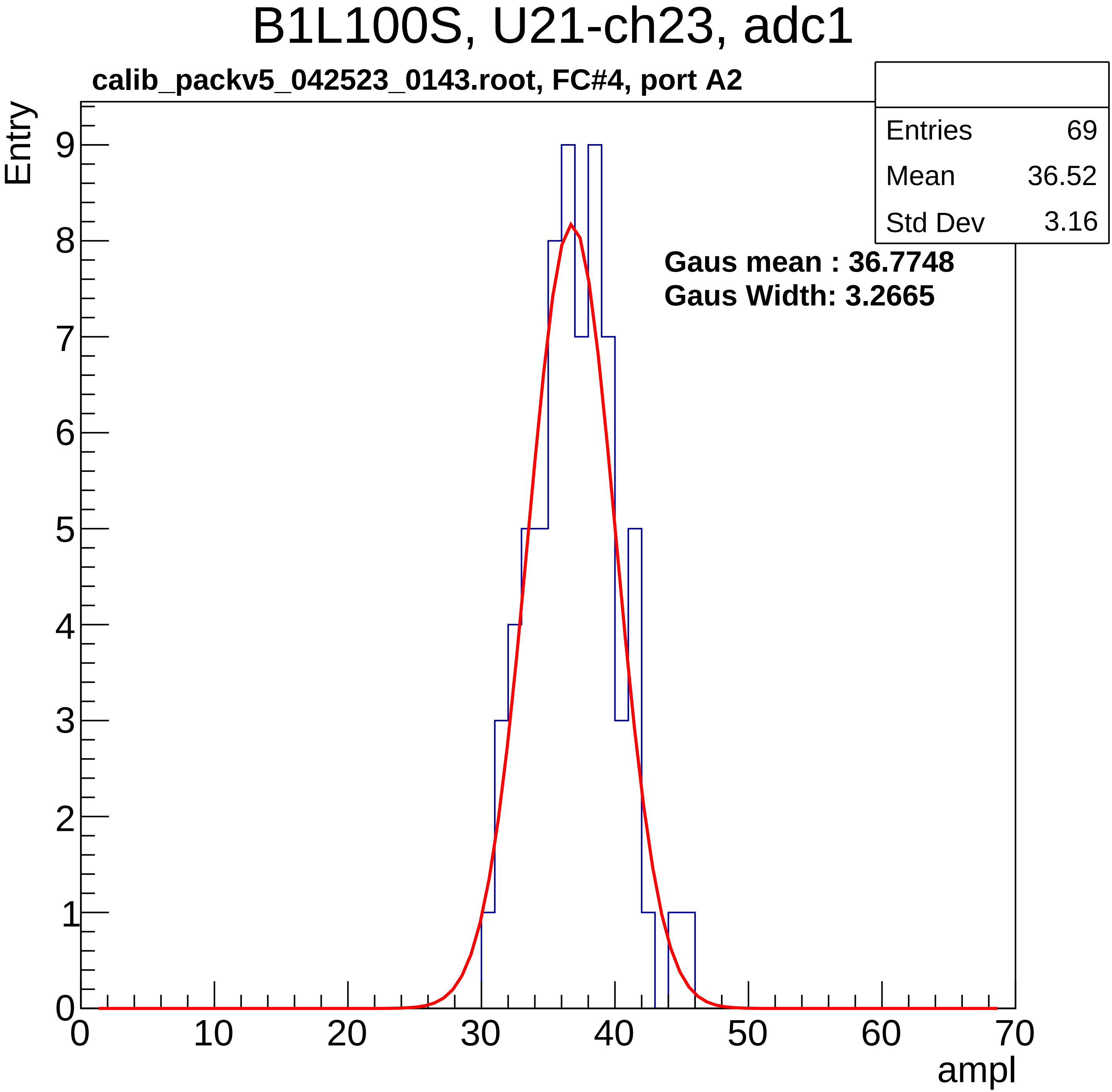


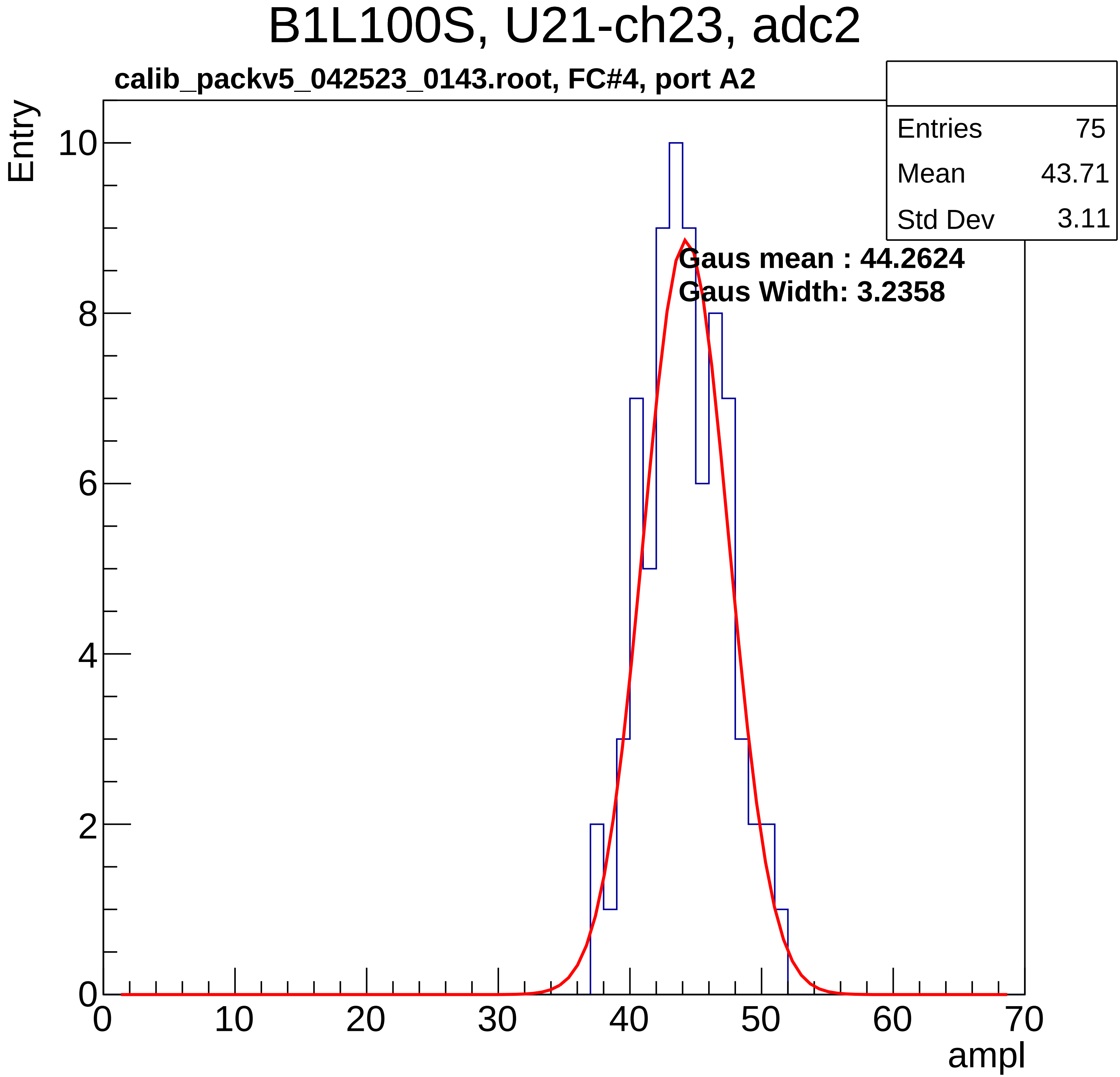

B1L100S, U21-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62
Std Dev 1.069
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

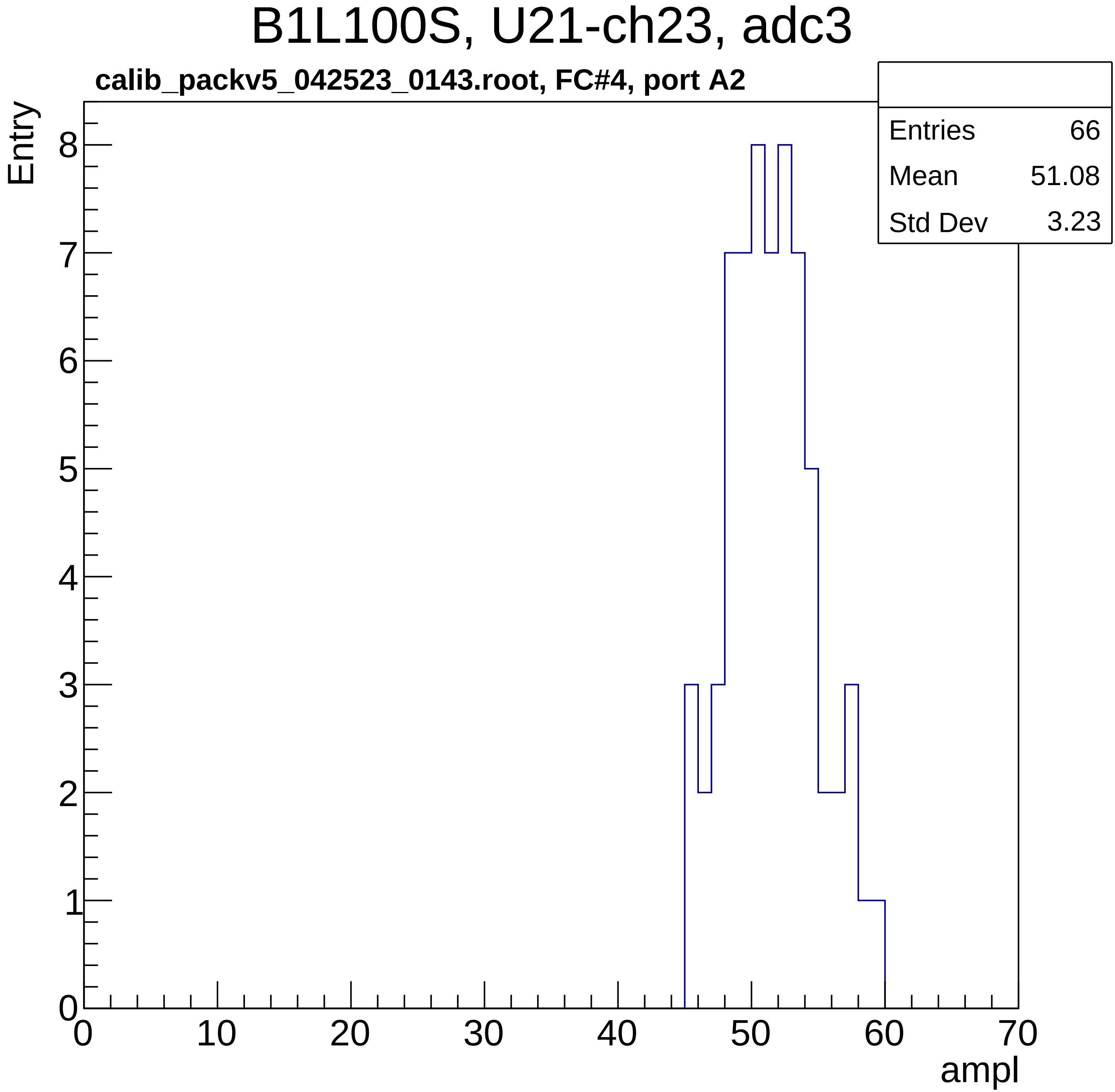
B1L100S, U21-ch22, adc7



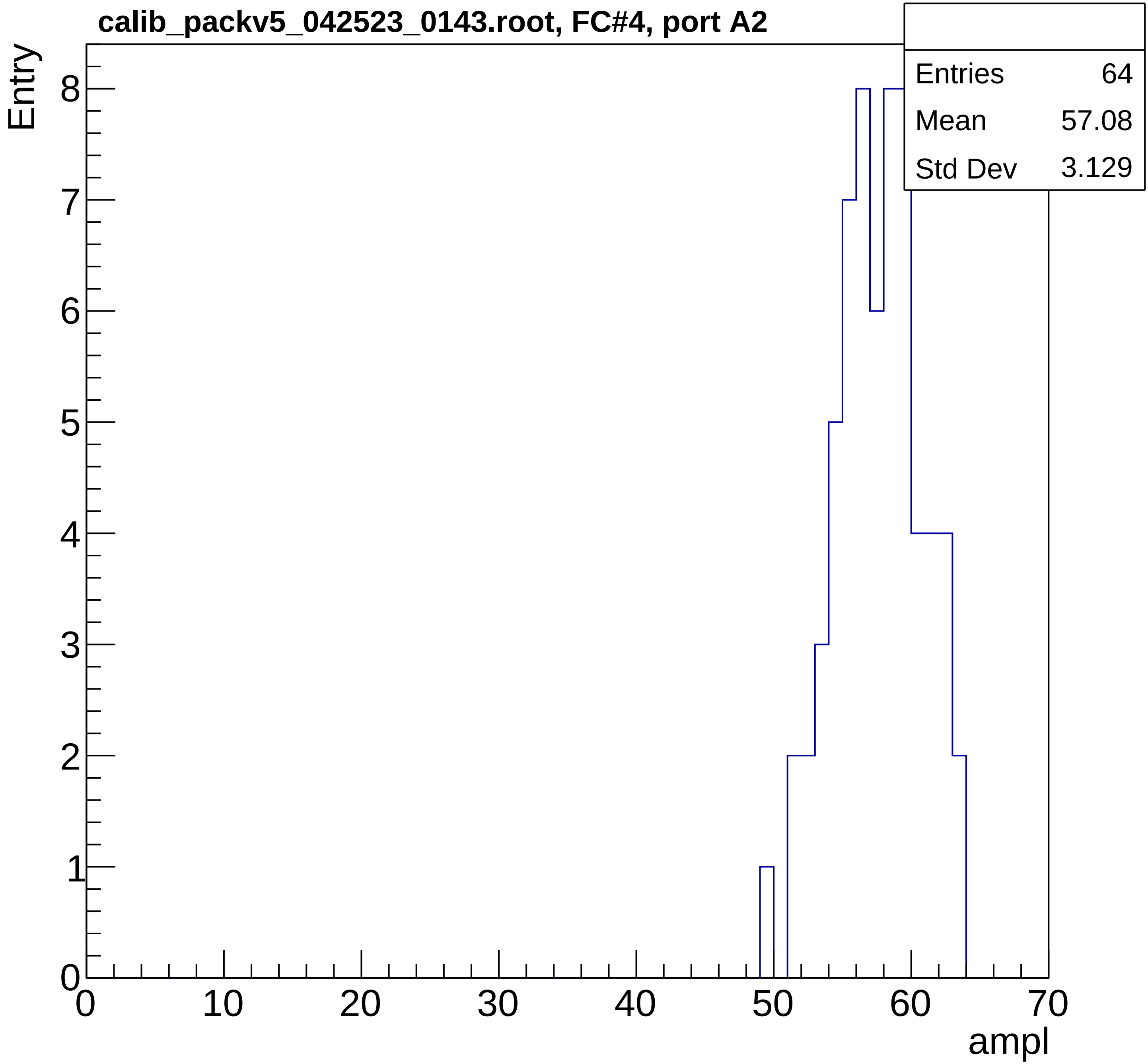

B1L100S, U21-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.89
Std Dev 7.104
Gaus mean : 30.7958
Gaus Width: 2.9902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.52
Std Dev 3.16
Gaus mean : 36.7748
Gaus Width: 3.2665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

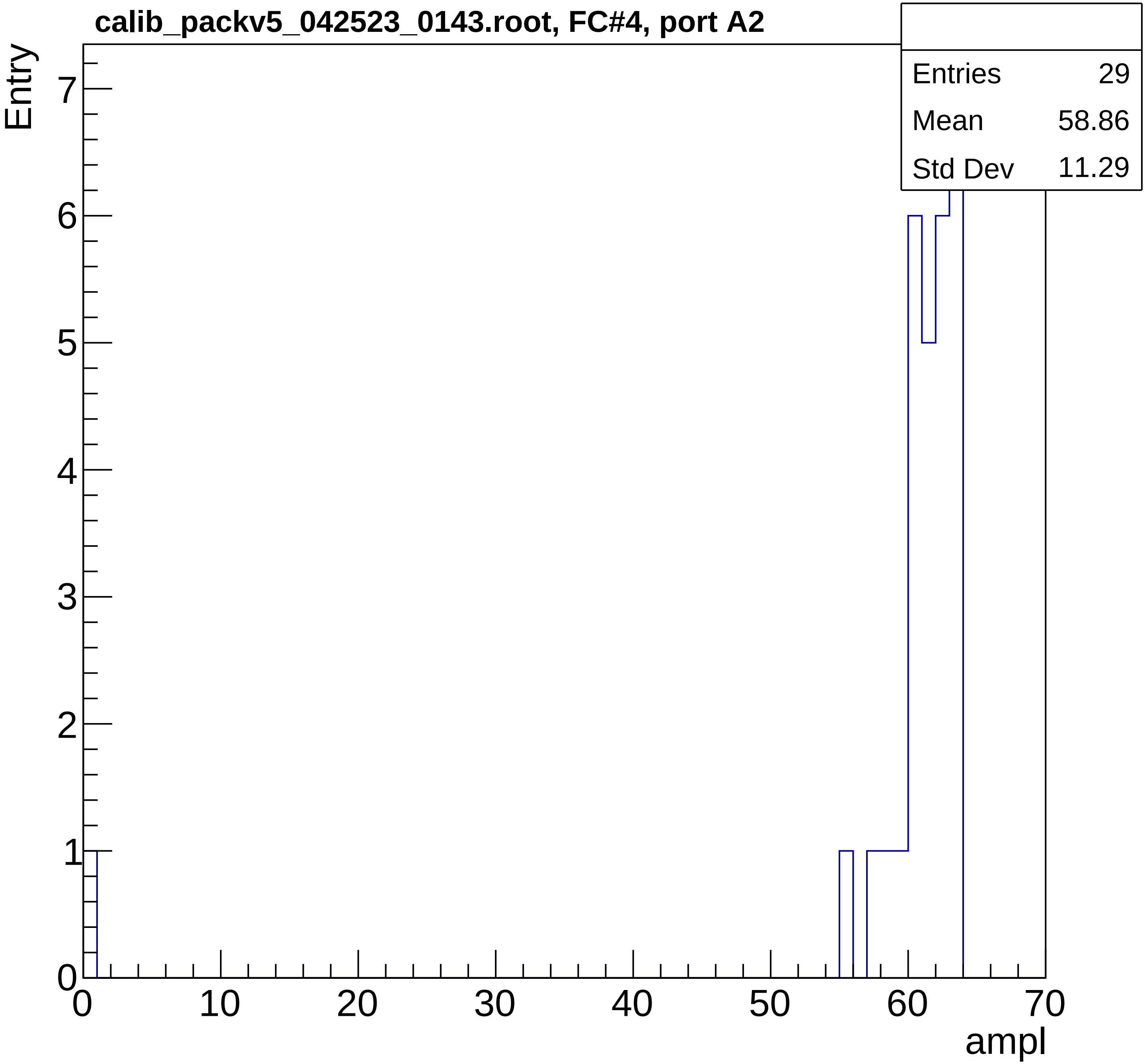

B1L100S, U21-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70
Entries 75
Mean 43.71
Std Dev 3.11
Gaus mean : 44.2624
Gaus Width: 3.2358

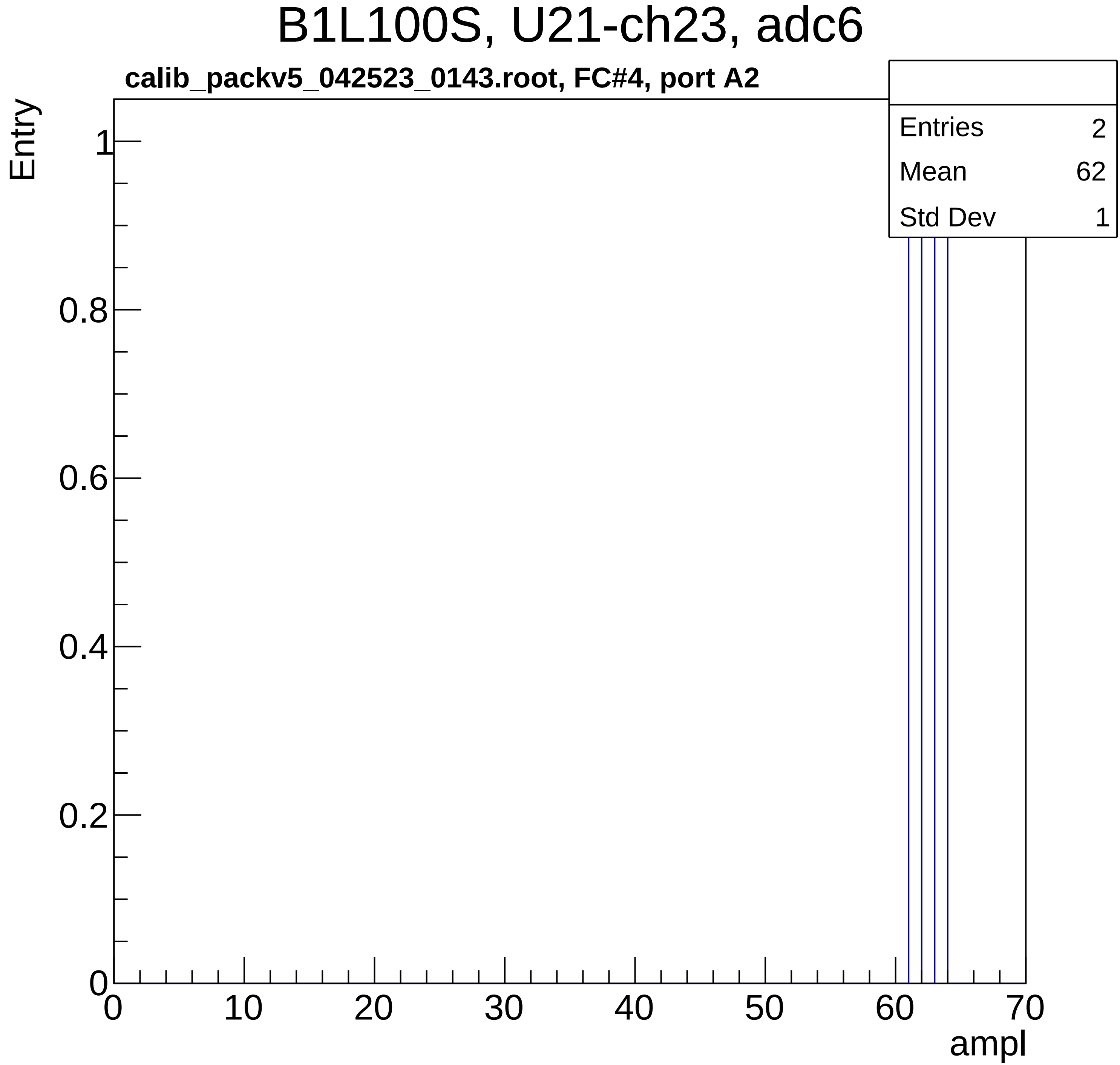

B1L100S, U21-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 51.08
Std Dev 3.23
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

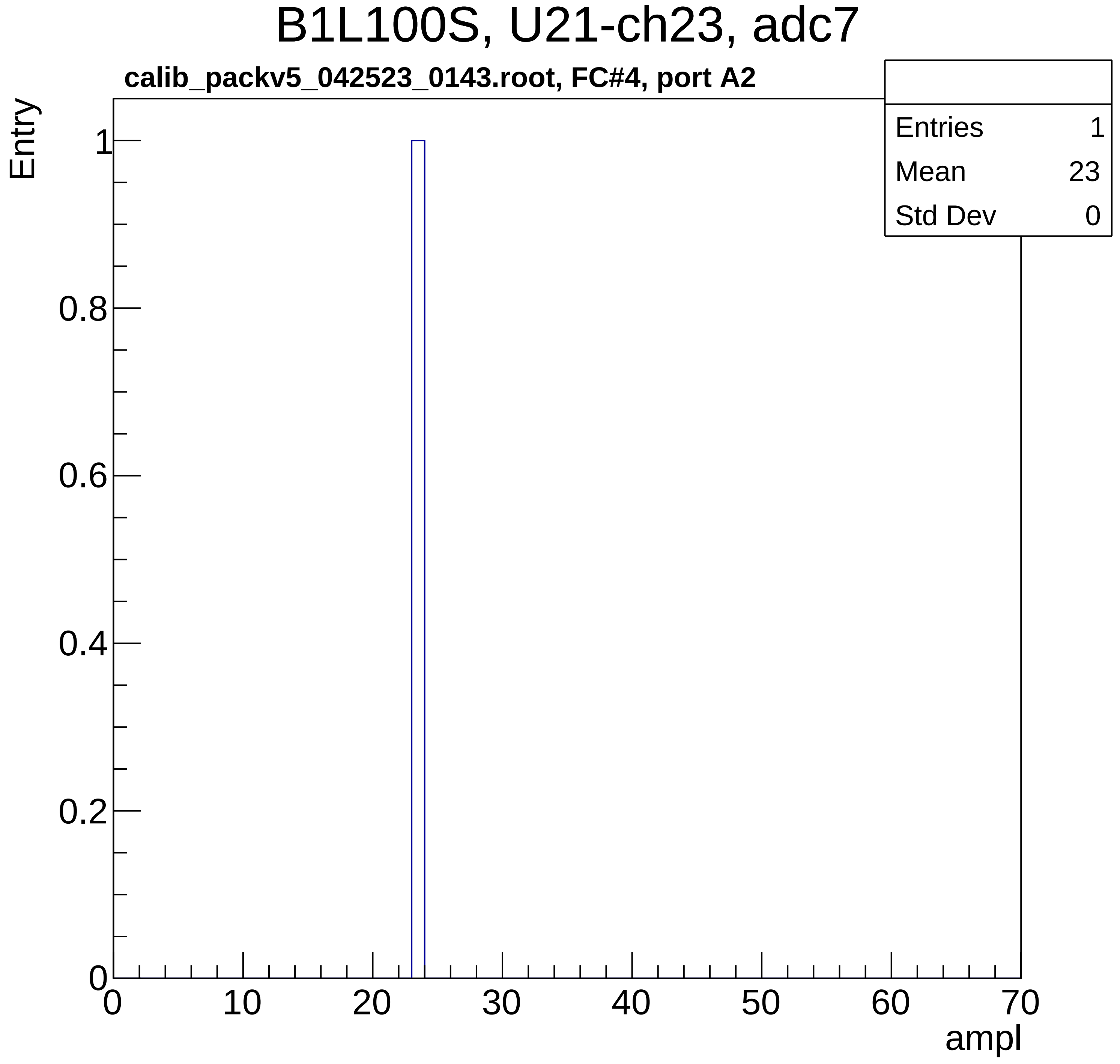
B1L100S, U21-ch23, adc4

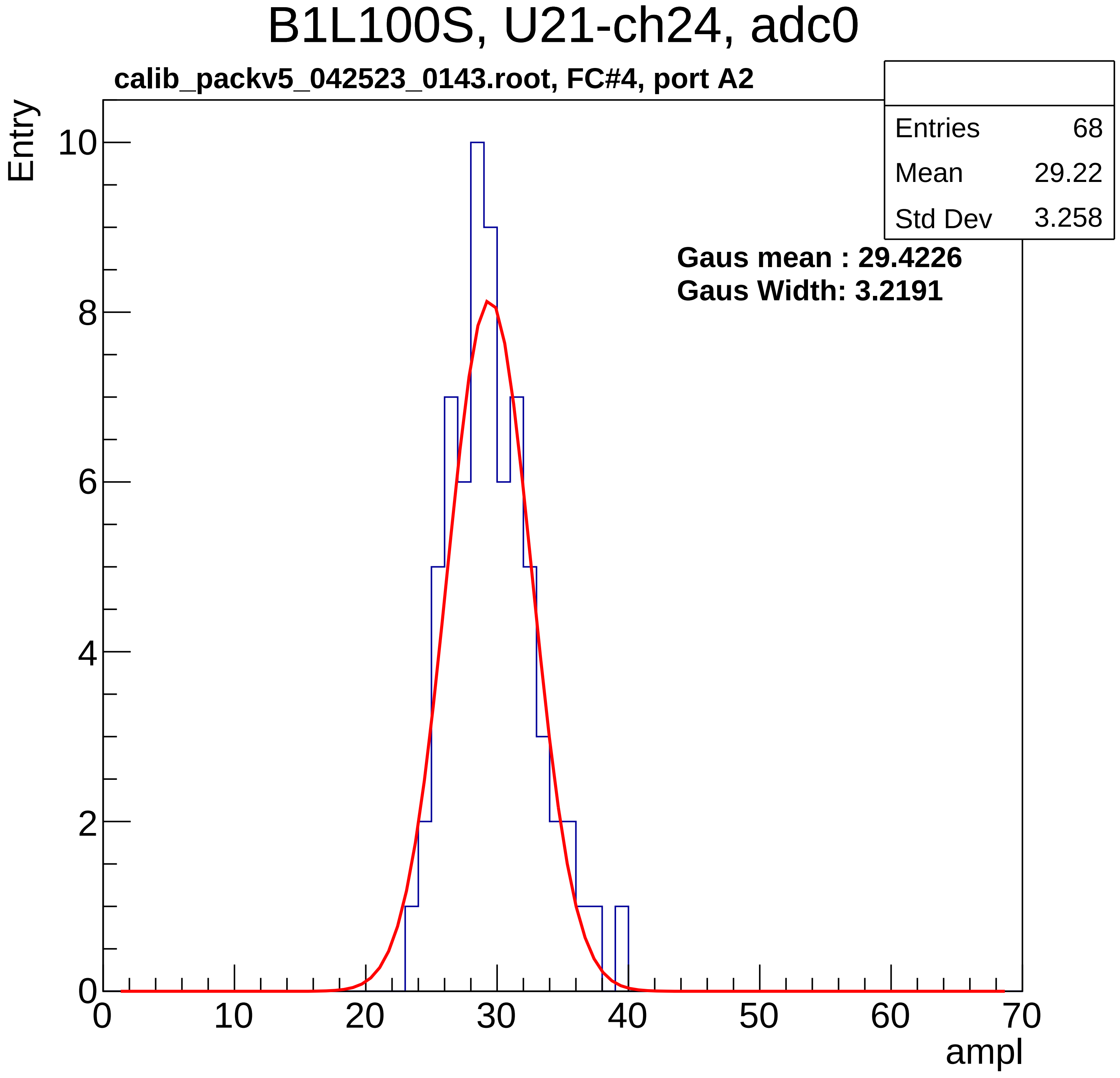


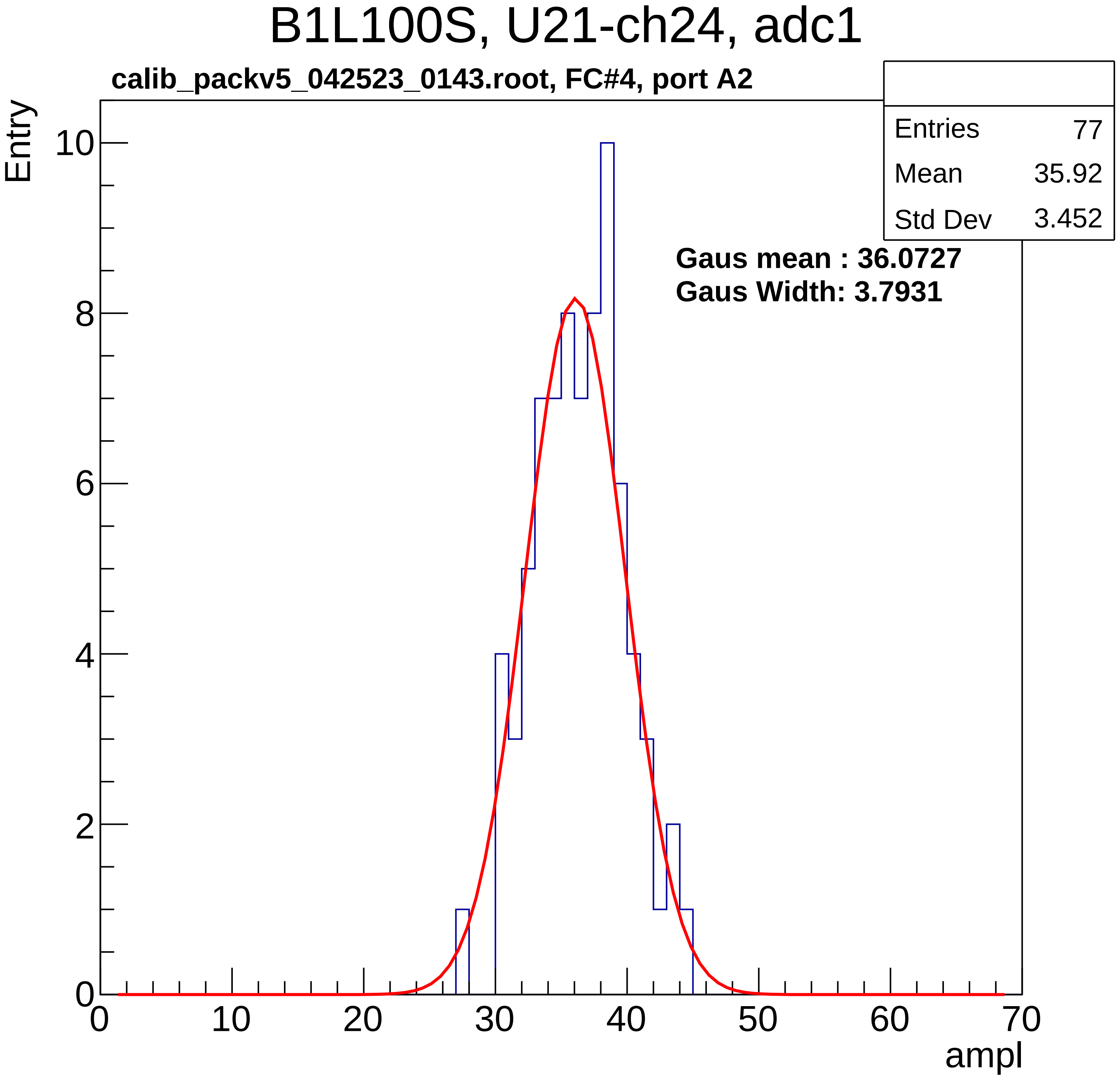
B1L100S, U21-ch23, adc5

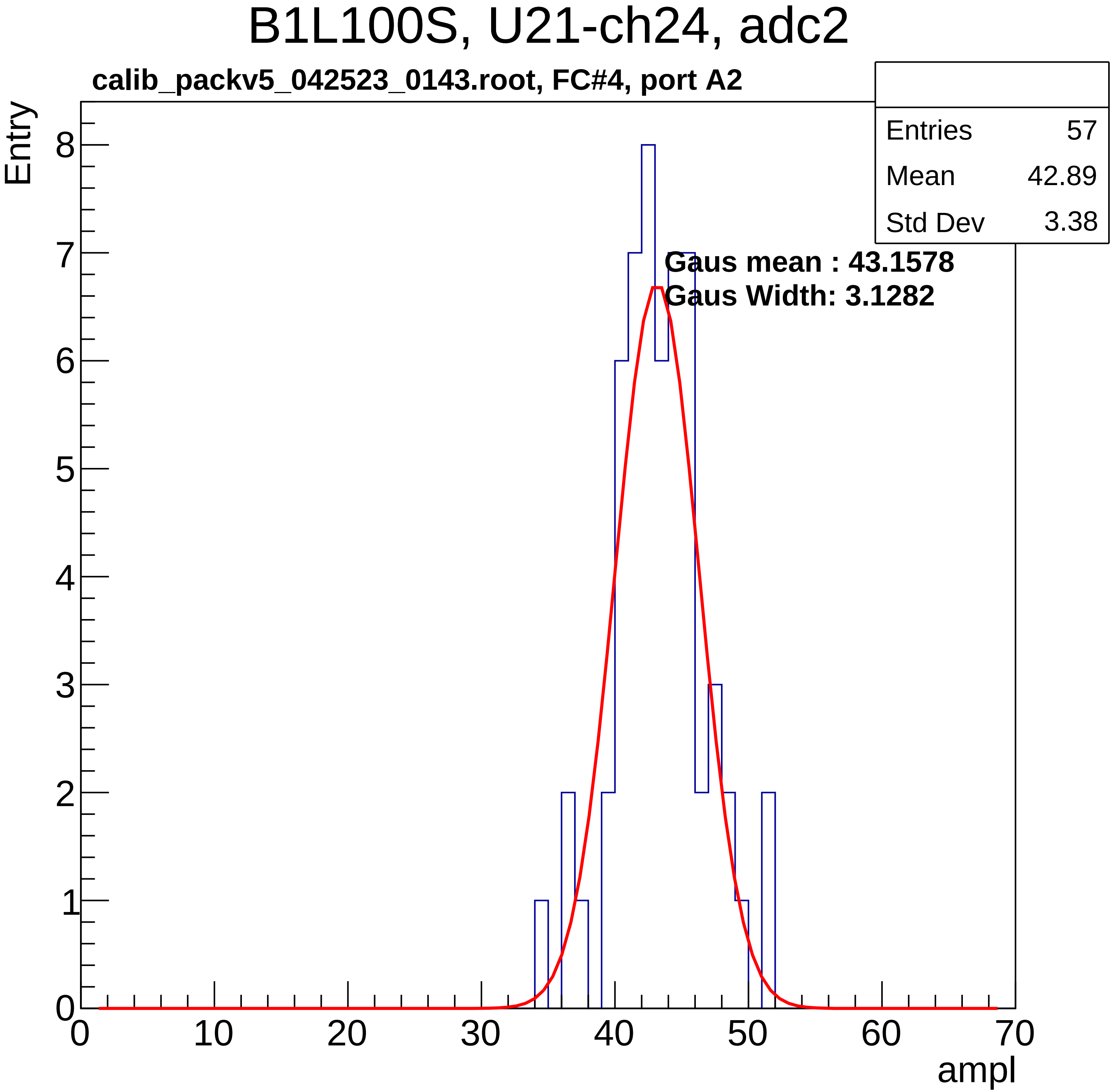



B1L100S, U21-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

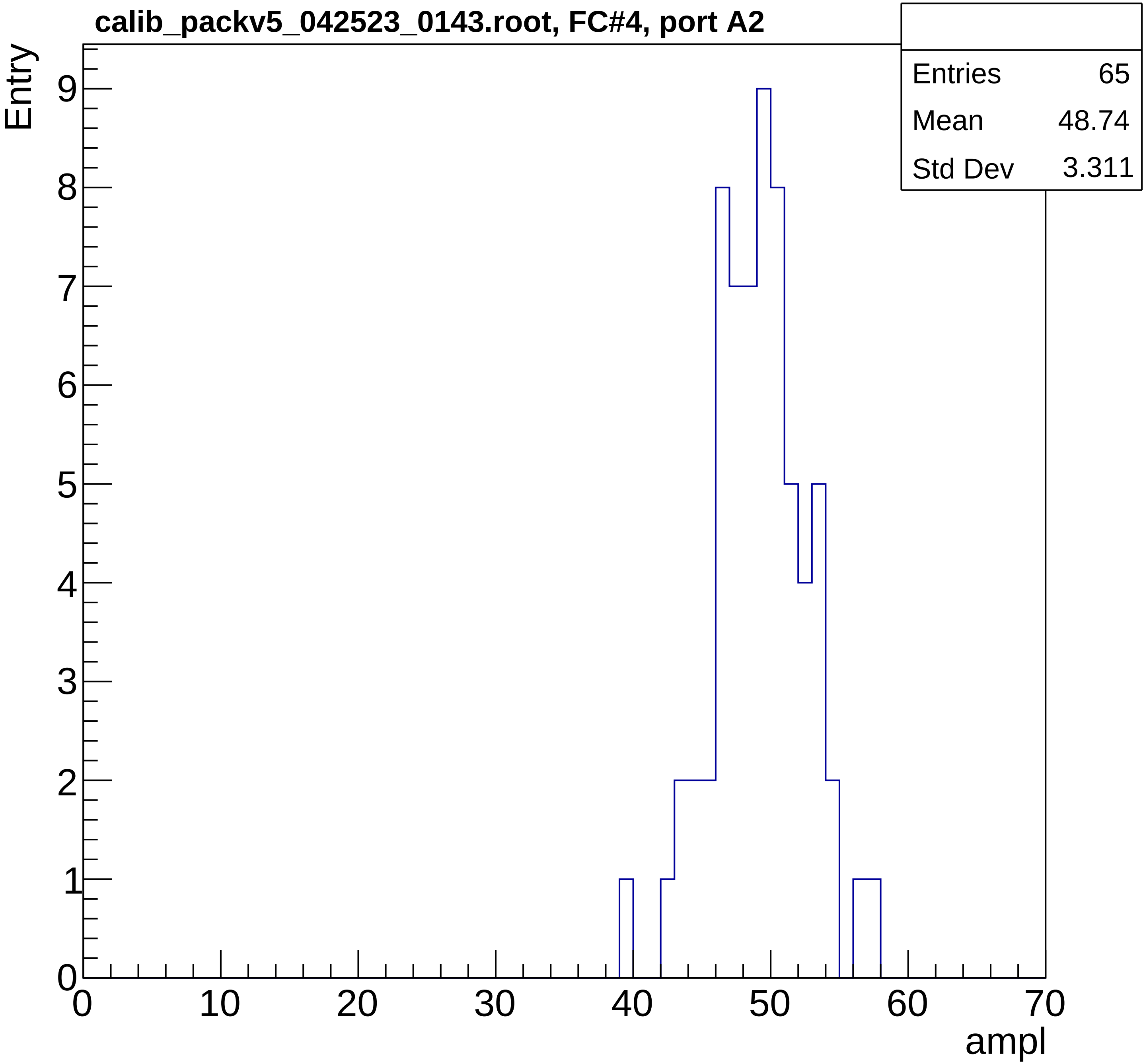

B1L100S, U21-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 29.22
Std Dev 3.258
Gaus mean : 29.4226
Gaus Width: 3.2191
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

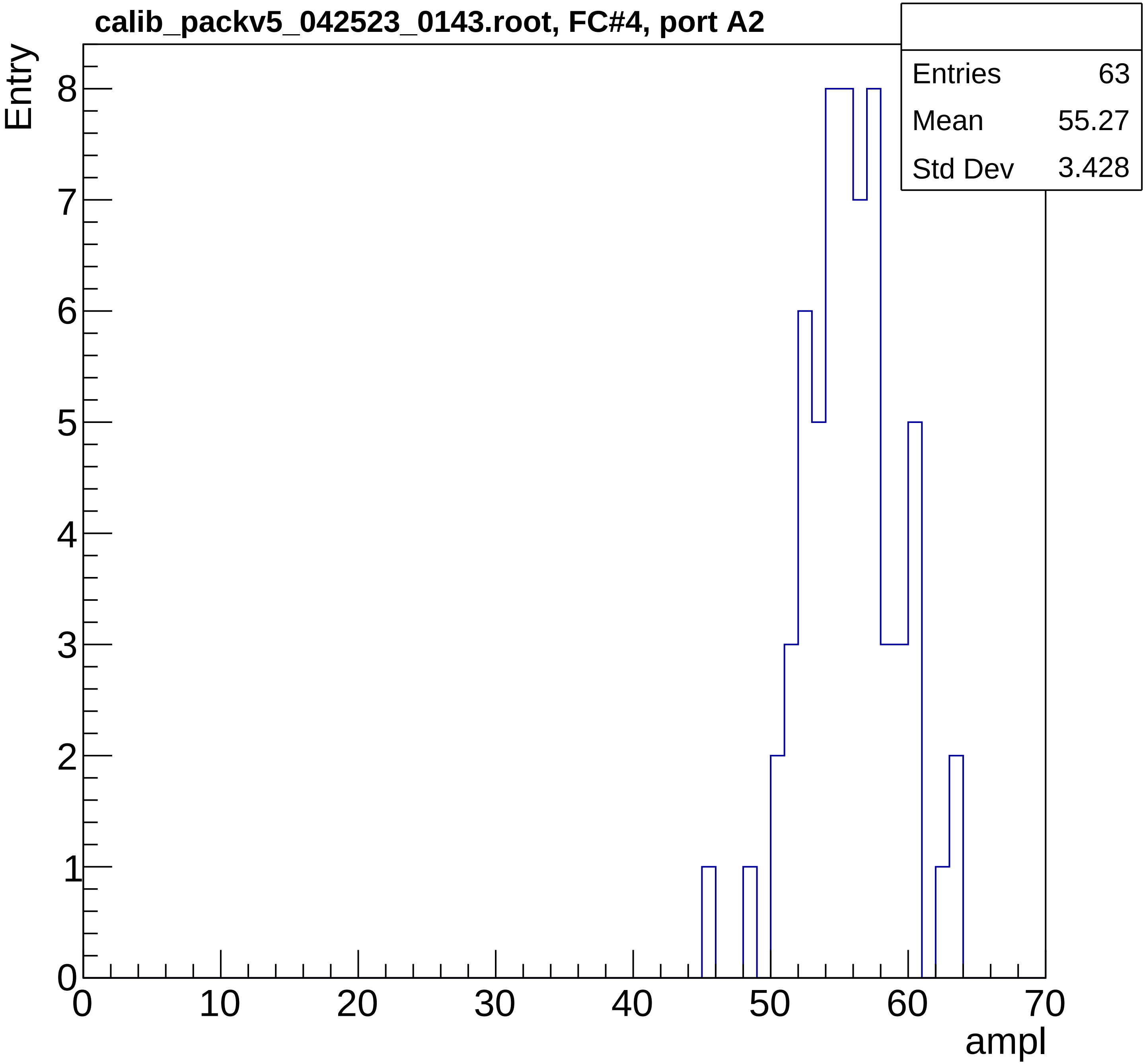

B1L100S, U21-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 35.92
Std Dev 3.452
Gaus mean : 36.0727
Gaus Width: 3.7931
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

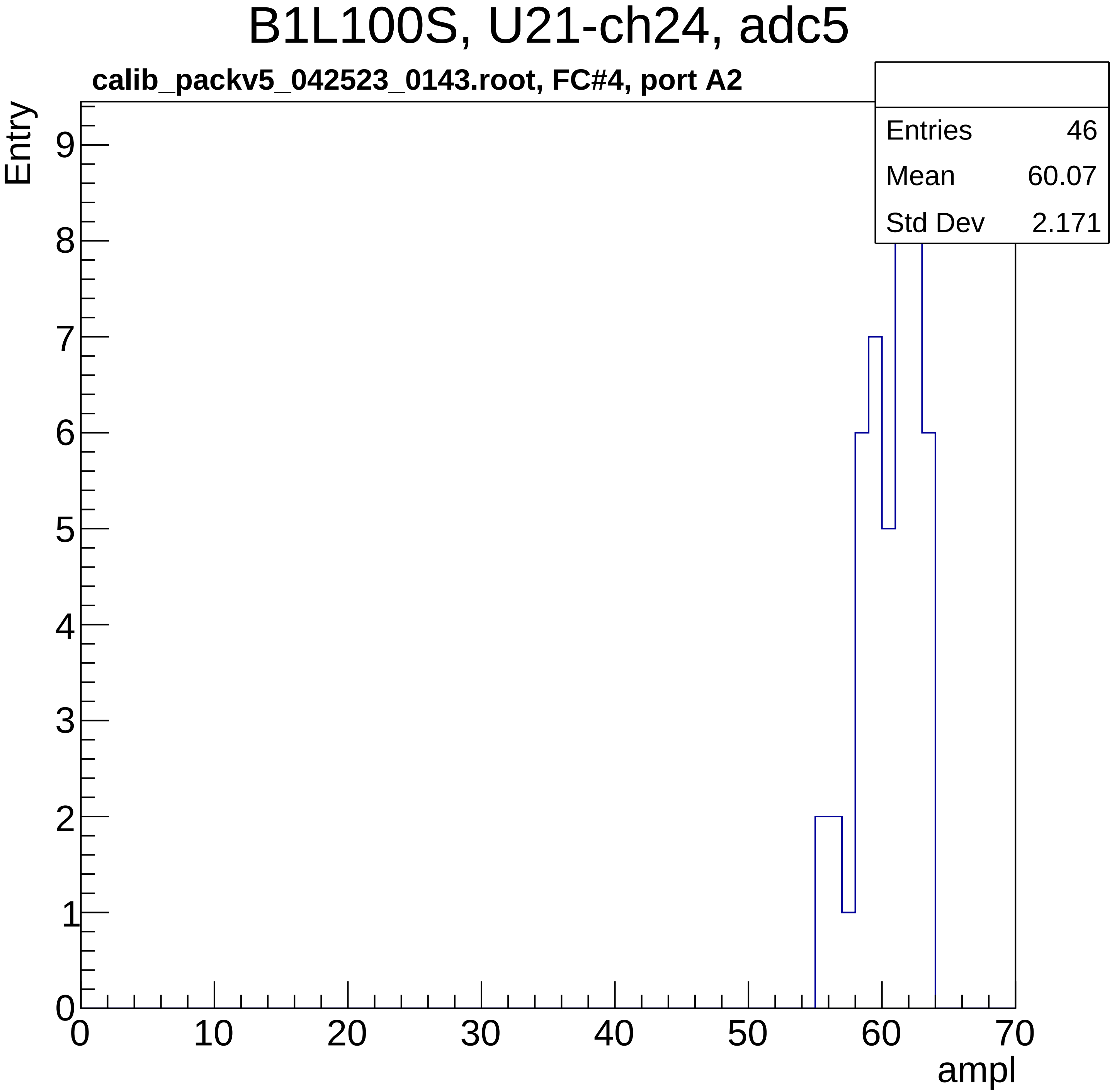

B1L100S, U21-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.89
Std Dev 3.38
Gaus mean : 43.1578
Gaus Width: 3.1282
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

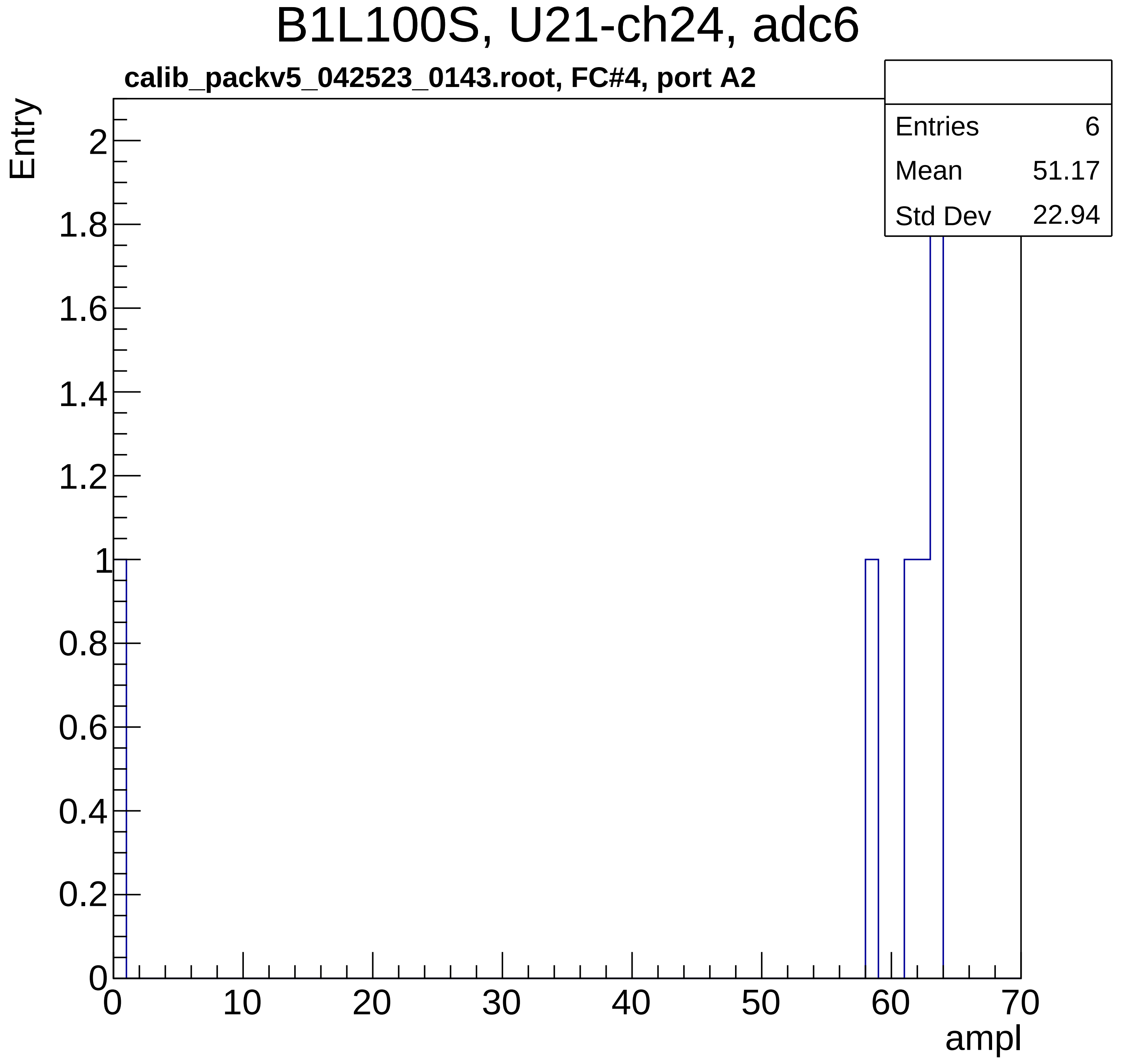
B1L100S, U21-ch24, adc3



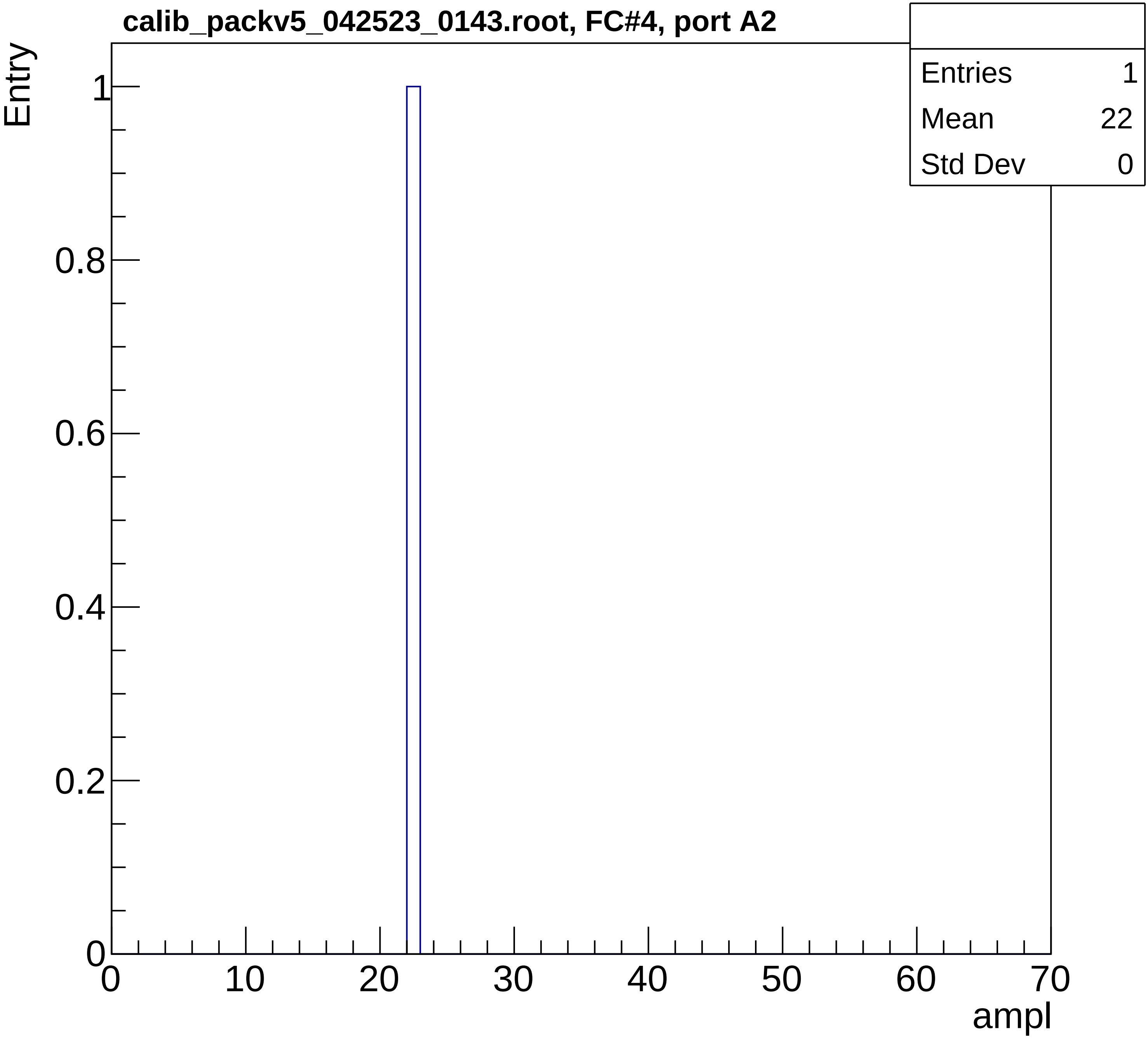
B1L100S, U21-ch24, adc4

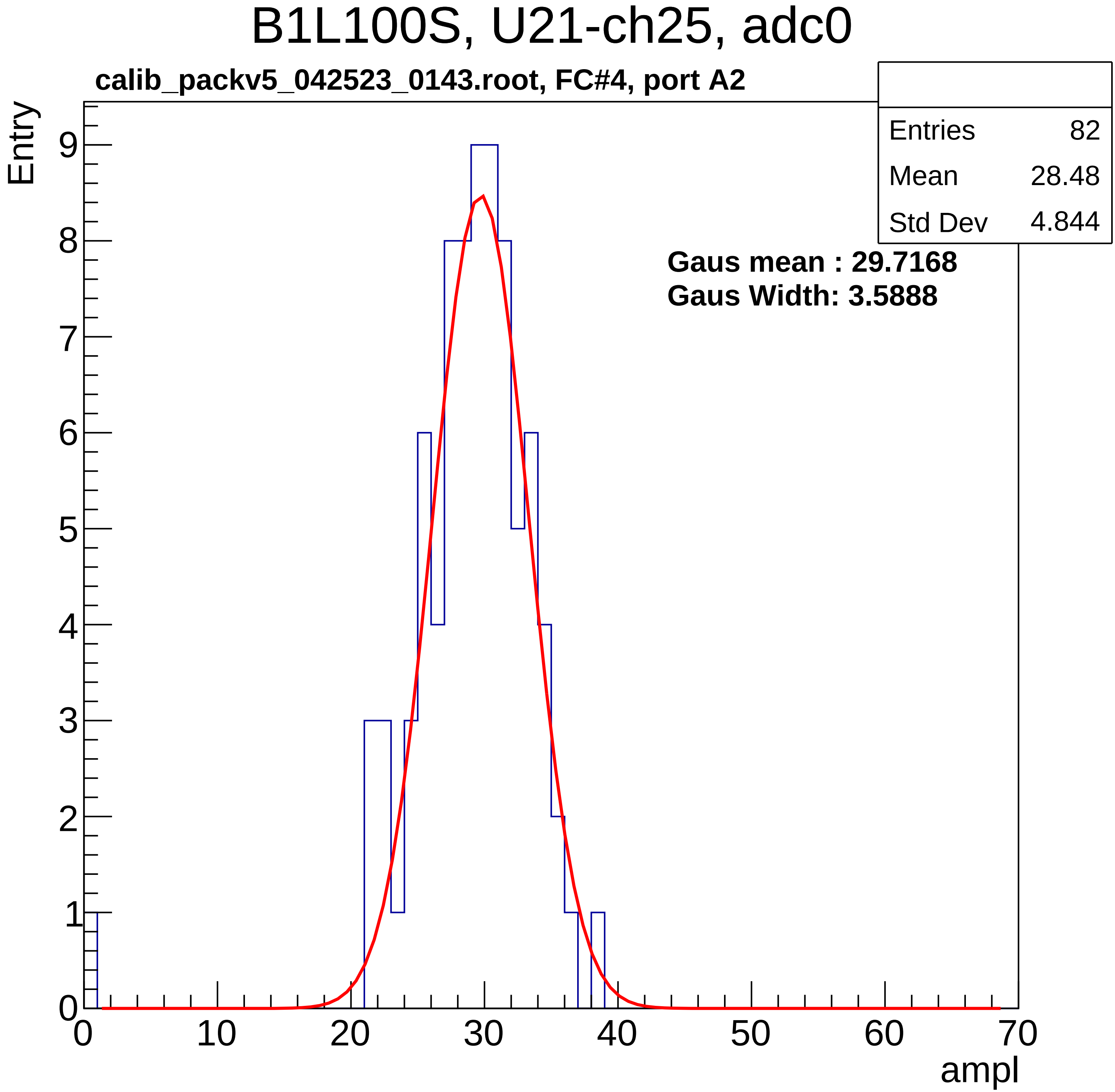


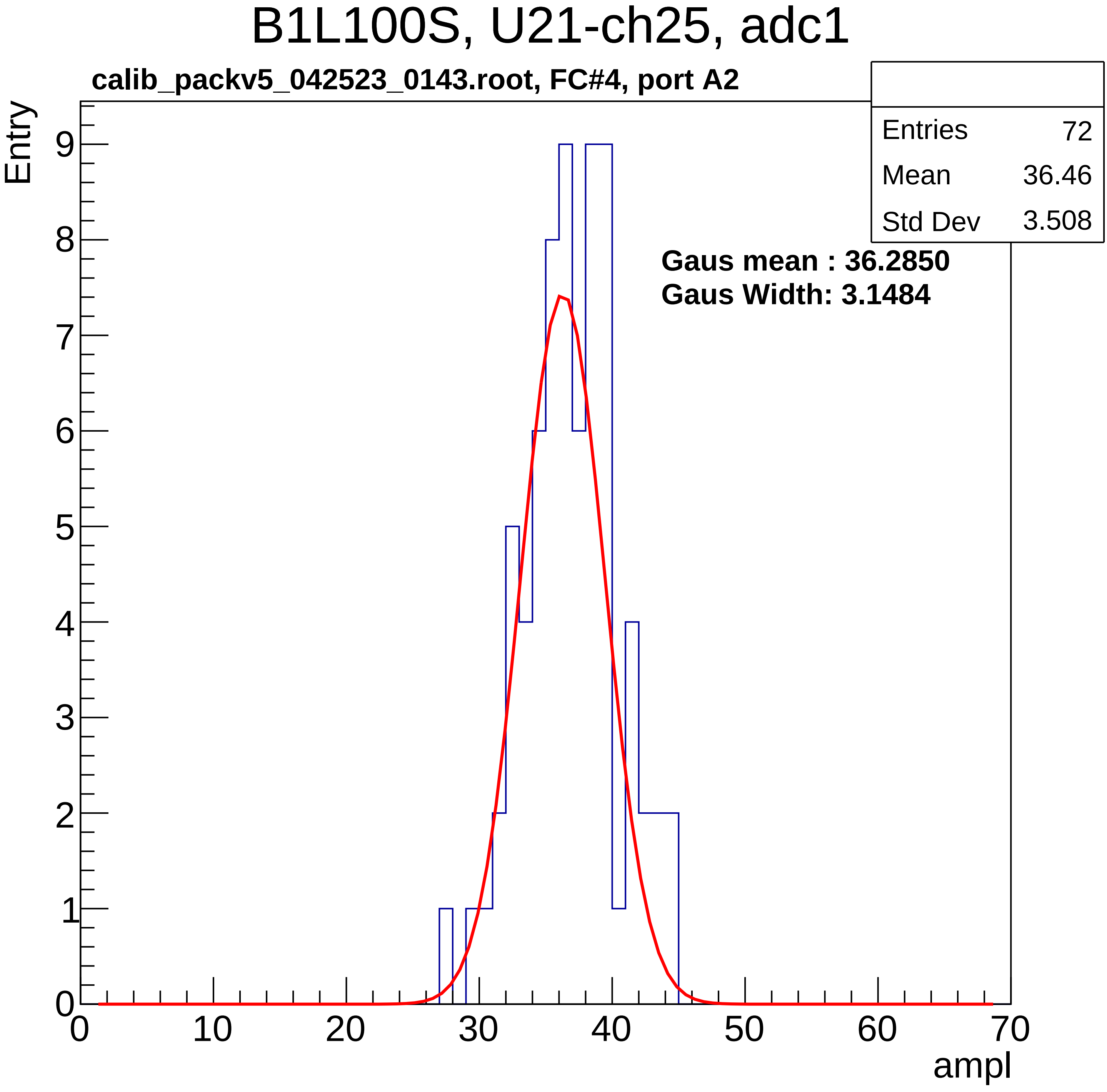

B1L100S, U21-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 60.07
Std Dev 2.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

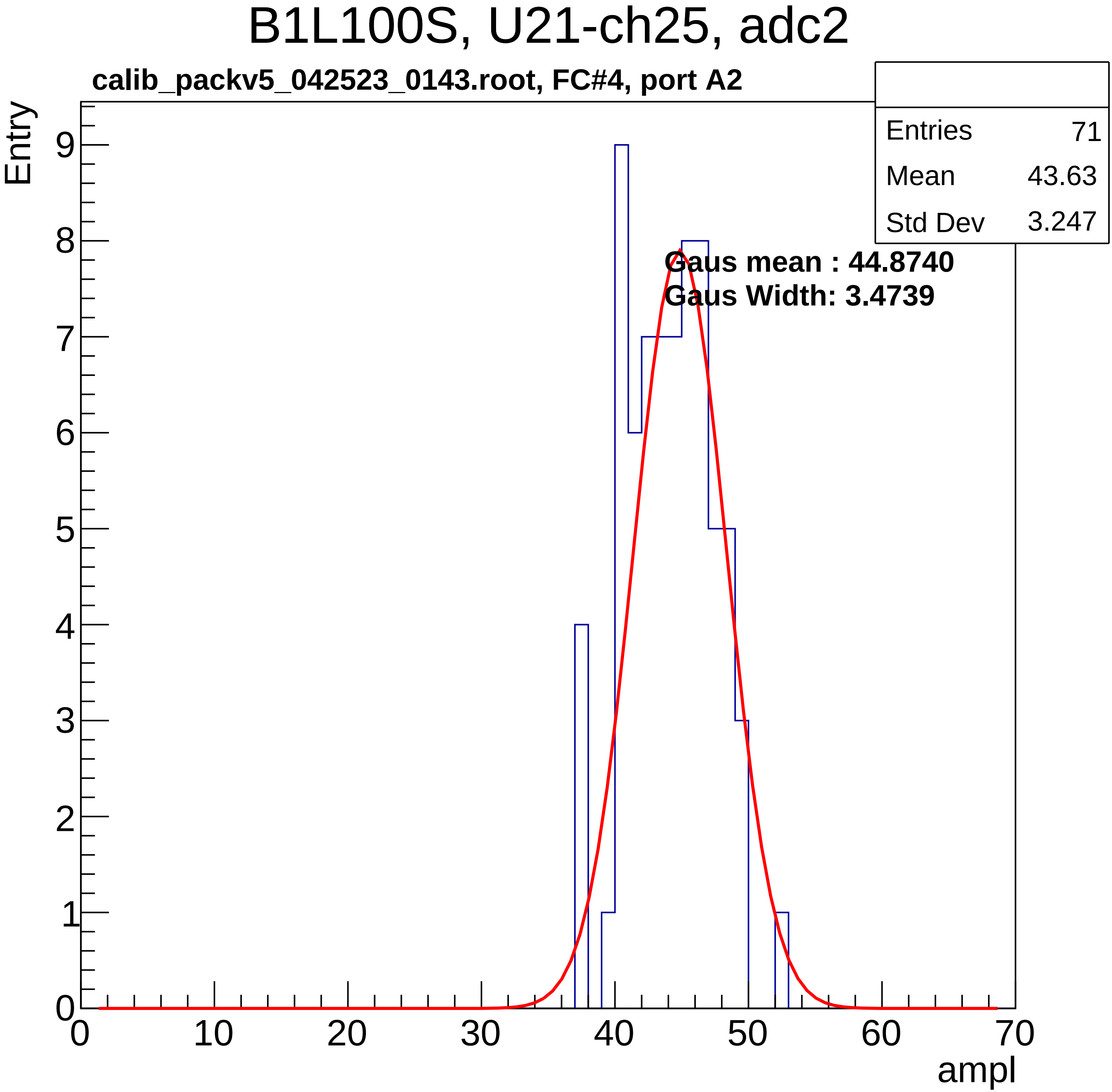

B1L100S, U21-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.17
Std Dev 22.94
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch24, adc7

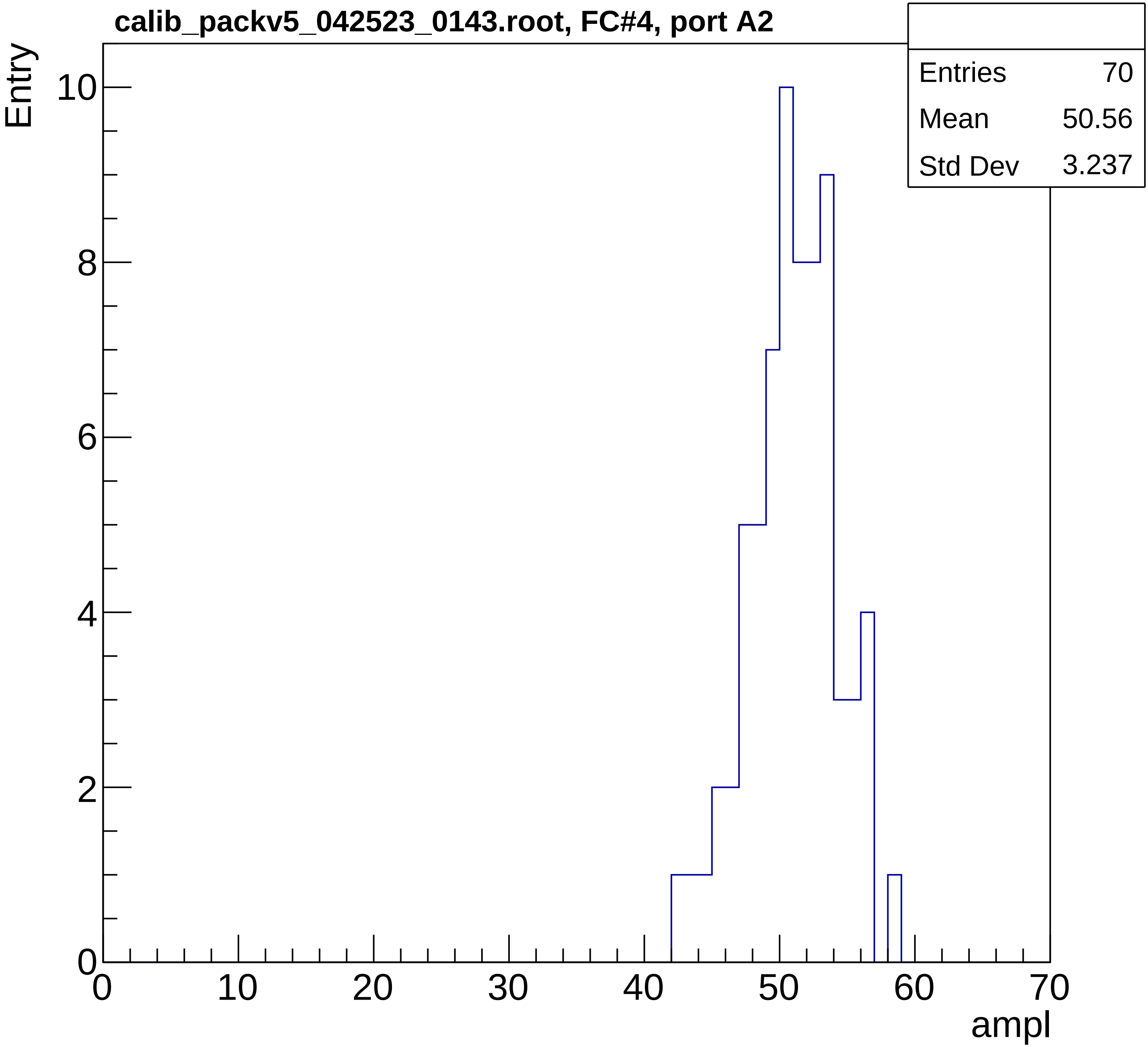


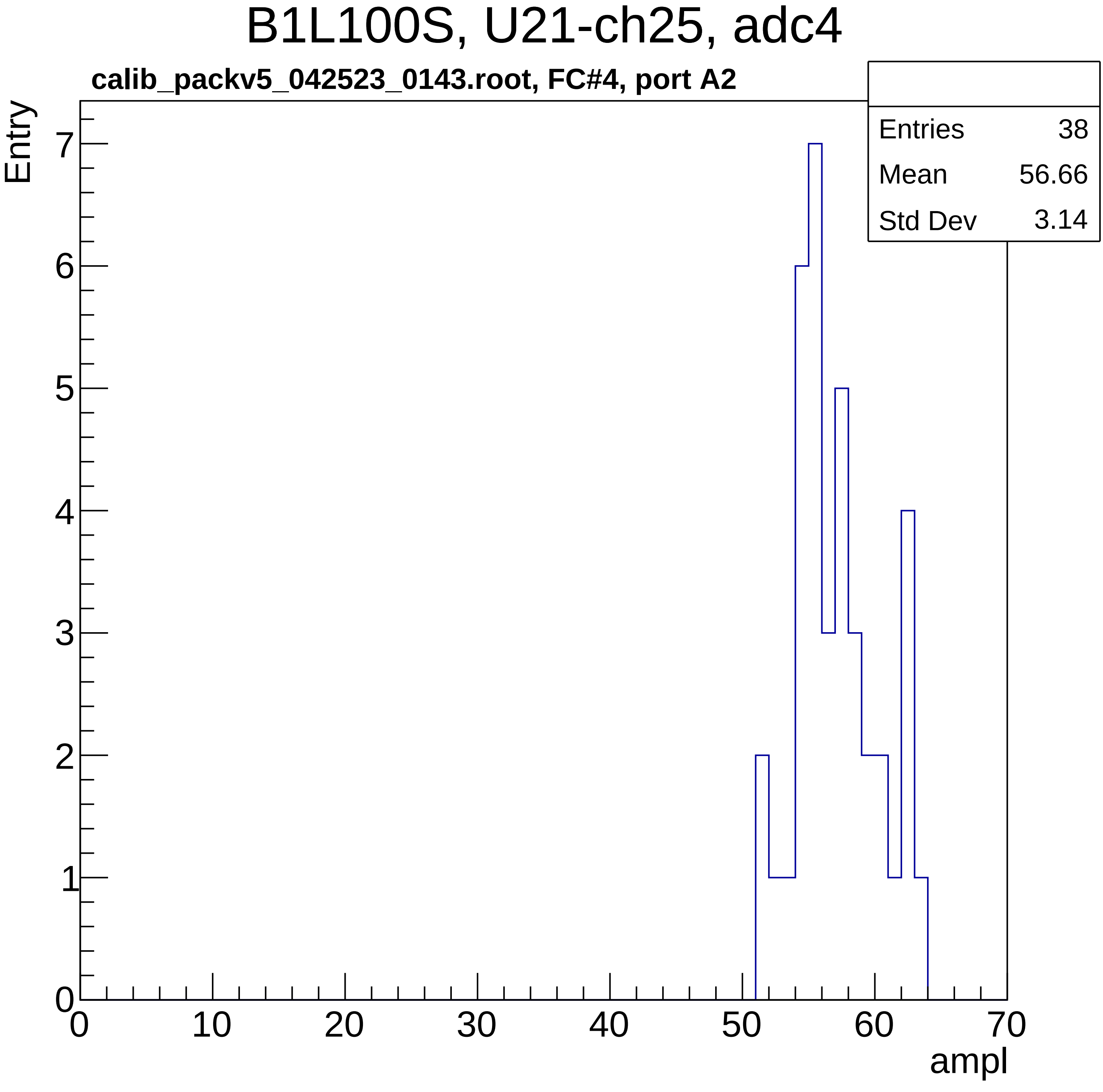

B1L100S, U21-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 28.48
Std Dev 4.844
Gaus mean : 29.7168
Gaus Width: 3.5888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.46
Std Dev 3.508
Gaus mean : 36.2850
Gaus Width: 3.1484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

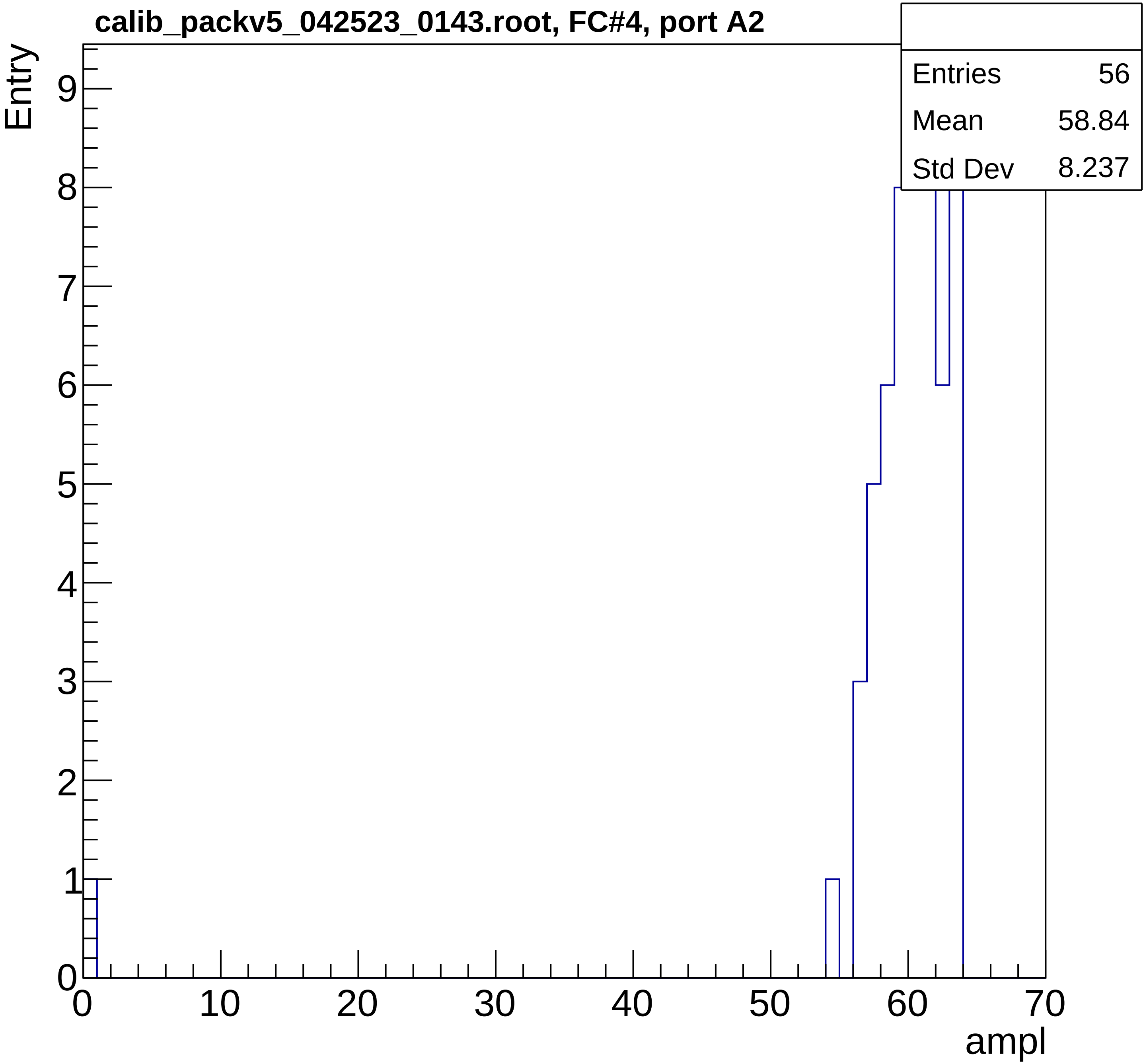

B1L100S, U21-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 43.63
Std Dev 3.247
Gaus mean : 44.8740
Gaus Width: 3.4739
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

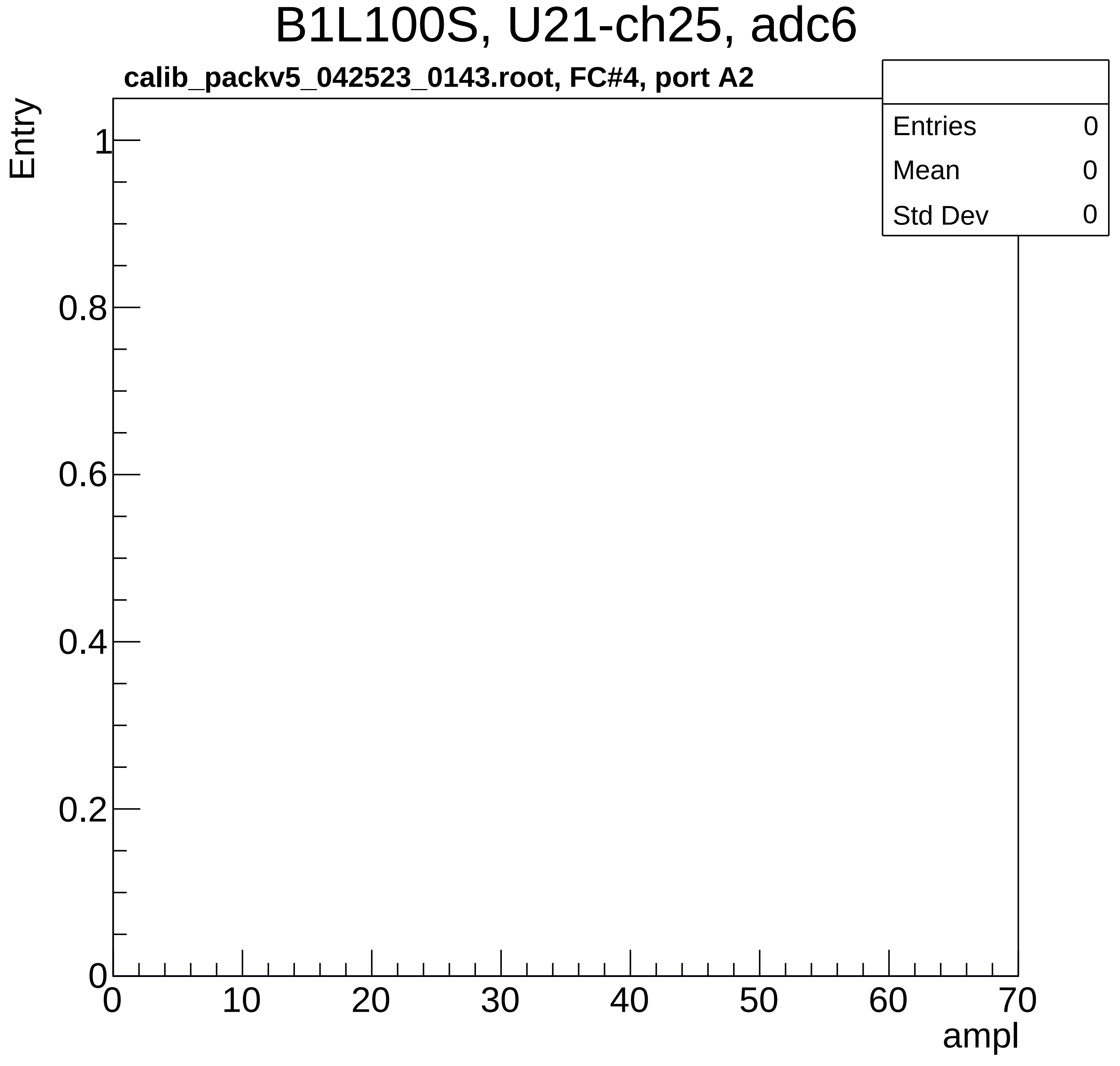
B1L100S, U21-ch25, adc3




B1L100S, U21-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 56.66
Std Dev 3.14
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

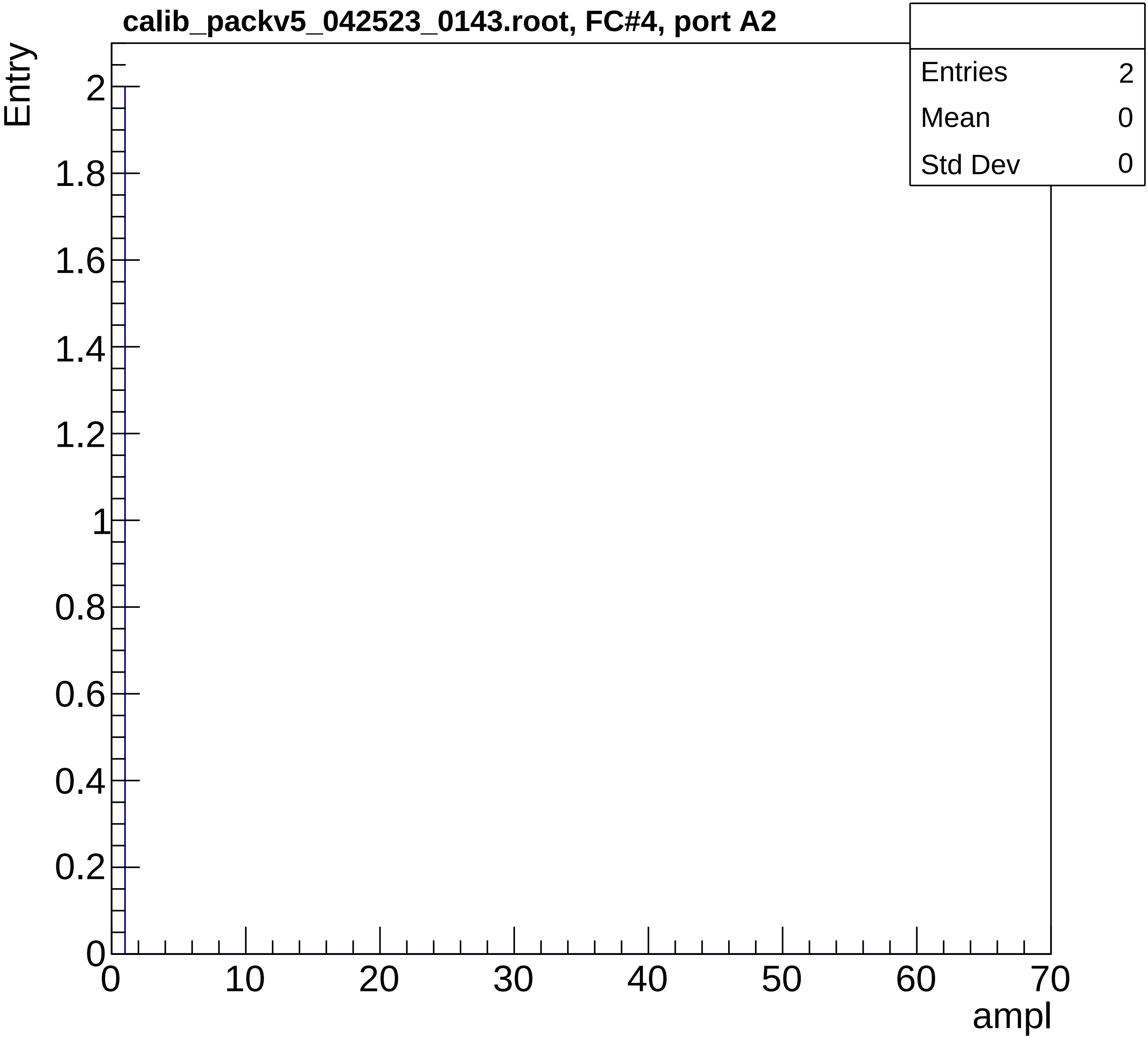
B1L100S, U21-ch25, adc5

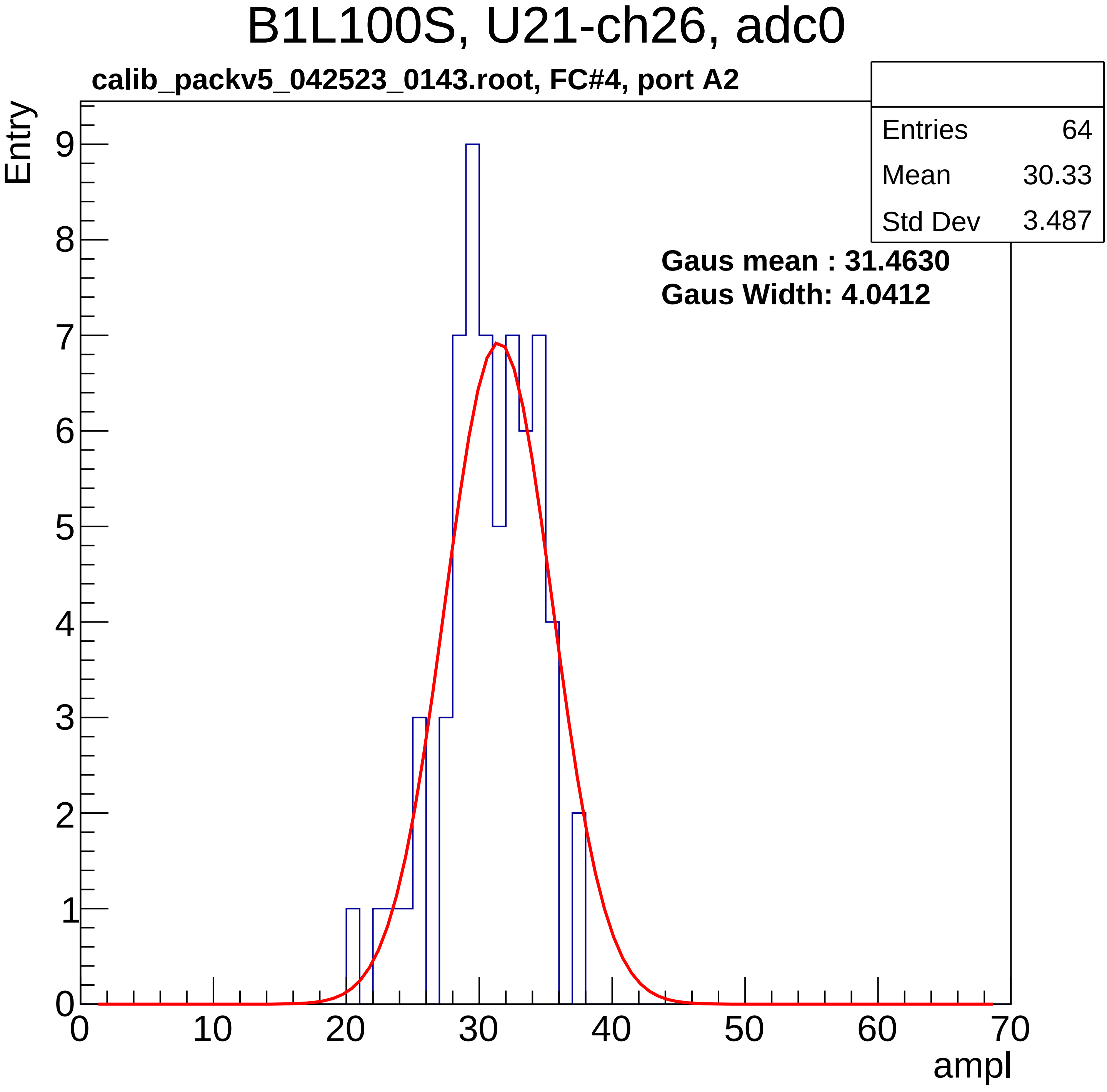


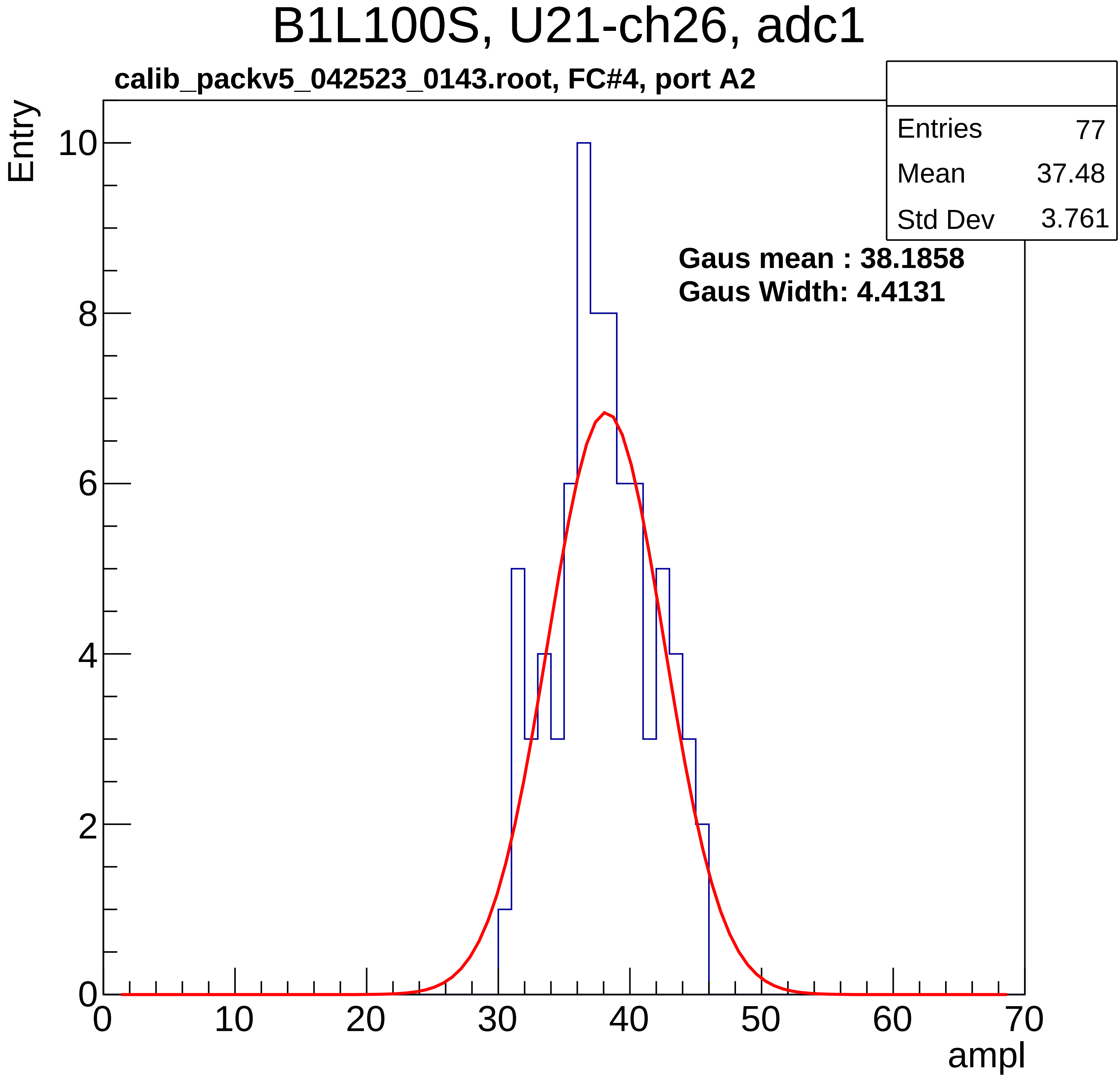

B1L100S, U21-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

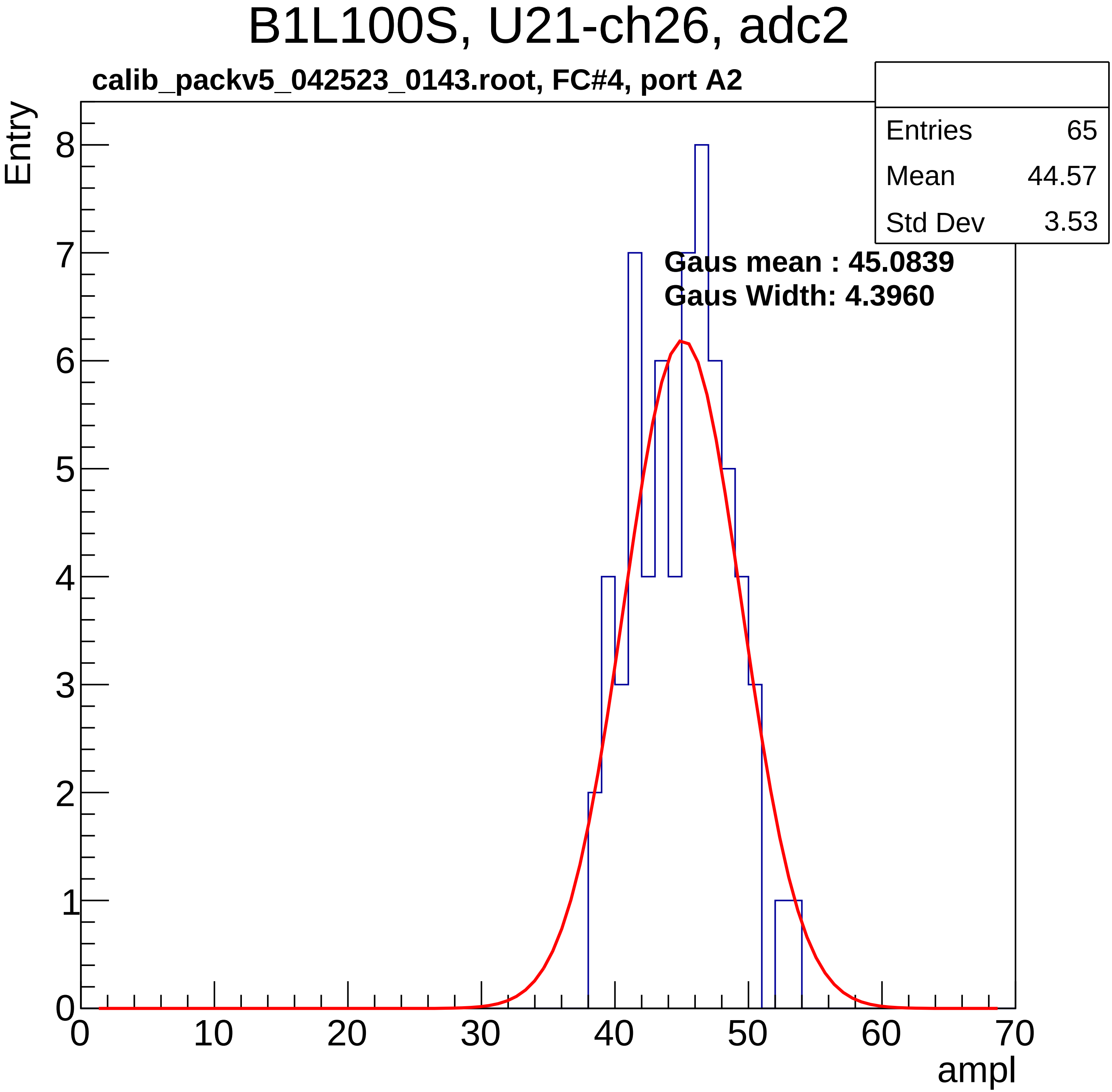
B1L100S, U21-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2



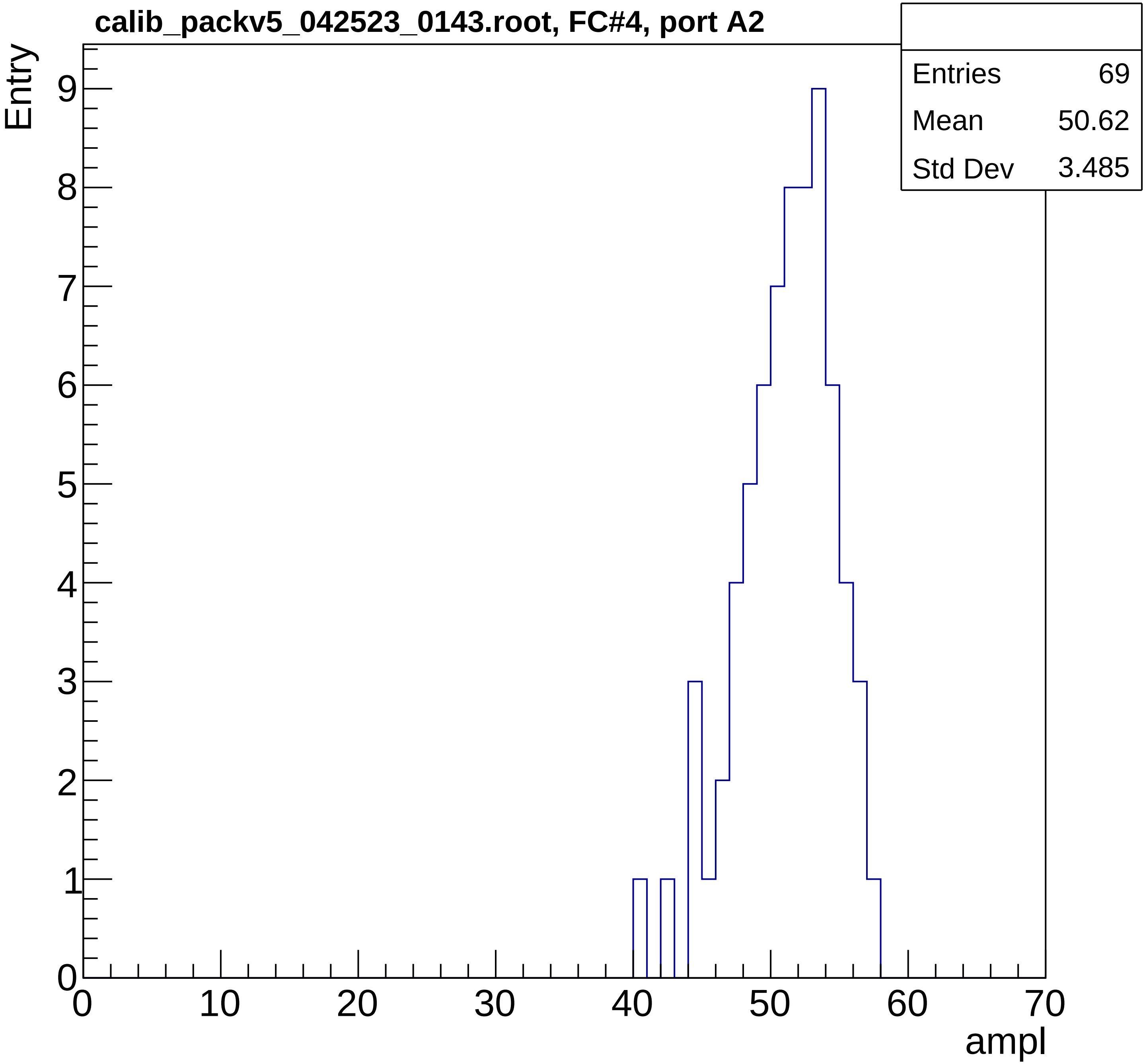

B1L100S, U21-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.33
Std Dev 3.487
Gaus mean : 31.4630
Gaus Width: 4.0412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

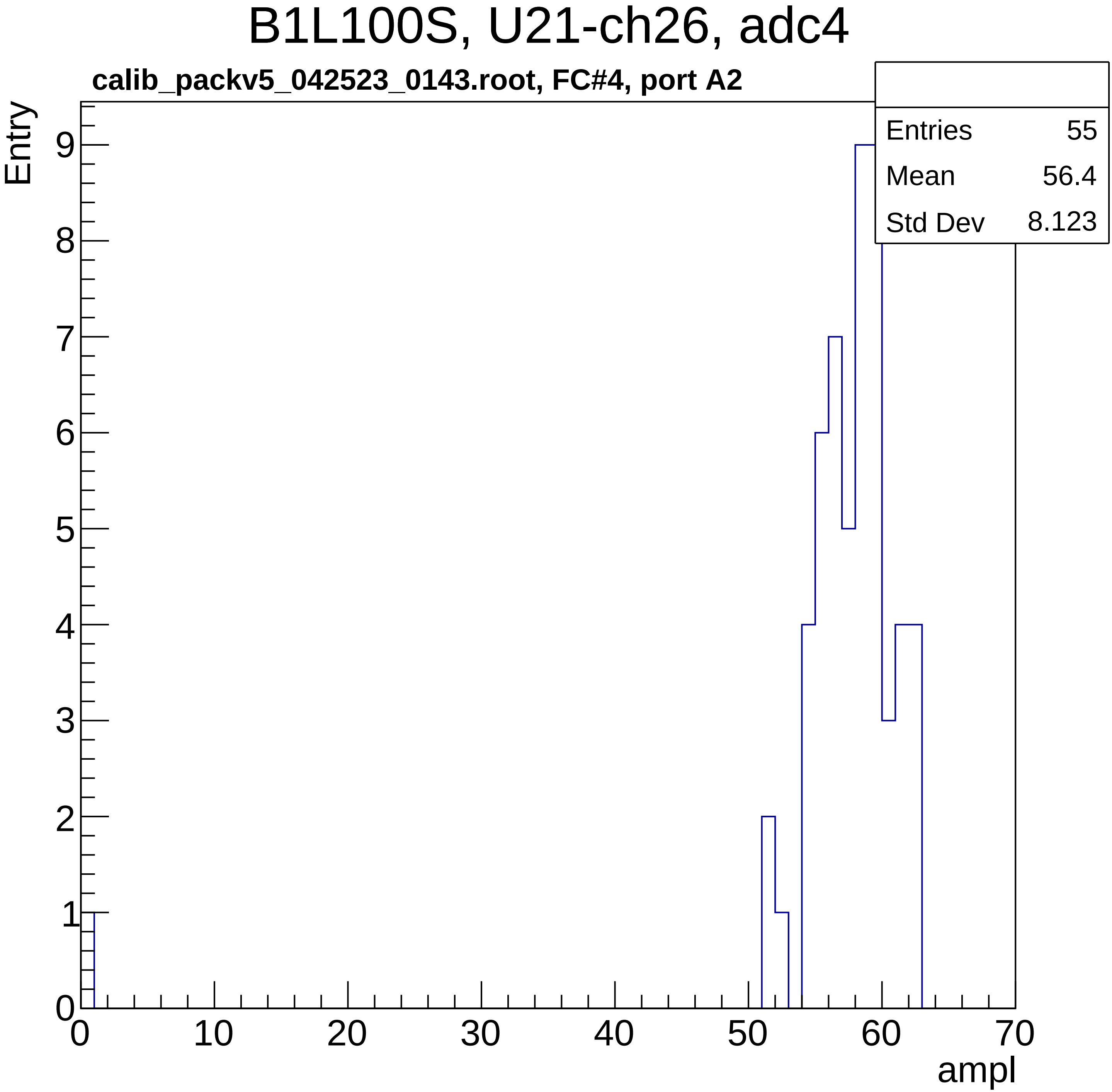

B1L100S, U21-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.48
Std Dev 3.761
Gaus mean : 38.1858
Gaus Width: 4.4131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.57
Std Dev 3.53
Gaus mean : 45.0839
Gaus Width: 4.3960
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

B1L100S, U21-ch26, adc3

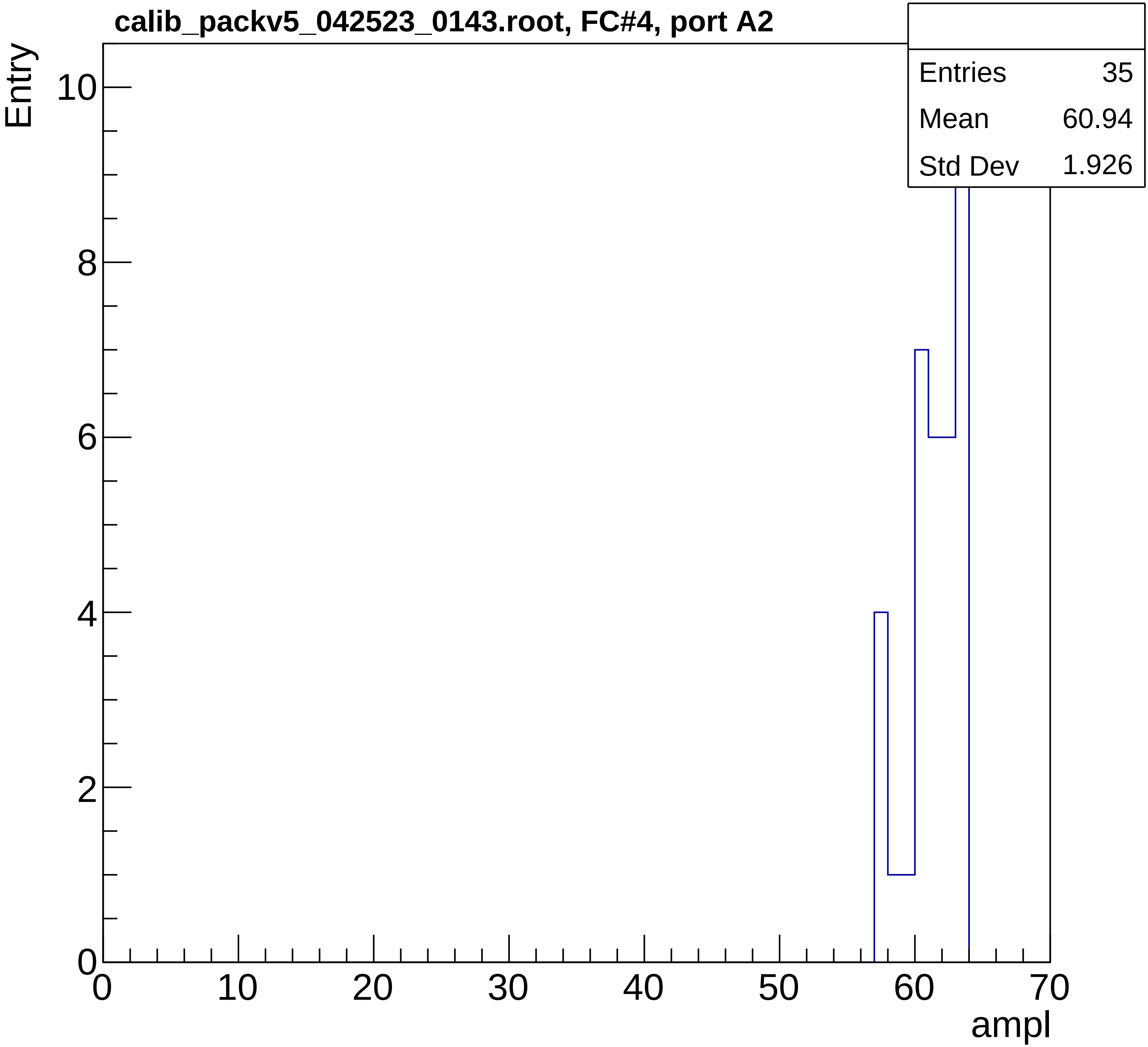
calib_packv5_042523_0143.root, FC#4, port A2

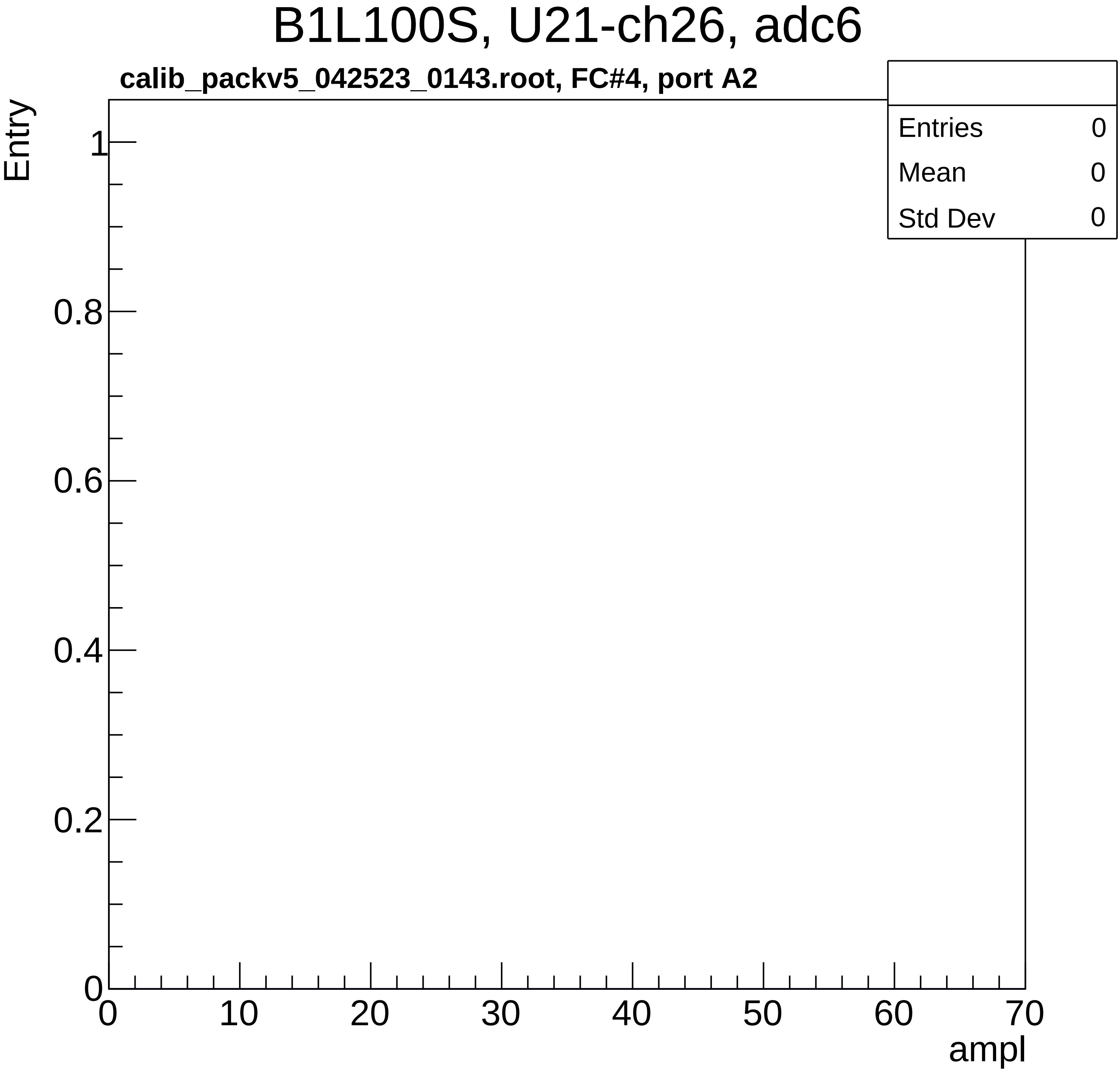



B1L100S, U21-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.4
Std Dev 8.123
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

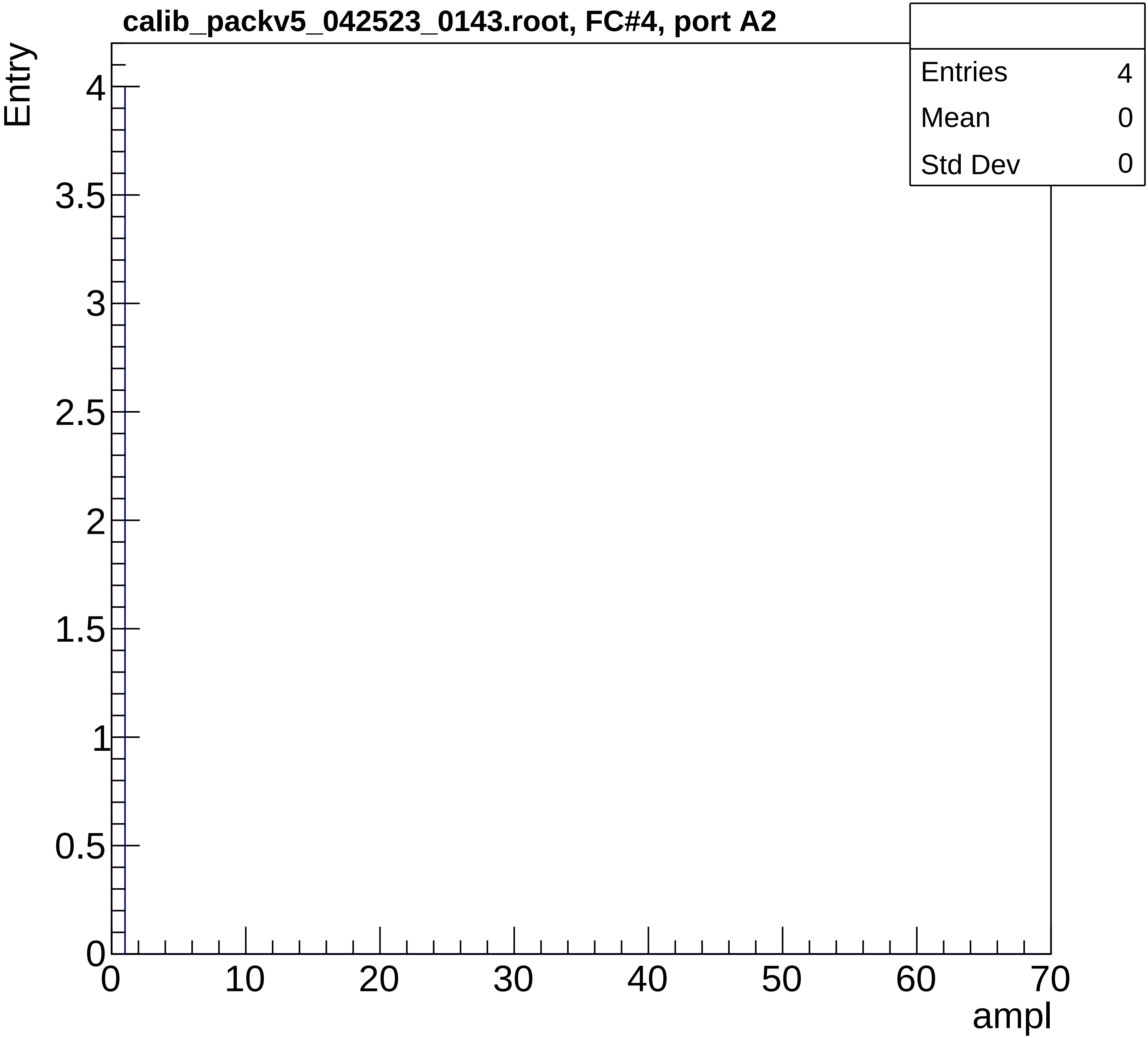
B1L100S, U21-ch26, adc5

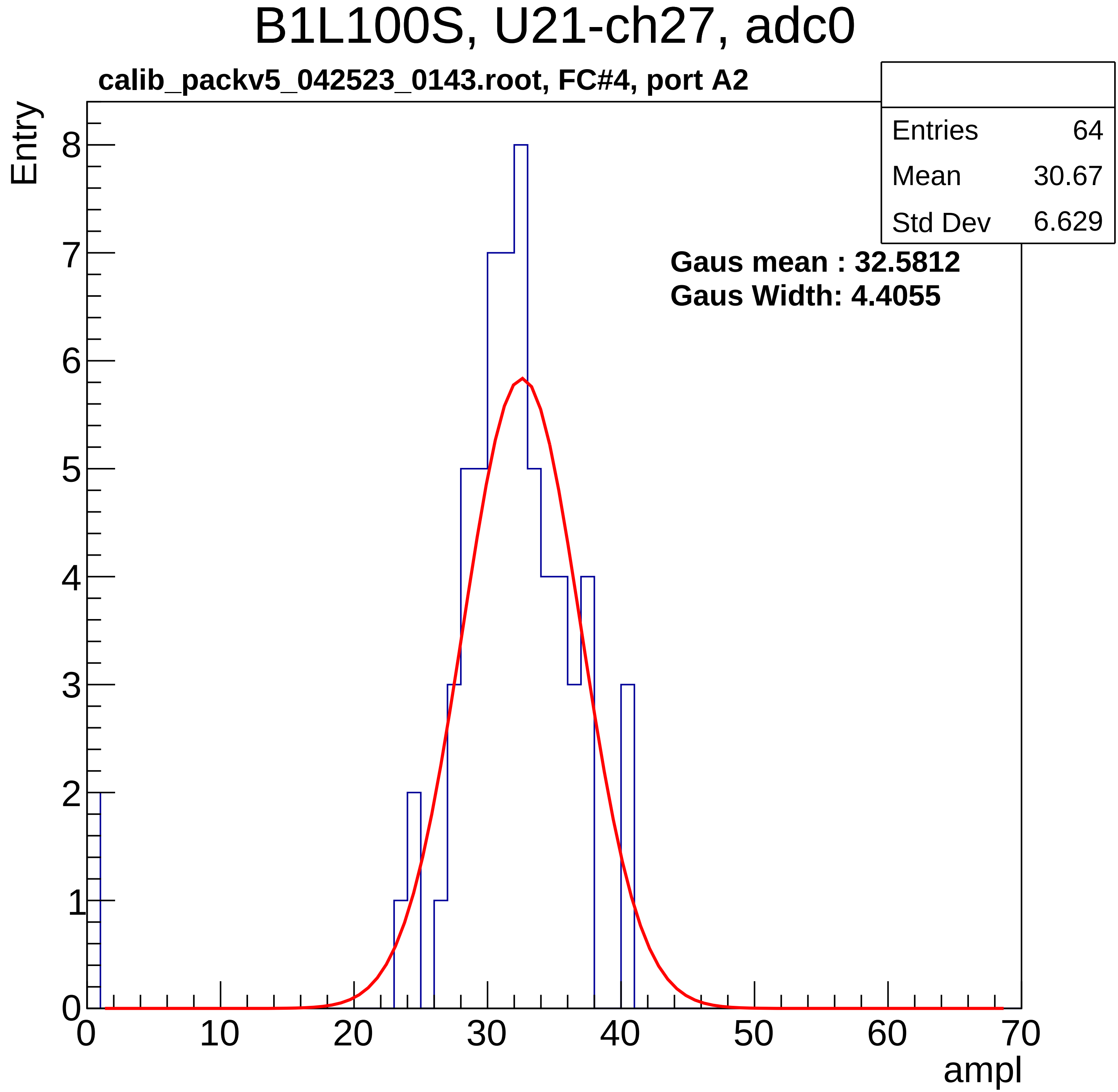
calib_packv5_042523_0143.root, FC#4, port A2

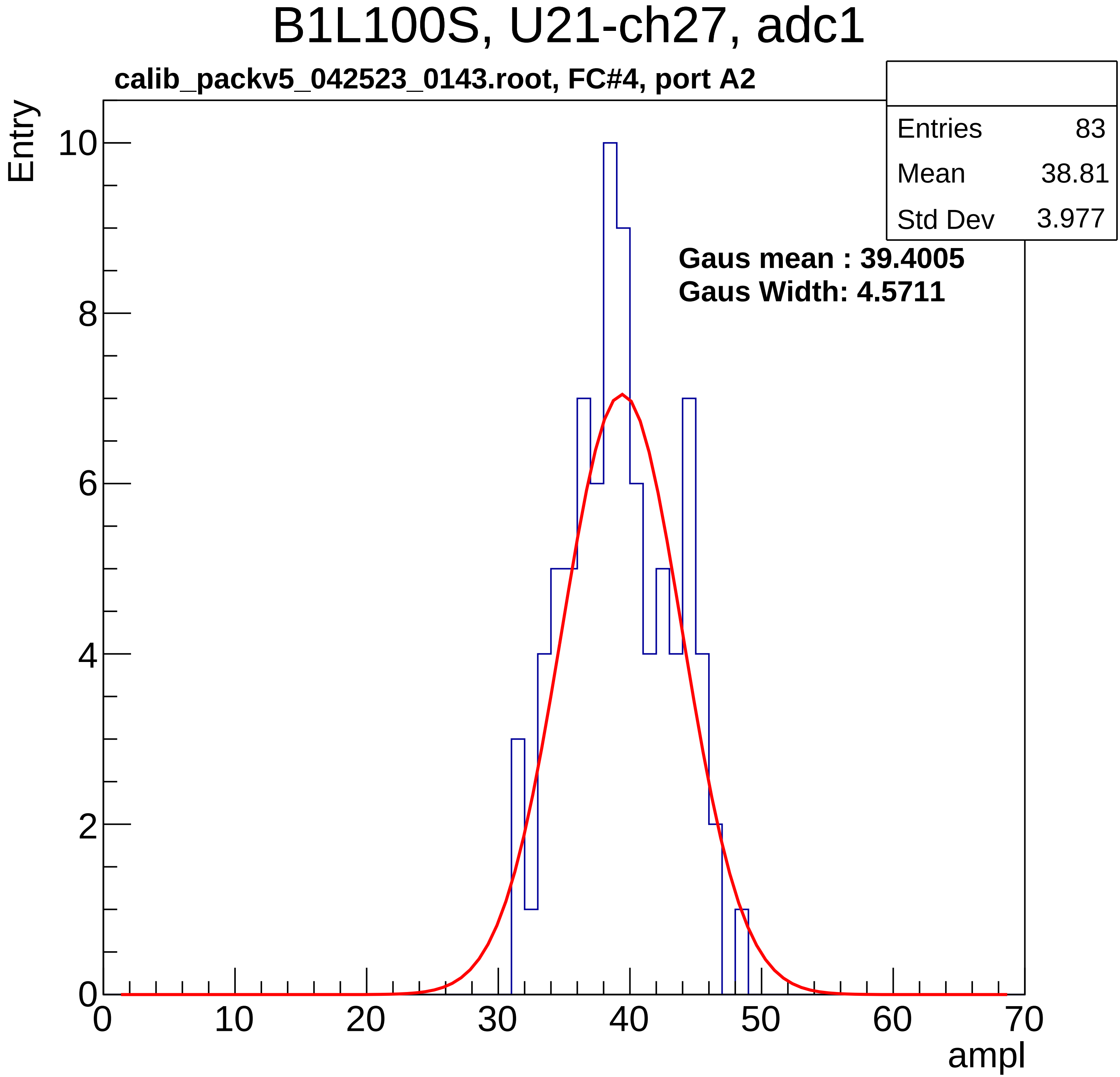



B1L100S, U21-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

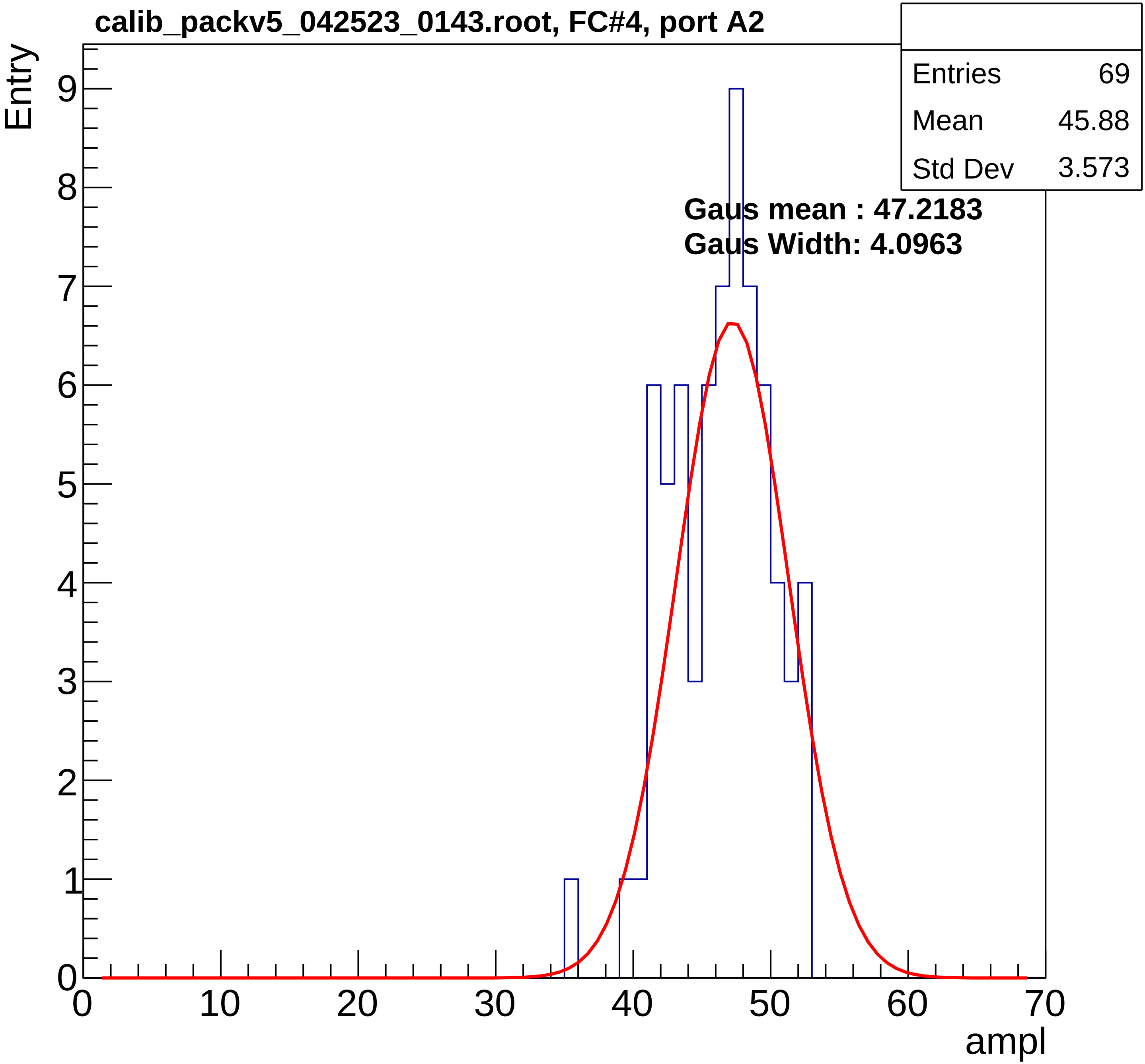
B1L100S, U21-ch26, adc7

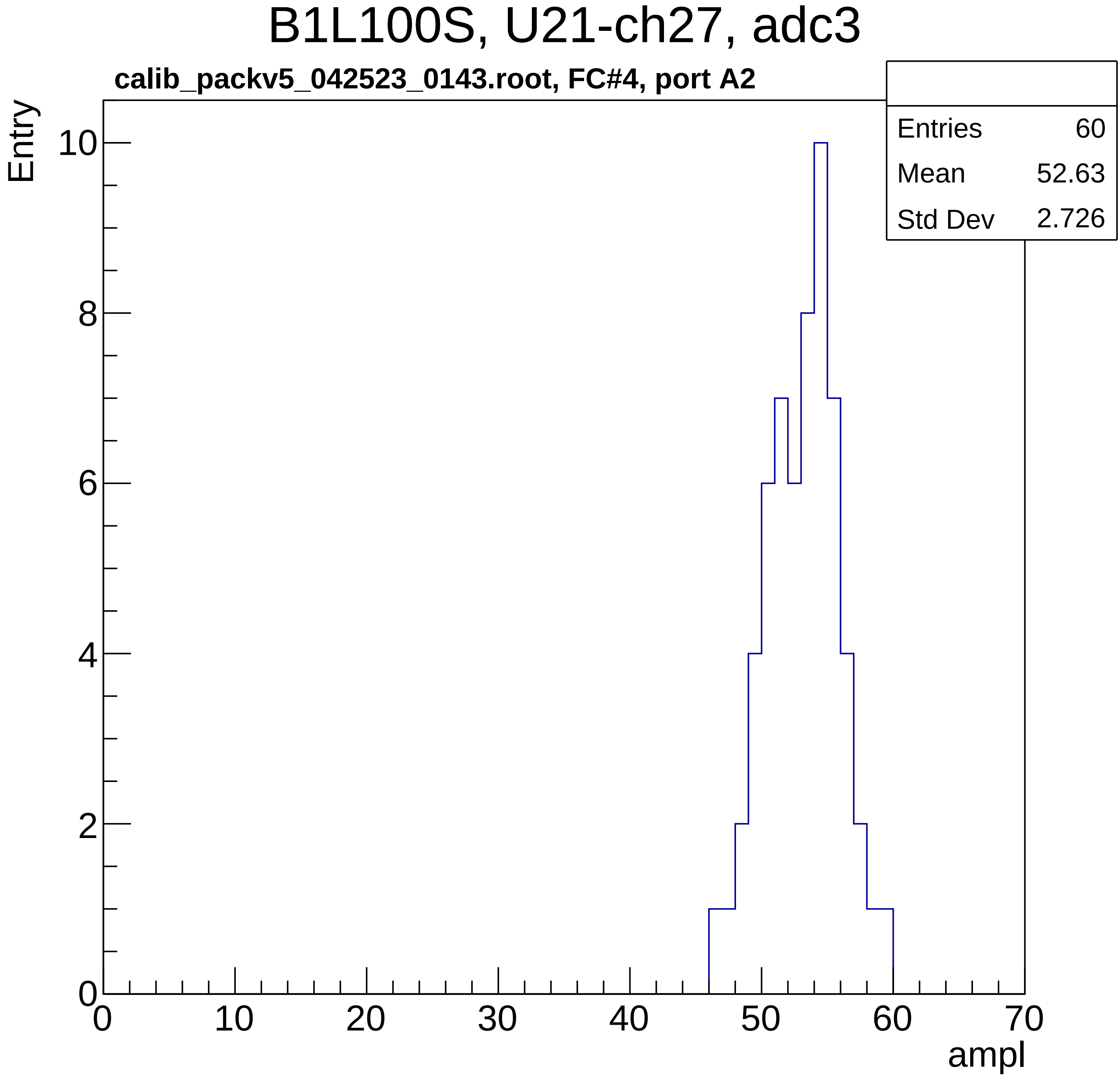


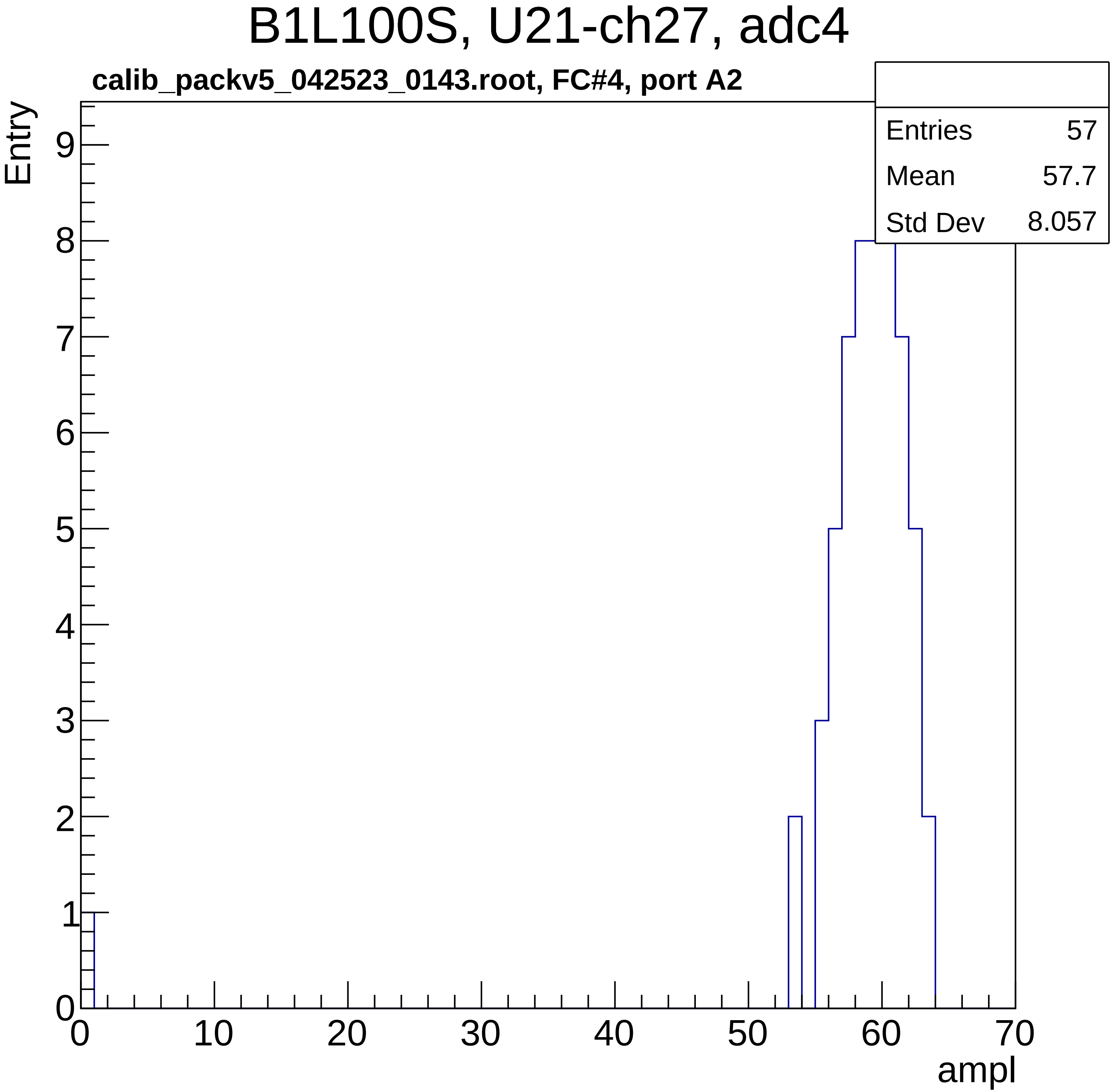

B1L100S, U21-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.67
Std Dev 6.629
Gaus mean : 32.5812
Gaus Width: 4.4055
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

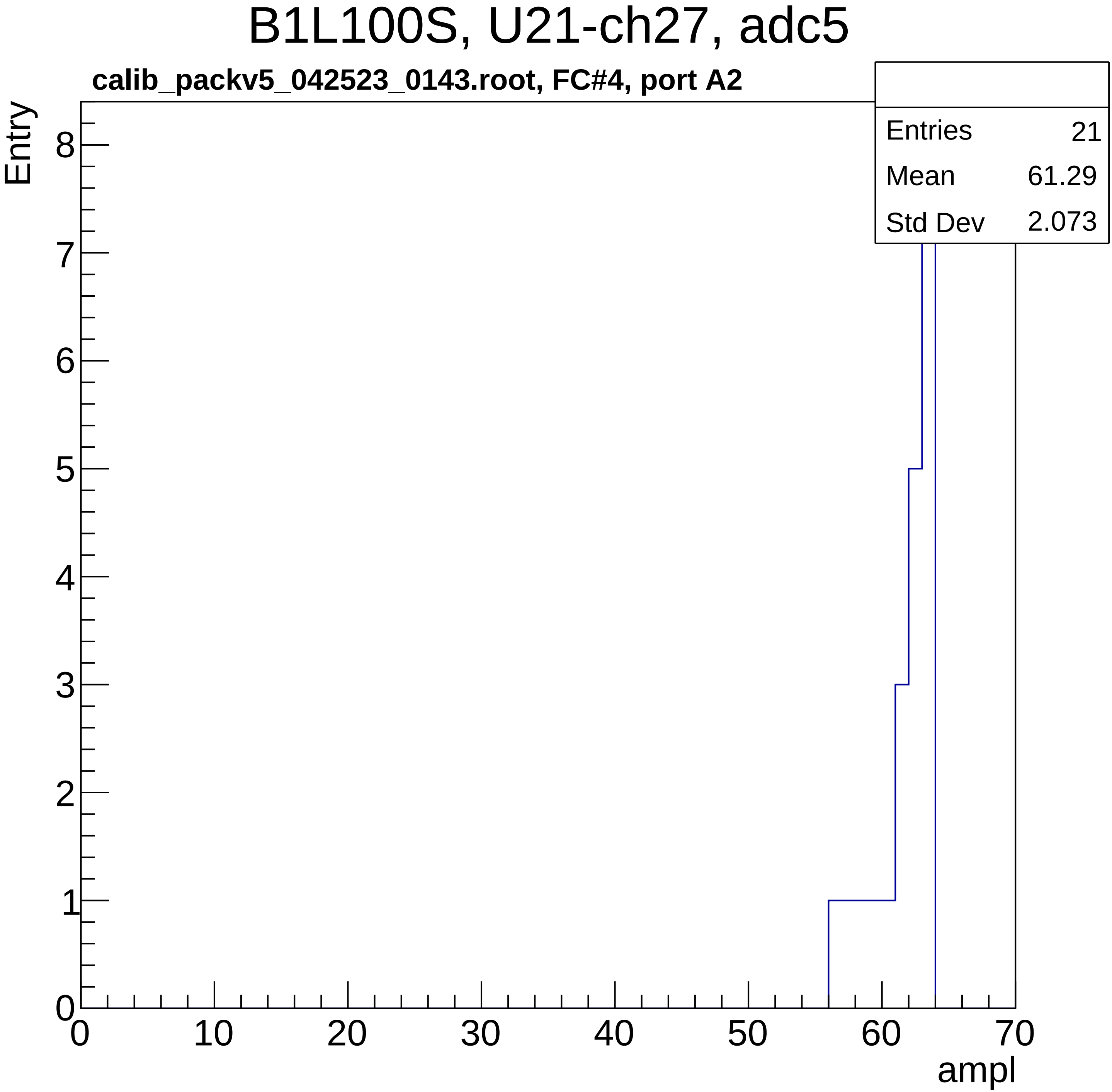

B1L100S, U21-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 38.81
Std Dev 3.977
Gaus mean : 39.4005
Gaus Width: 4.5711
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

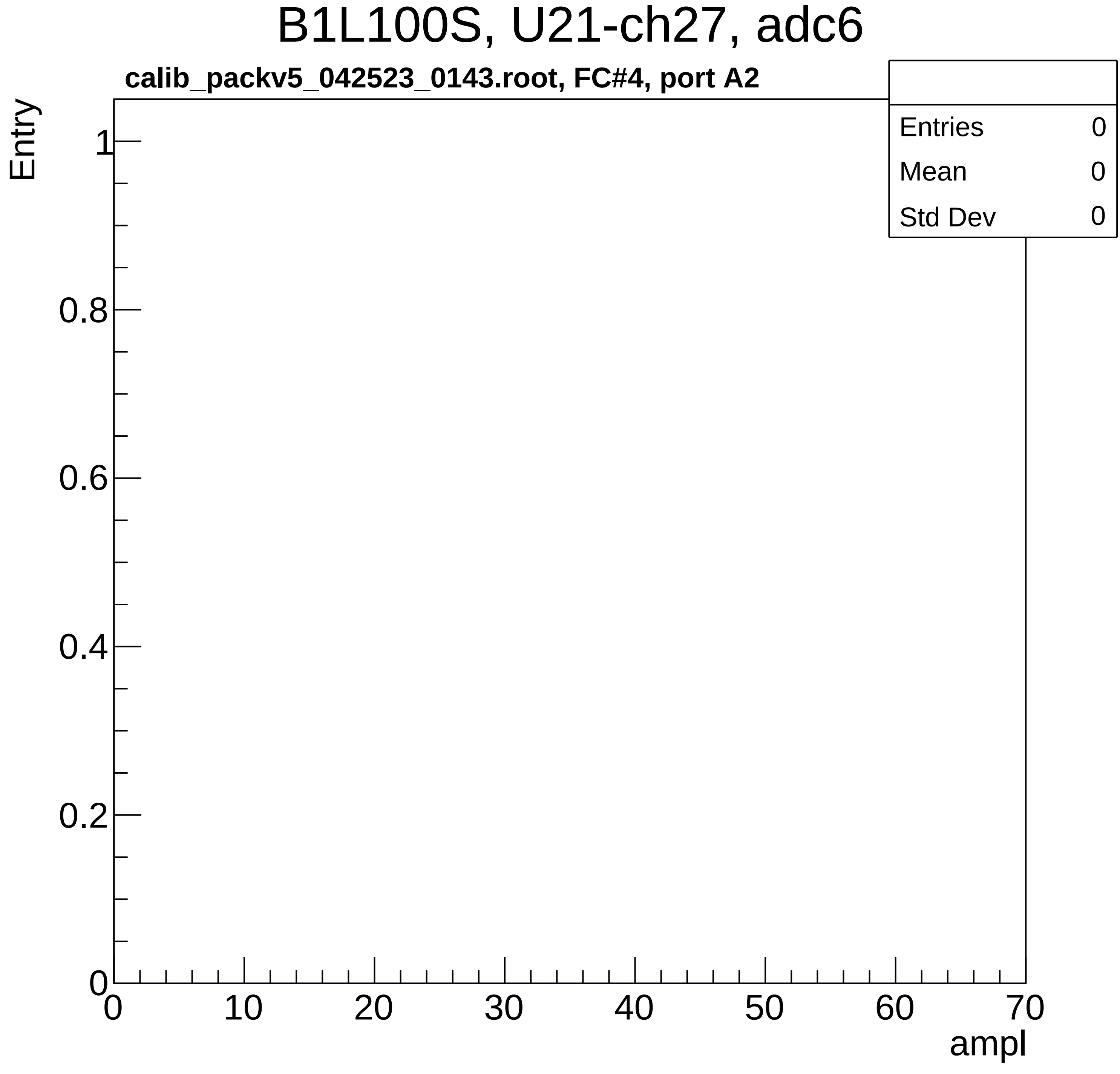
B1L100S, U21-ch27, adc2




B1L100S, U21-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 52.63
Std Dev 2.726
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

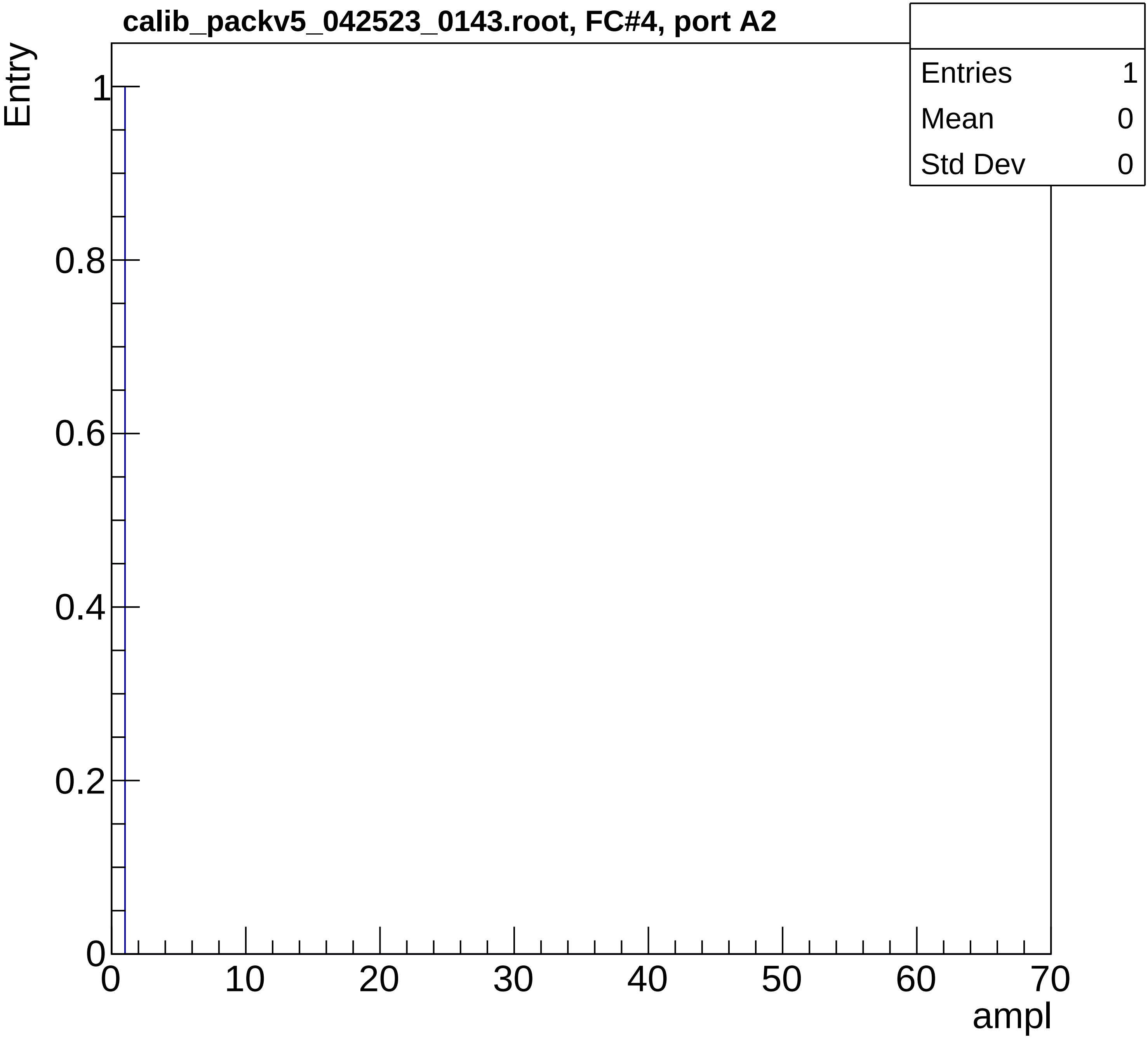

B1L100S, U21-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.7
Std Dev 8.057
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

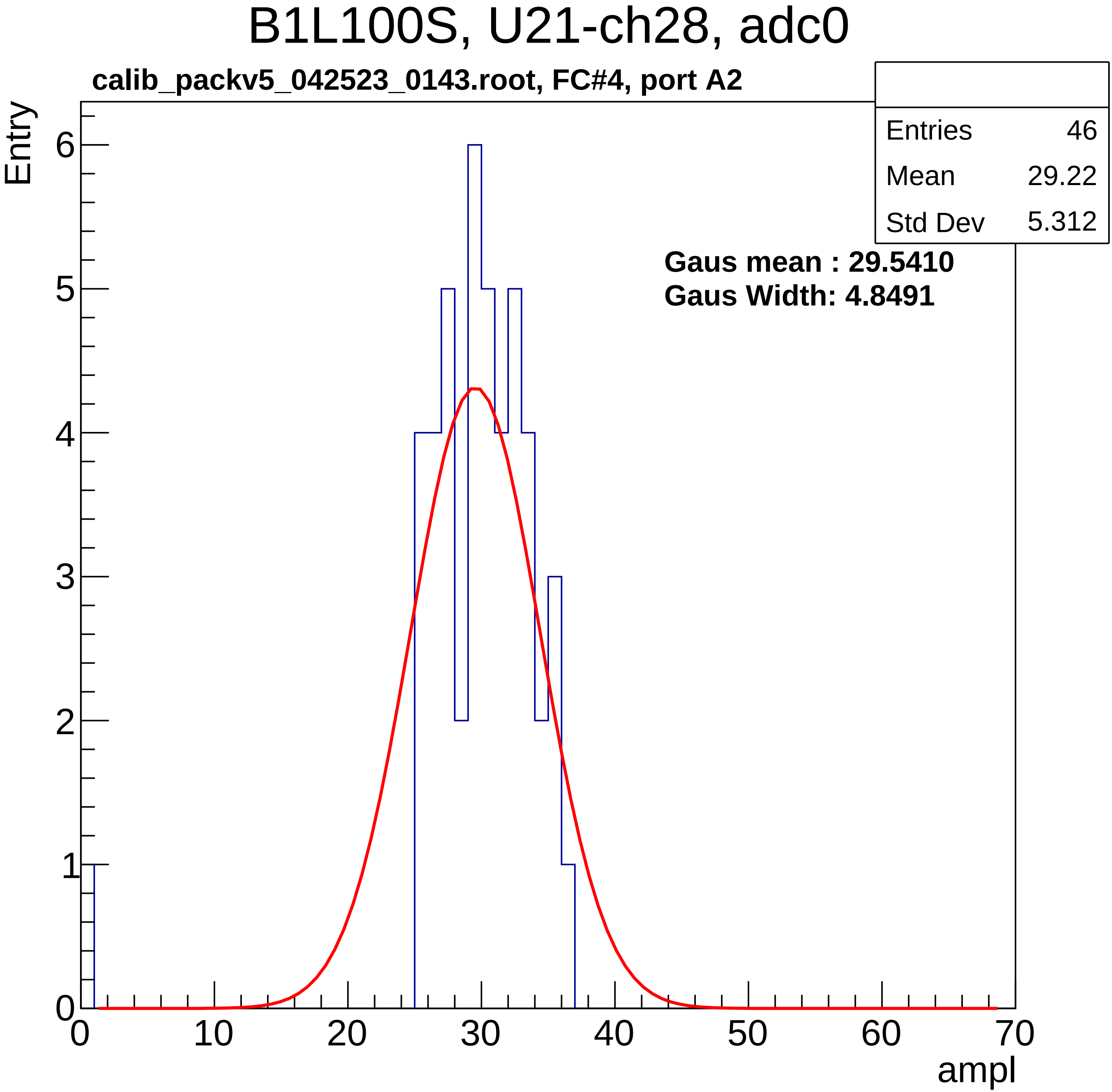

B1L100S, U21-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 21
Mean 61.29
Std Dev 2.073
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

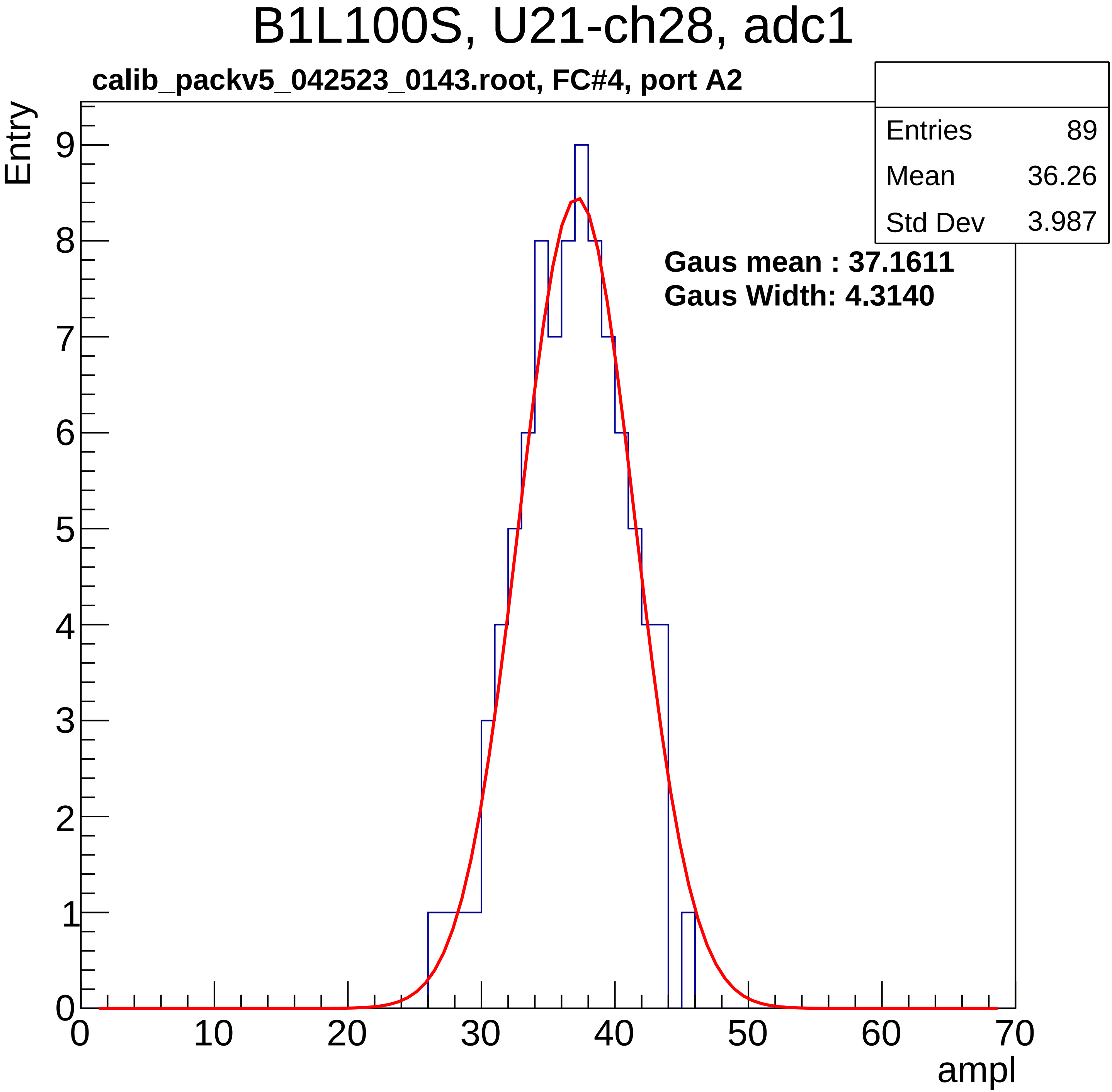

B1L100S, U21-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 0
Mean 0
Std Dev 0
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

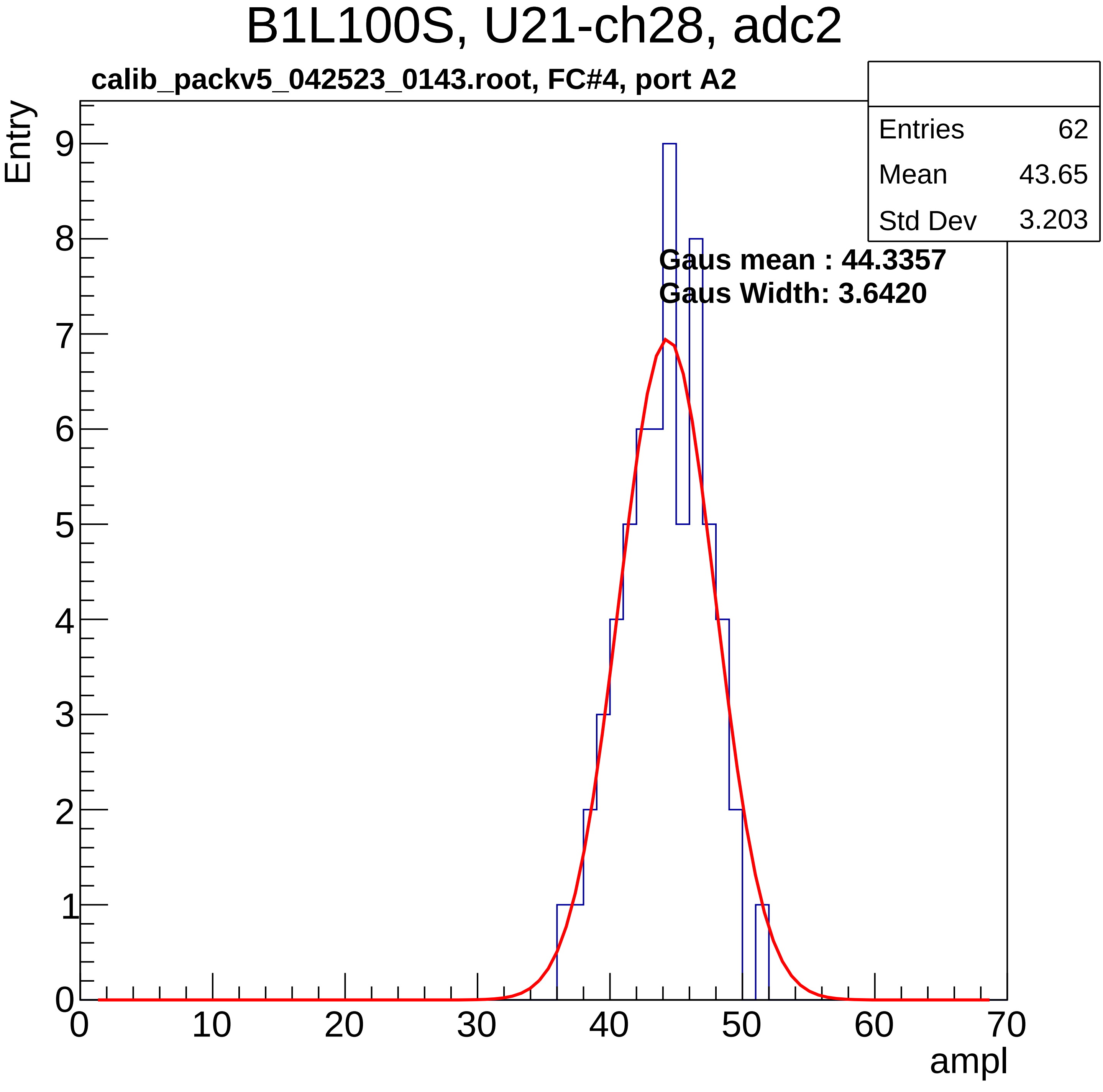
B1L100S, U21-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

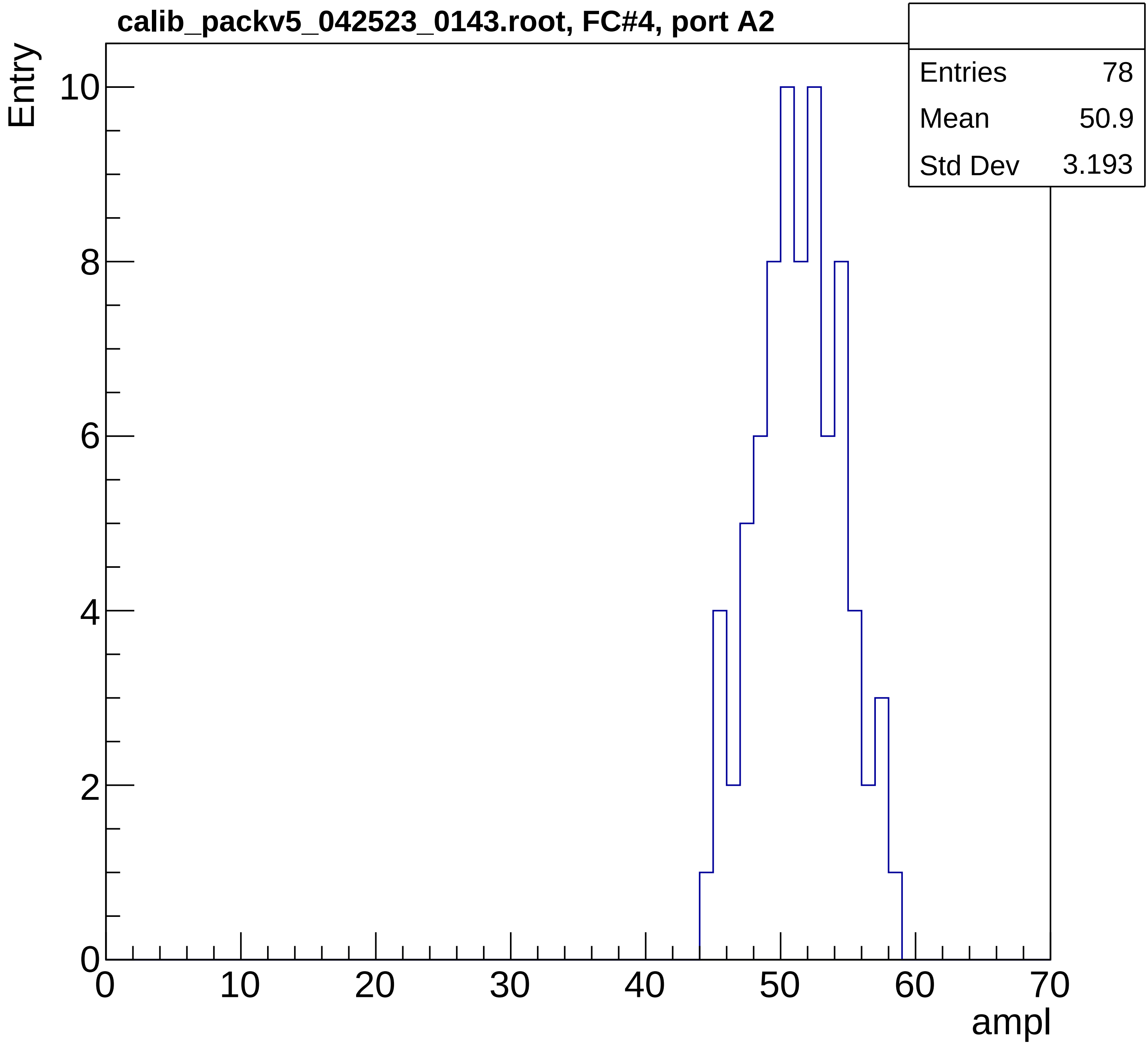



B1L100S, U21-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 46
Mean 29.22
Std Dev 5.312
Gaus mean : 29.5410
Gaus Width: 4.8491
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 36.26
Std Dev 3.987
Gaus mean : 37.1611
Gaus Width: 4.3140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

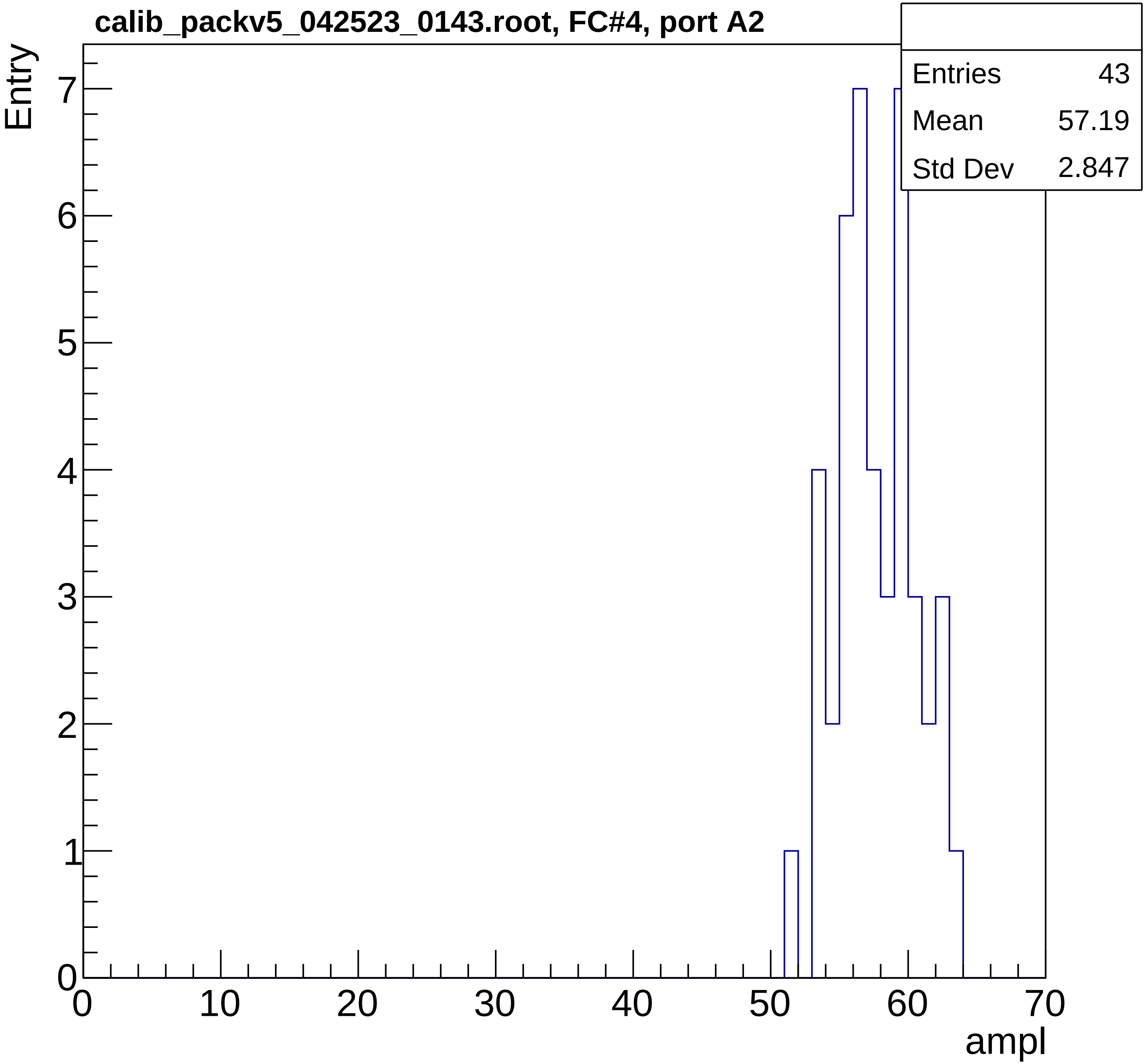

B1L100S, U21-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.65
Std Dev 3.203
Gaus mean : 44.3357
Gaus Width: 3.6420
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

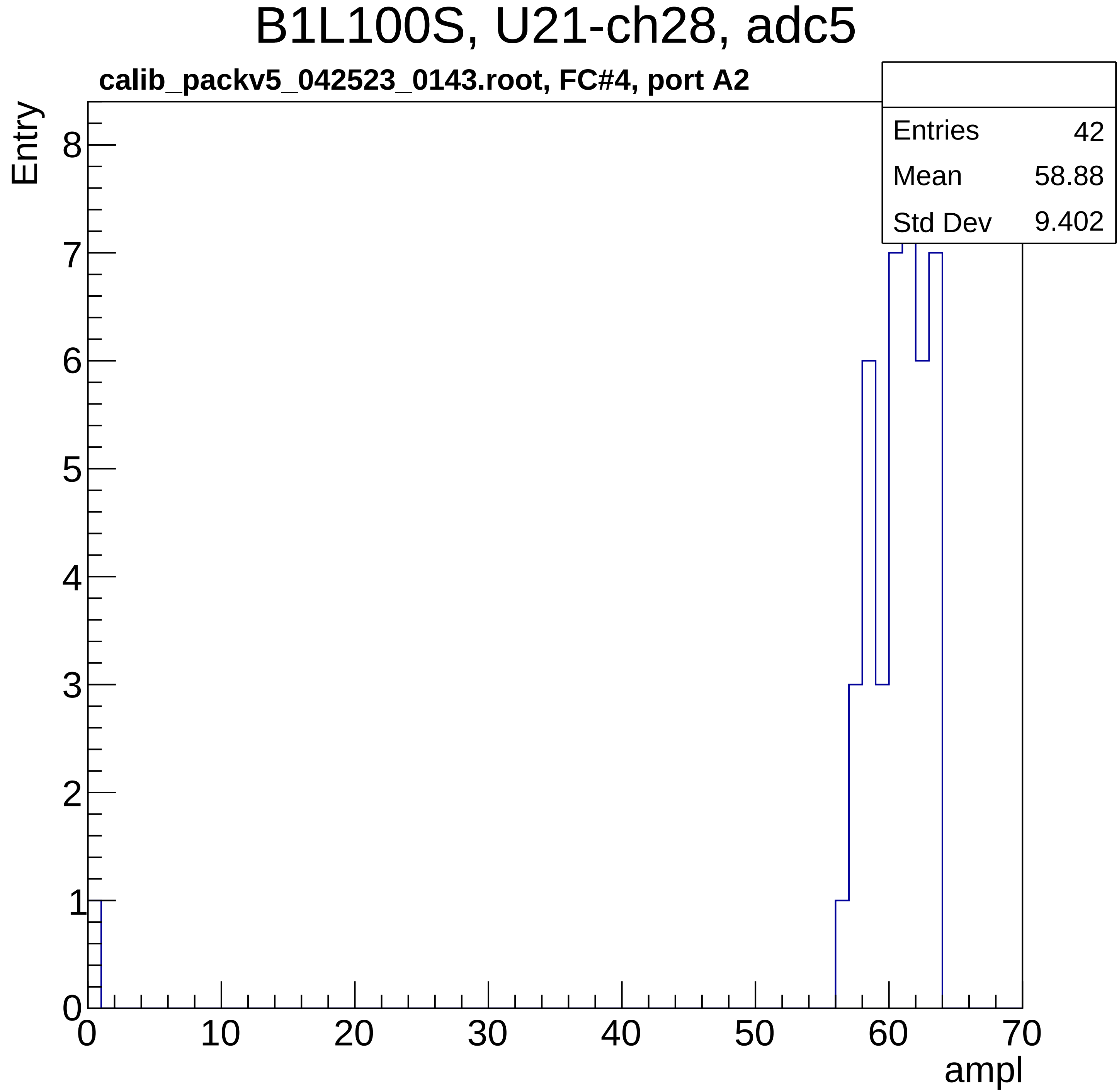
B1L100S, U21-ch28, adc3

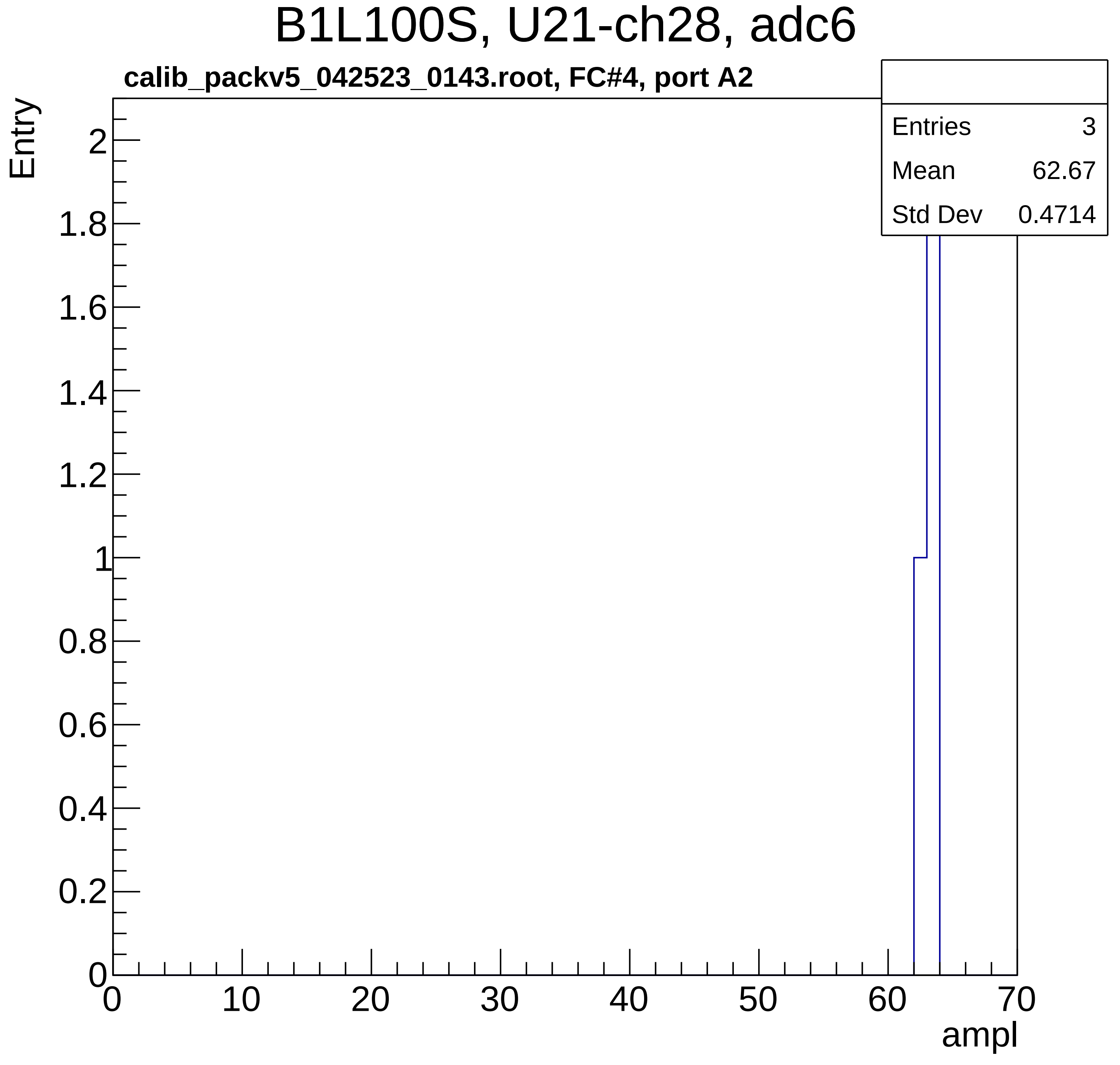


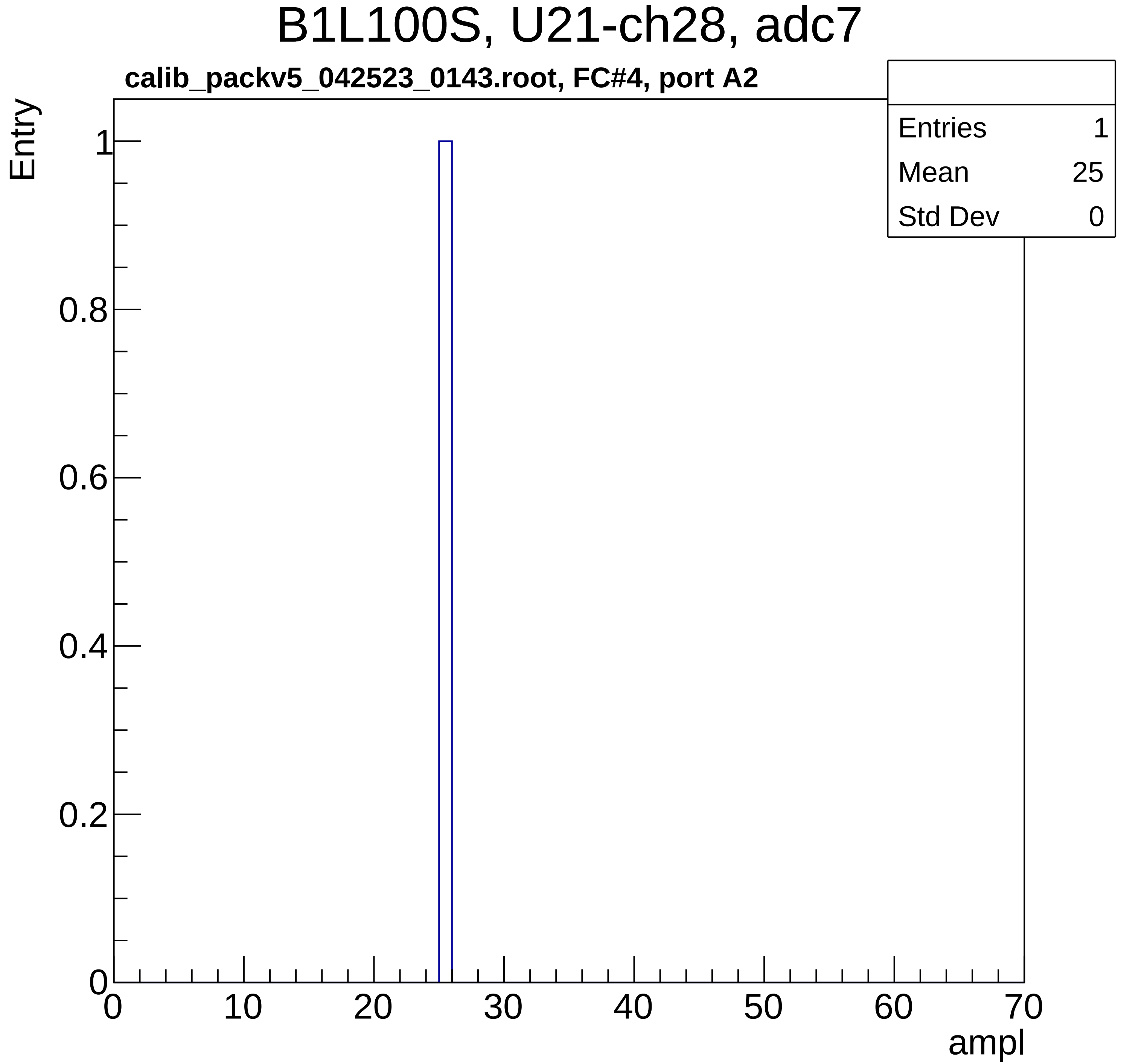
B1L100S, U21-ch28, adc4

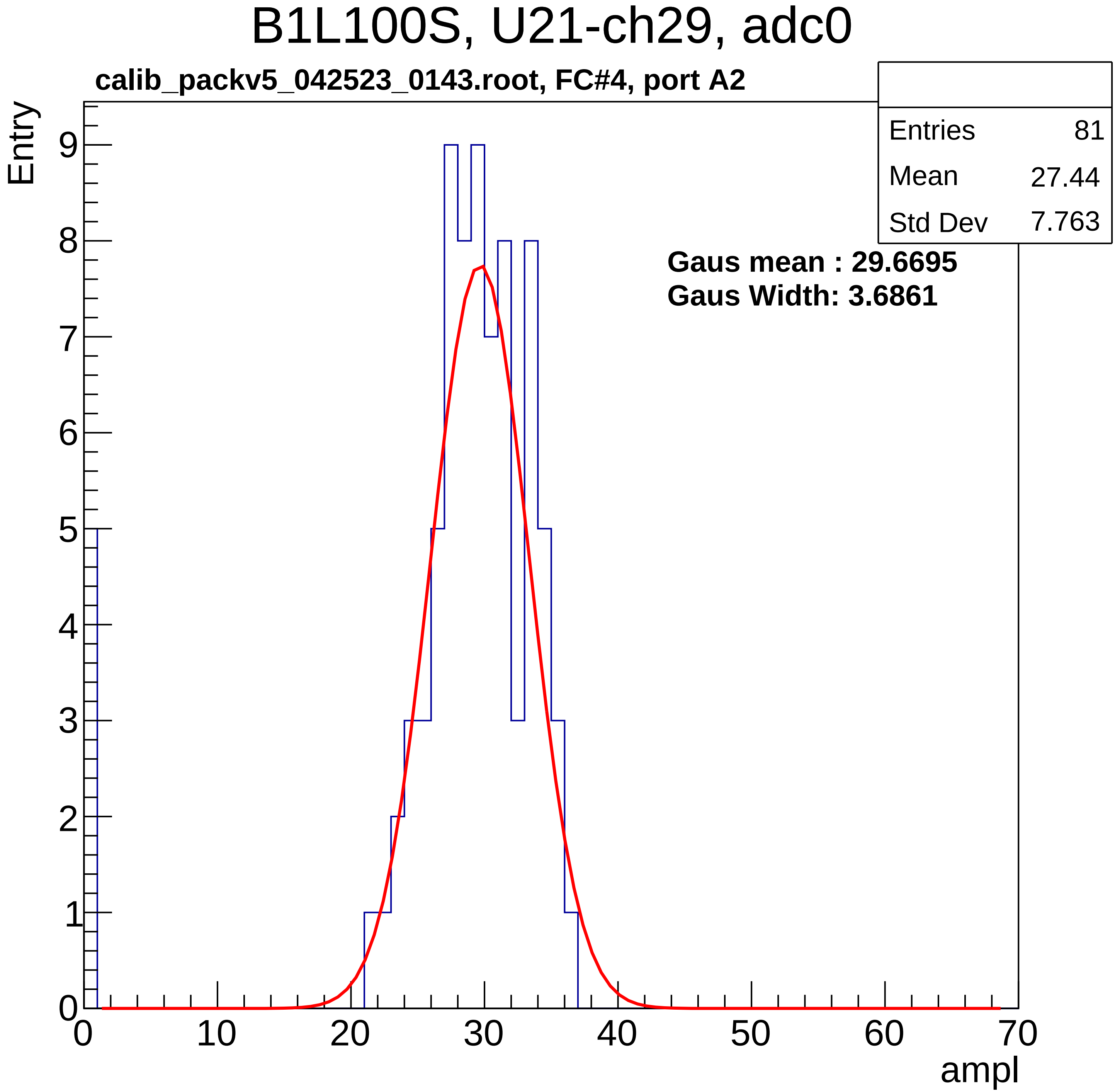
calib_packv5_042523_0143.root, FC#4, port A2

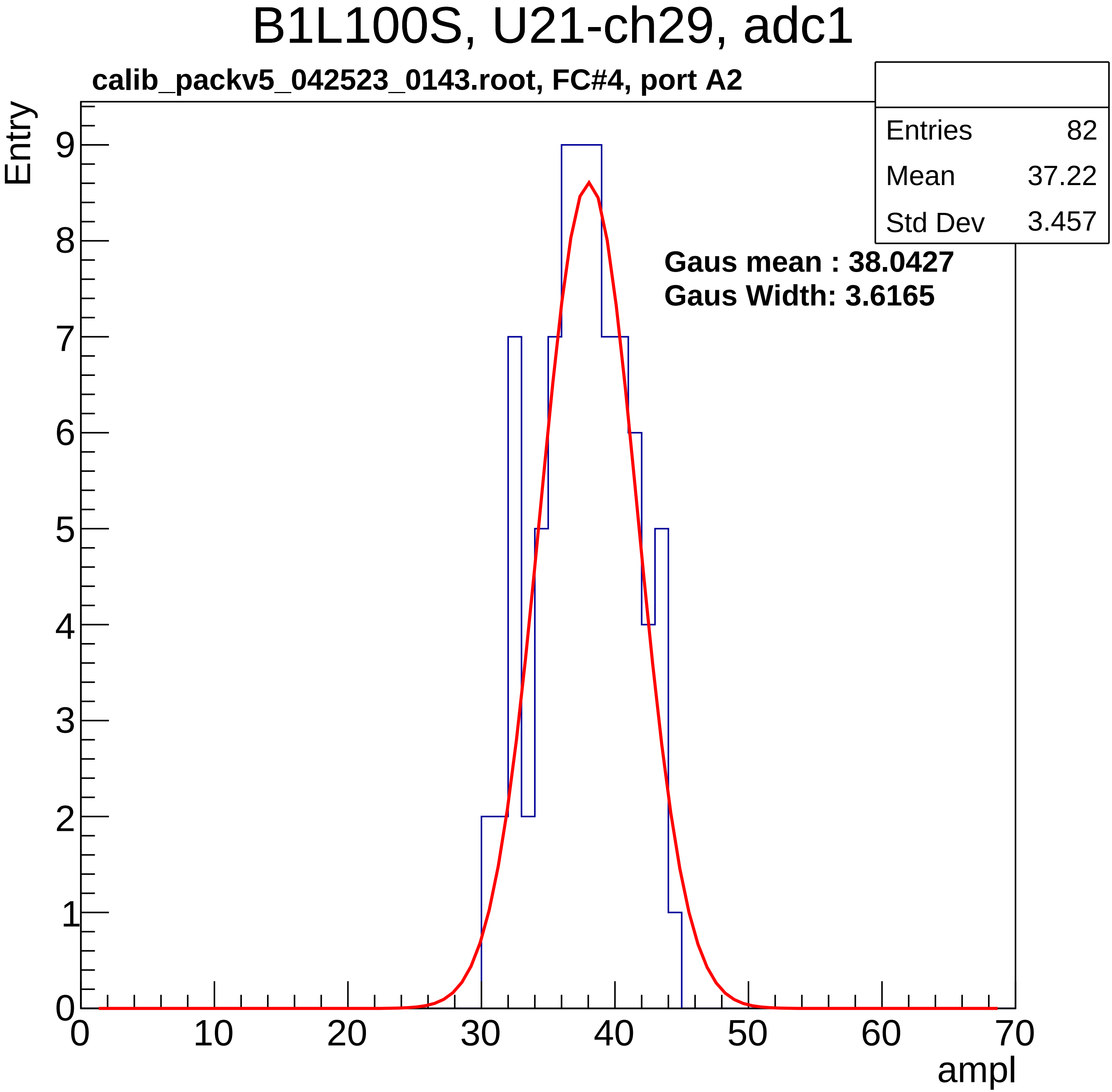


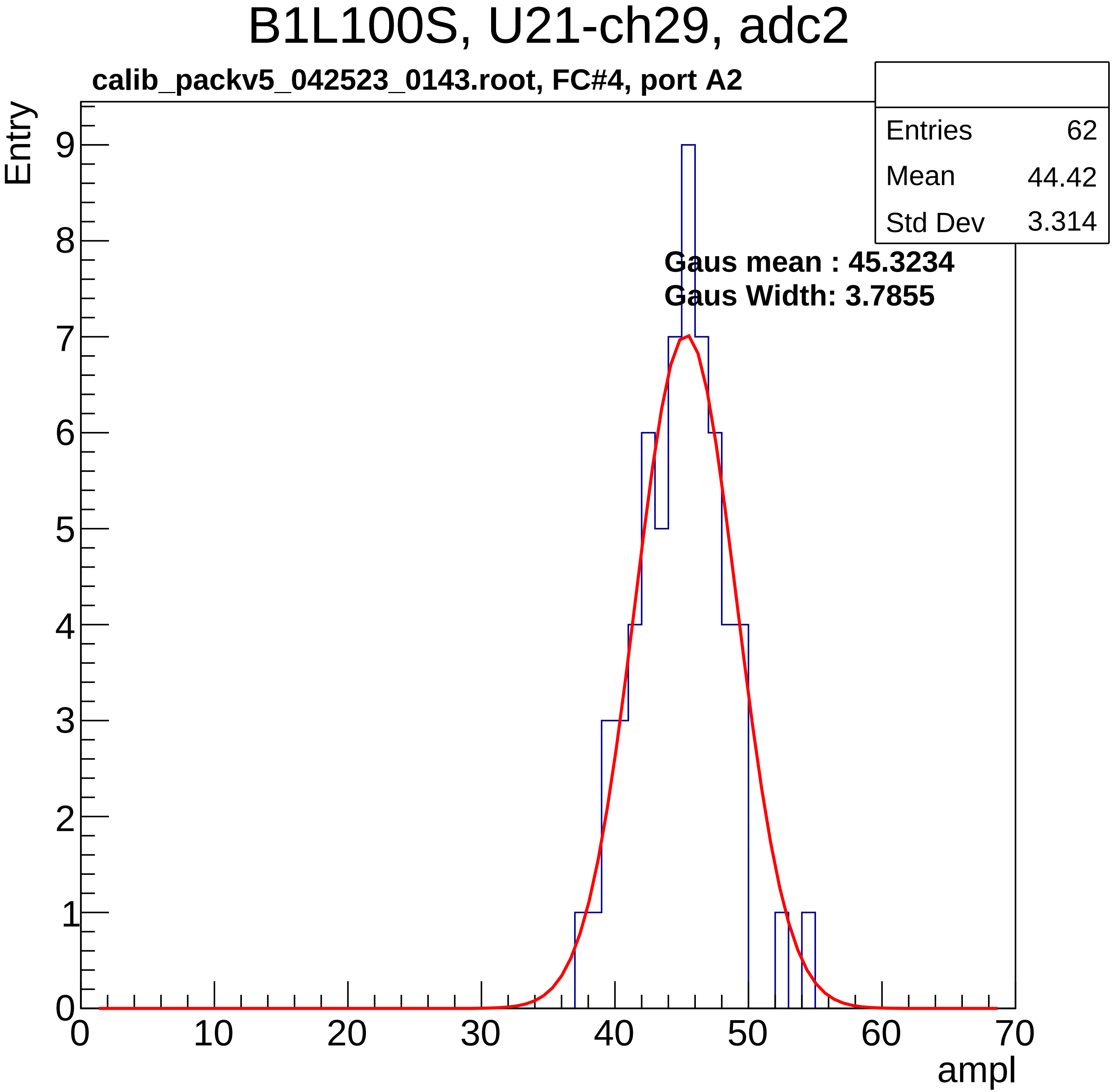

B1L100S, U21-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.88
Std Dev 9.402
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

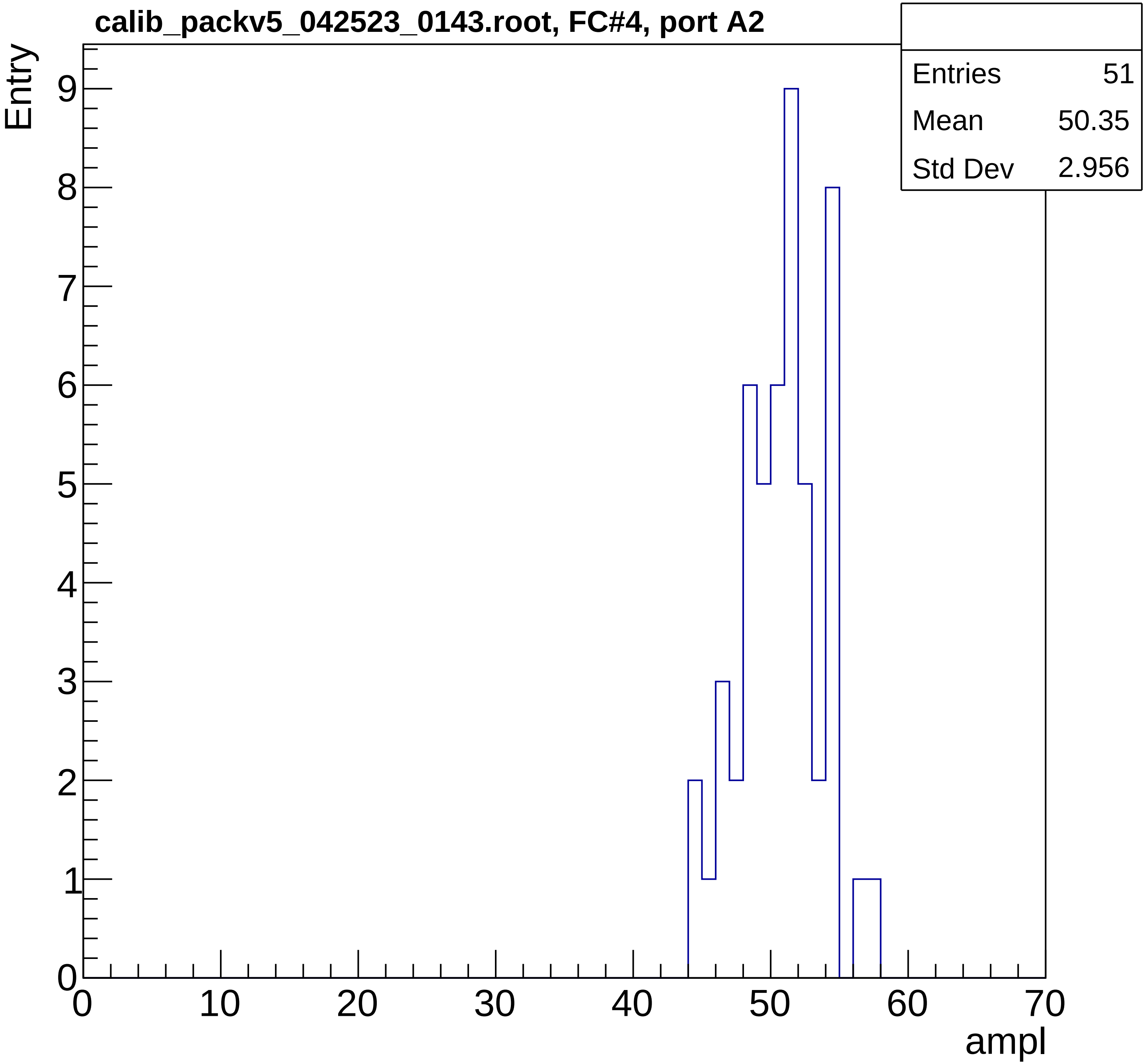

B1L100S, U21-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 27.44
Std Dev 7.763
Gaus mean : 29.6695
Gaus Width: 3.6861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 37.22
Std Dev 3.457
Gaus mean : 38.0427
Gaus Width: 3.6165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

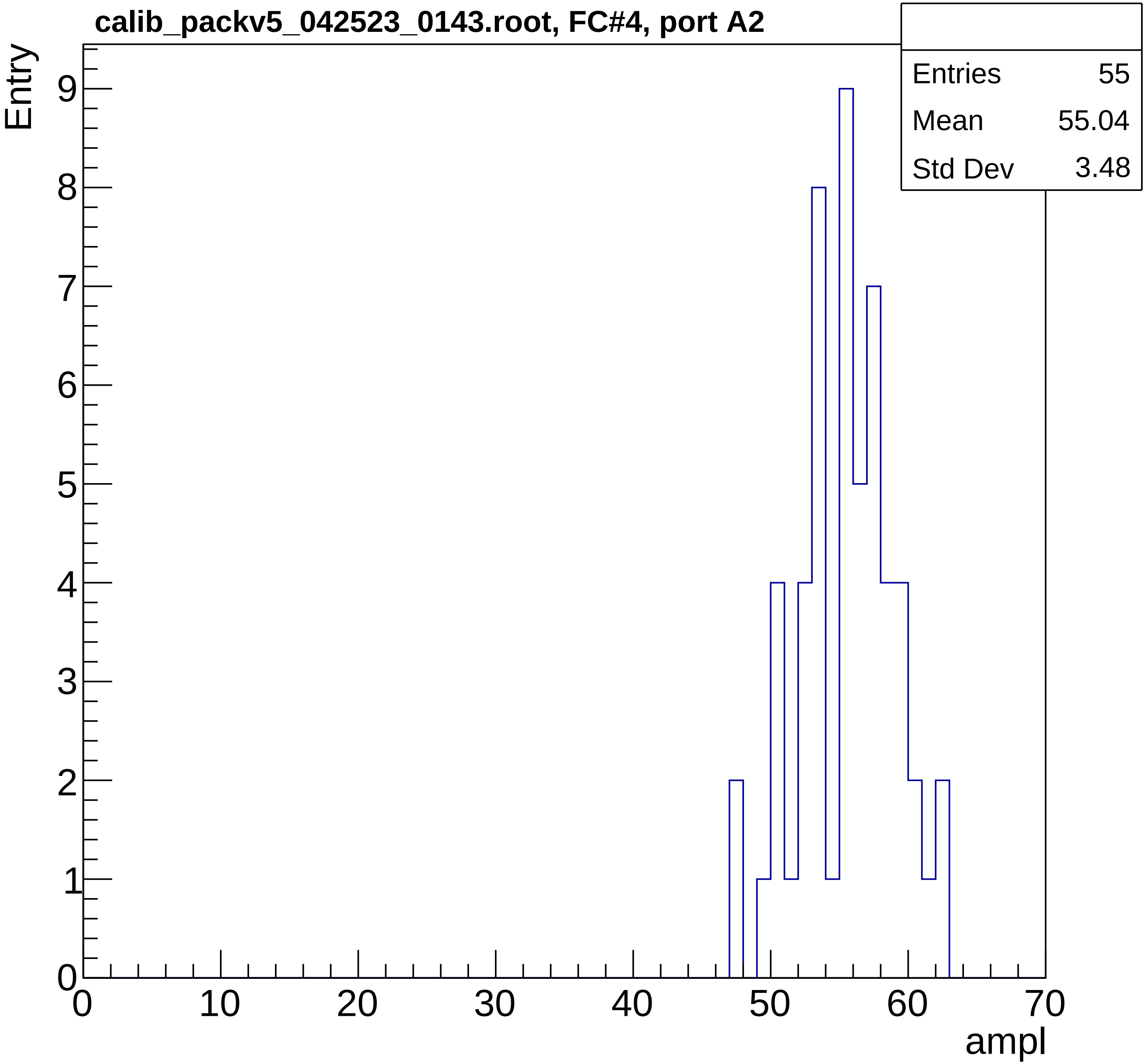

B1L100S, U21-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 62
Mean 44.42
Std Dev 3.314
Gaus mean : 45.3234
Gaus Width: 3.7855
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

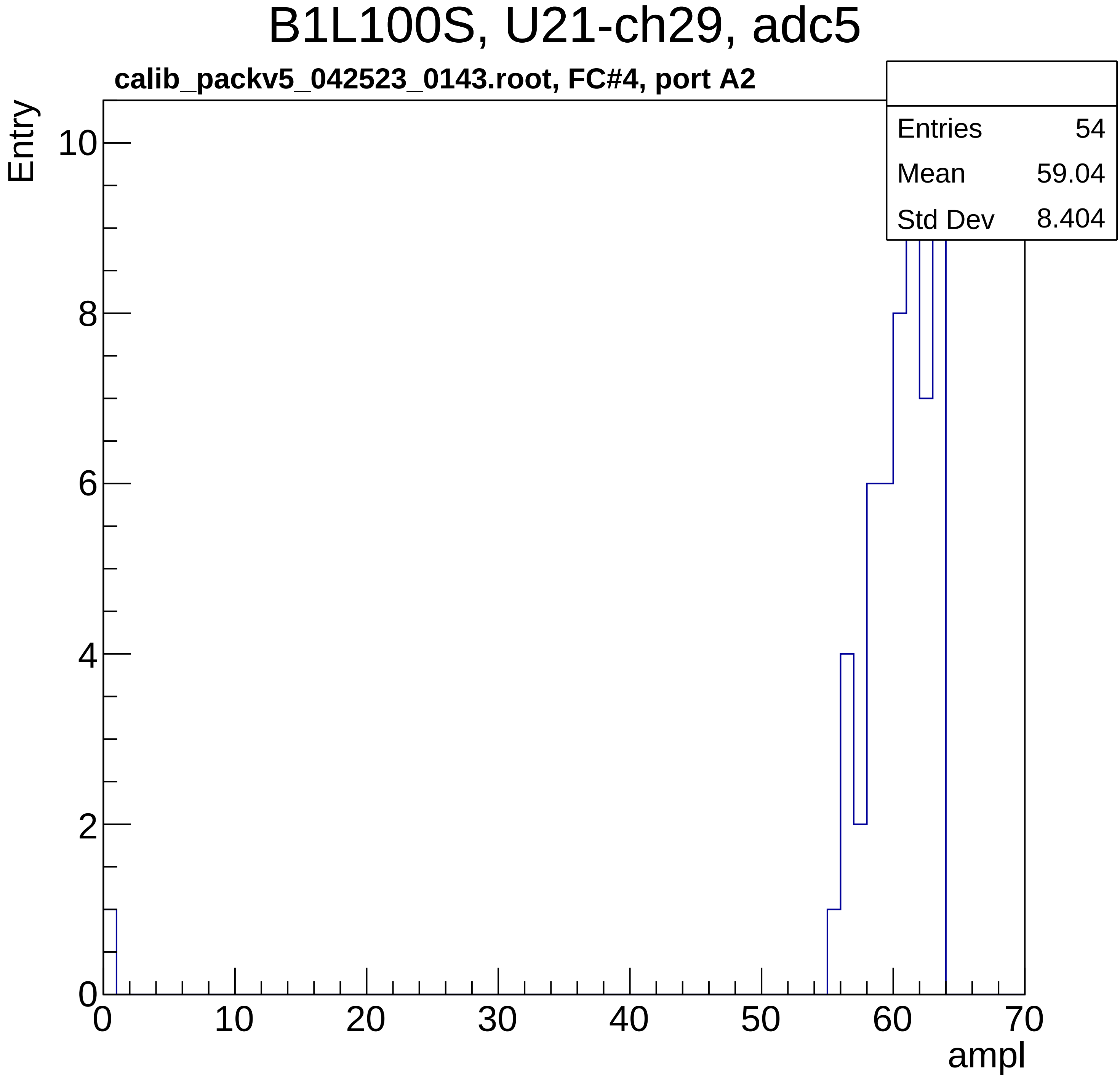
B1L100S, U21-ch29, adc3



B1L100S, U21-ch29, adc4

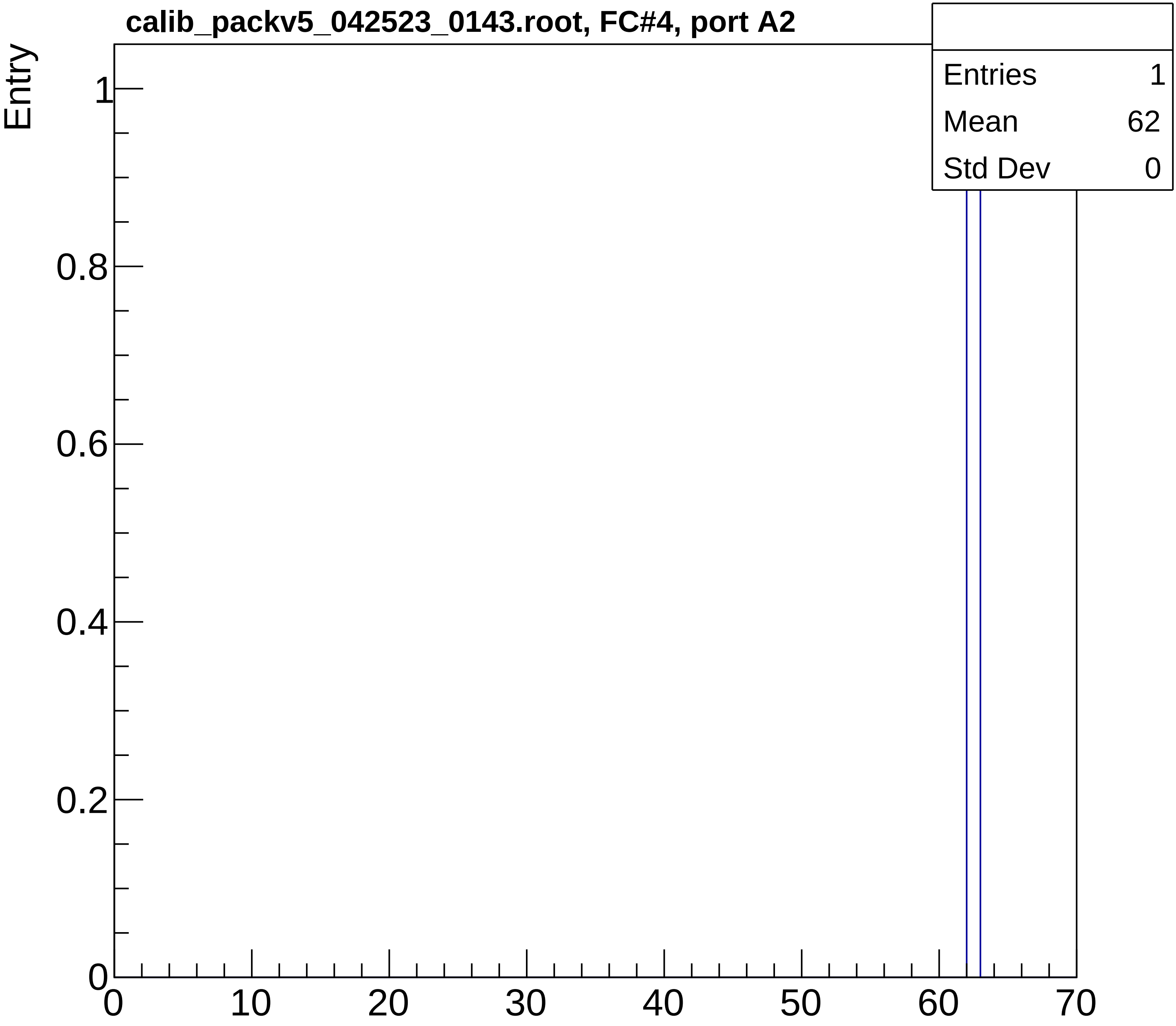
calib_packv5_042523_0143.root, FC#4, port A2

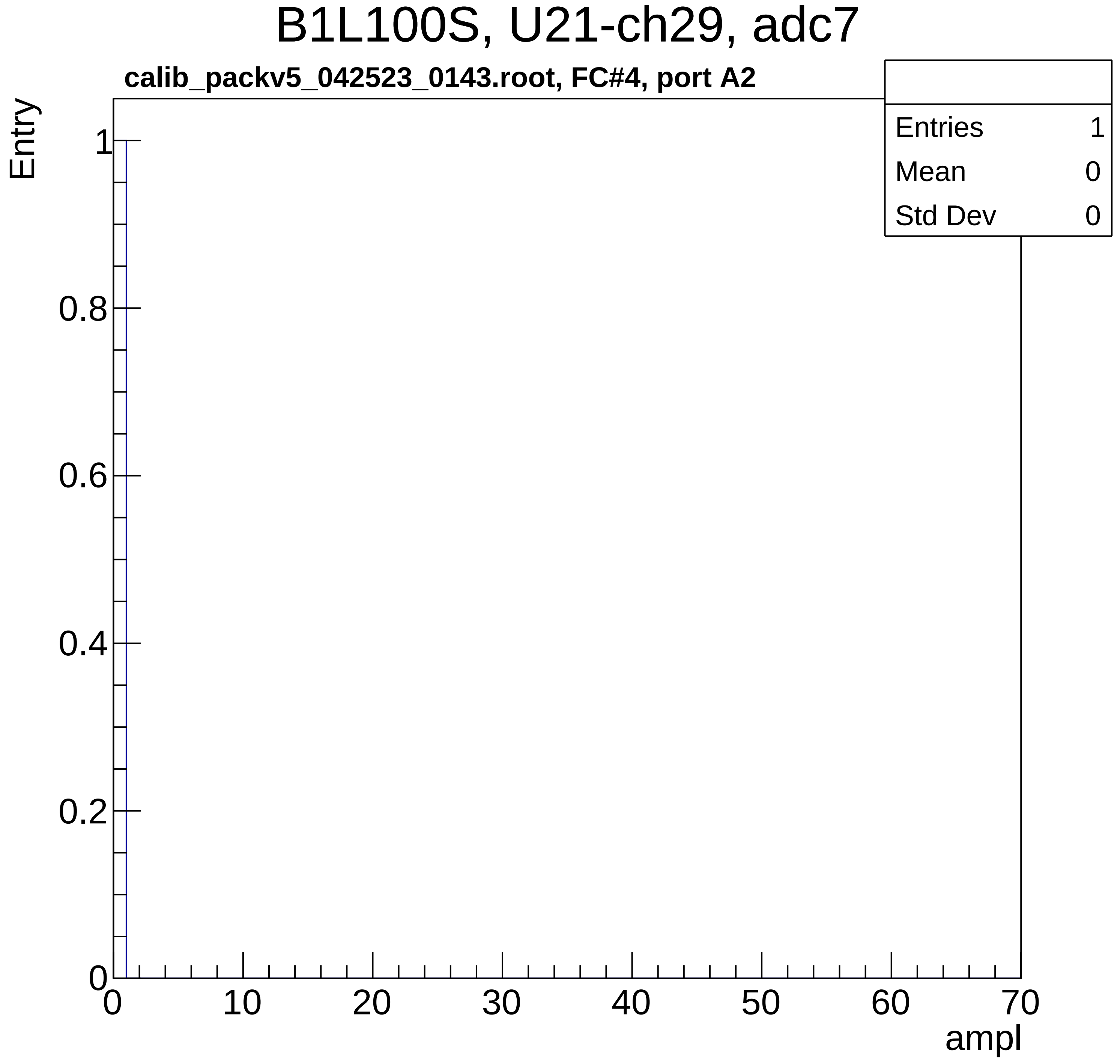


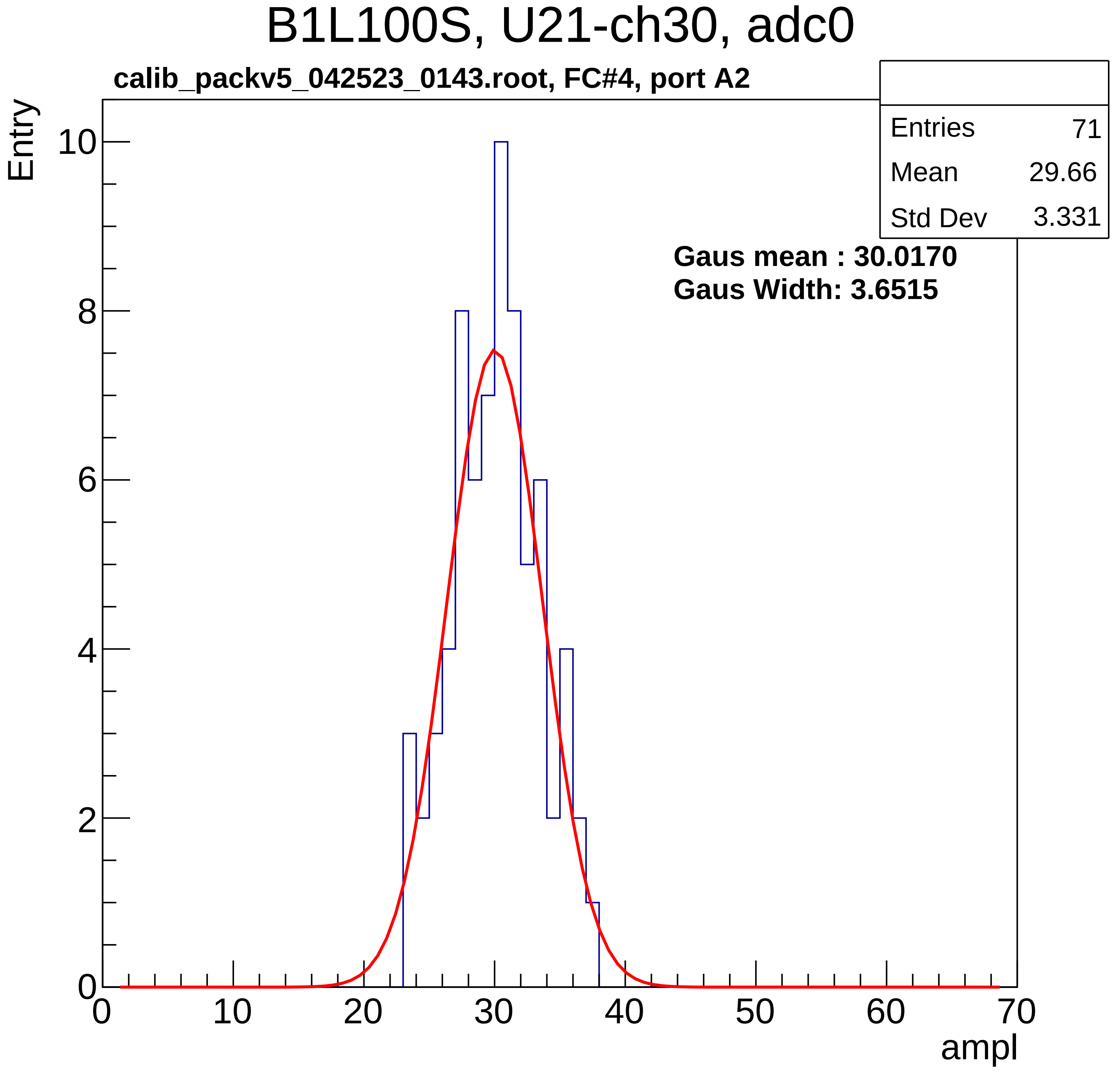

B1L100S, U21-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 54
Mean 59.04
Std Dev 8.404
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

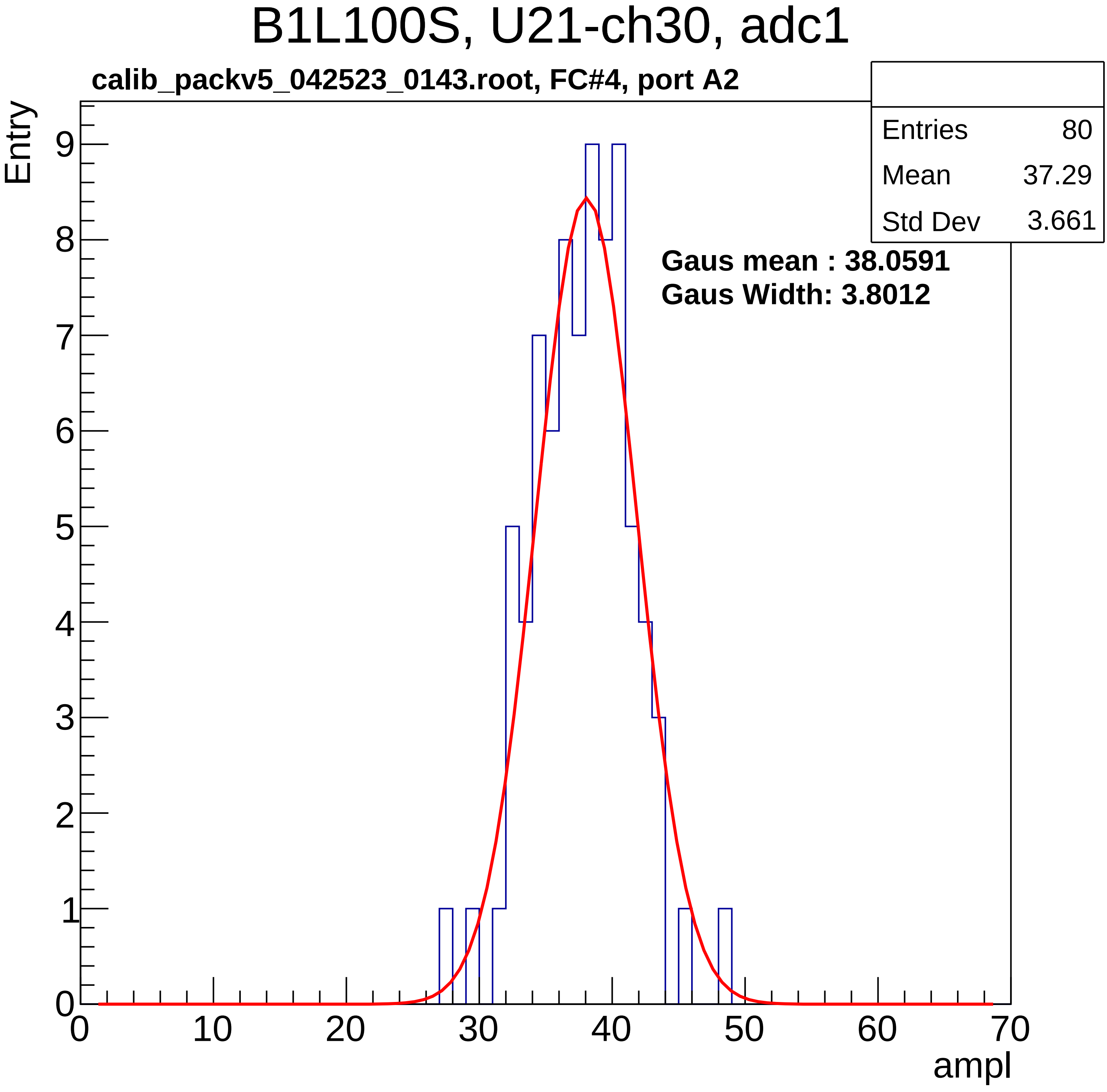
B1L100S, U21-ch29, adc6

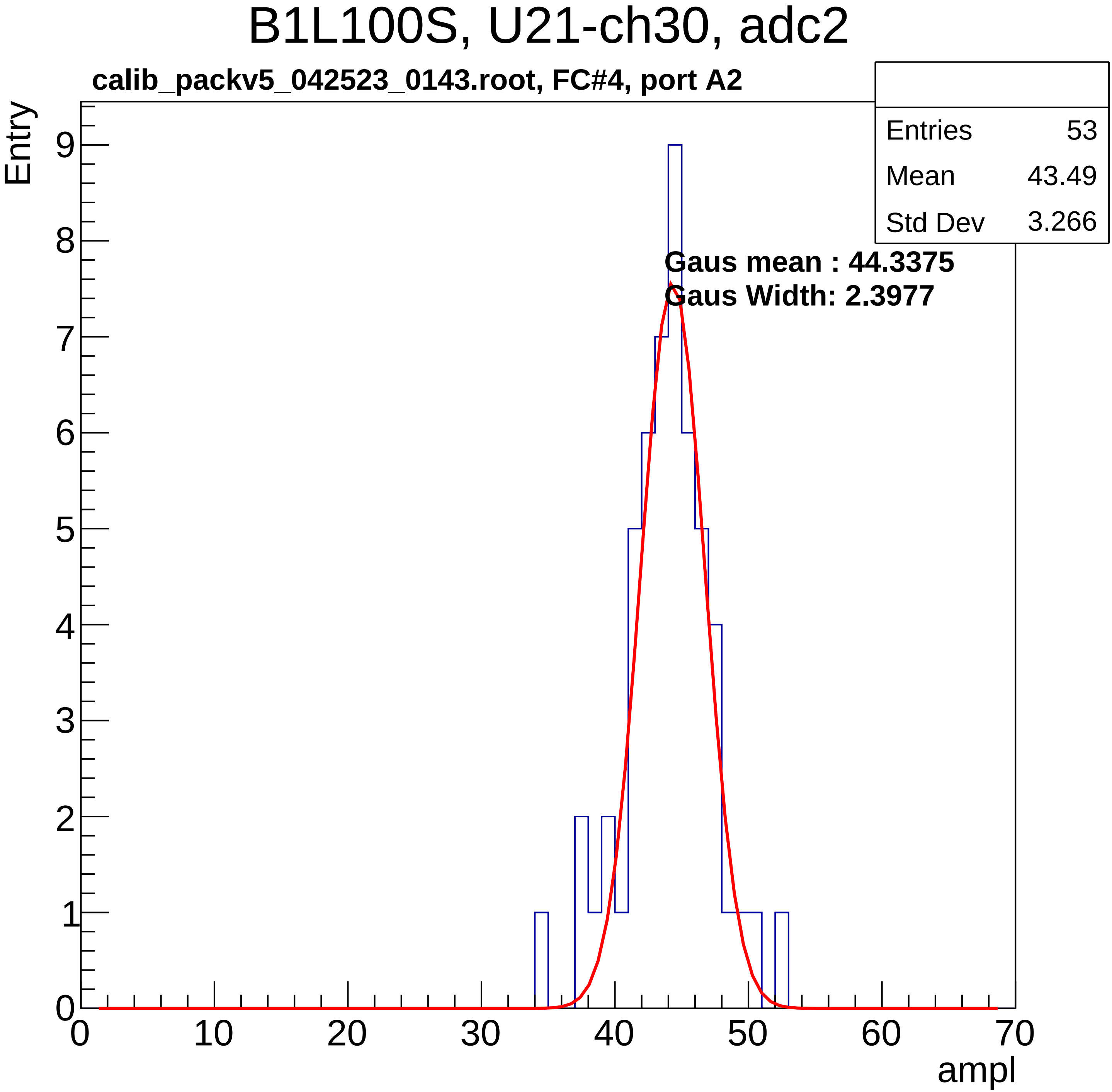
calib_packv5_042523_0143.root, FC#4, port A2

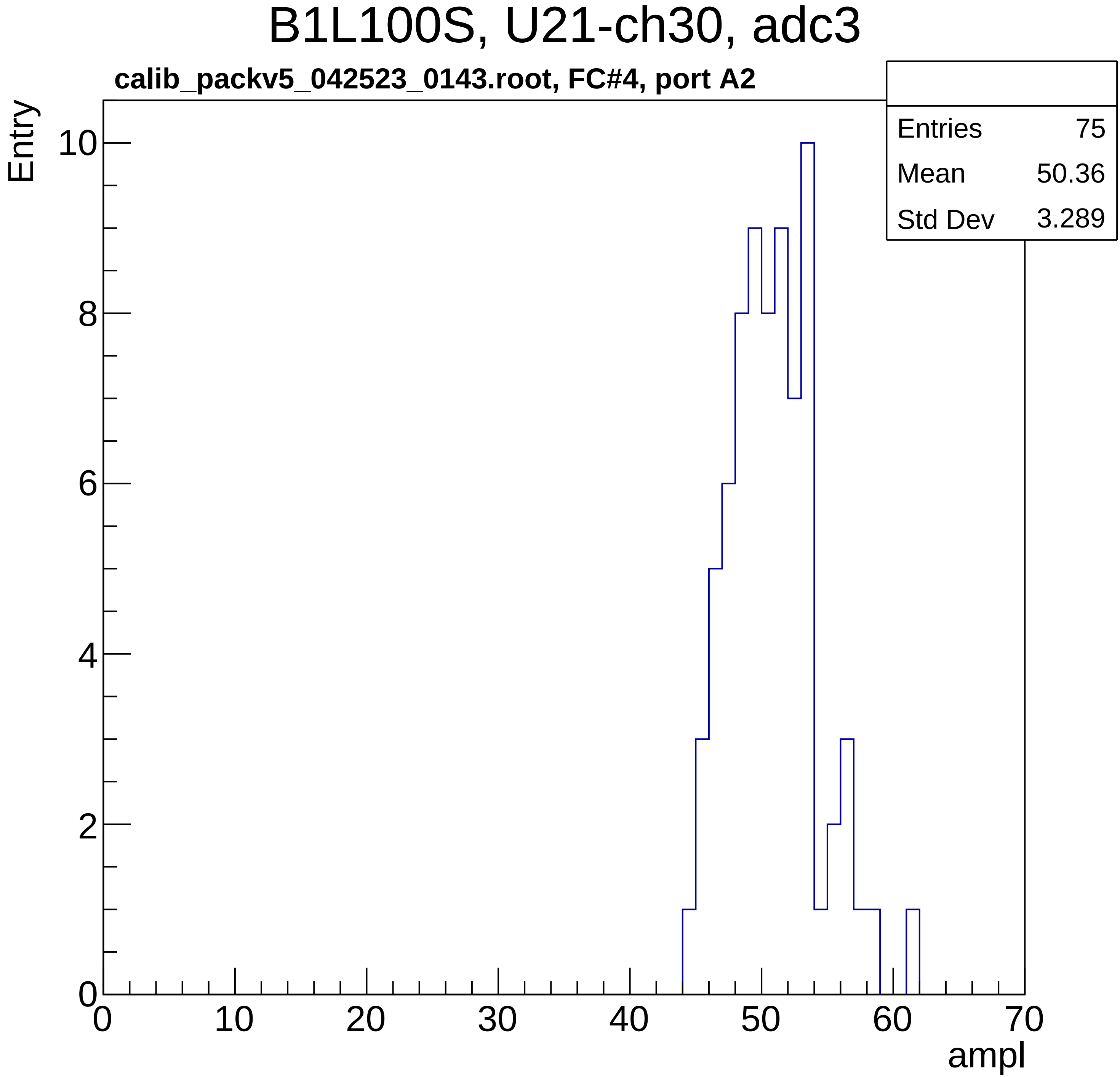


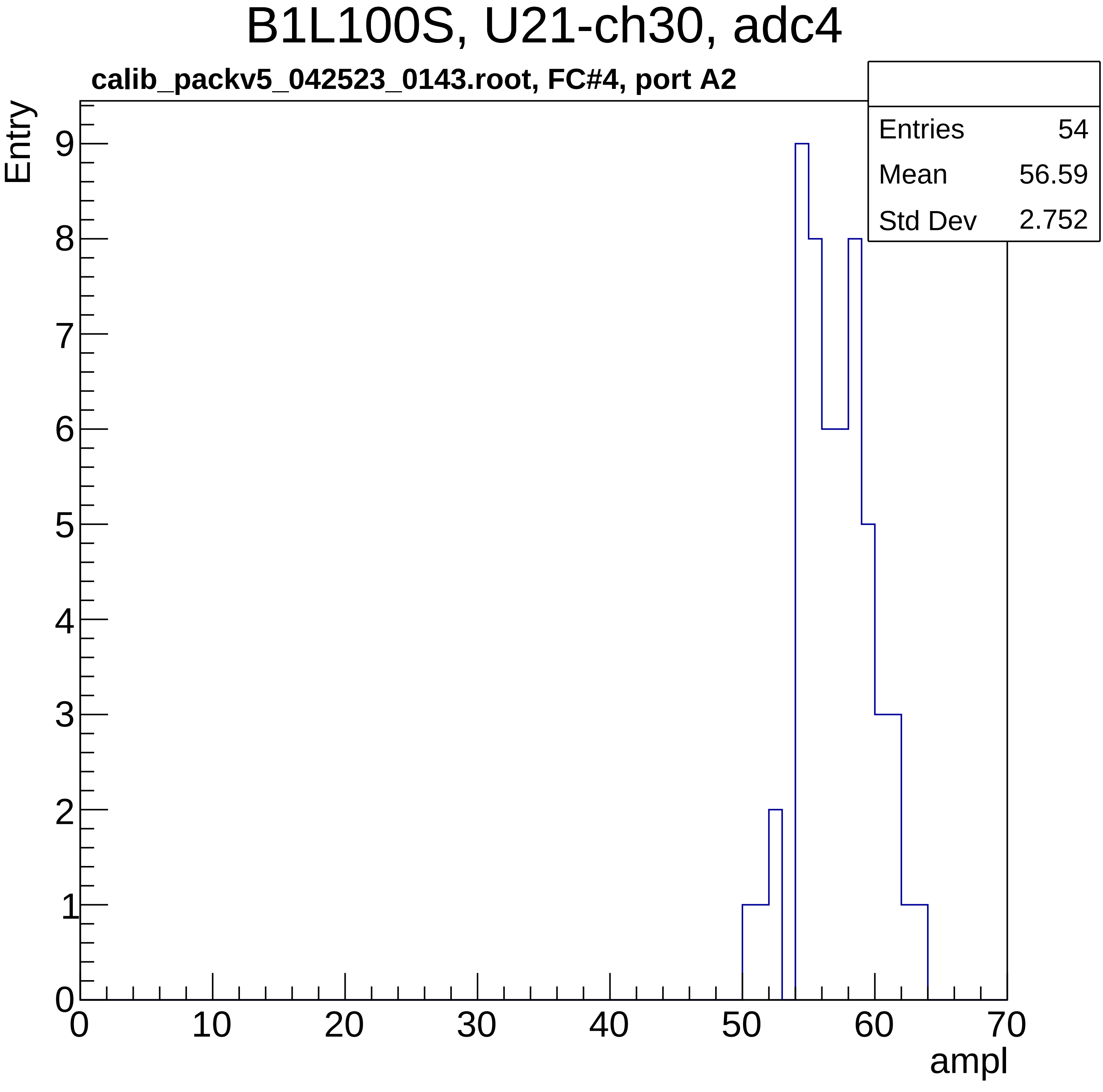

B1L100S, U21-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

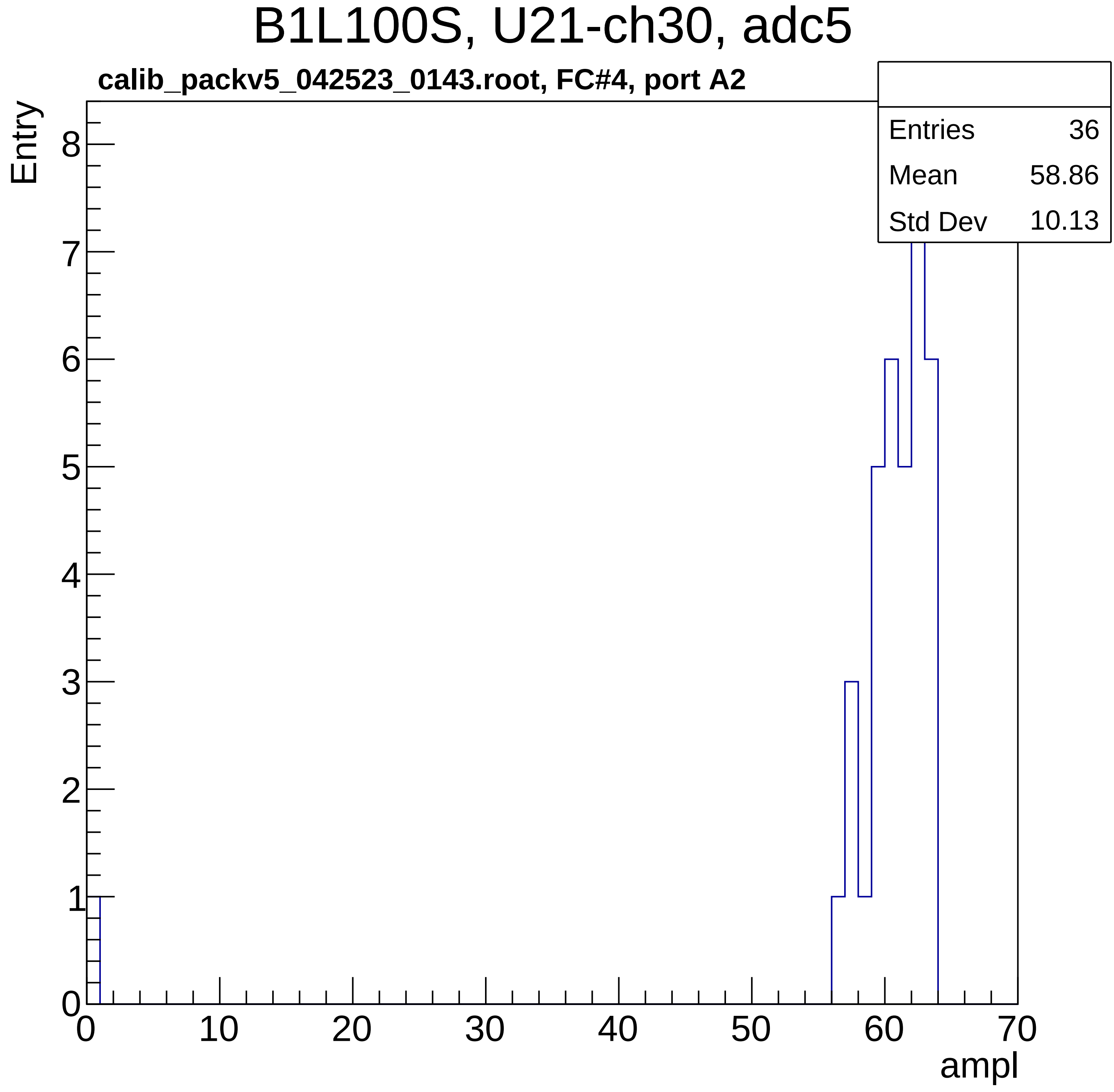

B1L100S, U21-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 71
Mean 29.66
Std Dev 3.331
Gaus mean : 30.0170
Gaus Width: 3.6515
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.29
Std Dev 3.661
Gaus mean : 38.0591
Gaus Width: 3.8012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

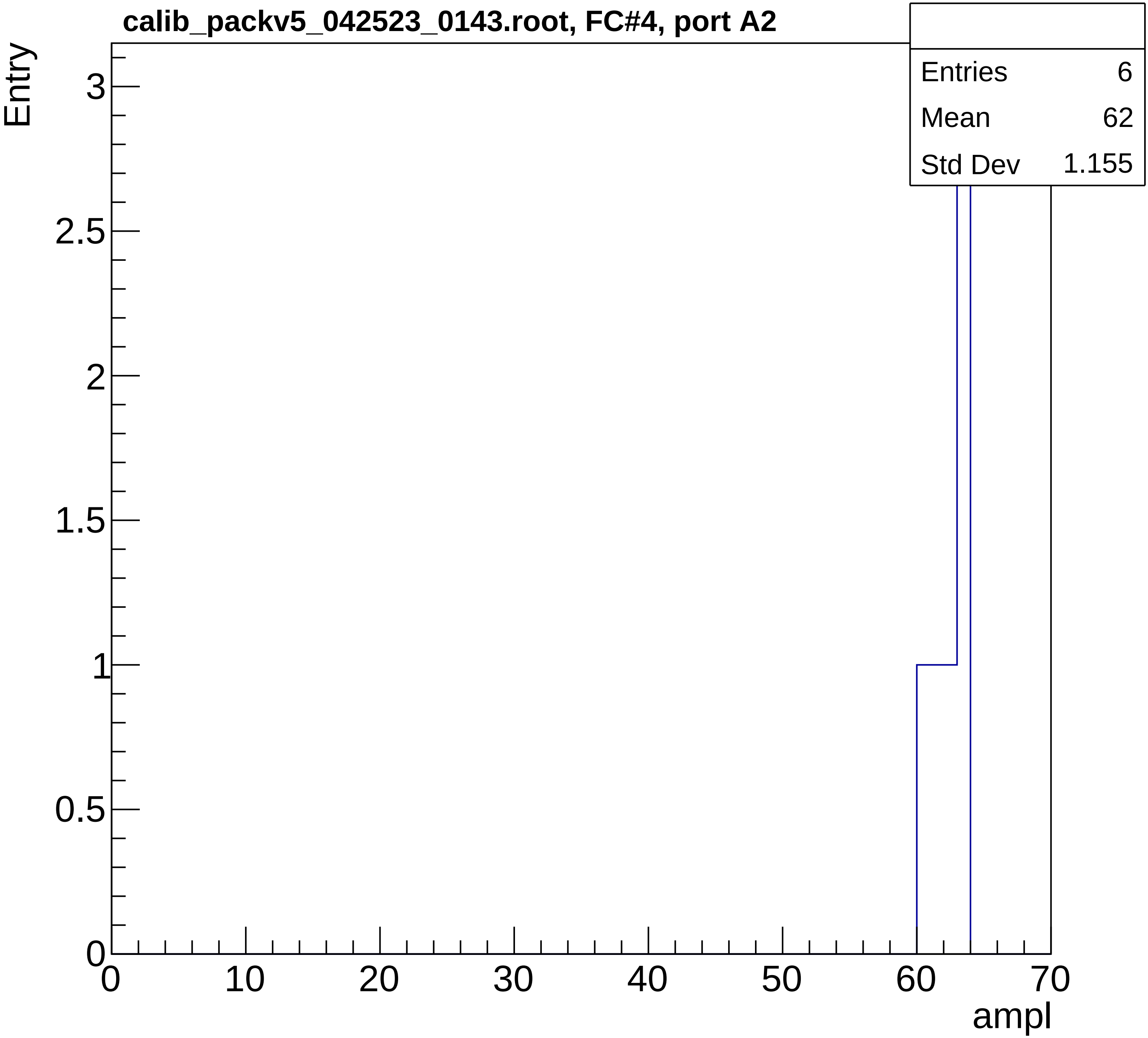

B1L100S, U21-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 53
Mean 43.49
Std Dev 3.266
Gaus mean : 44.3375
Gaus Width: 2.3977
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

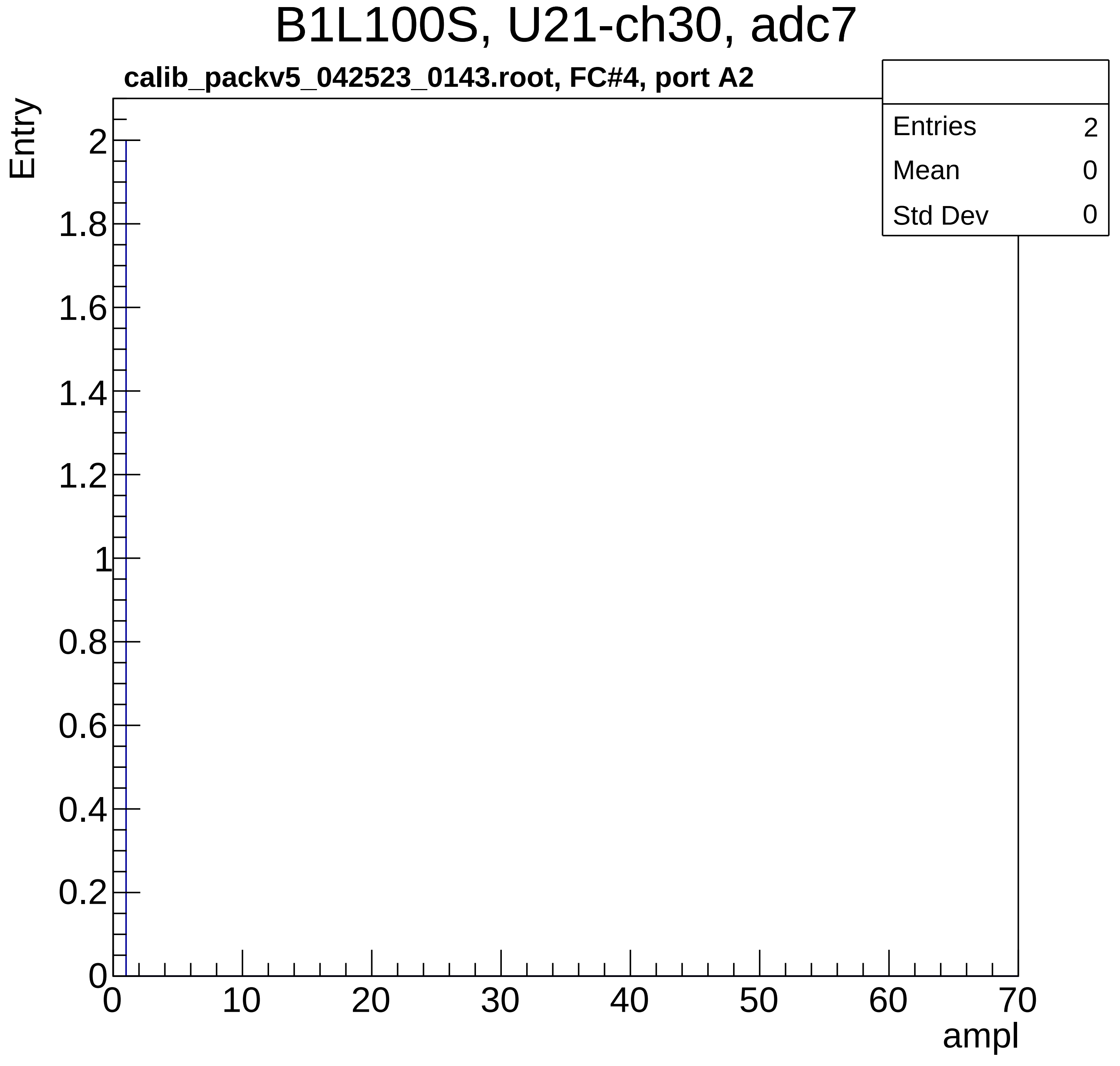

B1L100S, U21-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.36
Std Dev 3.289
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

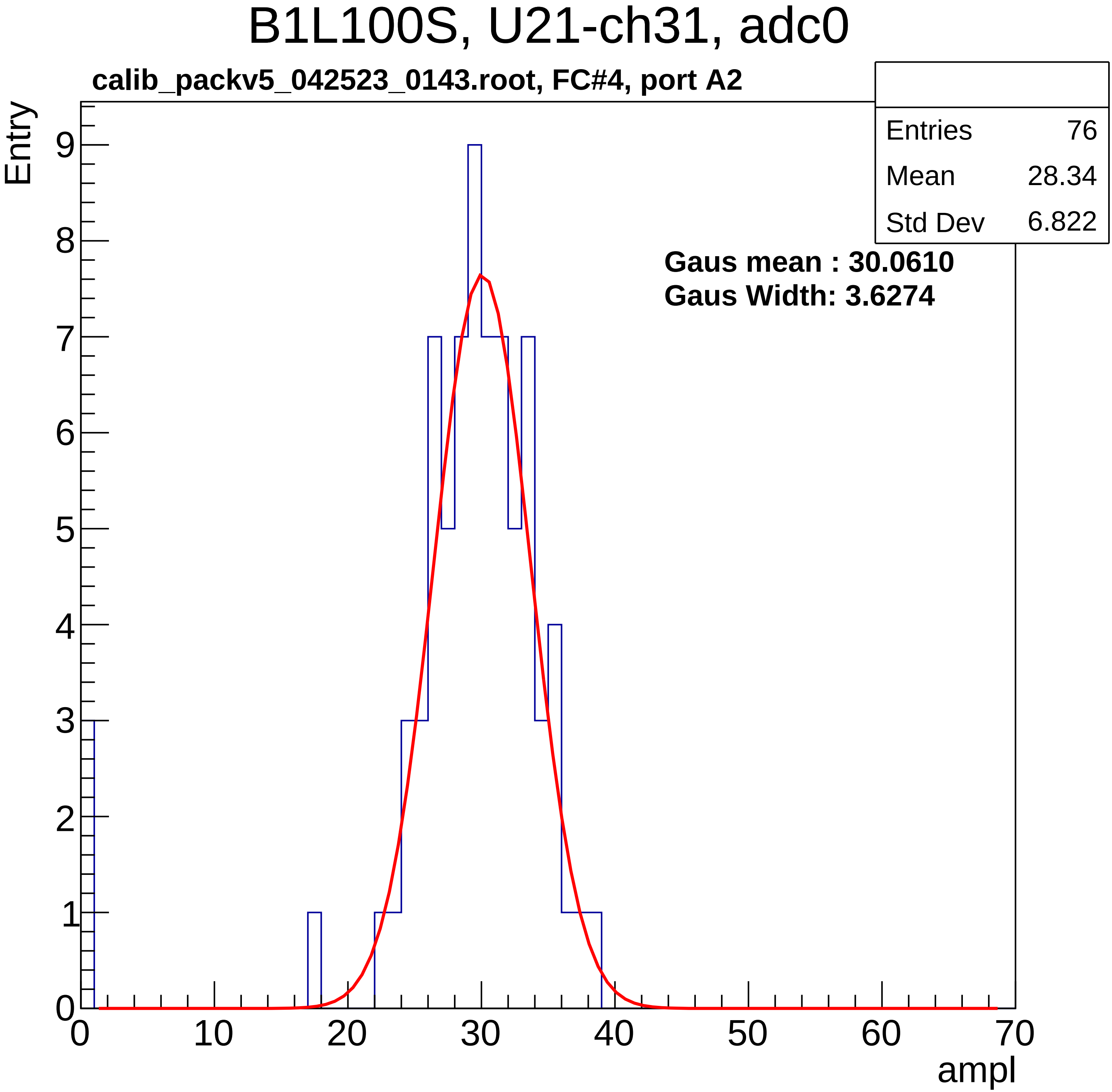

B1L100S, U21-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.59
Std Dev 2.752
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.86
Std Dev 10.13
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch30, adc6

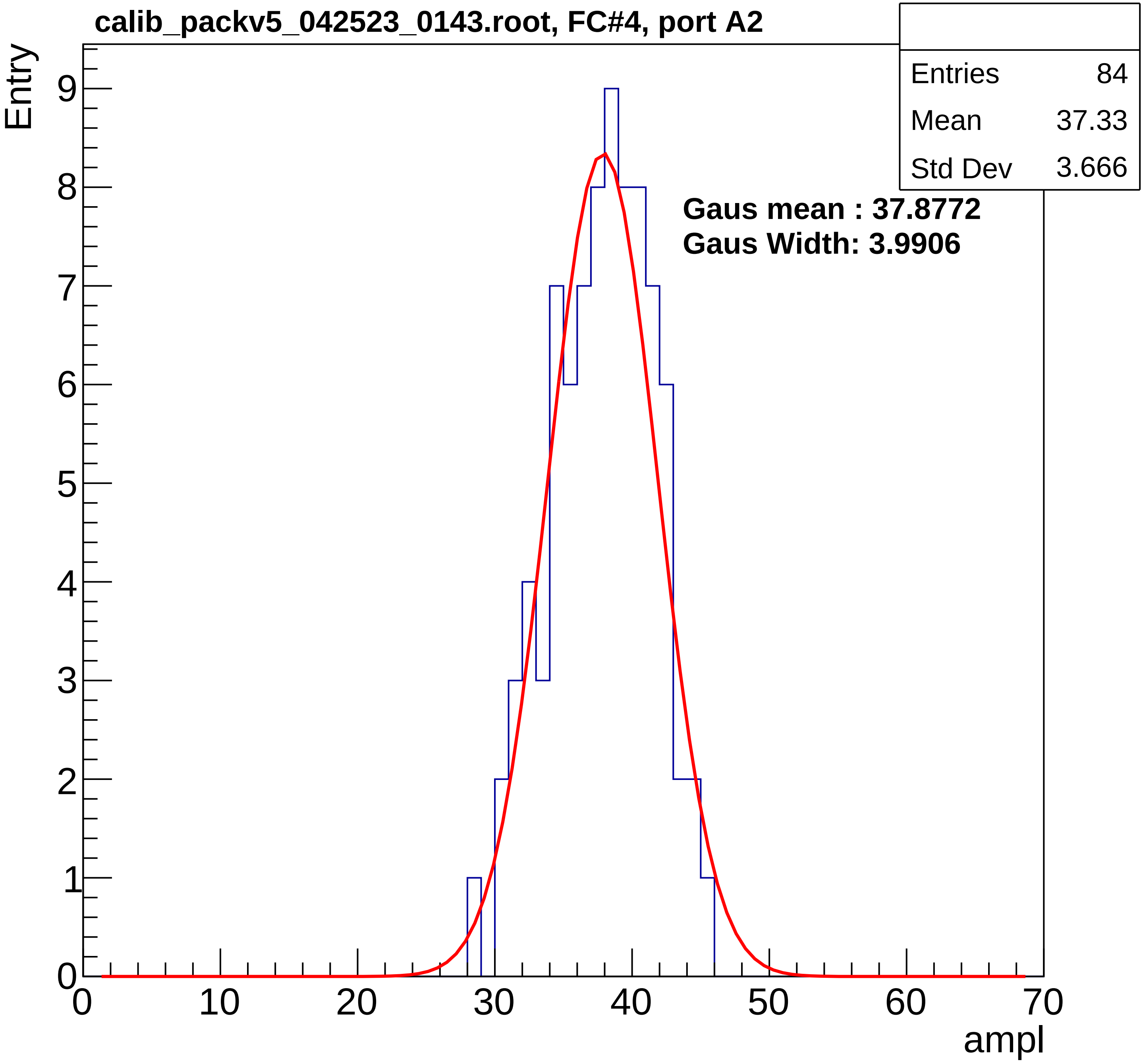



B1L100S, U21-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

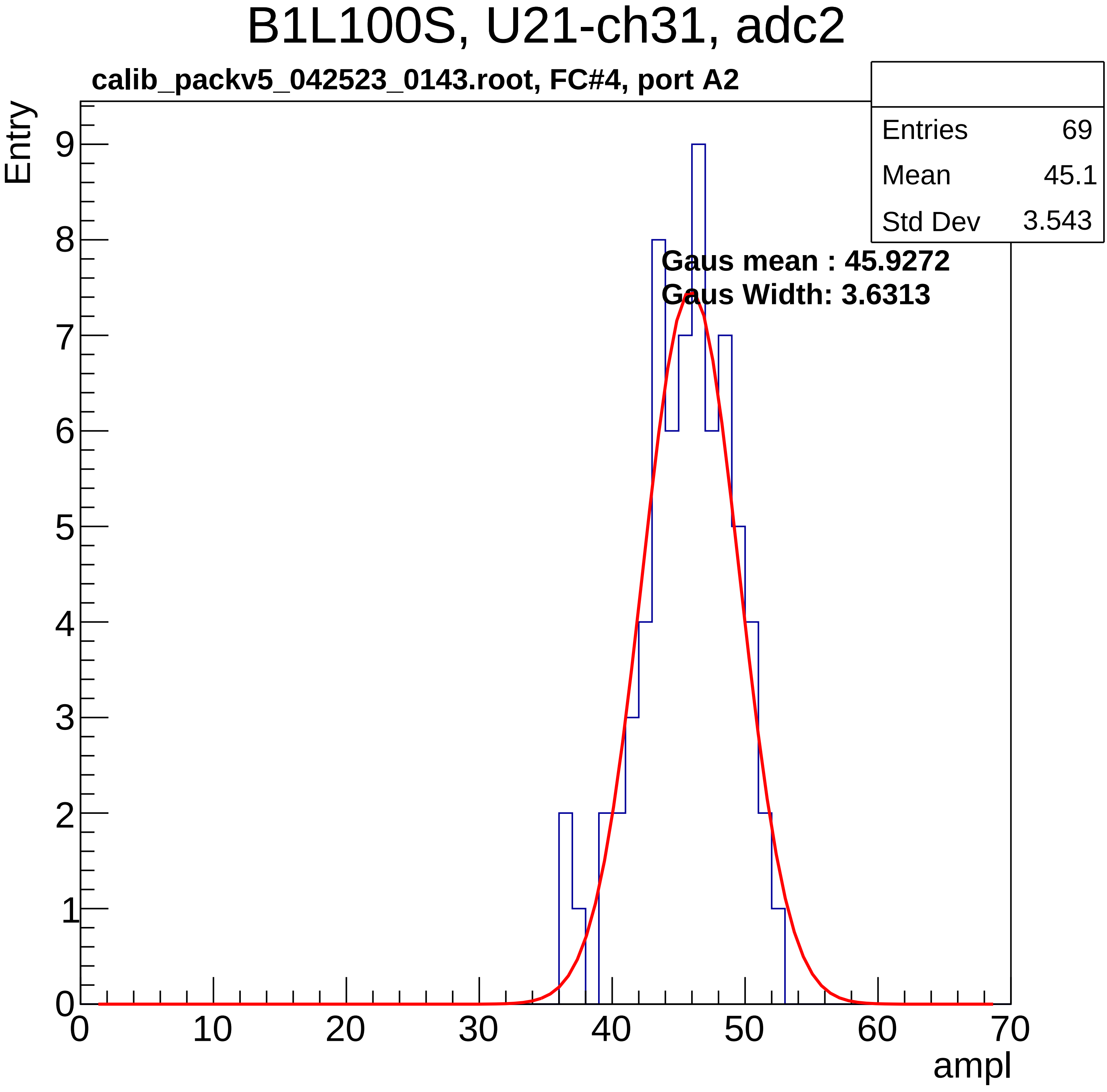

B1L100S, U21-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.34
Std Dev 6.822
Gaus mean : 30.0610
Gaus Width: 3.6274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

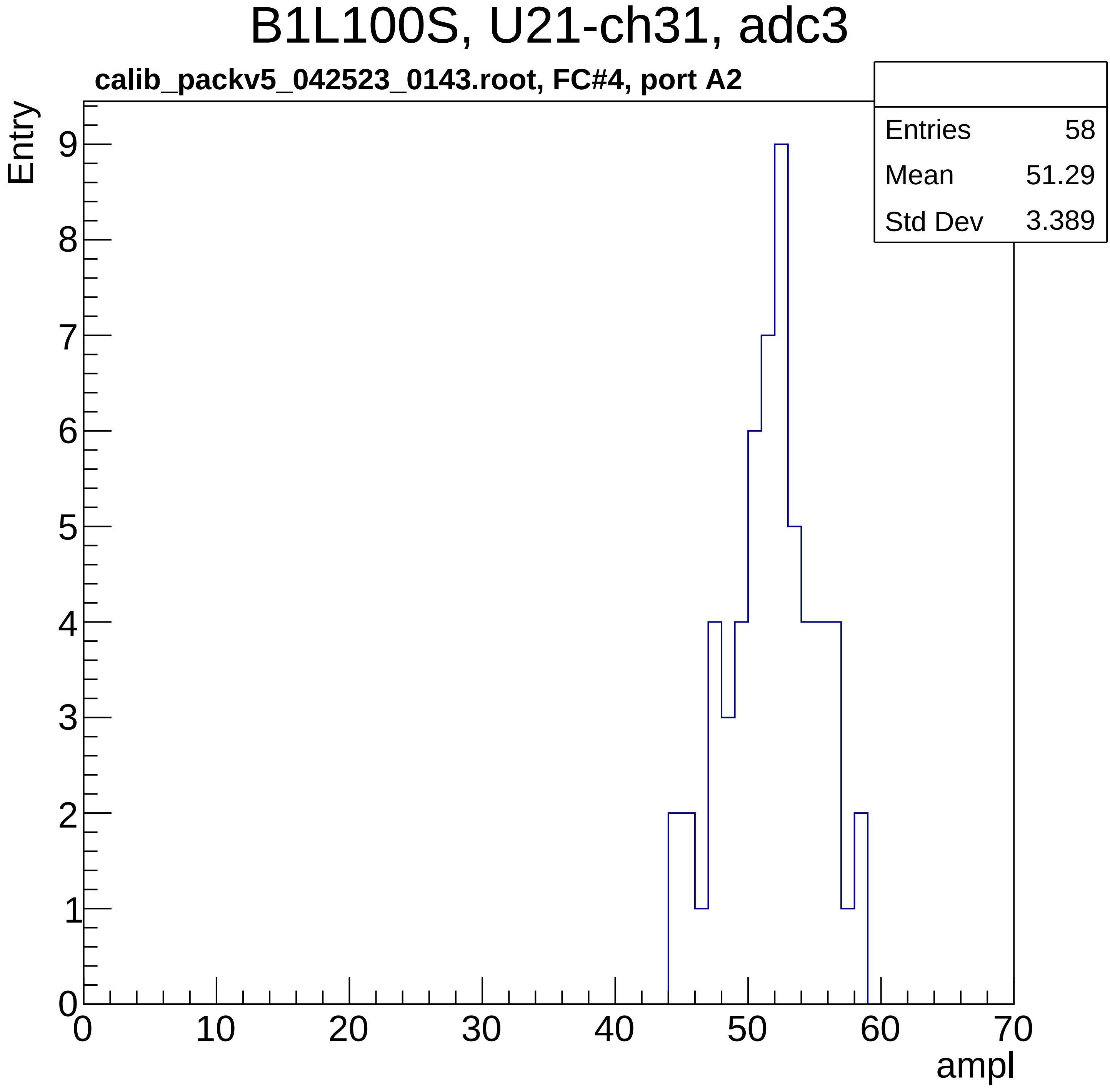
B1L100S, U21-ch31, adc1

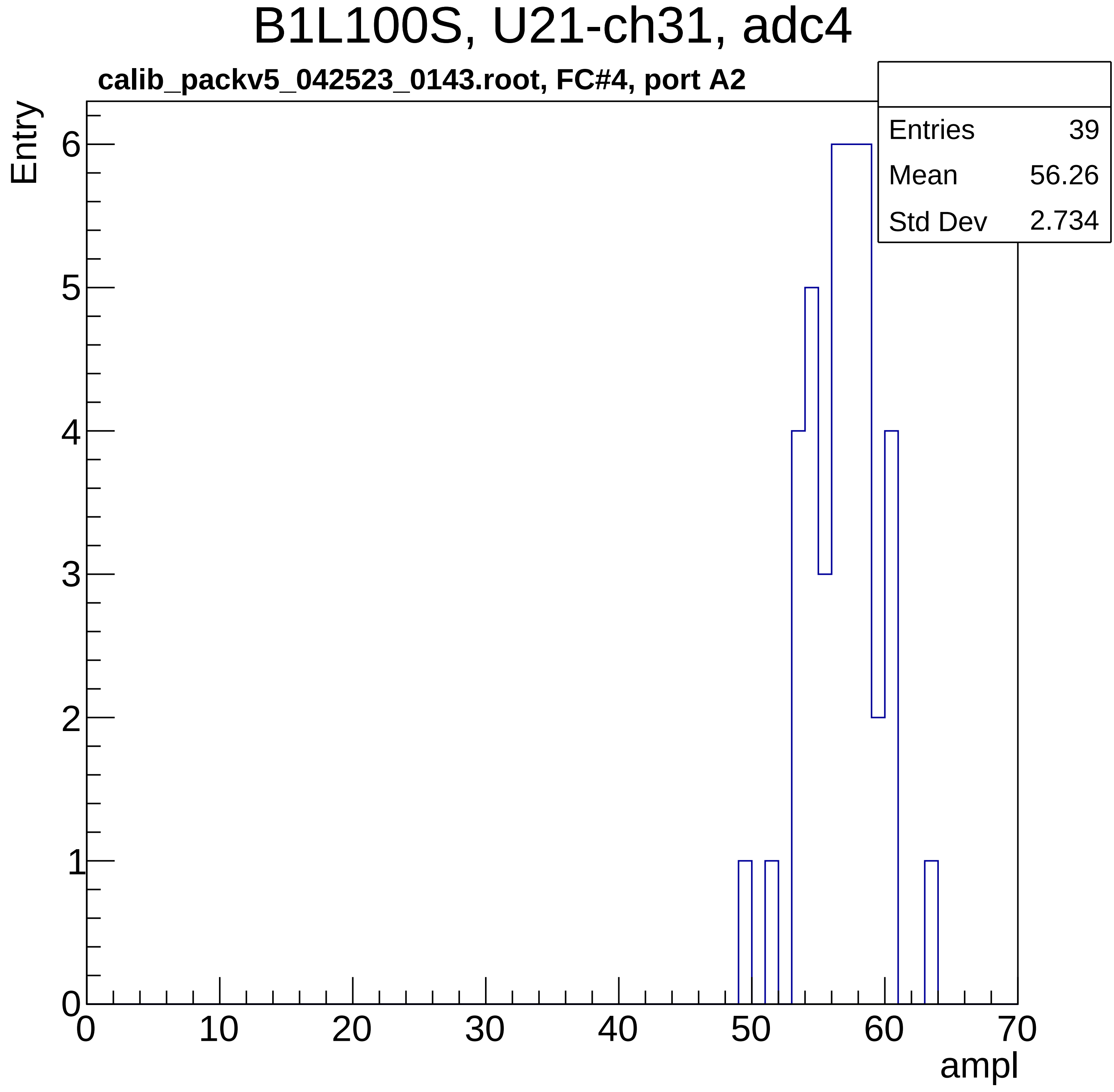
calib_packv5_042523_0143.root, FC#4, port A2

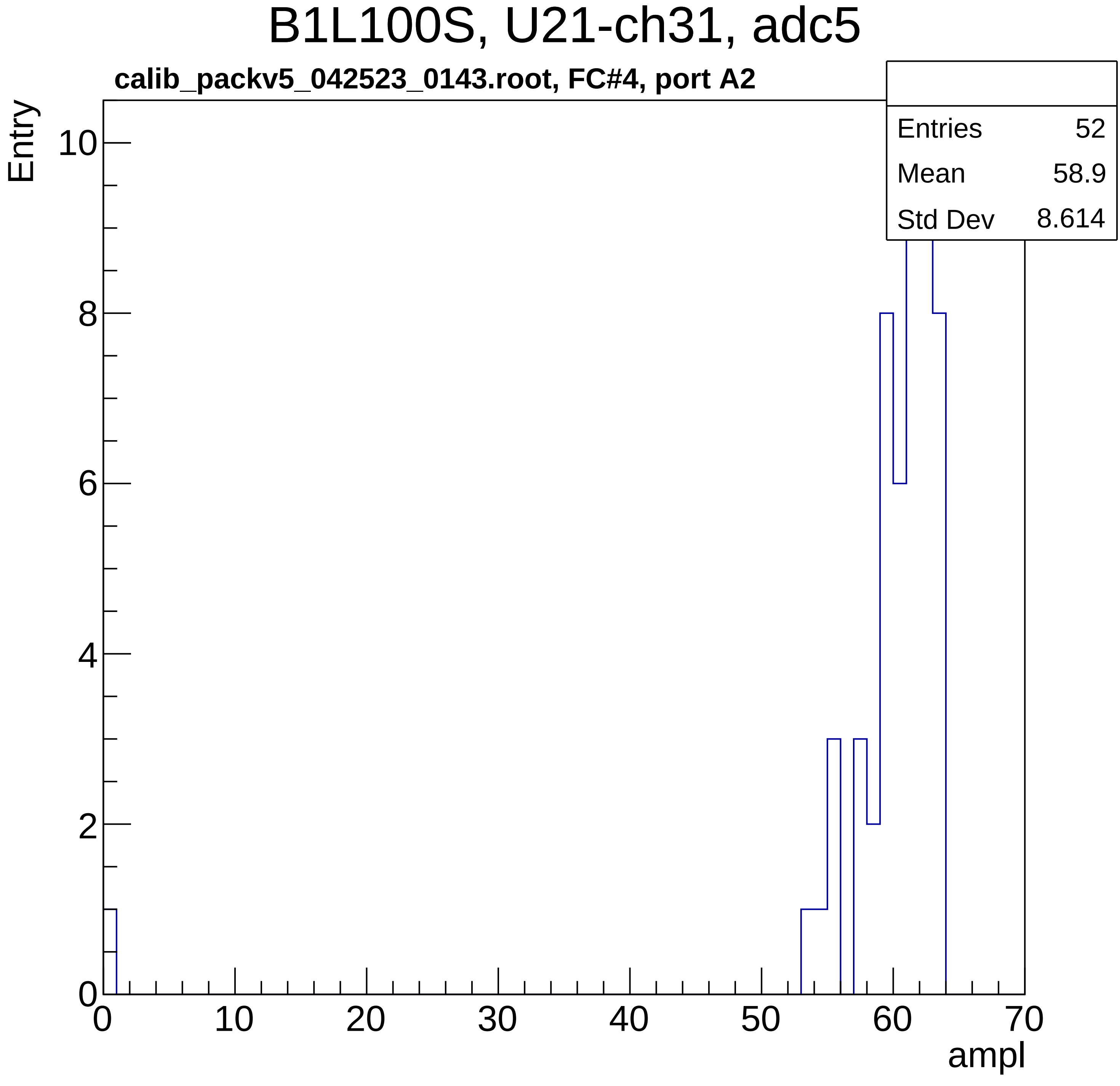


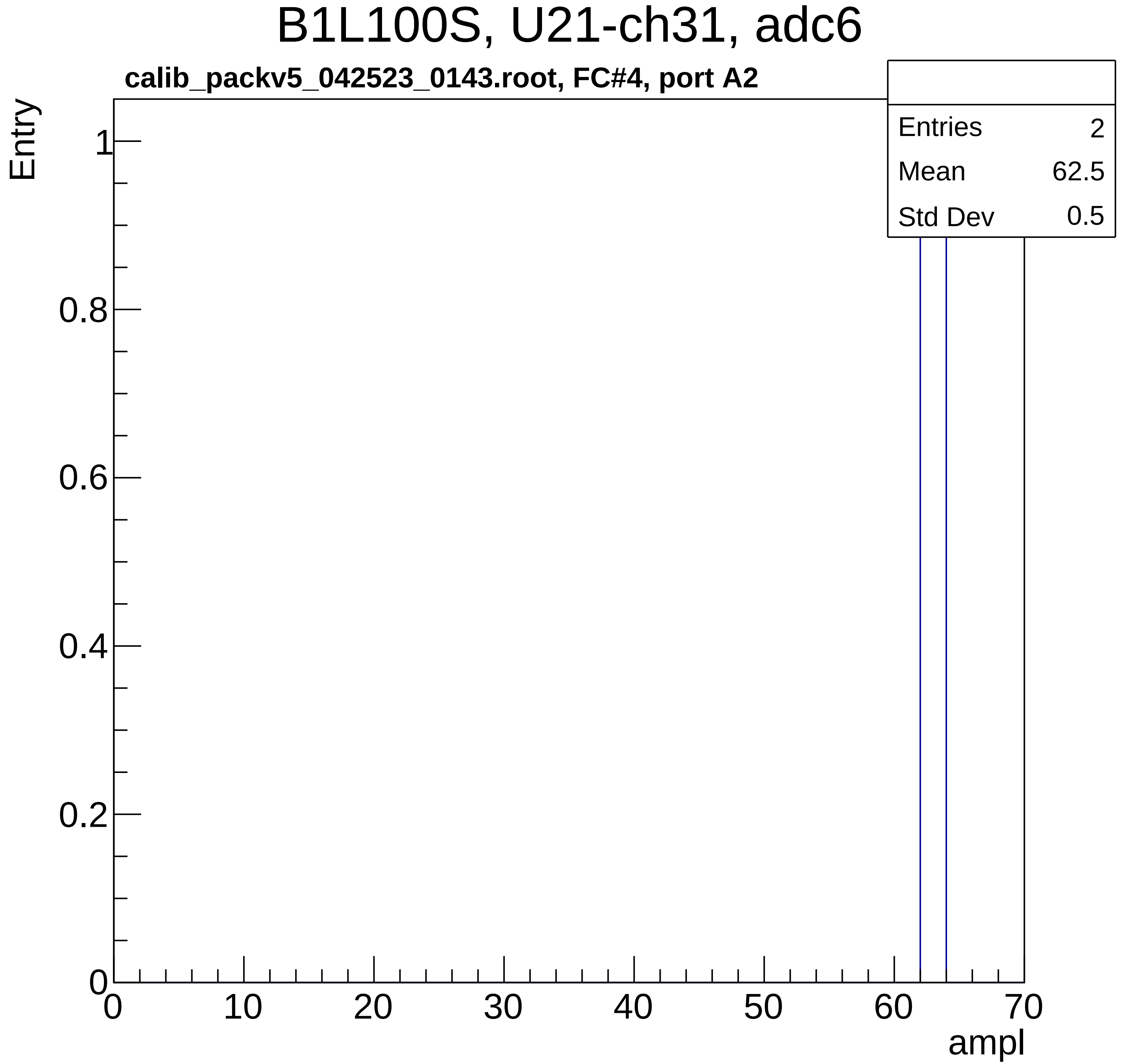
Gaus mean : 37.8772
Gaus Width: 3.9906

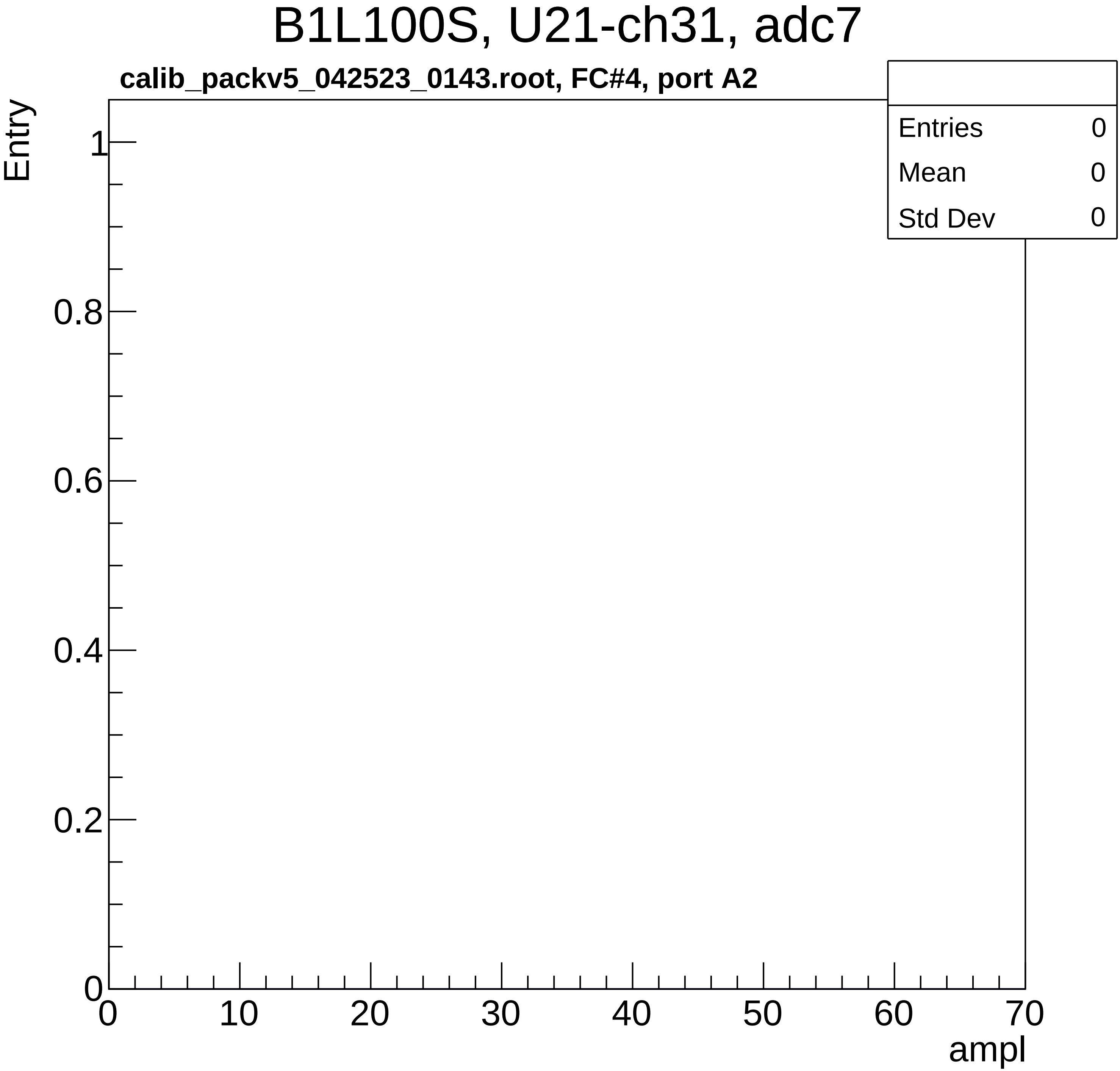

B1L100S, U21-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 45.1
Std Dev 3.543
Gaus mean : 45.9272
Gaus Width: 3.6313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

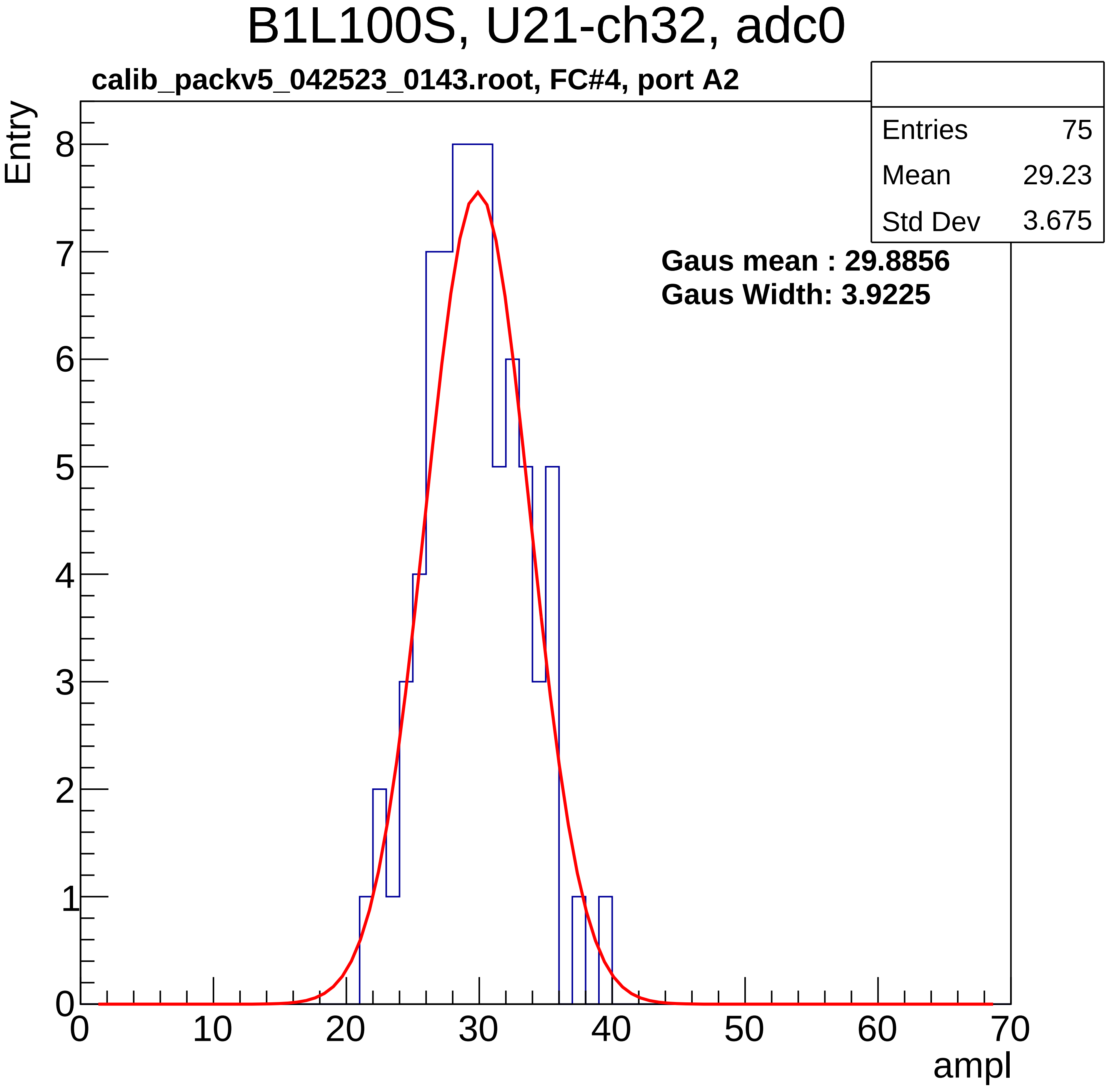

B1L100S, U21-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 51.29
Std Dev 3.389
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 56.26
Std Dev 2.734
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

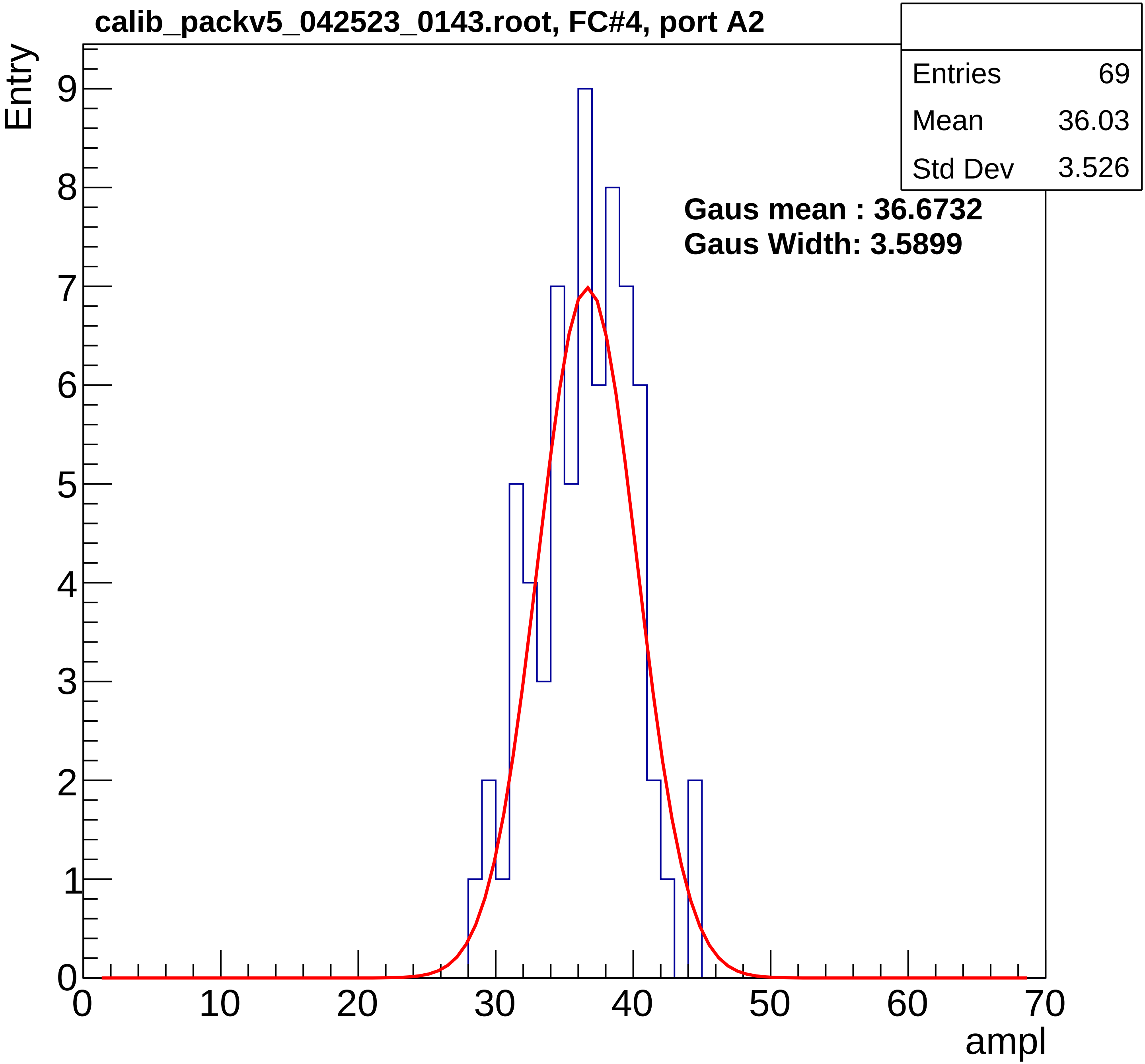

B1L100S, U21-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 52
Mean 58.9
Std Dev 8.614
0
2
4
6
8
10
0
10
20
30
40
50
60
70

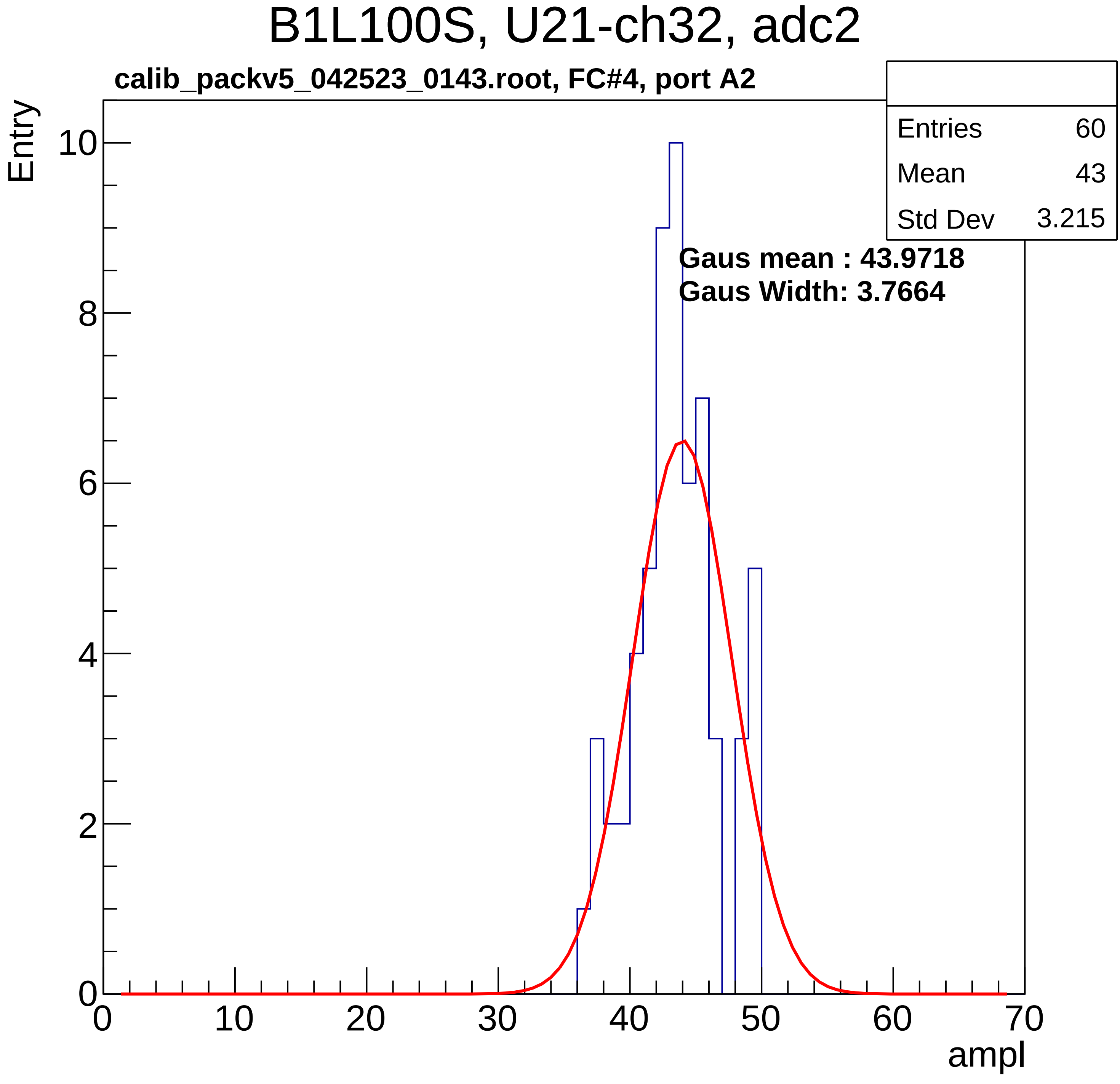

B1L100S, U21-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

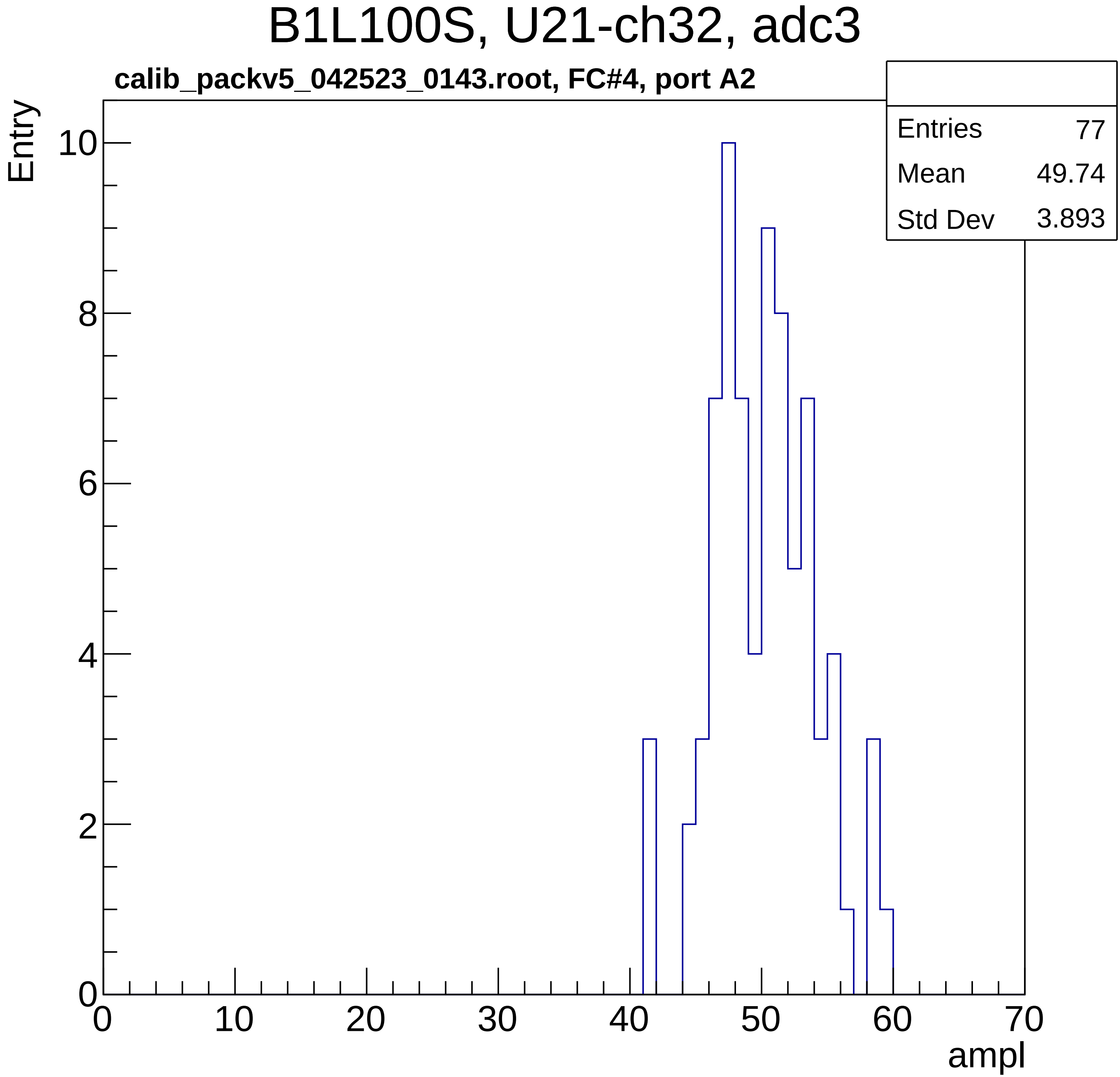

B1L100S, U21-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

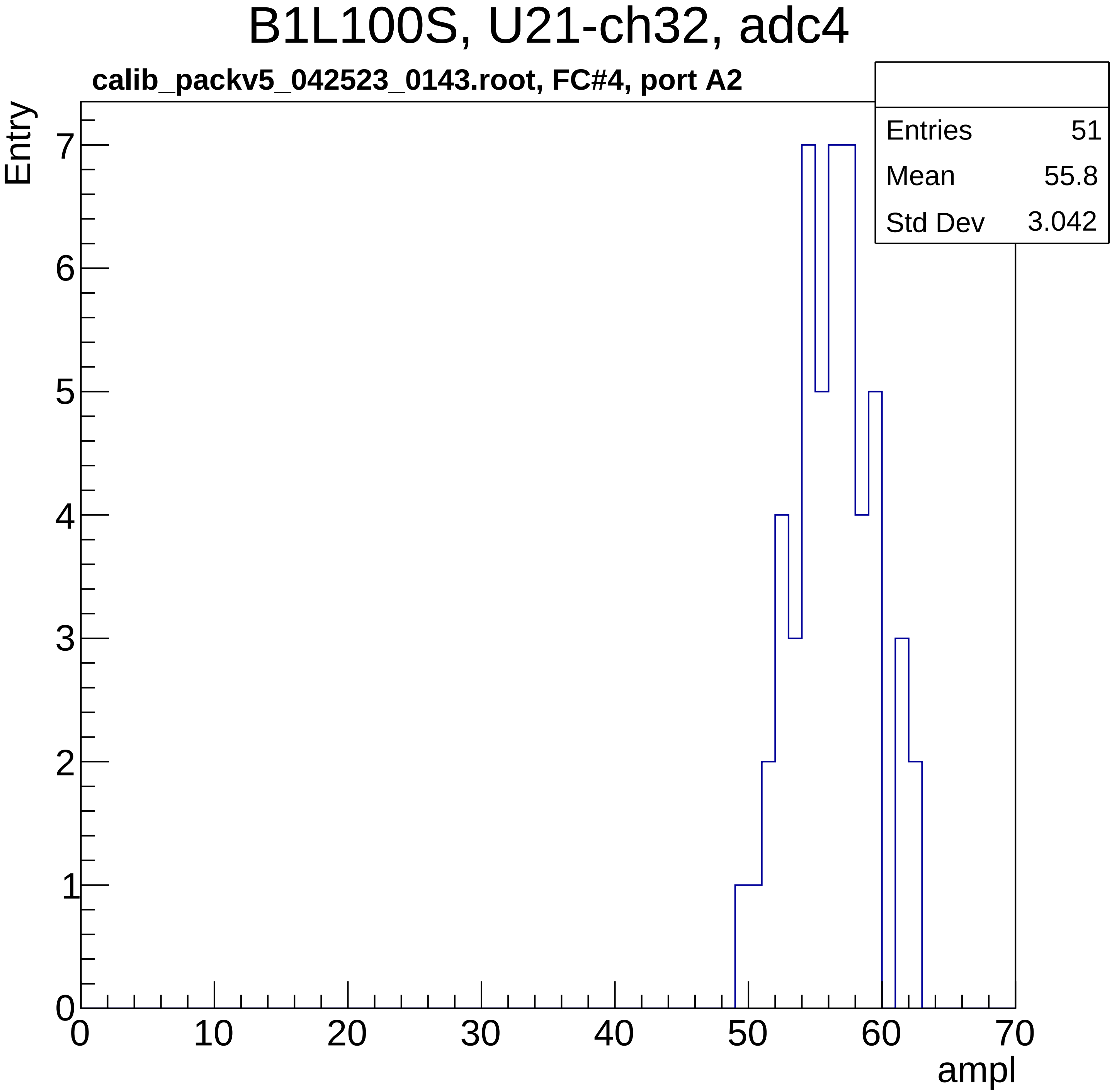

B1L100S, U21-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.23
Std Dev 3.675
Gaus mean : 29.8856
Gaus Width: 3.9225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

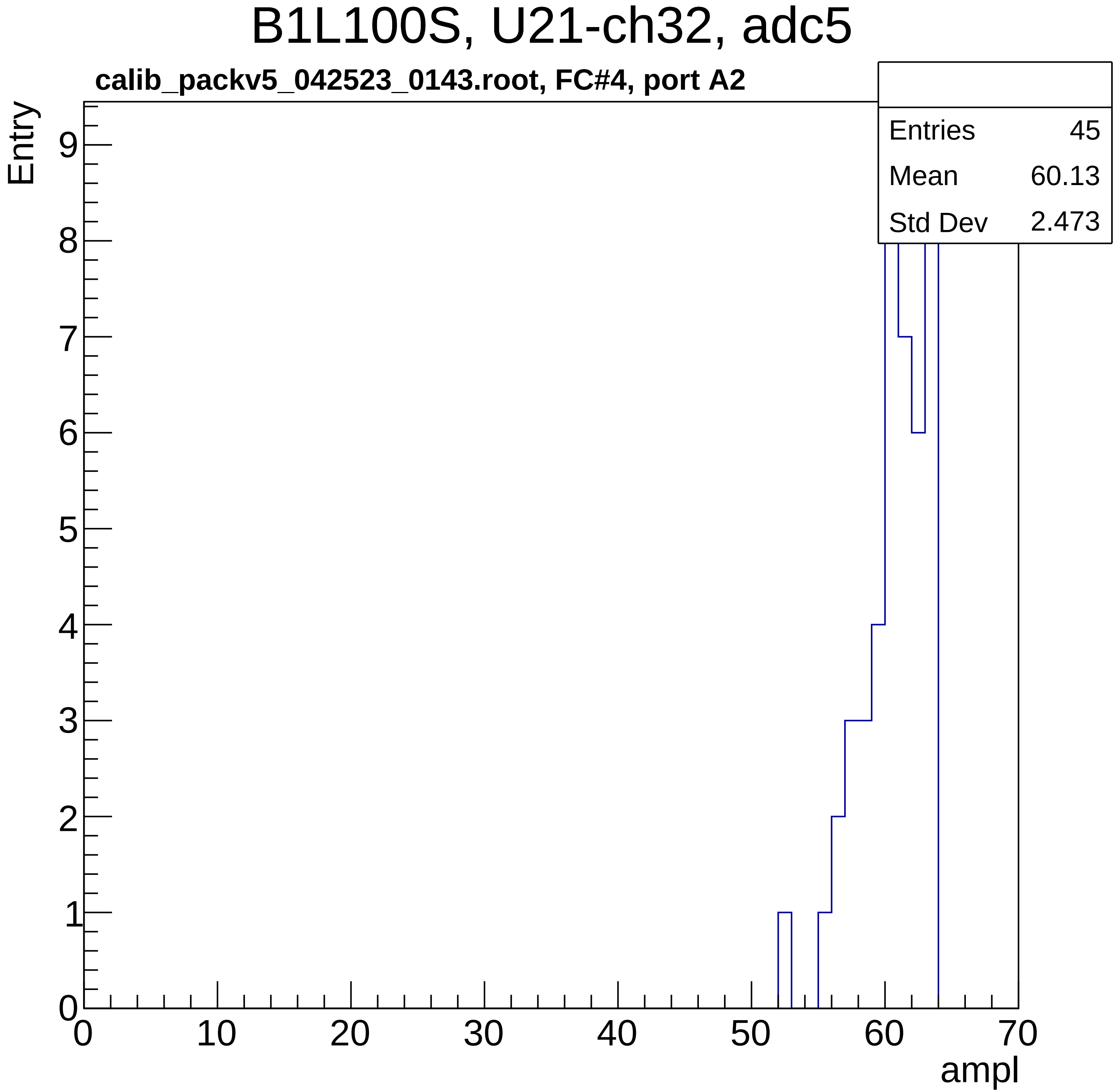
B1L100S, U21-ch32, adc1

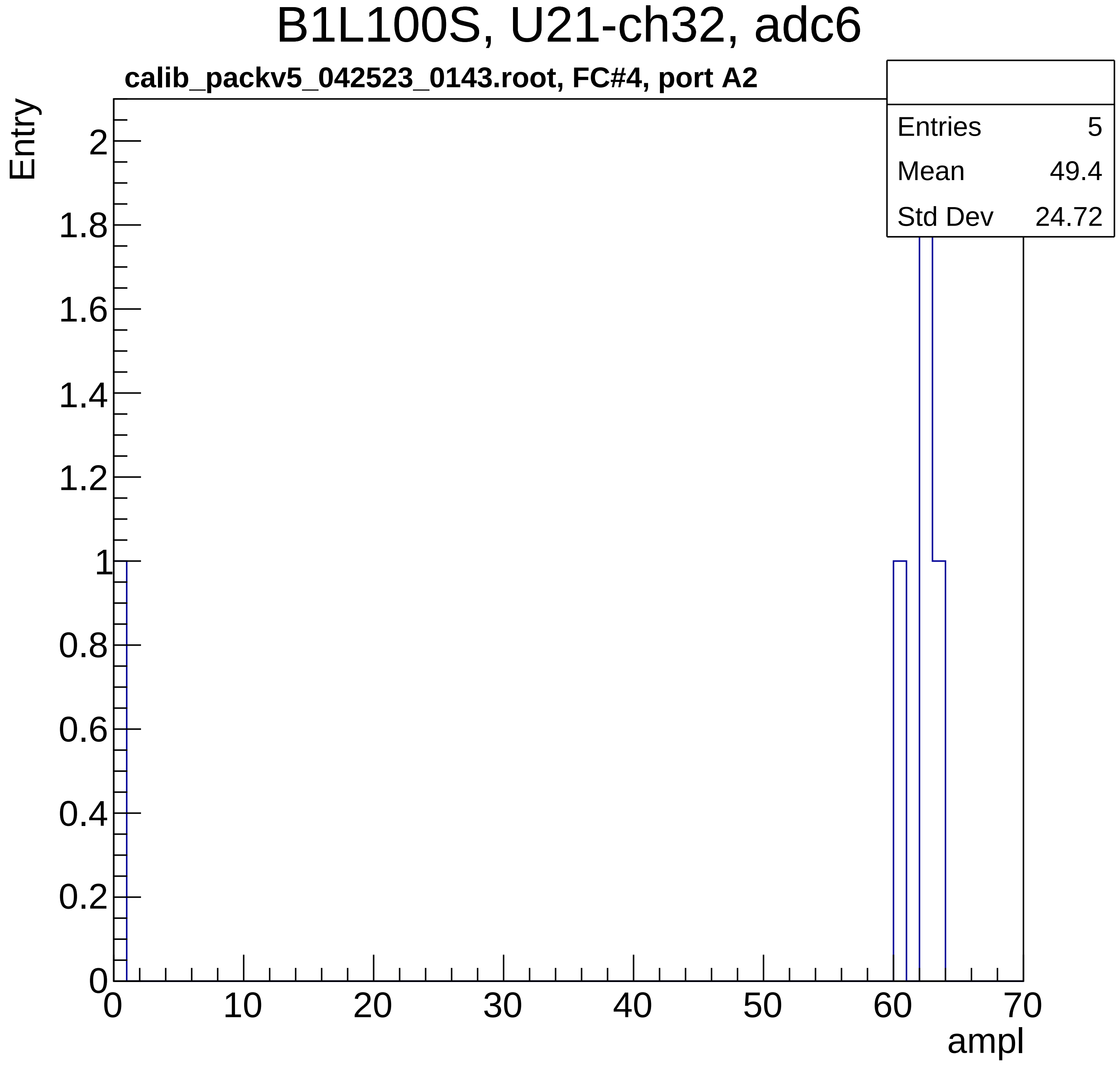


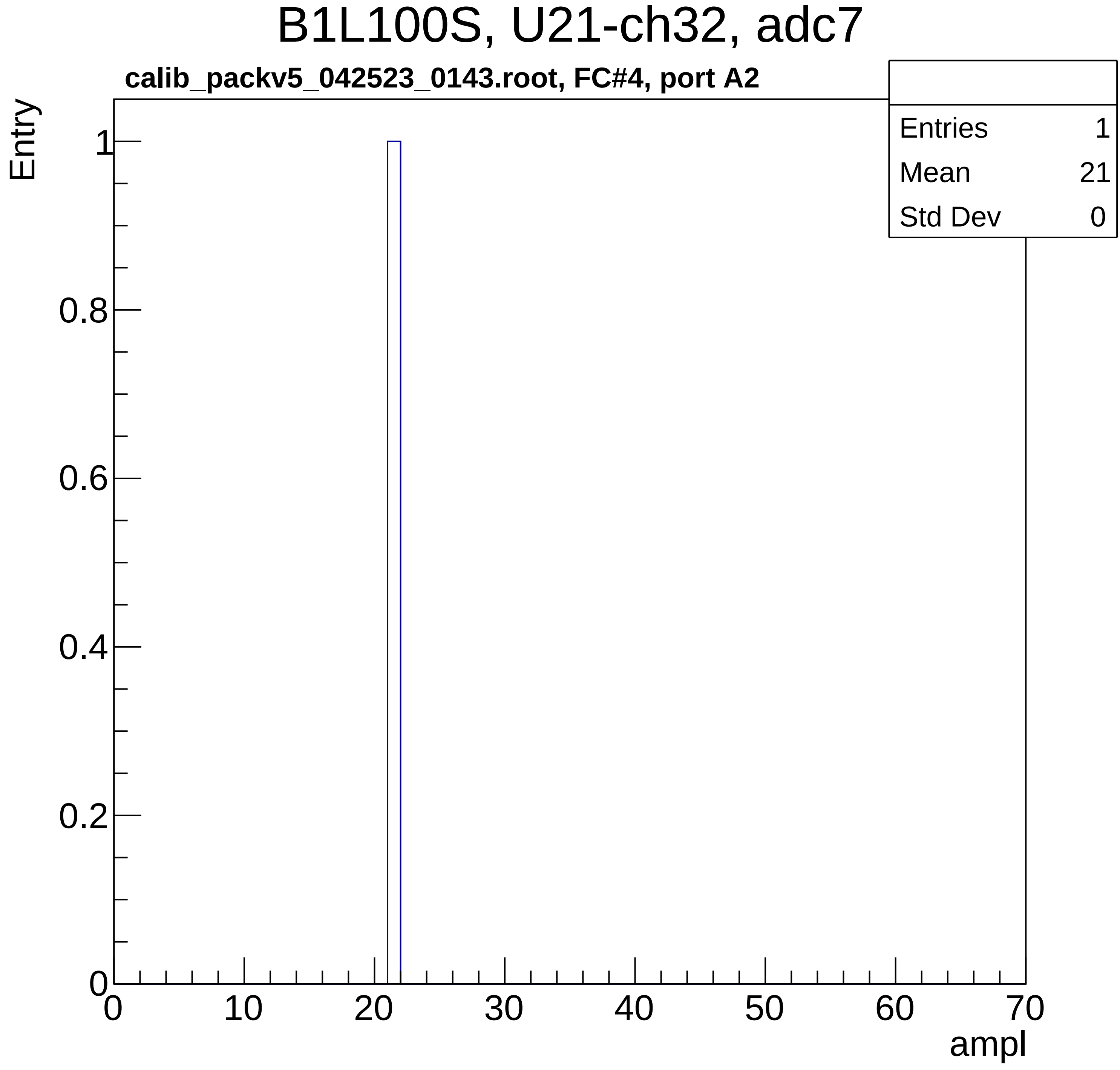

B1L100S, U21-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43
Std Dev 3.215
Gaus mean : 43.9718
Gaus Width: 3.7664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

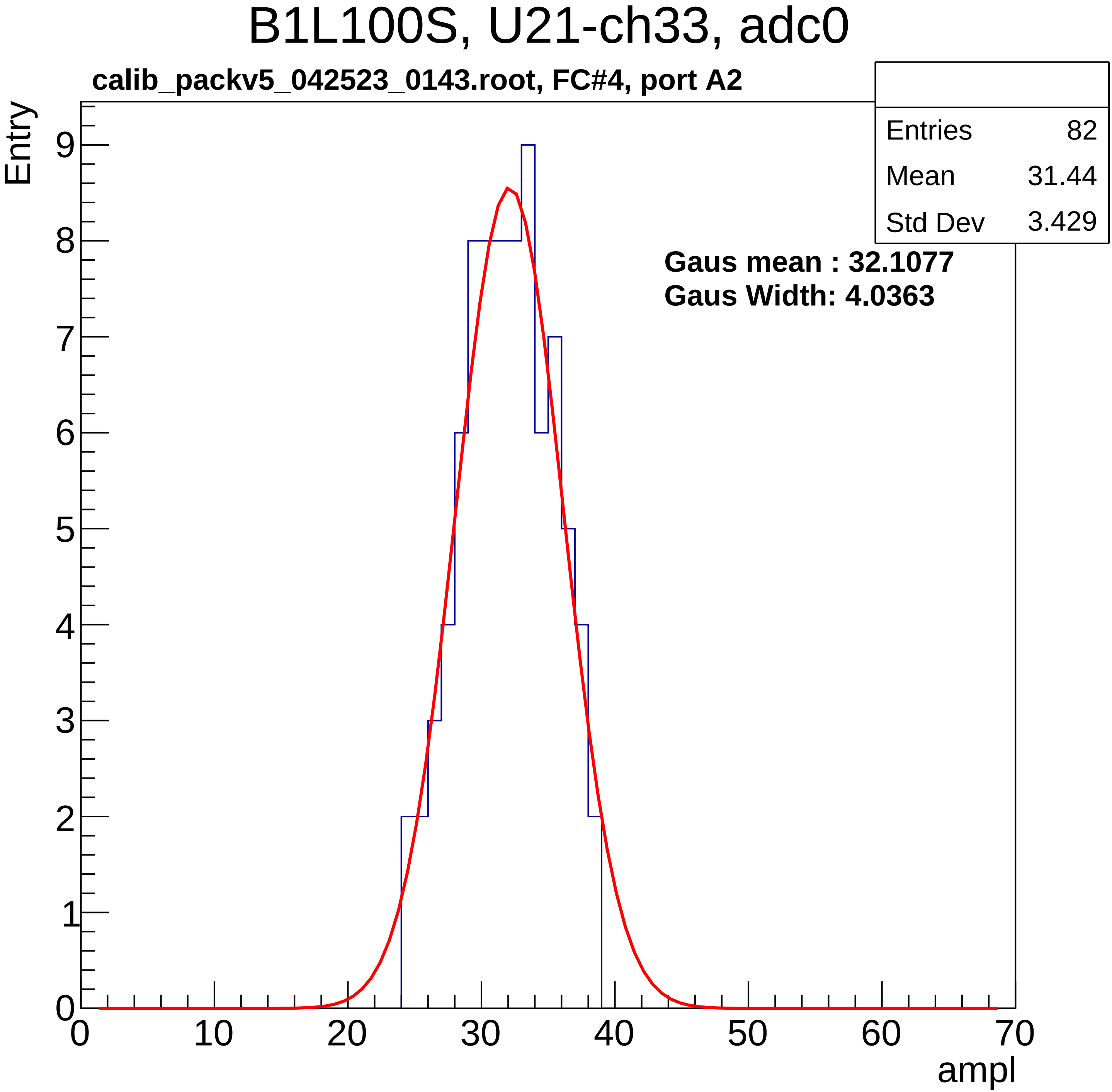

B1L100S, U21-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 49.74
Std Dev 3.893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.8
Std Dev 3.042
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

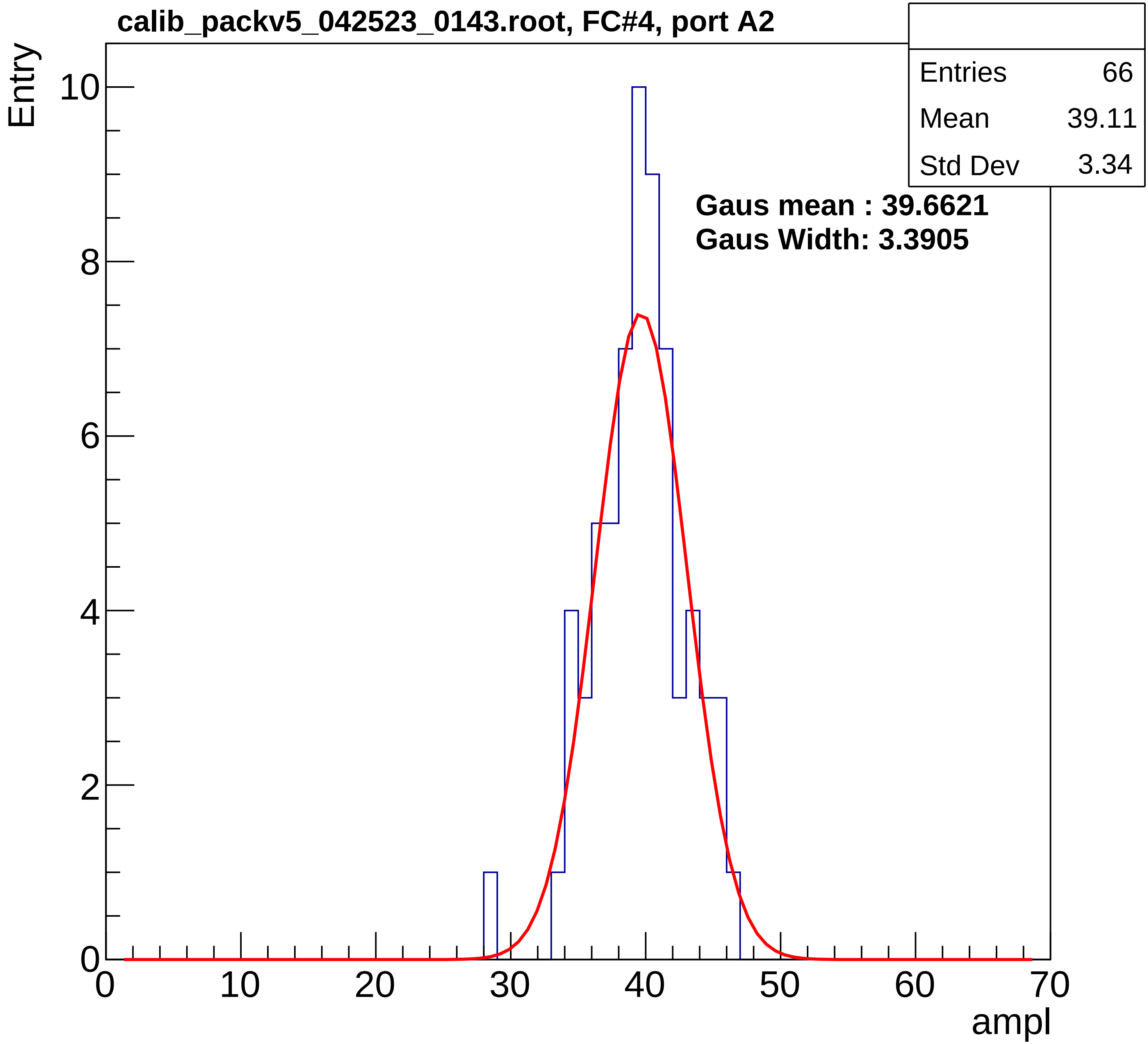

B1L100S, U21-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 60.13
Std Dev 2.473
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

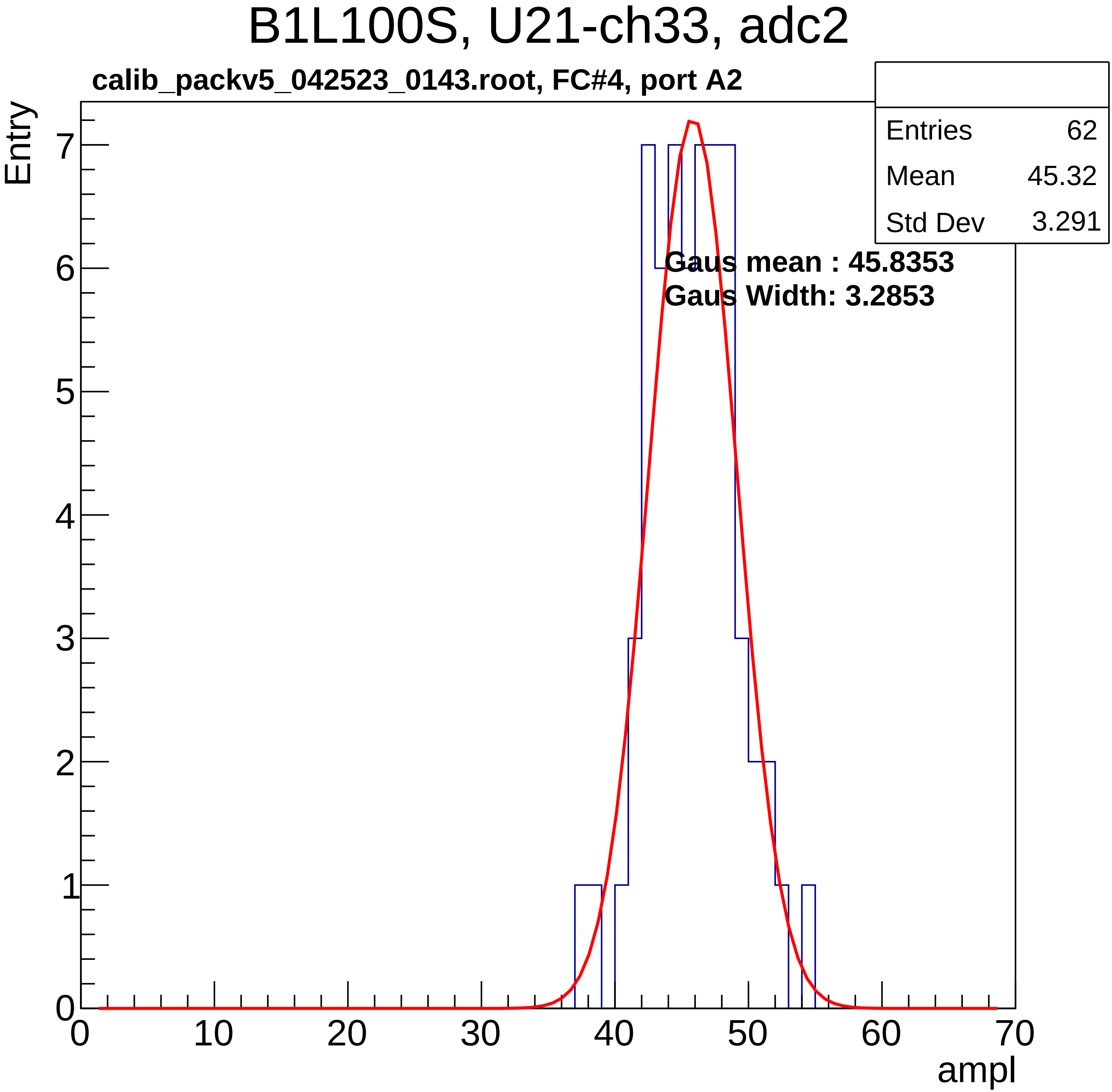

B1L100S, U21-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 5
Mean 49.4
Std Dev 24.72
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

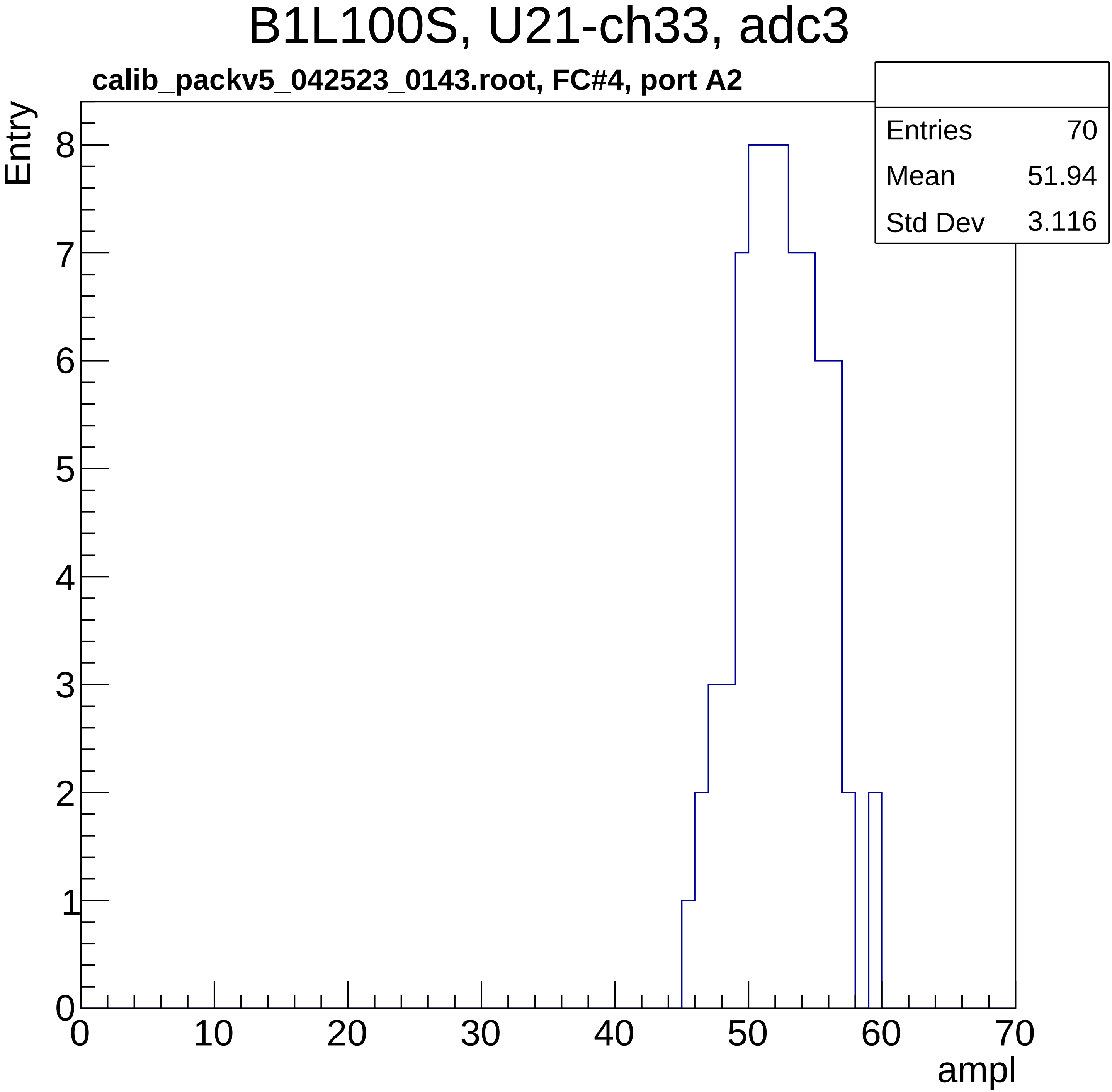

B1L100S, U21-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 31.44
Std Dev 3.429
Gaus mean : 32.1077
Gaus Width: 4.0363
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

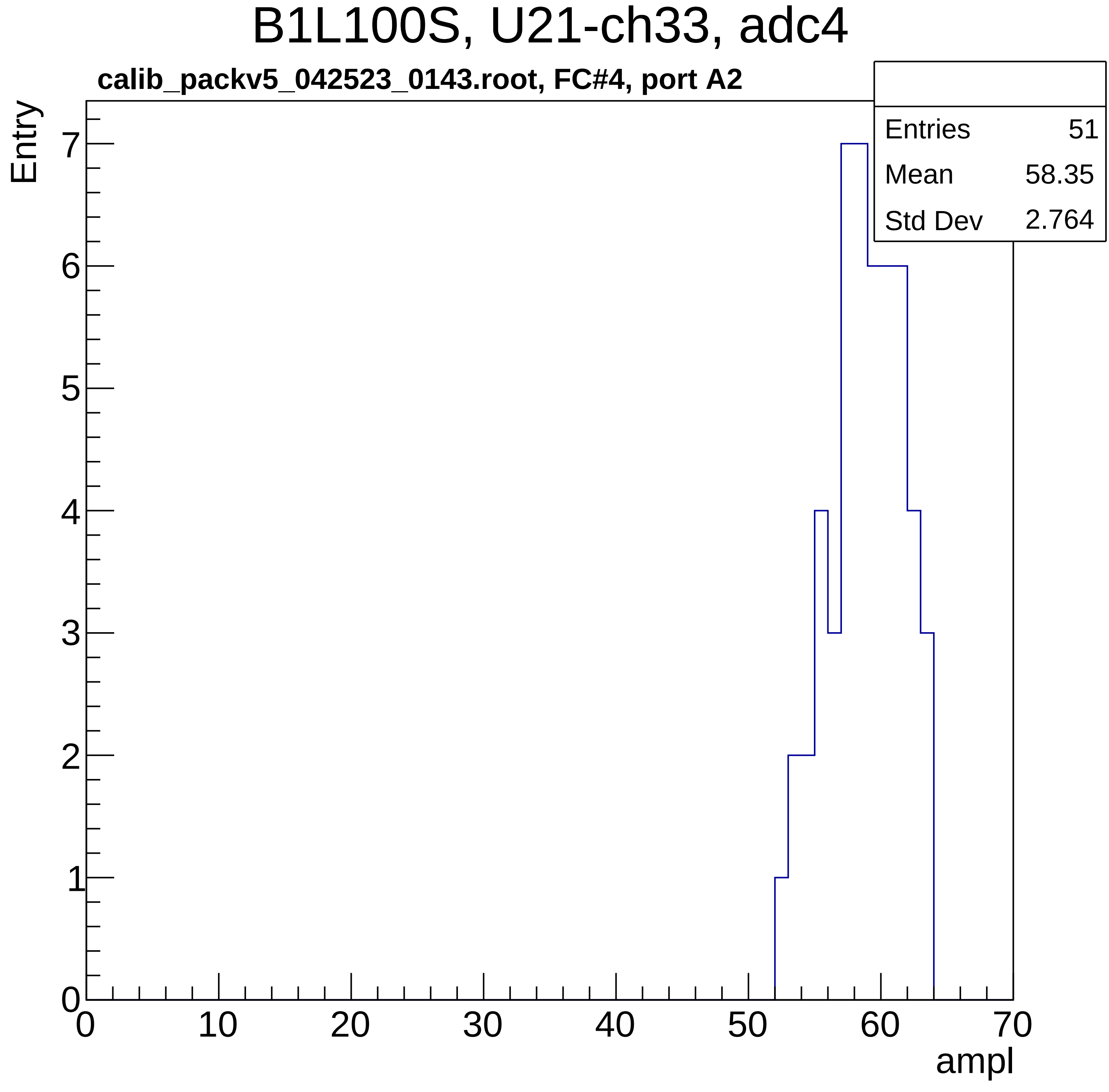
B1L100S, U21-ch33, adc1

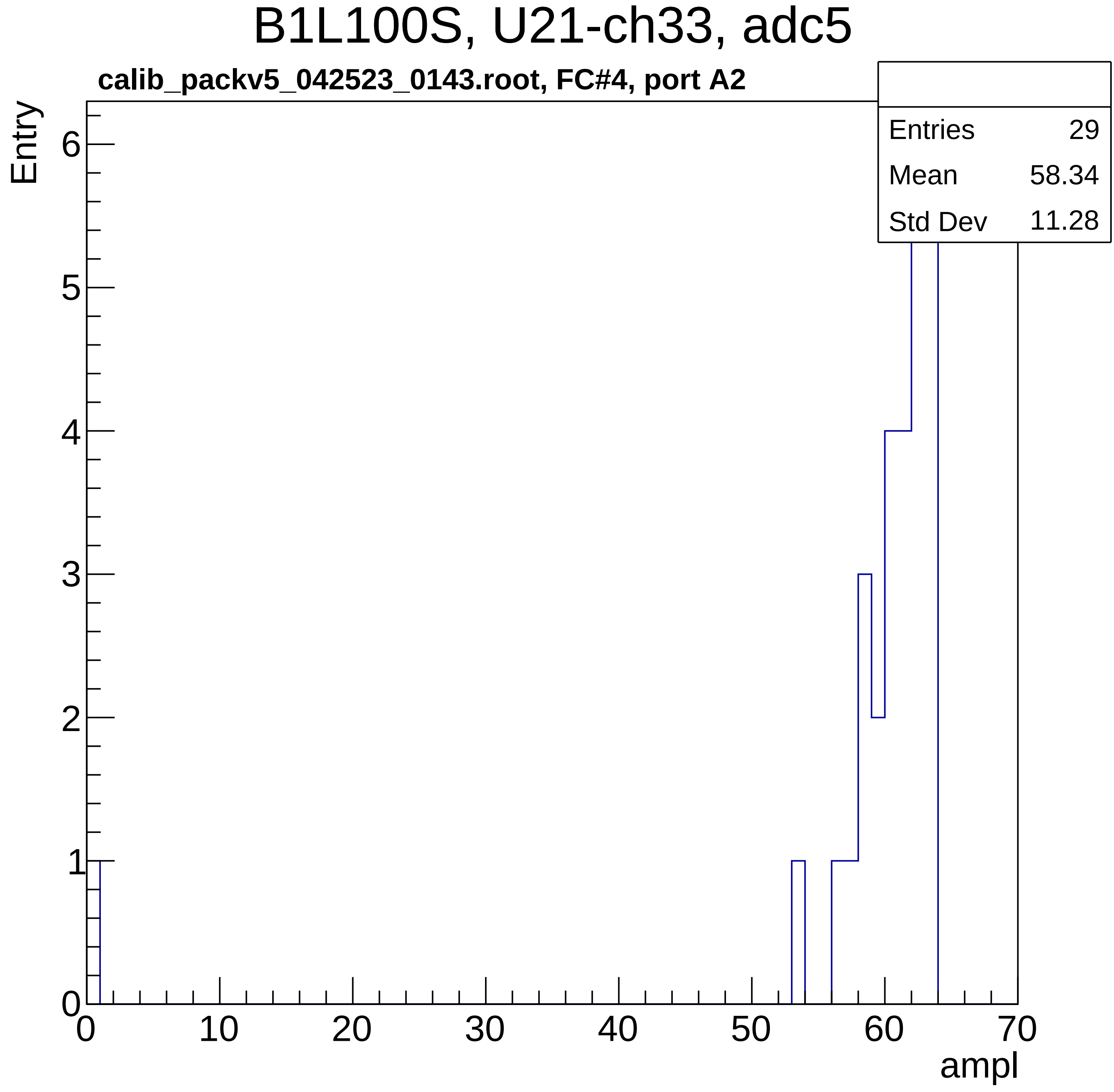




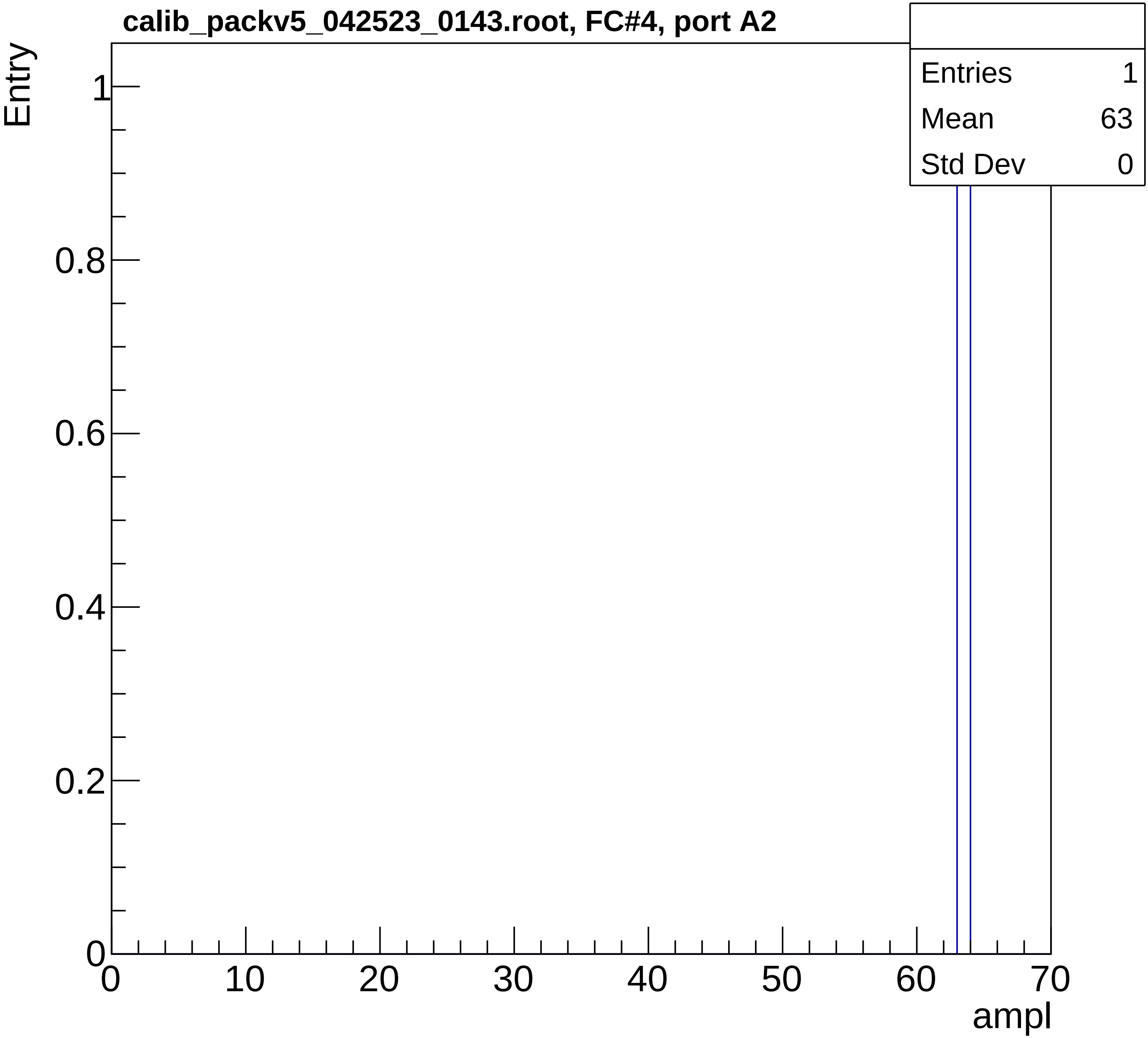
B1L100S, U21-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 45.32
Std Dev 3.291
Gaus mean : 45.8353
Gaus Width: 3.2853
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 51.94
Std Dev 3.116
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

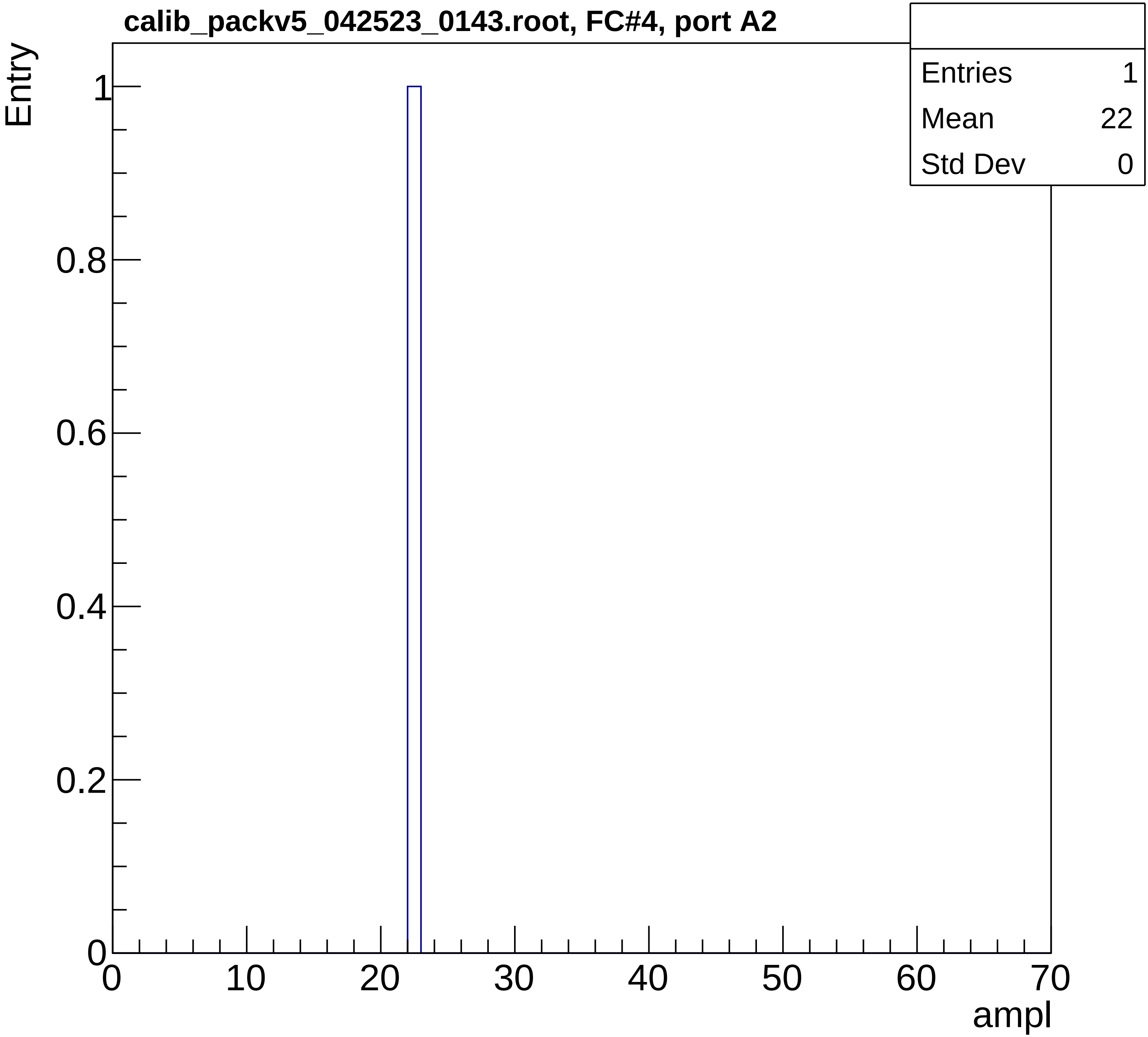

B1L100S, U21-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.35
Std Dev 2.764
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

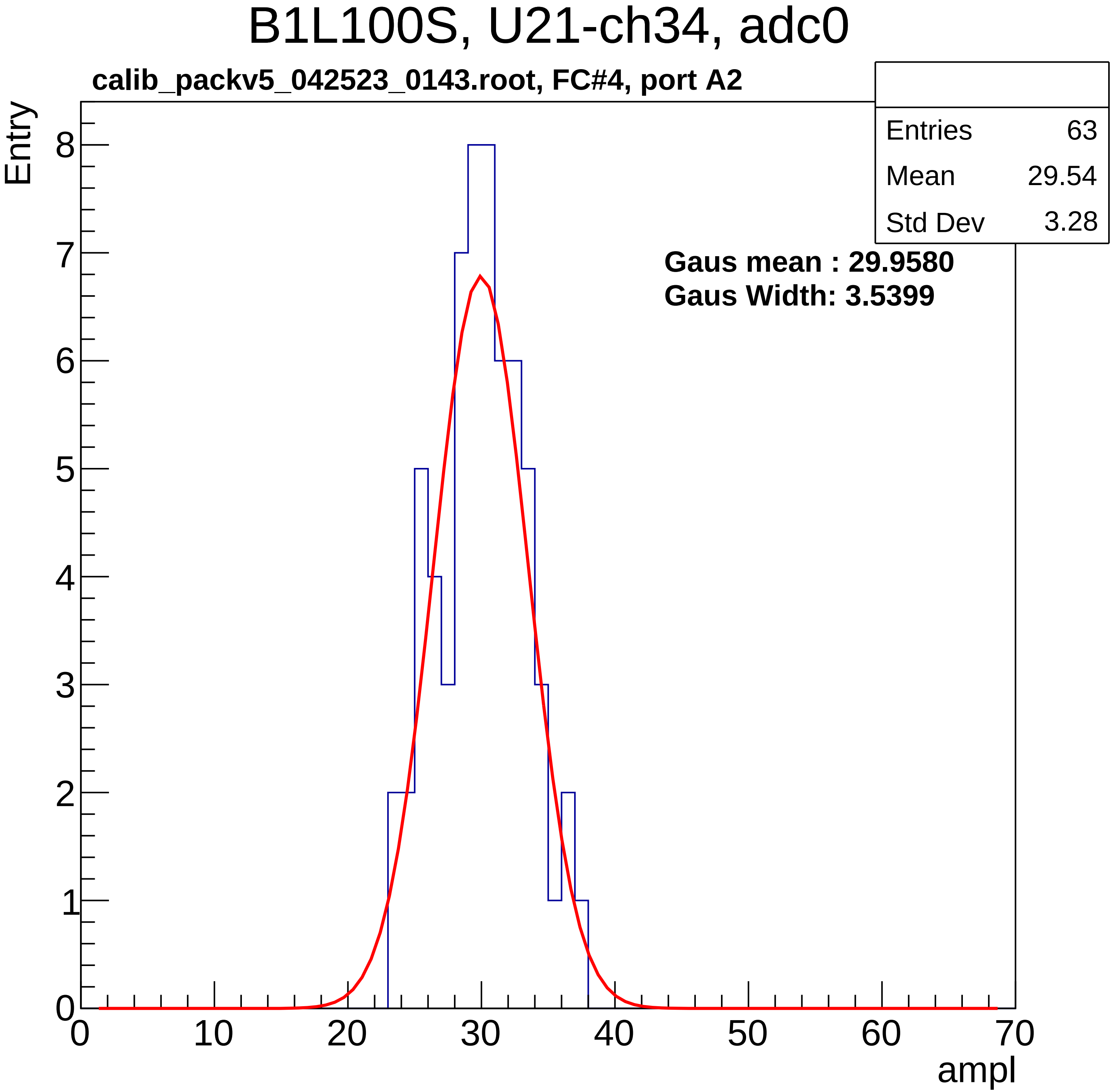

B1L100S, U21-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.34
Std Dev 11.28
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

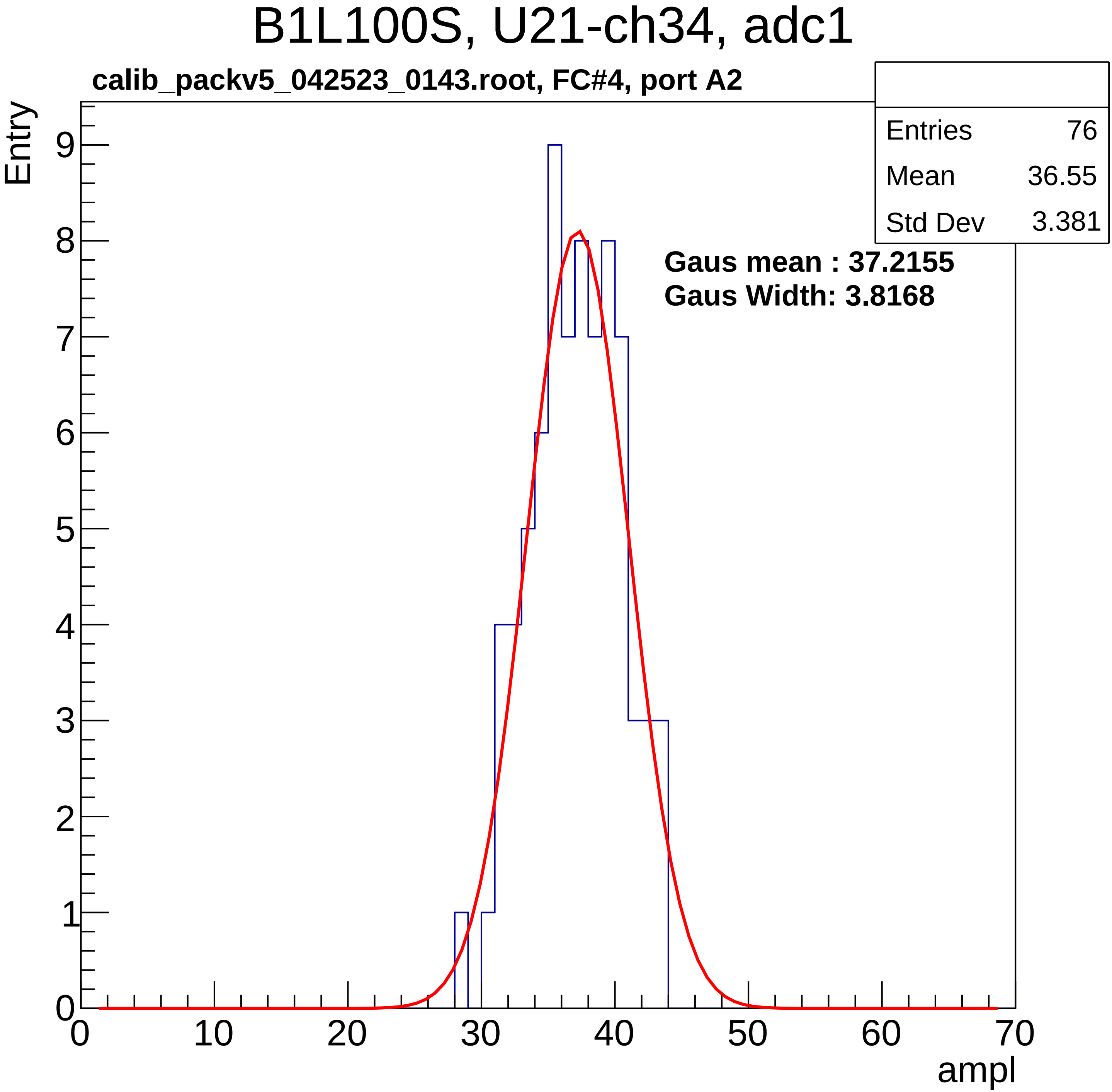
B1L100S, U21-ch33, adc6

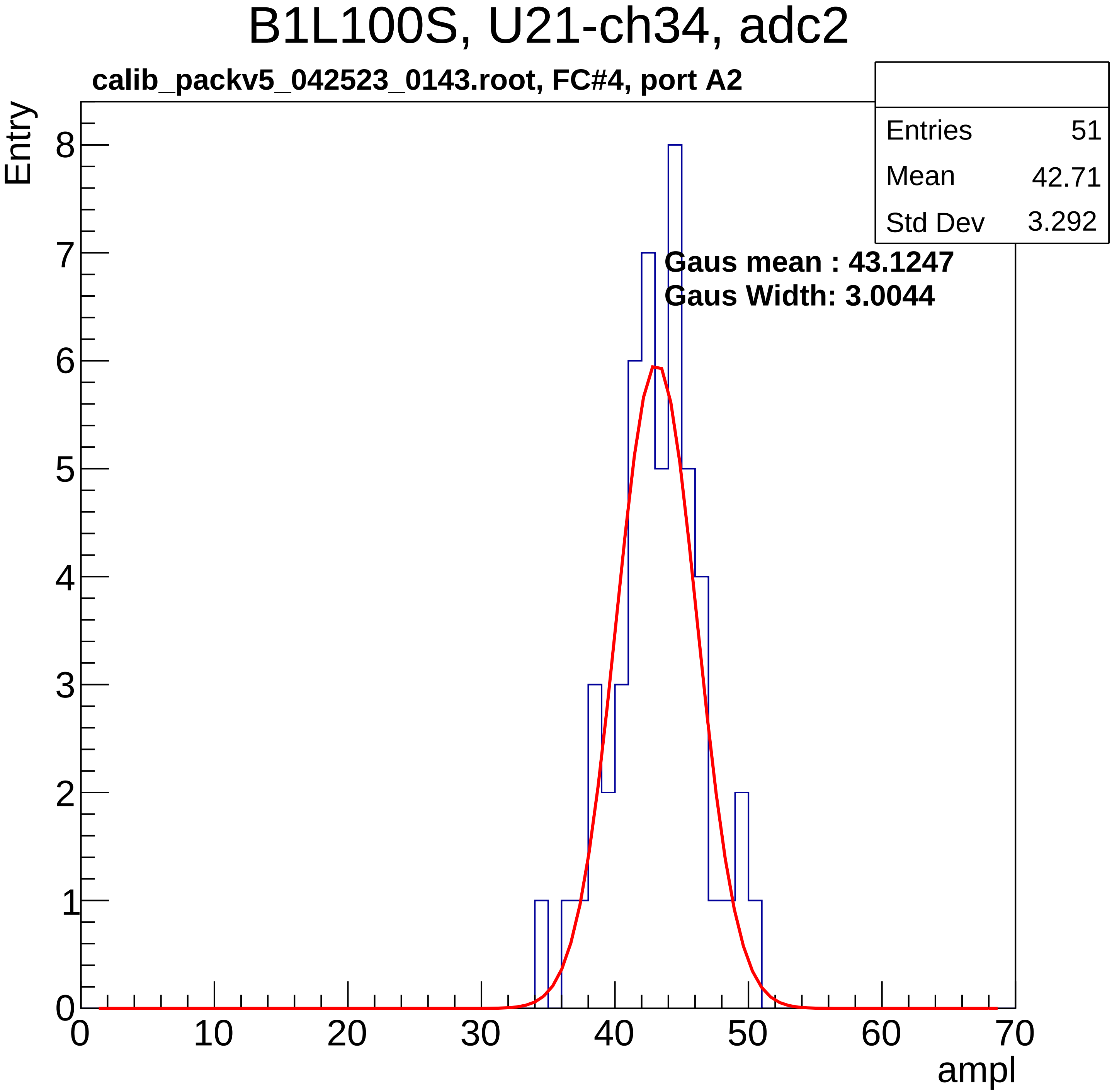


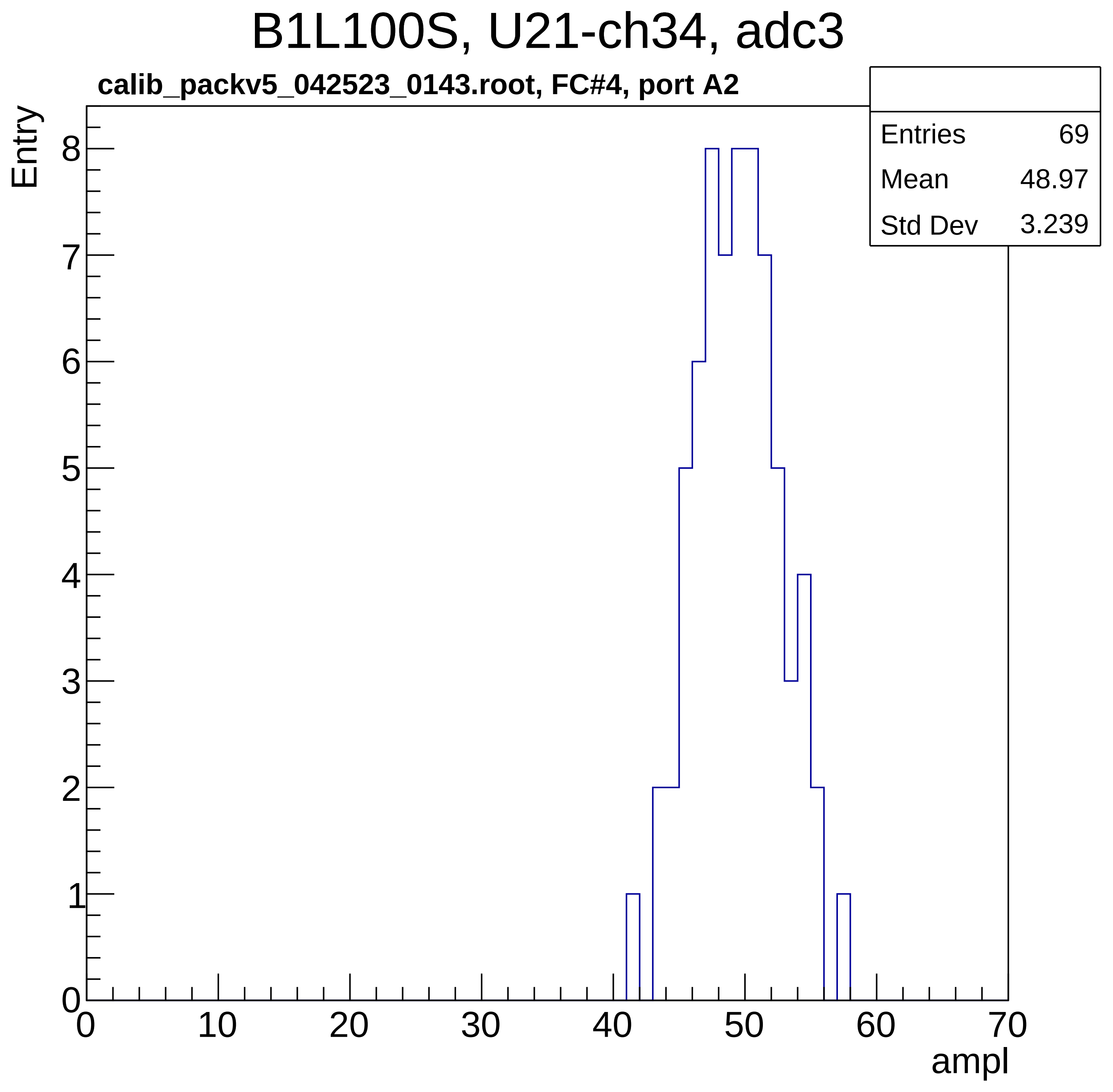
B1L100S, U21-ch33, adc7

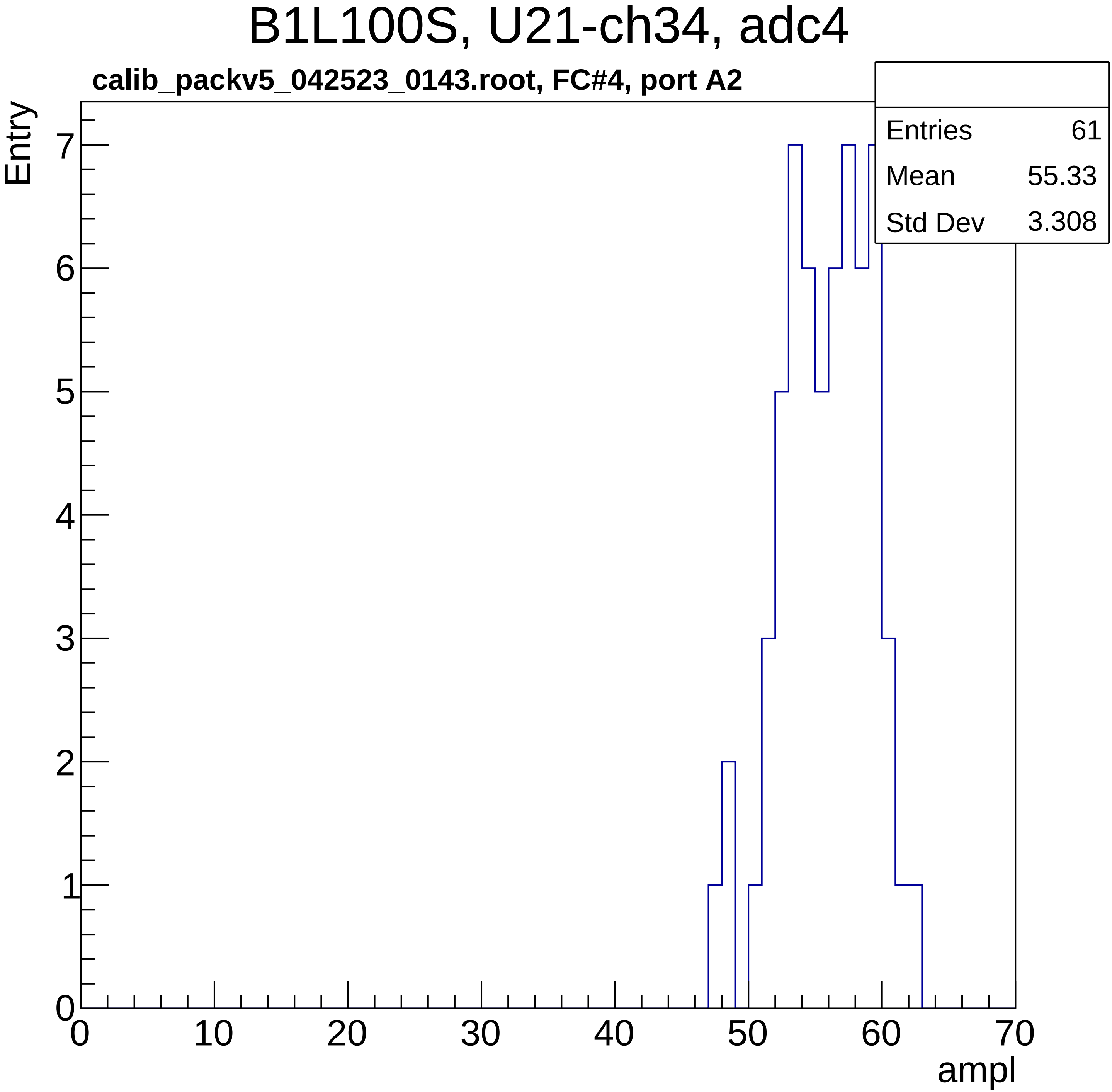


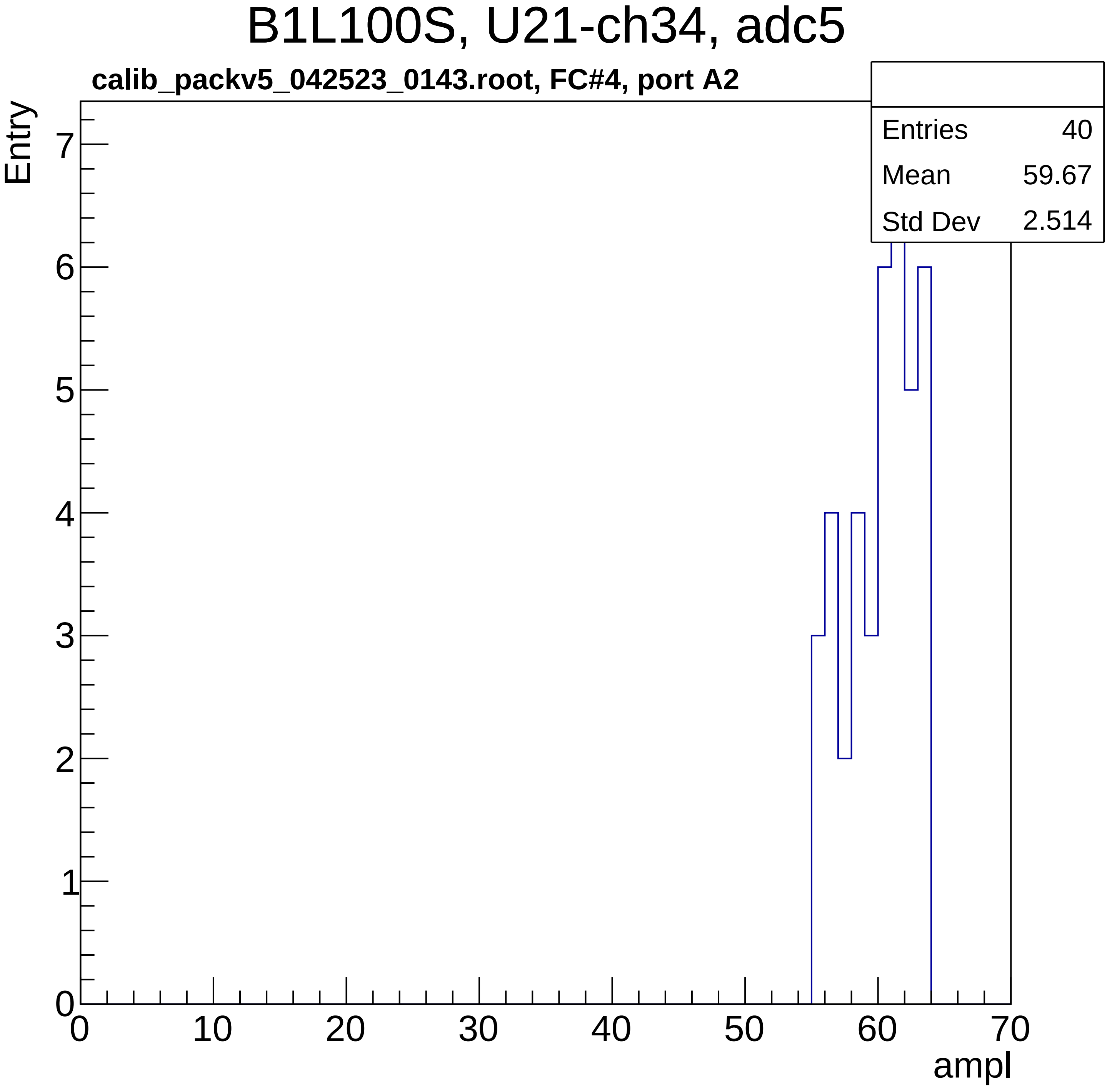

B1L100S, U21-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.54
Std Dev 3.28
Gaus mean : 29.9580
Gaus Width: 3.5399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

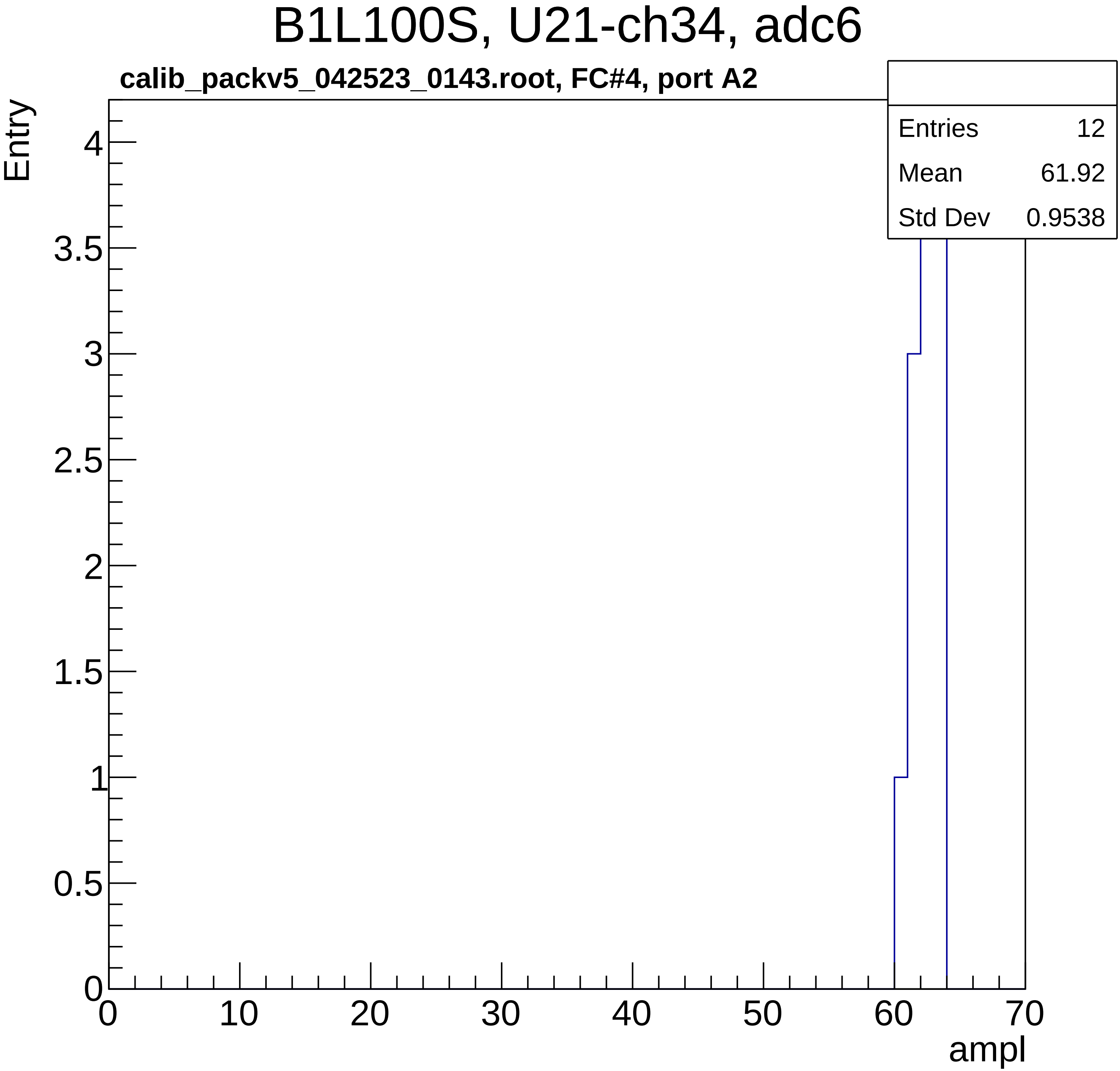

B1L100S, U21-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.55
Std Dev 3.381
Gaus mean : 37.2155
Gaus Width: 3.8168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 42.71
Std Dev 3.292
Gaus mean : 43.1247
Gaus Width: 3.0044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

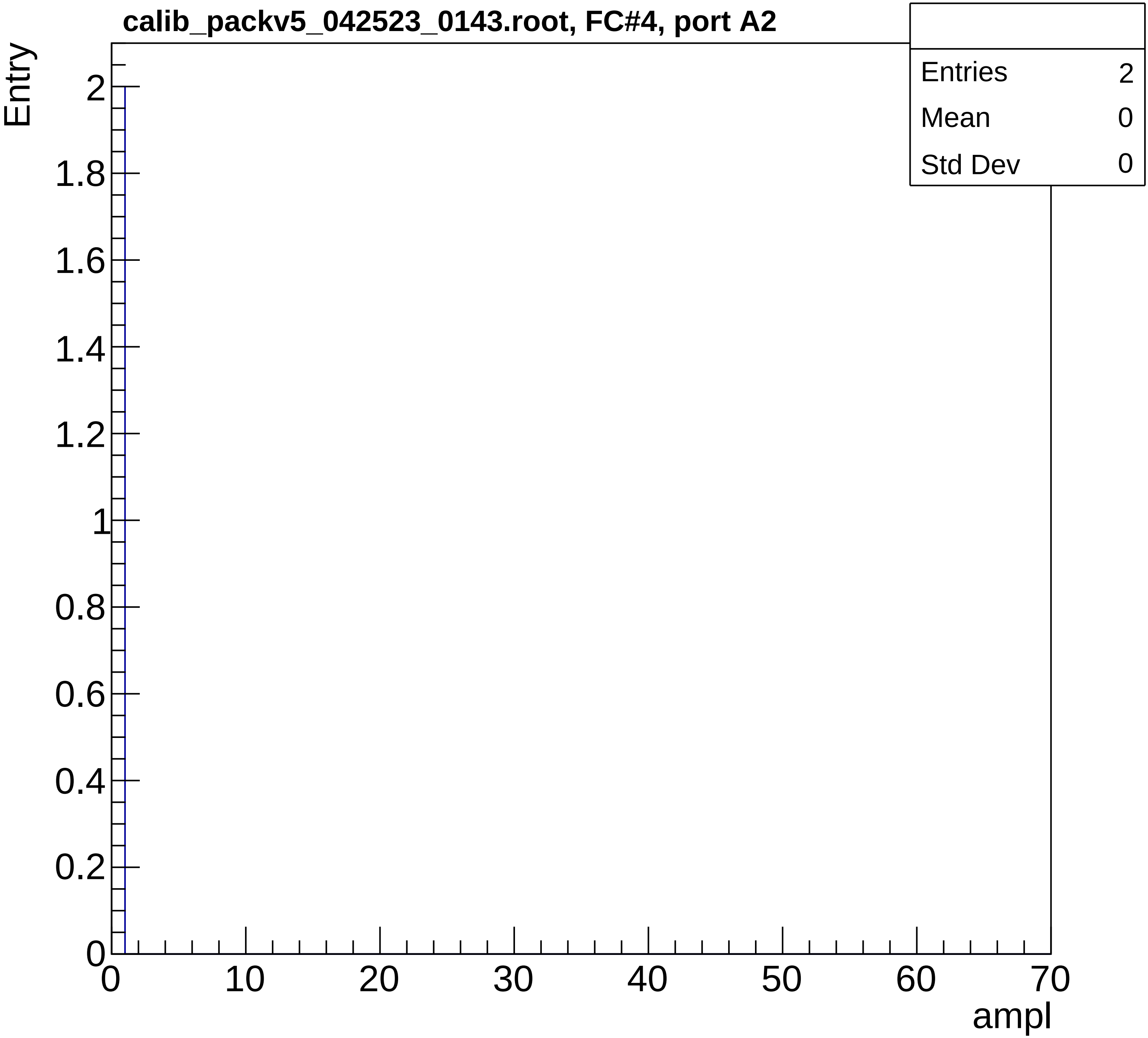

B1L100S, U21-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.97
Std Dev 3.239
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

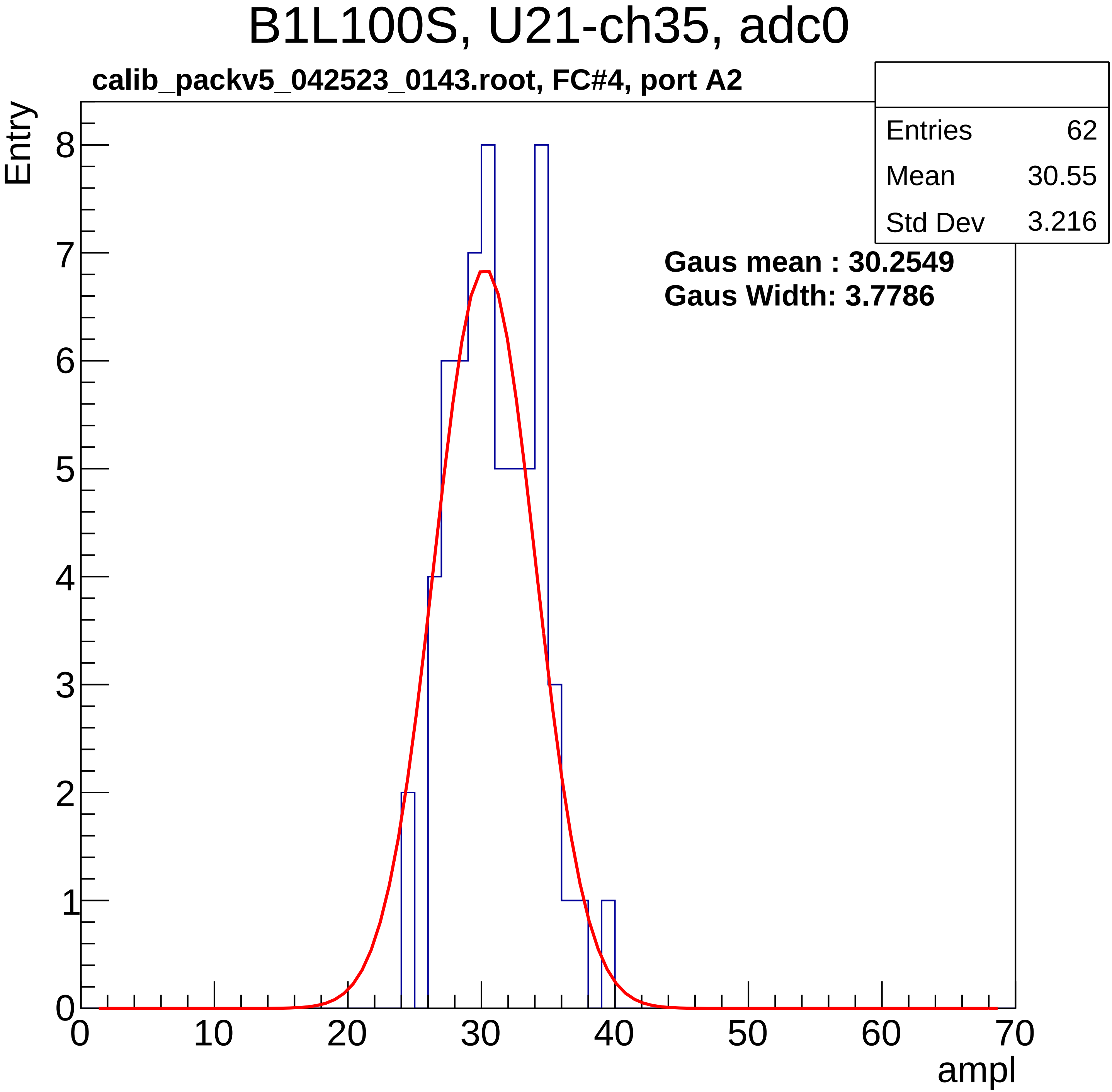

B1L100S, U21-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.33
Std Dev 3.308
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

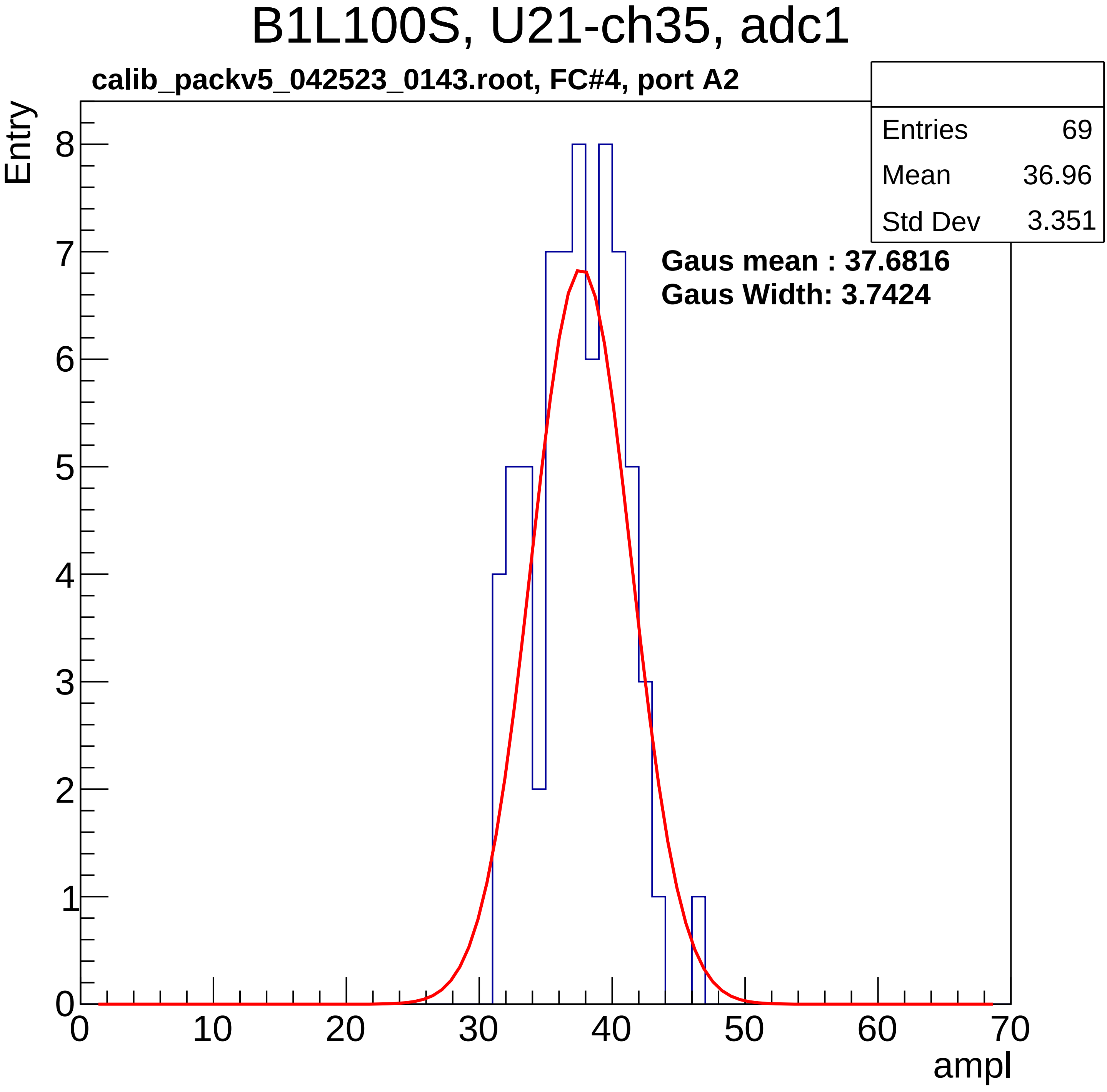

B1L100S, U21-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 59.67
Std Dev 2.514
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

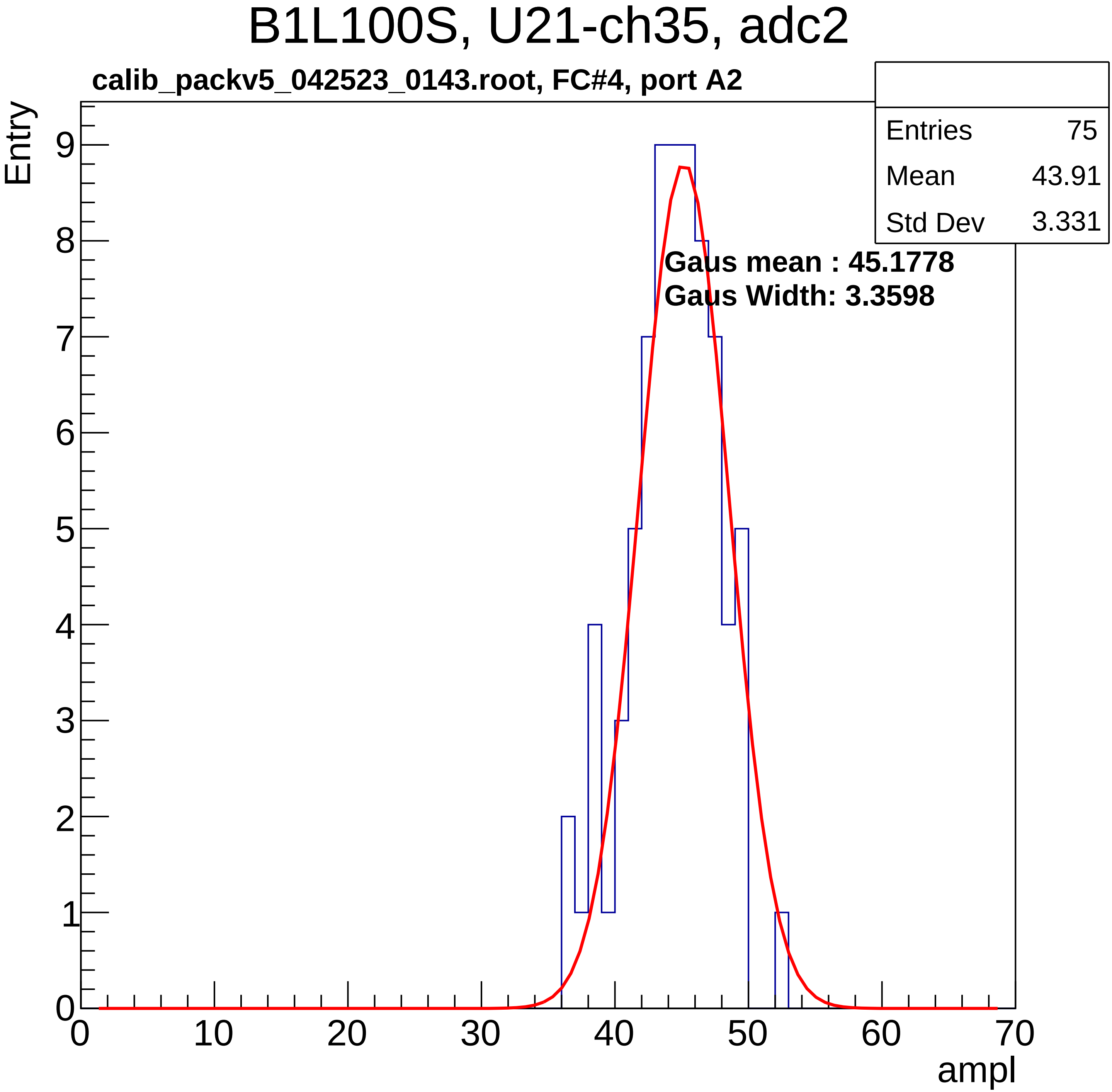

B1L100S, U21-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 61.92
Std Dev 0.9538
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

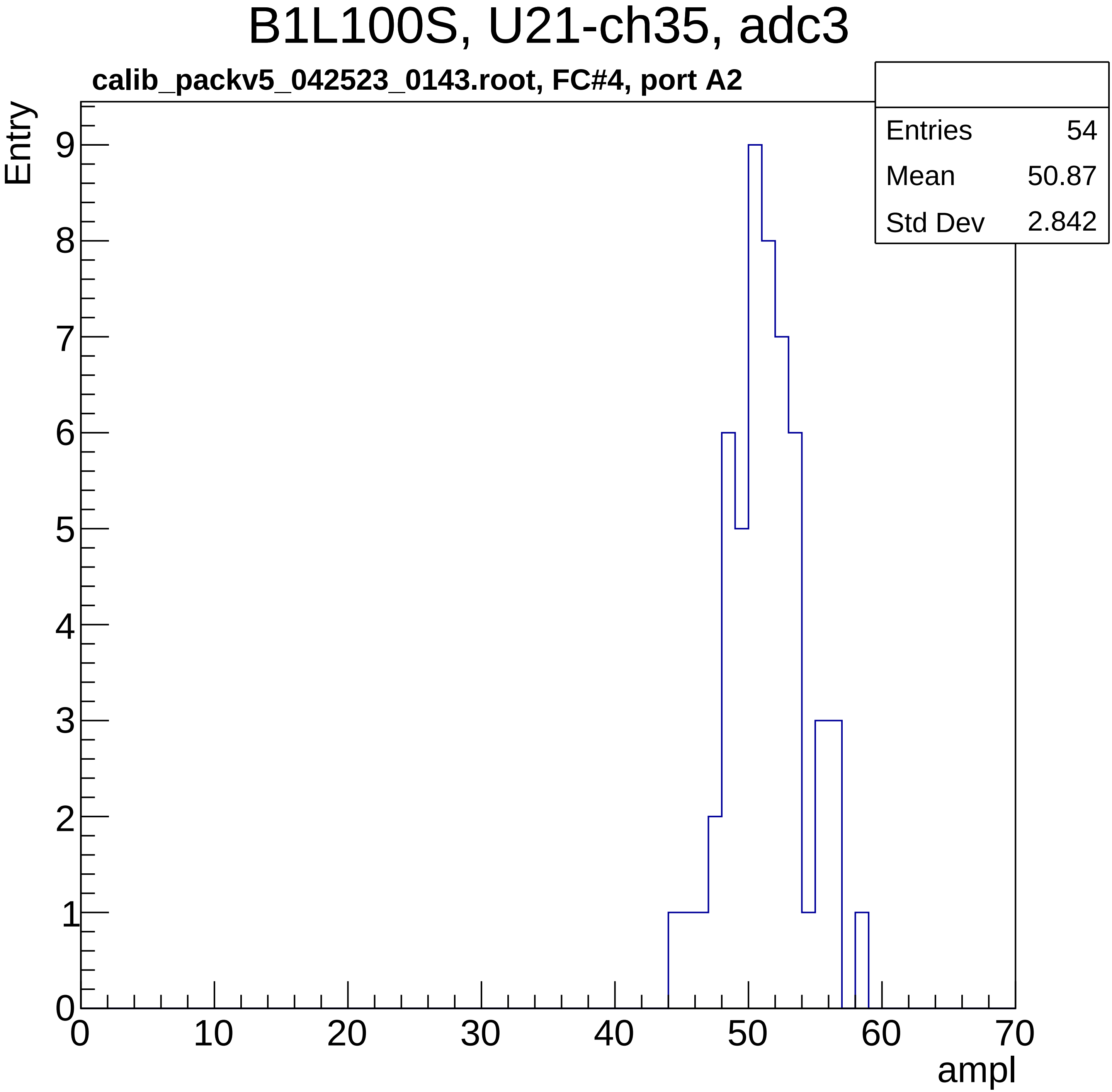
B1L100S, U21-ch34, adc7




B1L100S, U21-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30.55
Std Dev 3.216
Gaus mean : 30.2549
Gaus Width: 3.7786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

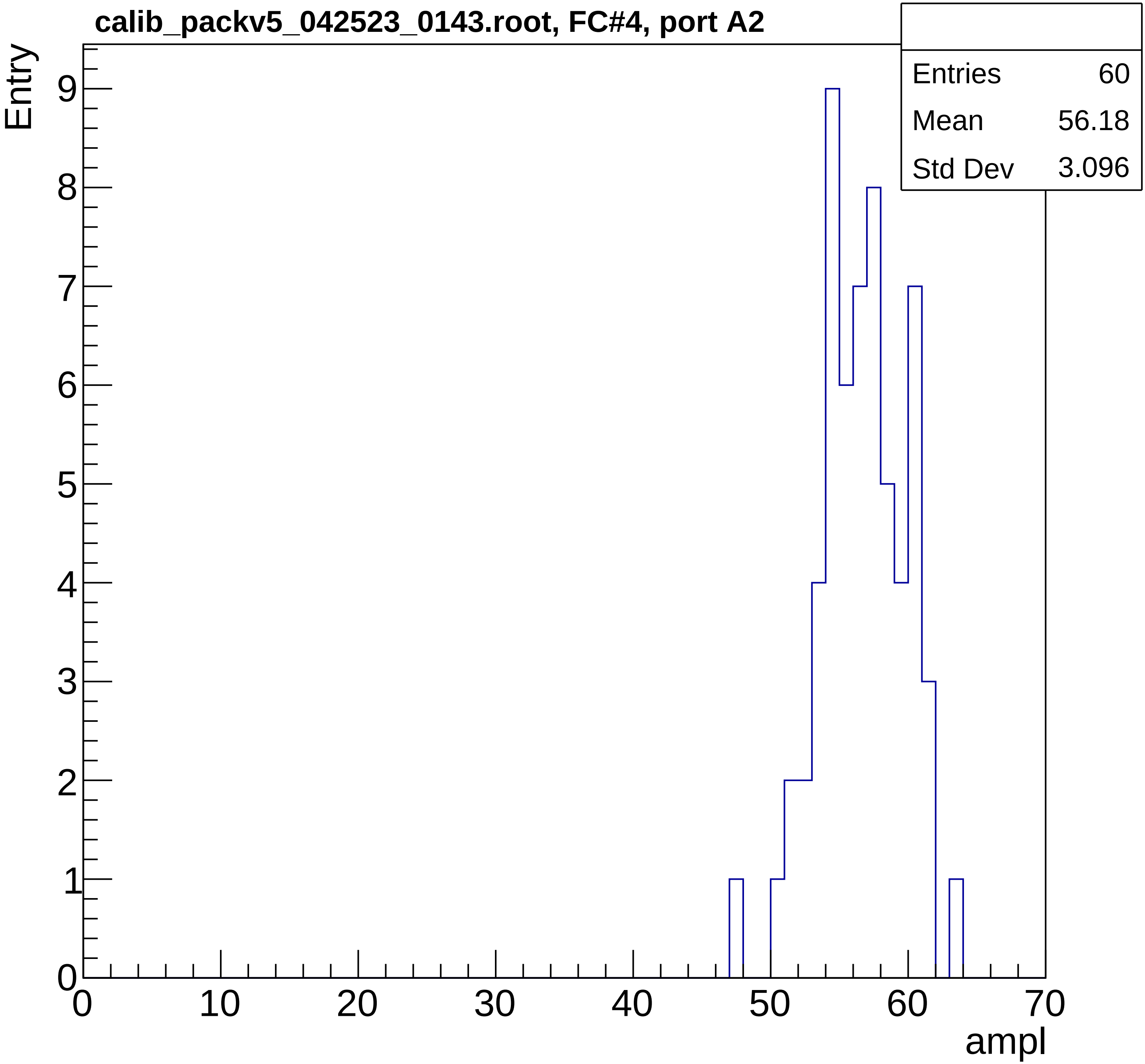

B1L100S, U21-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 69
Mean 36.96
Std Dev 3.351
Gaus mean : 37.6816
Gaus Width: 3.7424
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

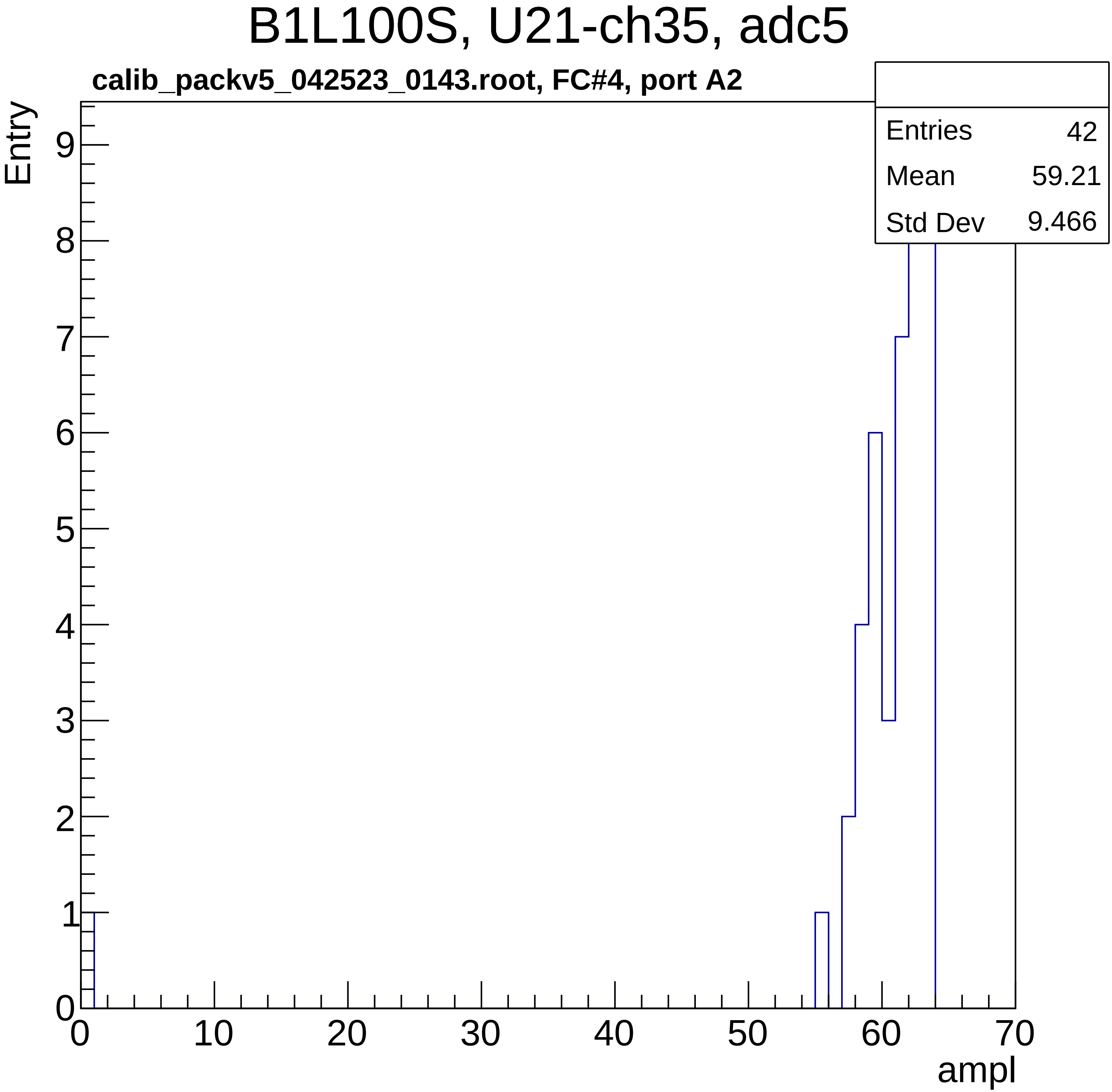

B1L100S, U21-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.91
Std Dev 3.331
Gaus mean : 45.1778
Gaus Width: 3.3598
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

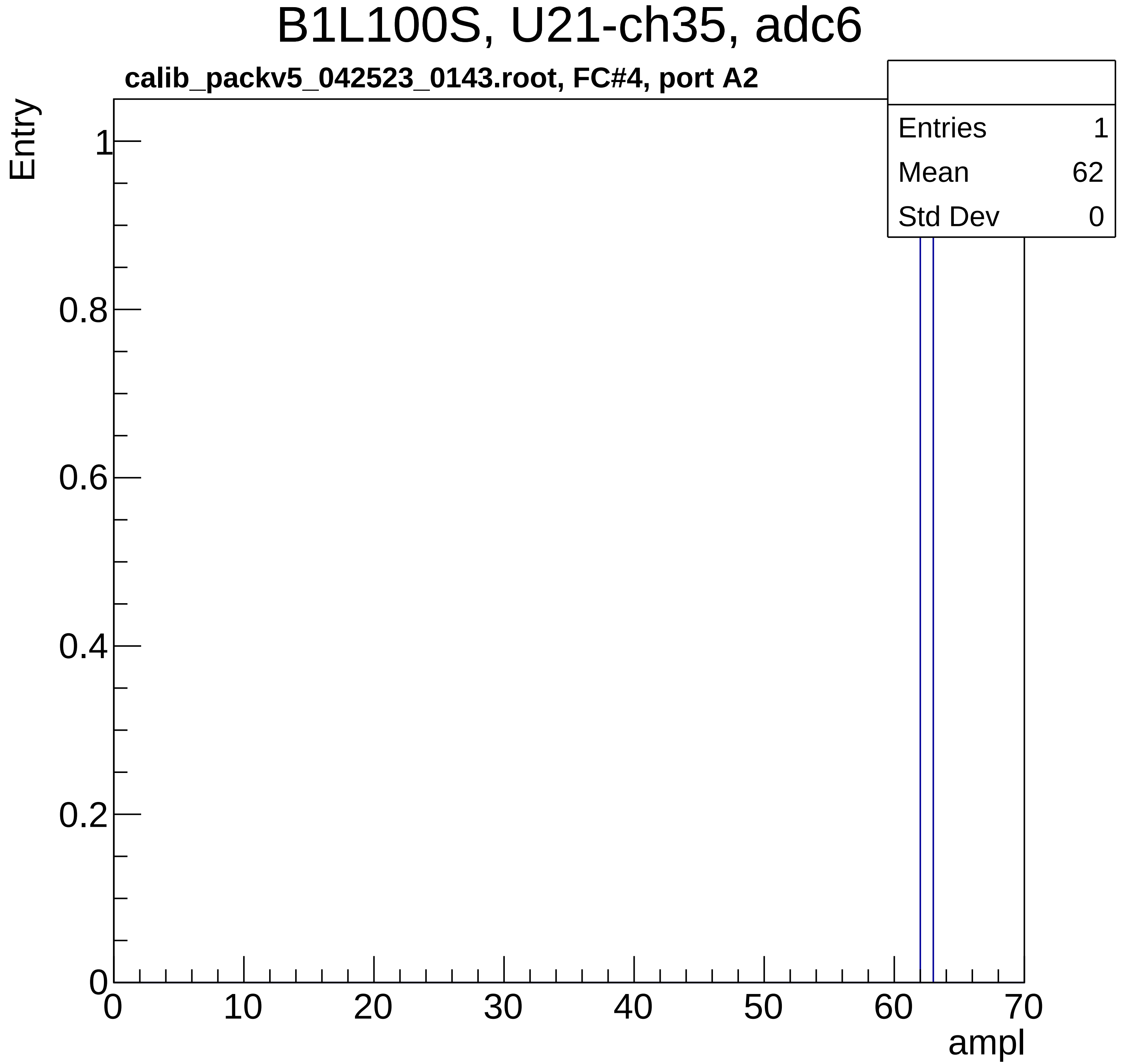

B1L100S, U21-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 50.87
Std Dev 2.842
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch35, adc4

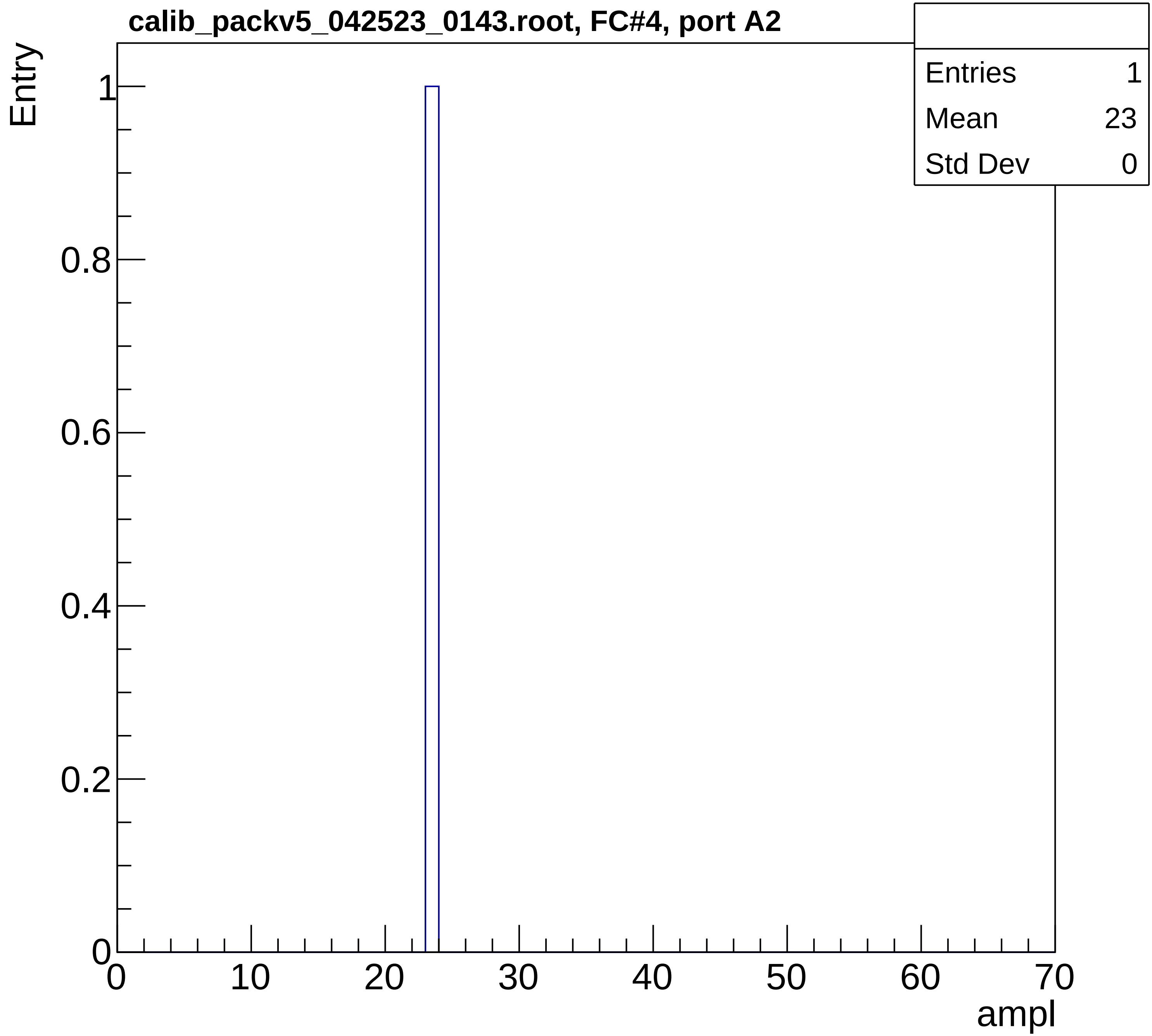
calib_packv5_042523_0143.root, FC#4, port A2

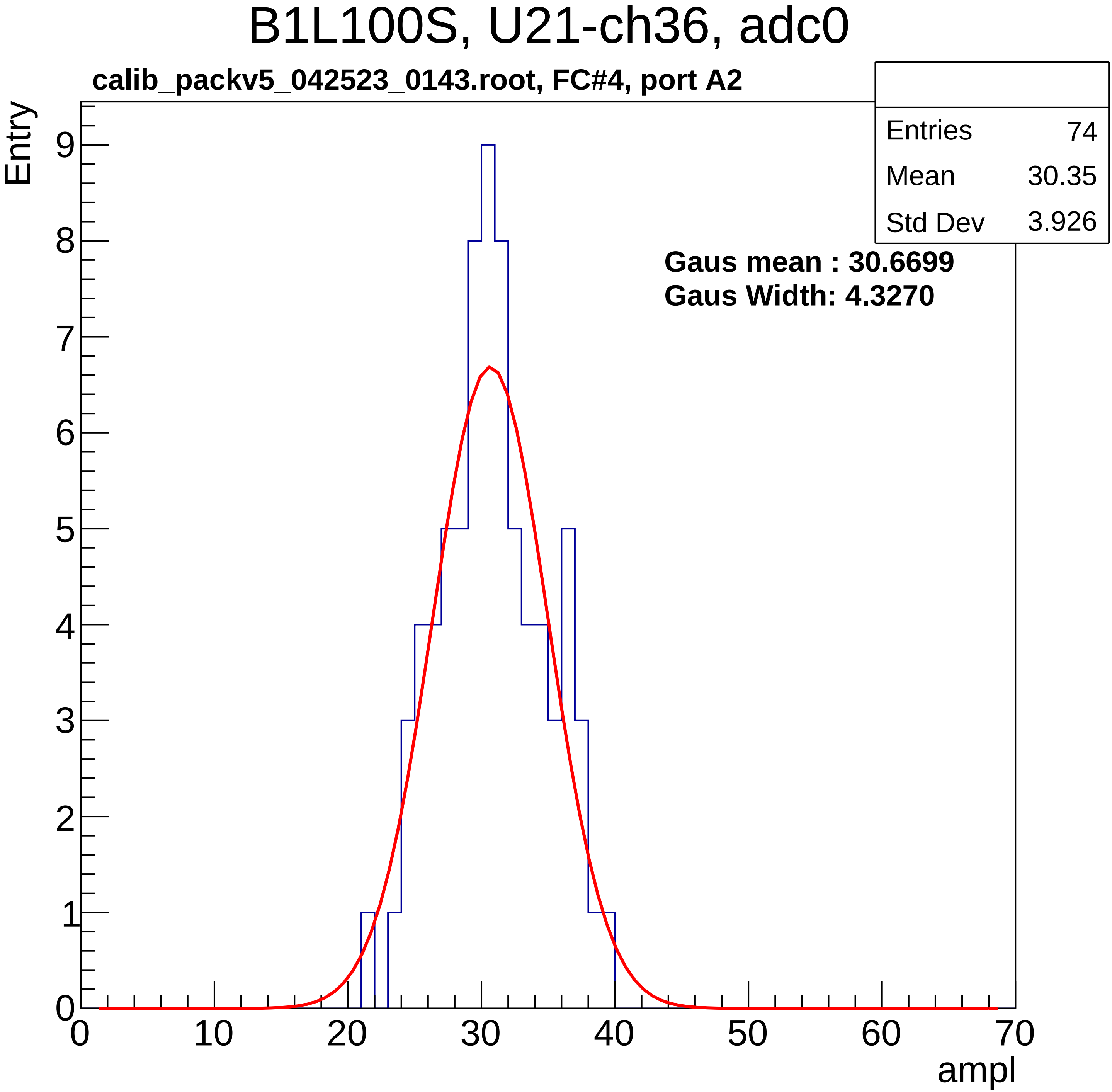


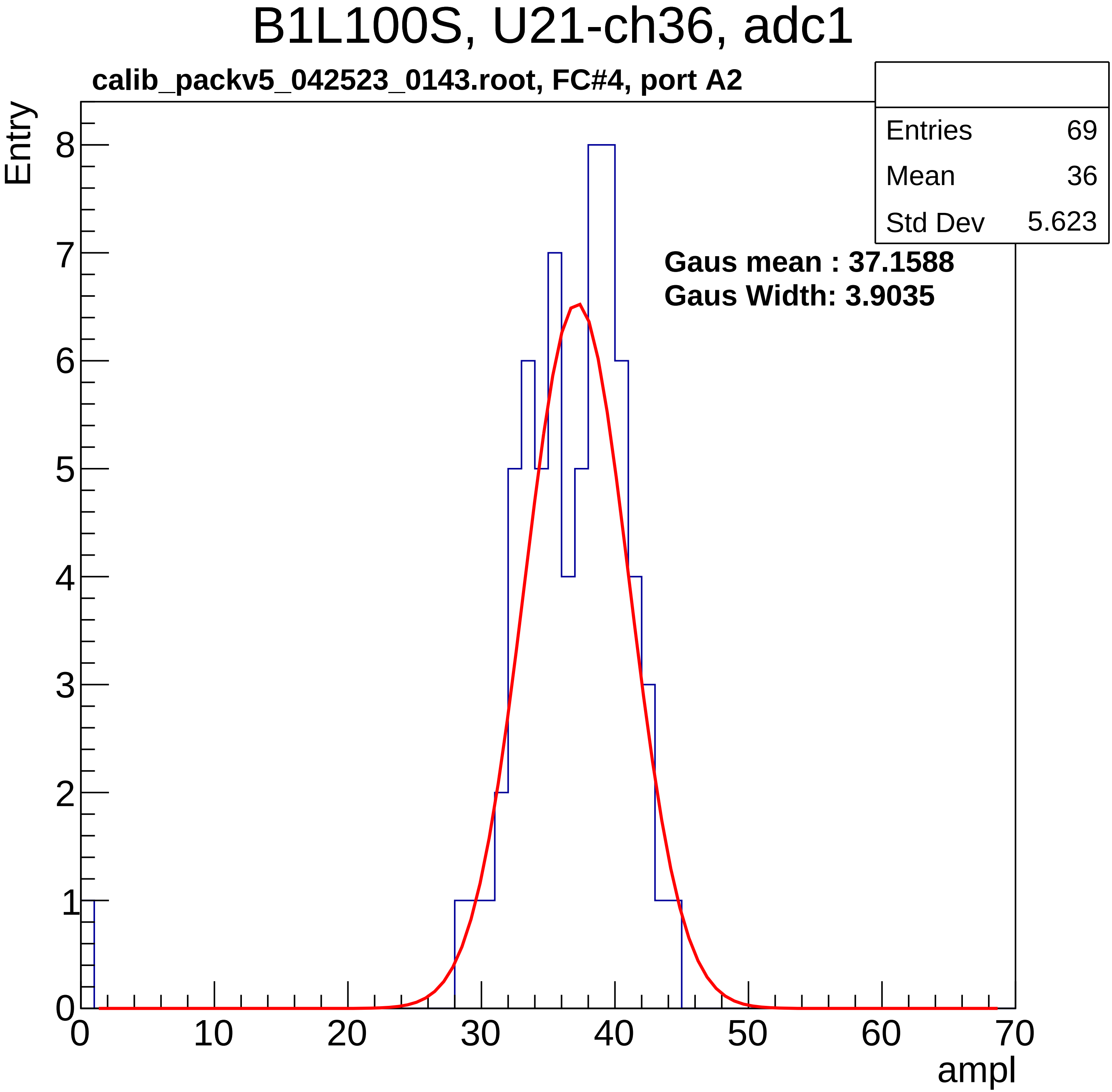

B1L100S, U21-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.21
Std Dev 9.466
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

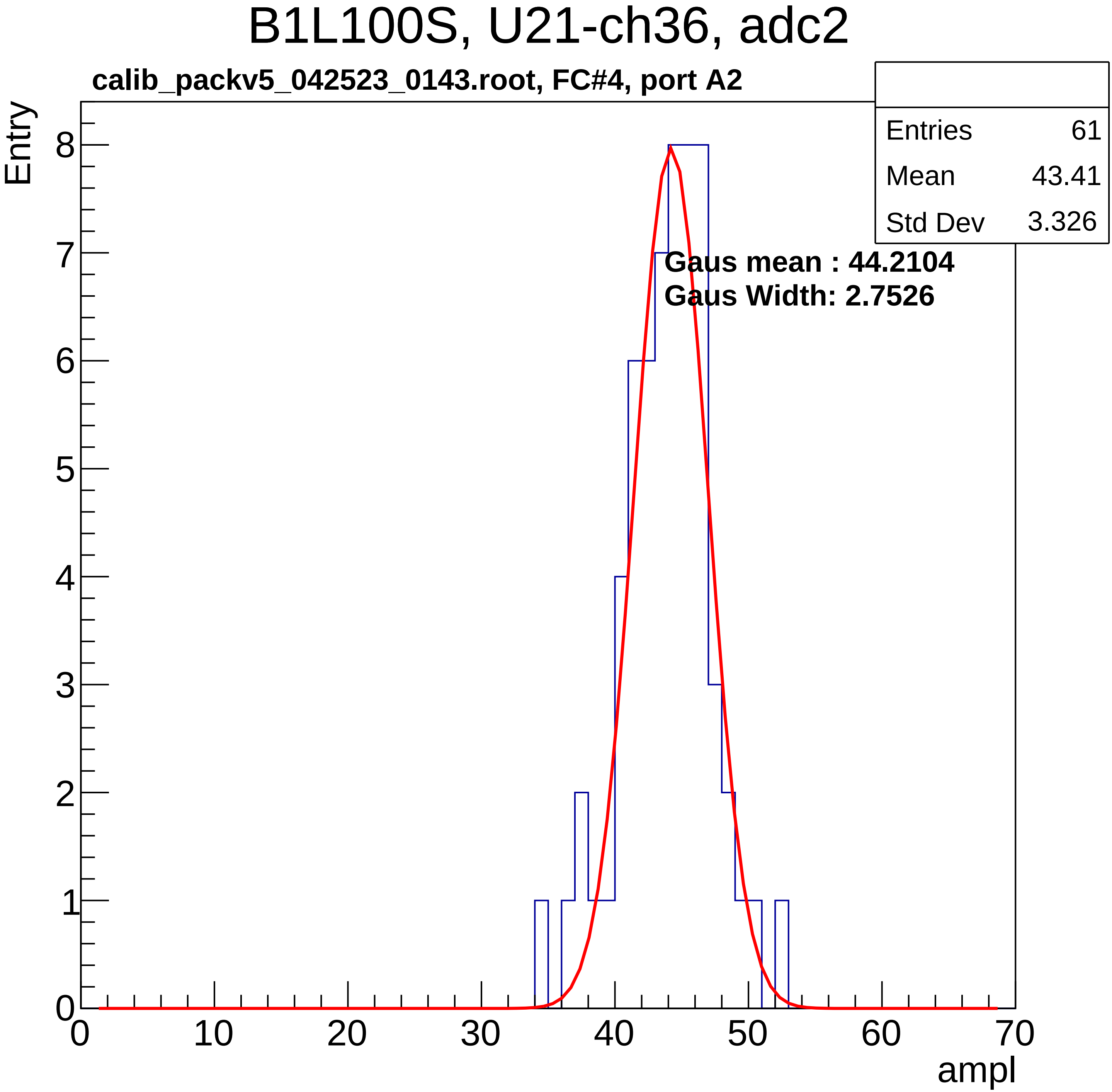

B1L100S, U21-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 1
Mean 62
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

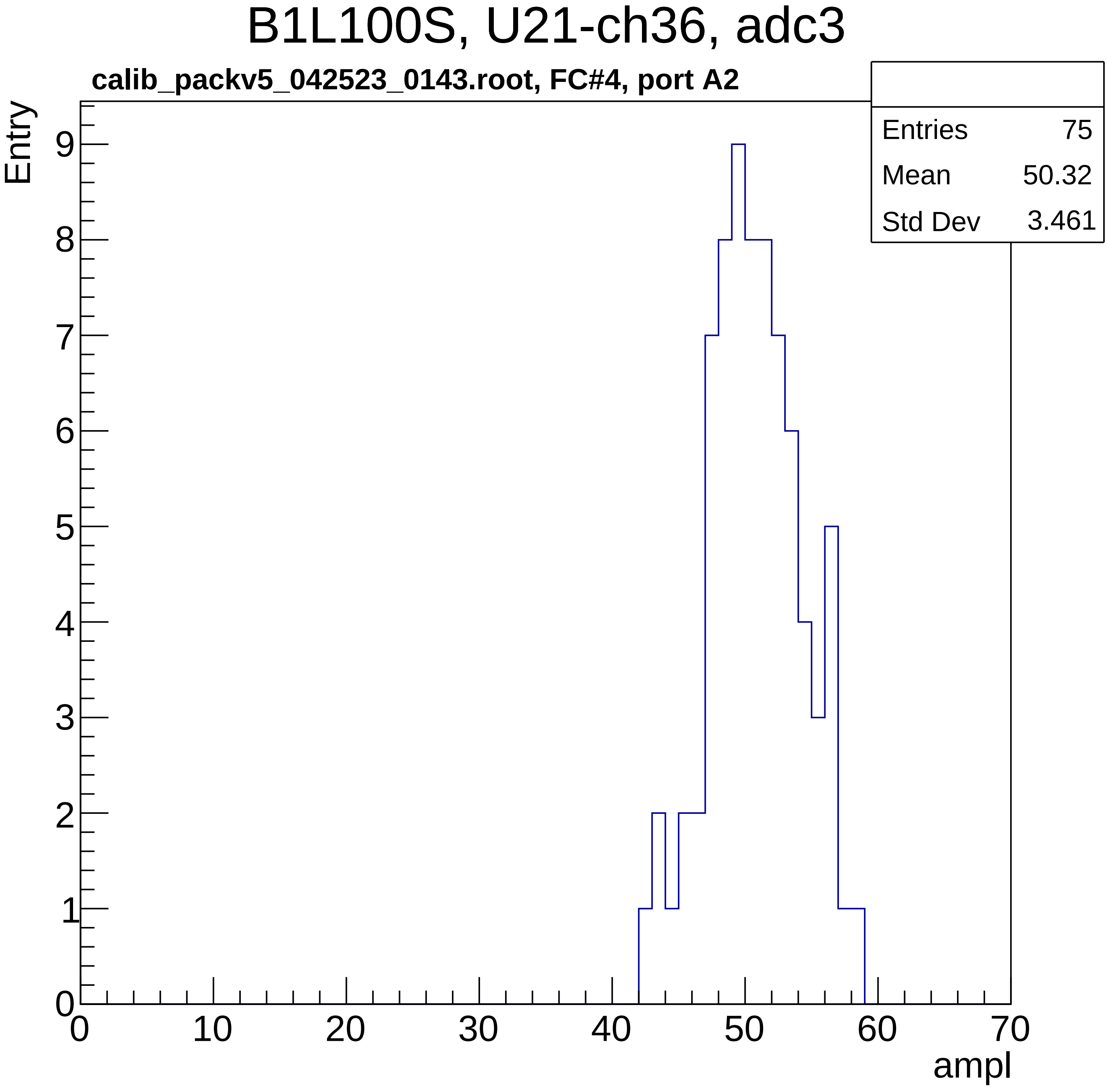
B1L100S, U21-ch35, adc7

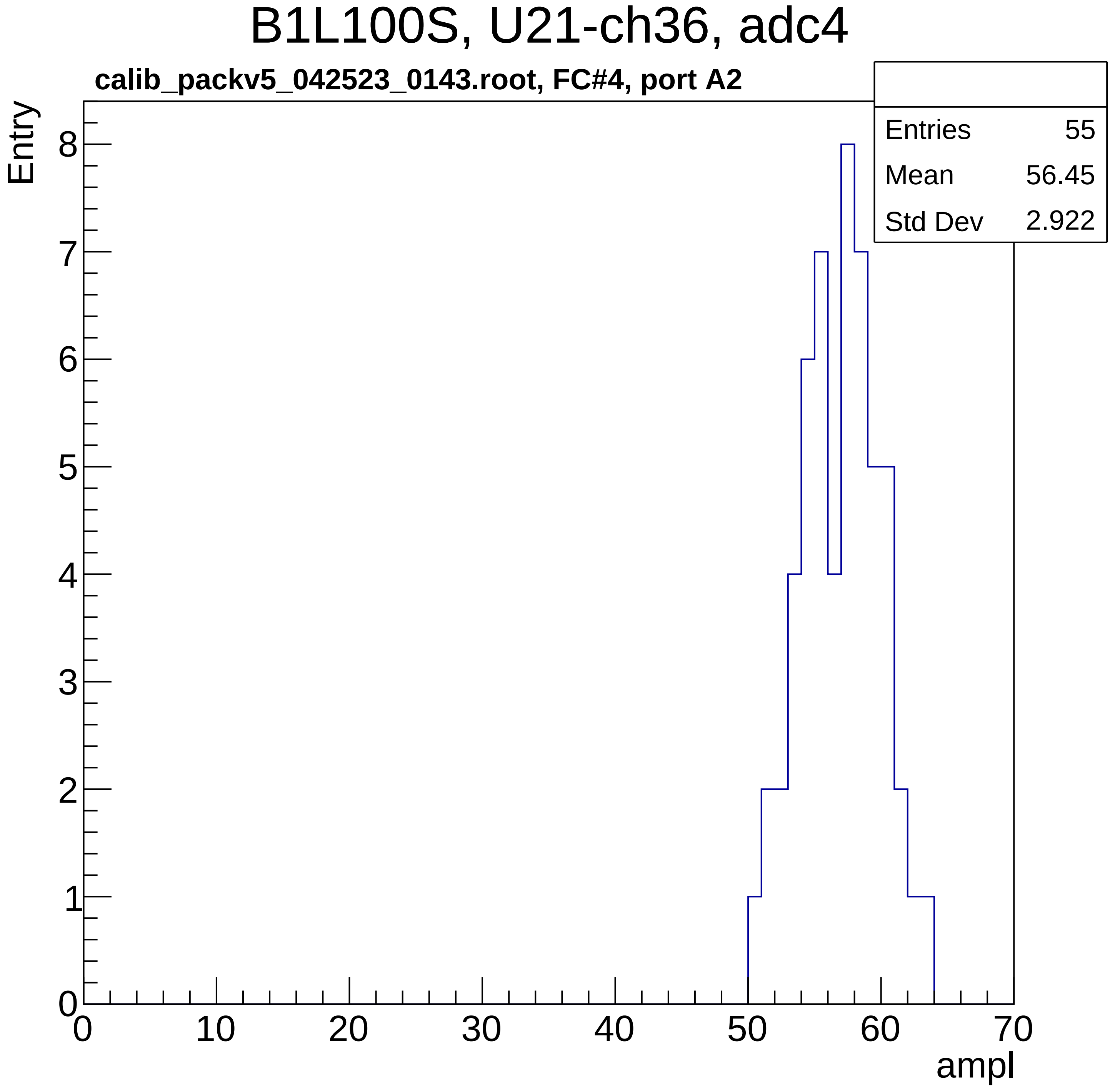


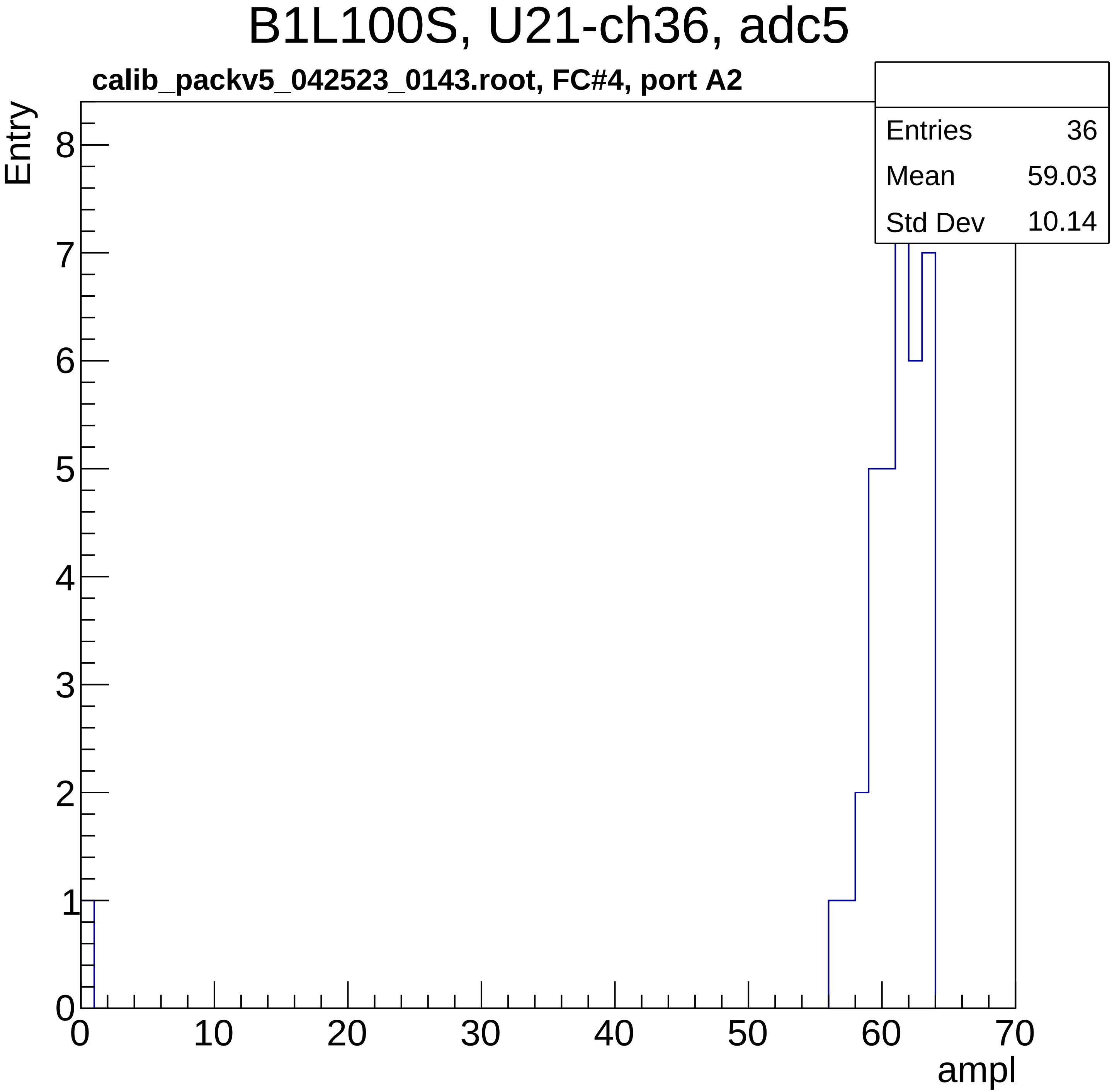

B1L100S, U21-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 74
Mean 30.35
Std Dev 3.926
Gaus mean : 30.6699
Gaus Width: 4.3270
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

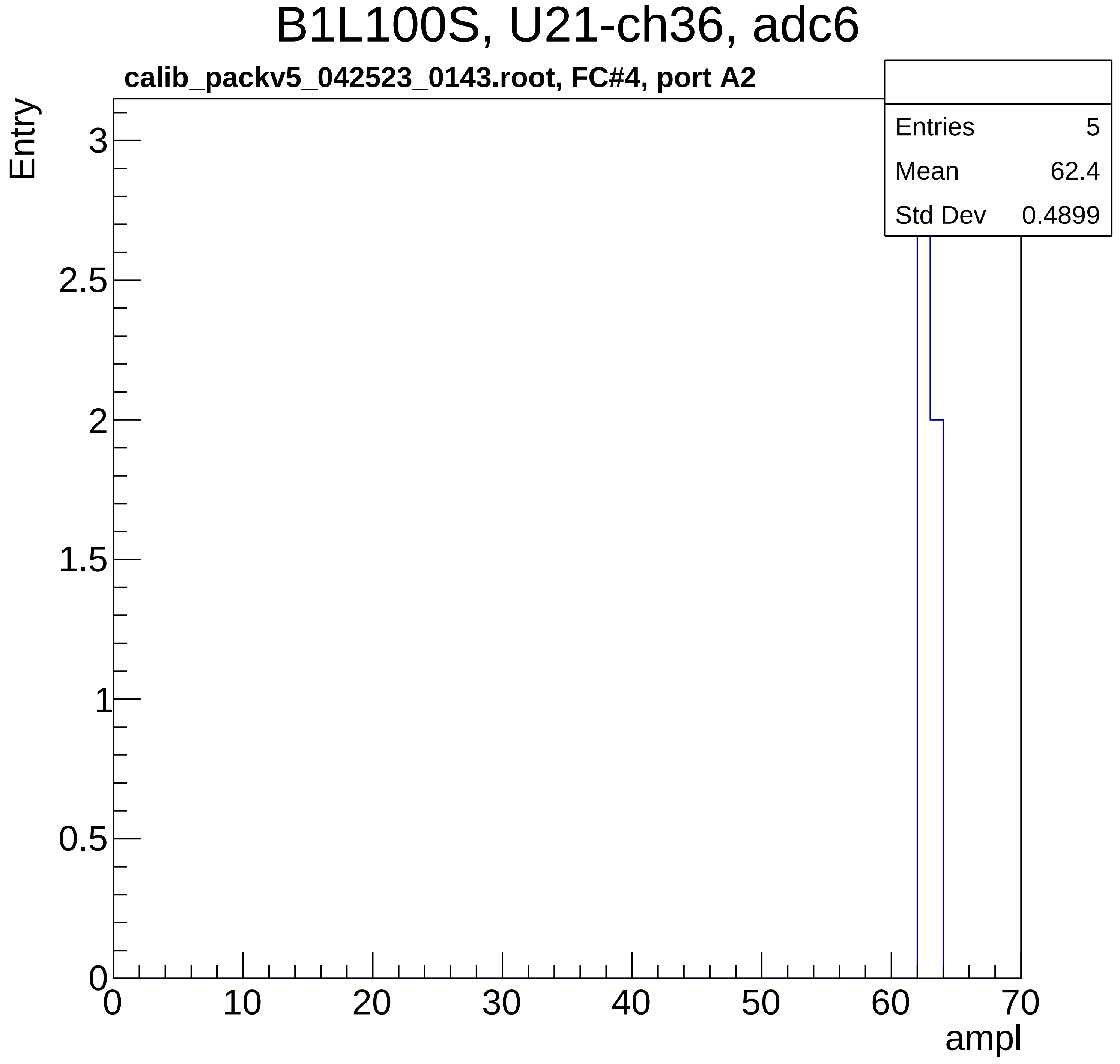

B1L100S, U21-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36
Std Dev 5.623
Gaus mean : 37.1588
Gaus Width: 3.9035
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

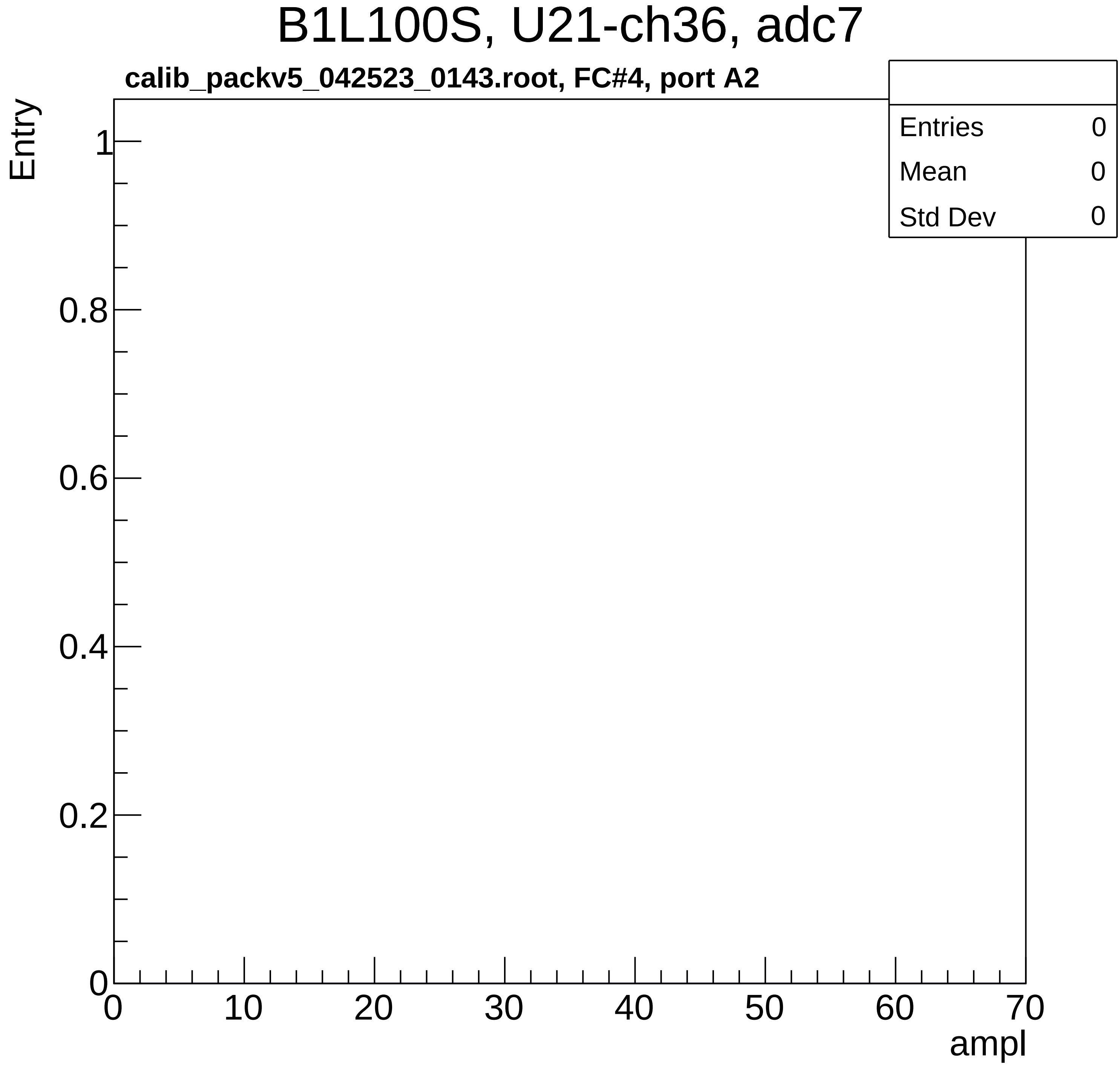

B1L100S, U21-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.41
Std Dev 3.326
Gaus mean : 44.2104
Gaus Width: 2.7526
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

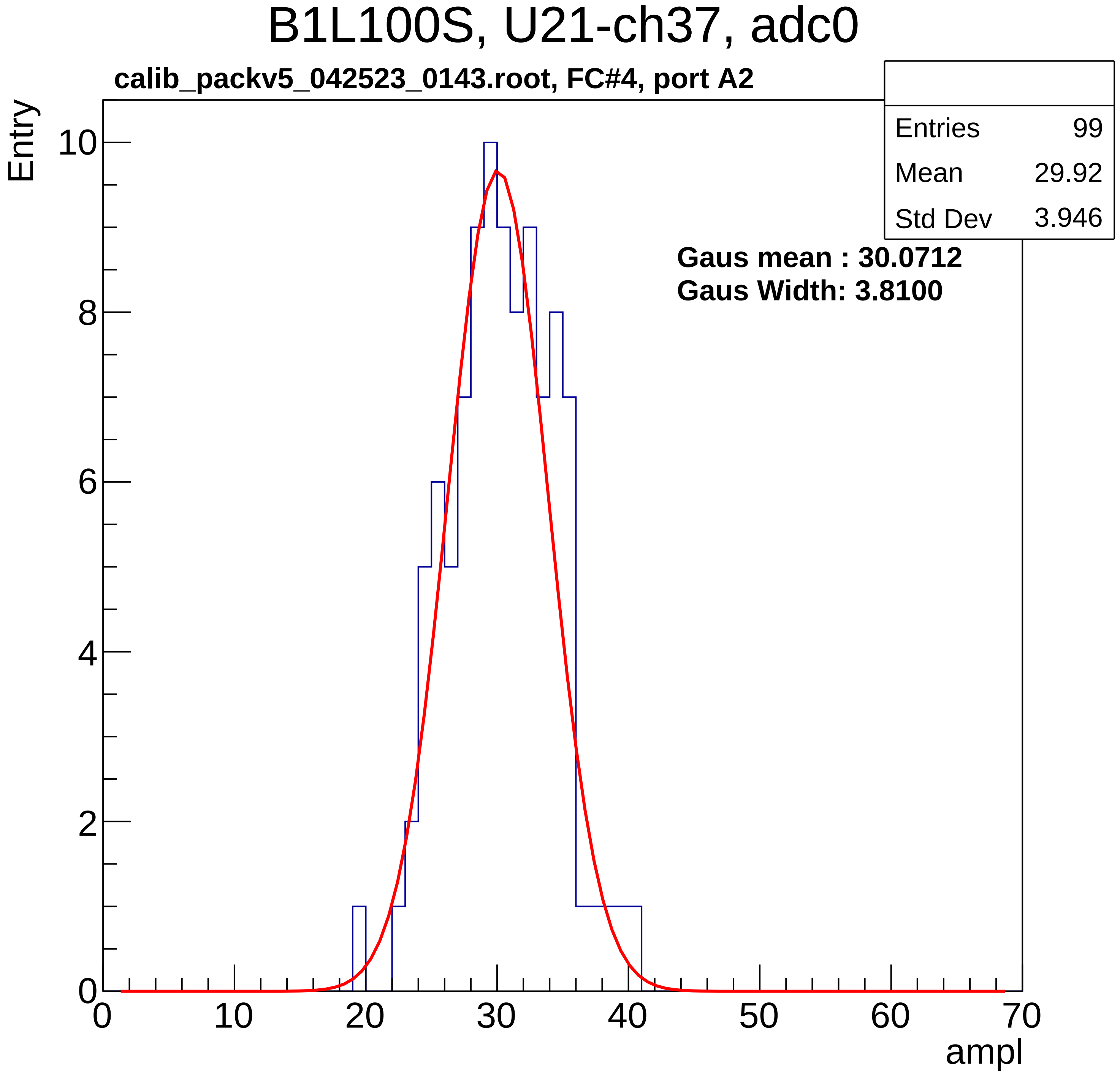

B1L100S, U21-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.32
Std Dev 3.461
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.45
Std Dev 2.922
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

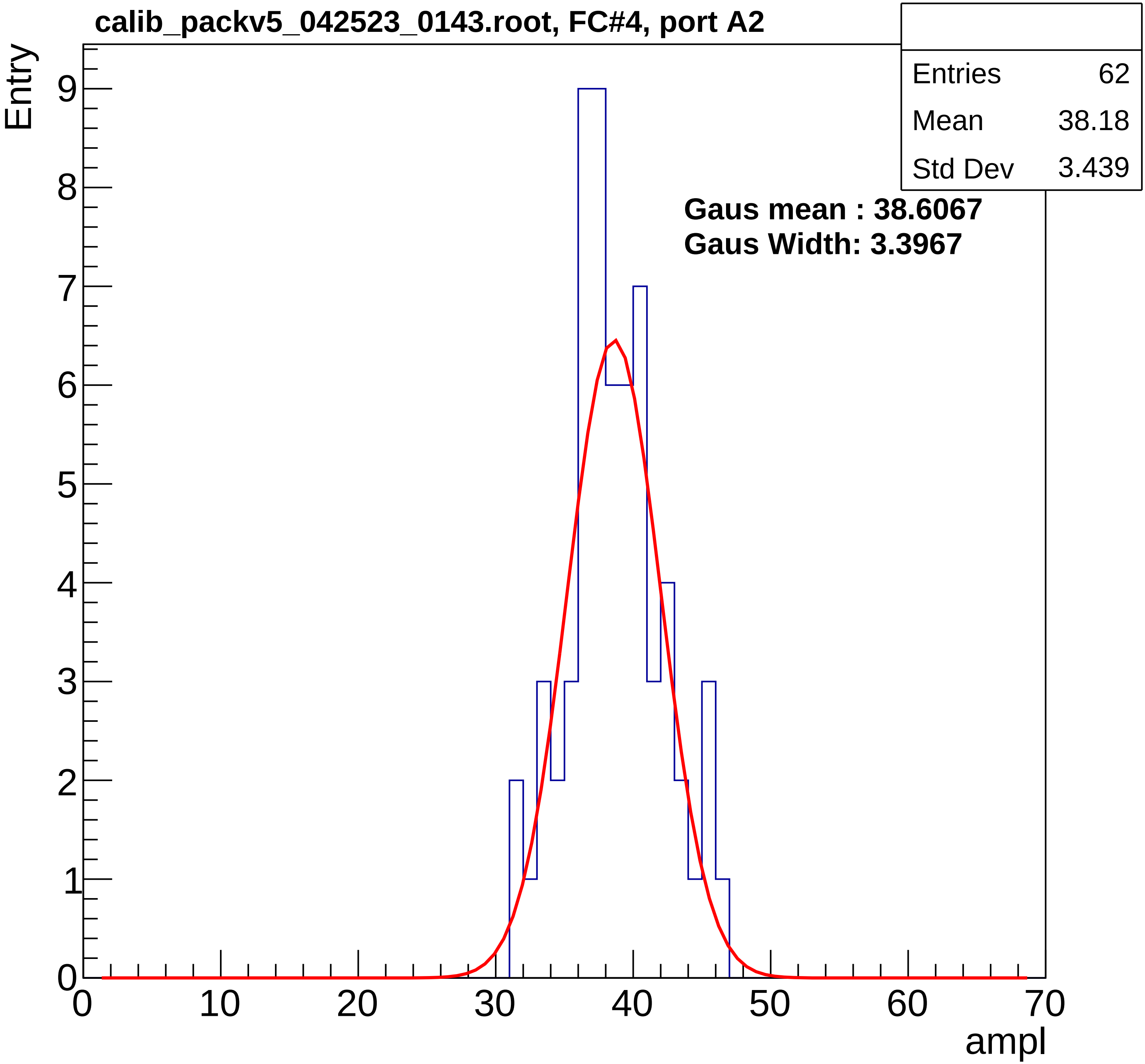

B1L100S, U21-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 36
Mean 59.03
Std Dev 10.14
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

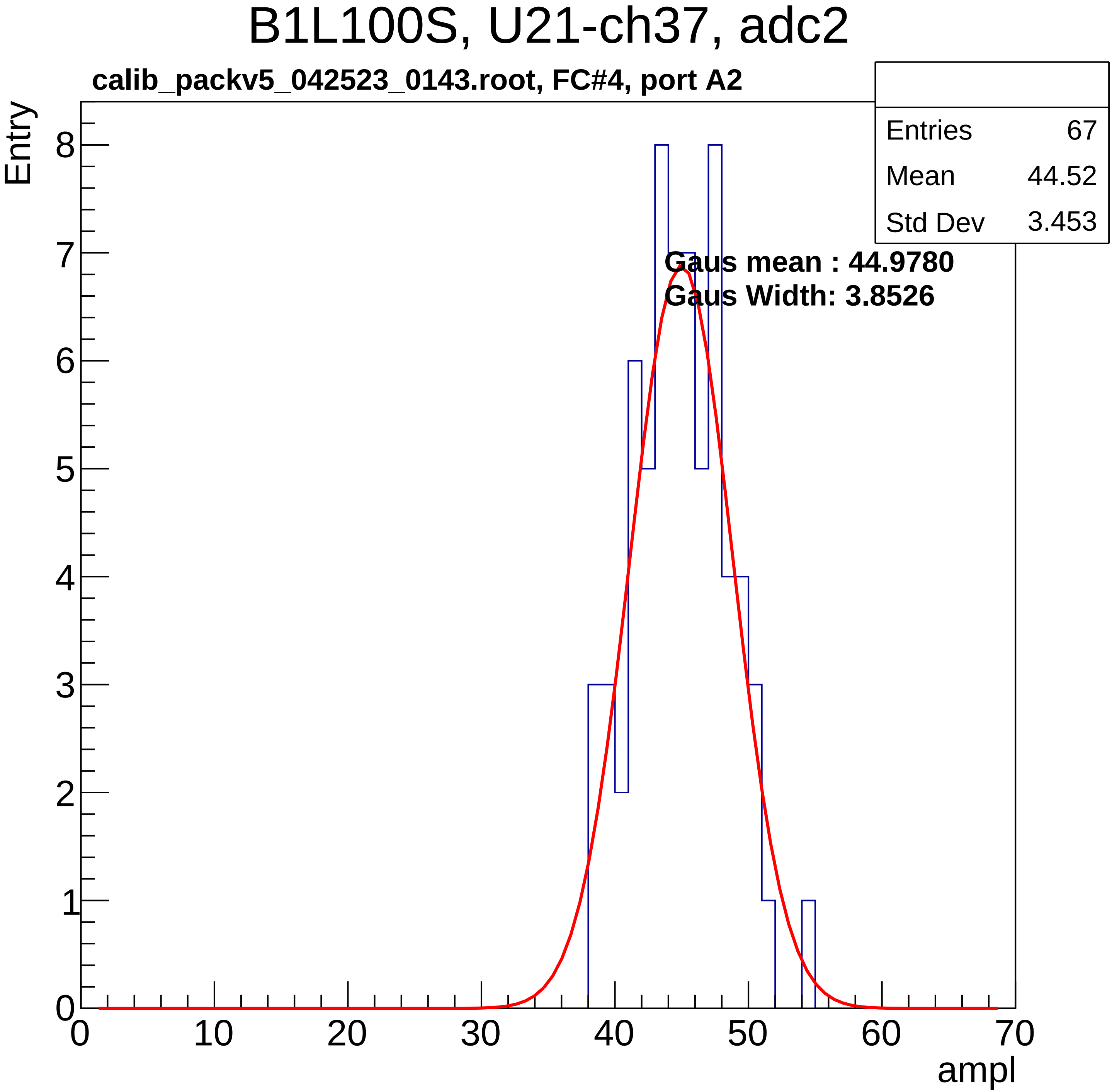

B1L100S, U21-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

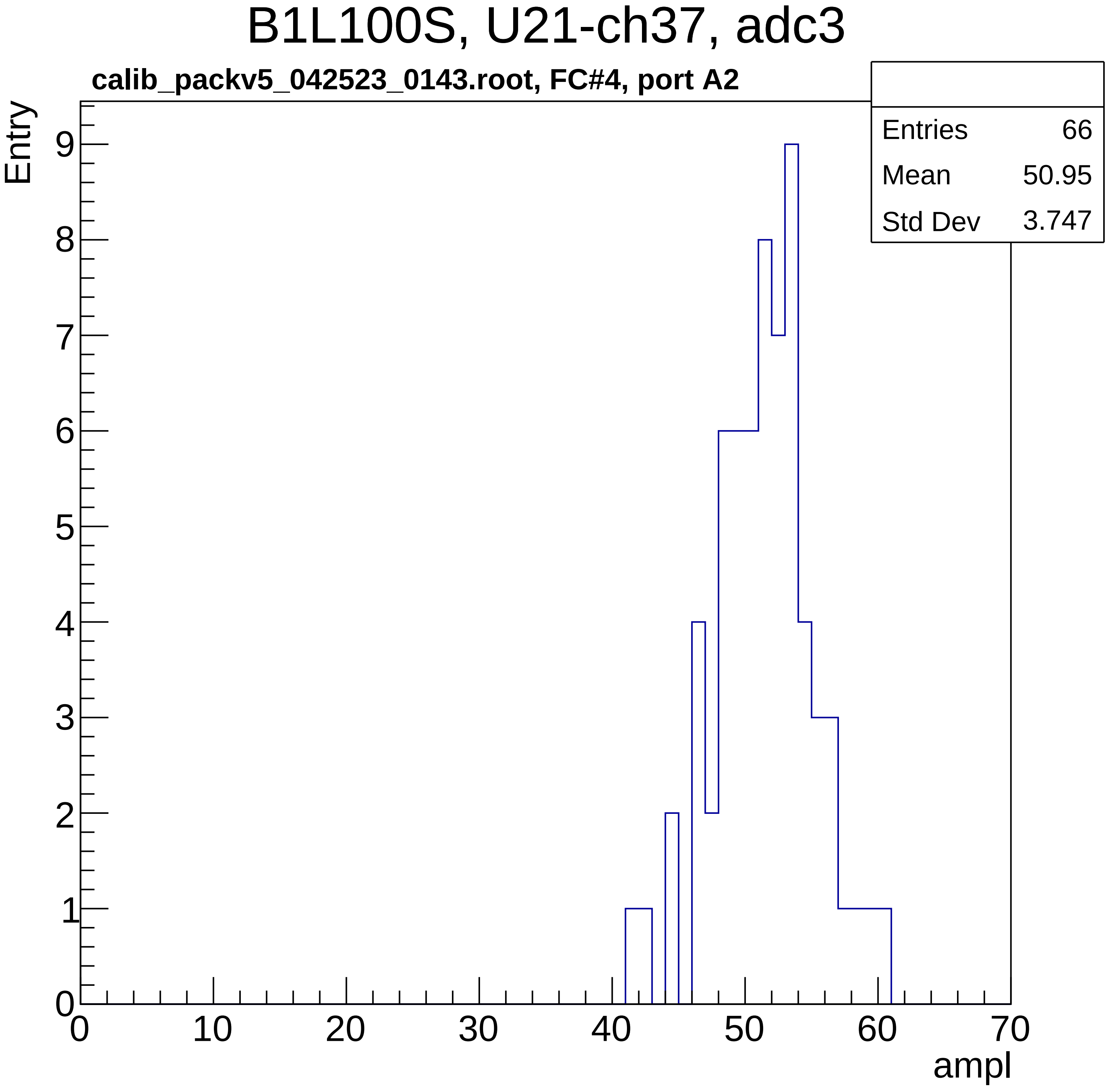

B1L100S, U21-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 99
Mean 29.92
Std Dev 3.946
Gaus mean : 30.0712
Gaus Width: 3.8100
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

B1L100S, U21-ch37, adc1

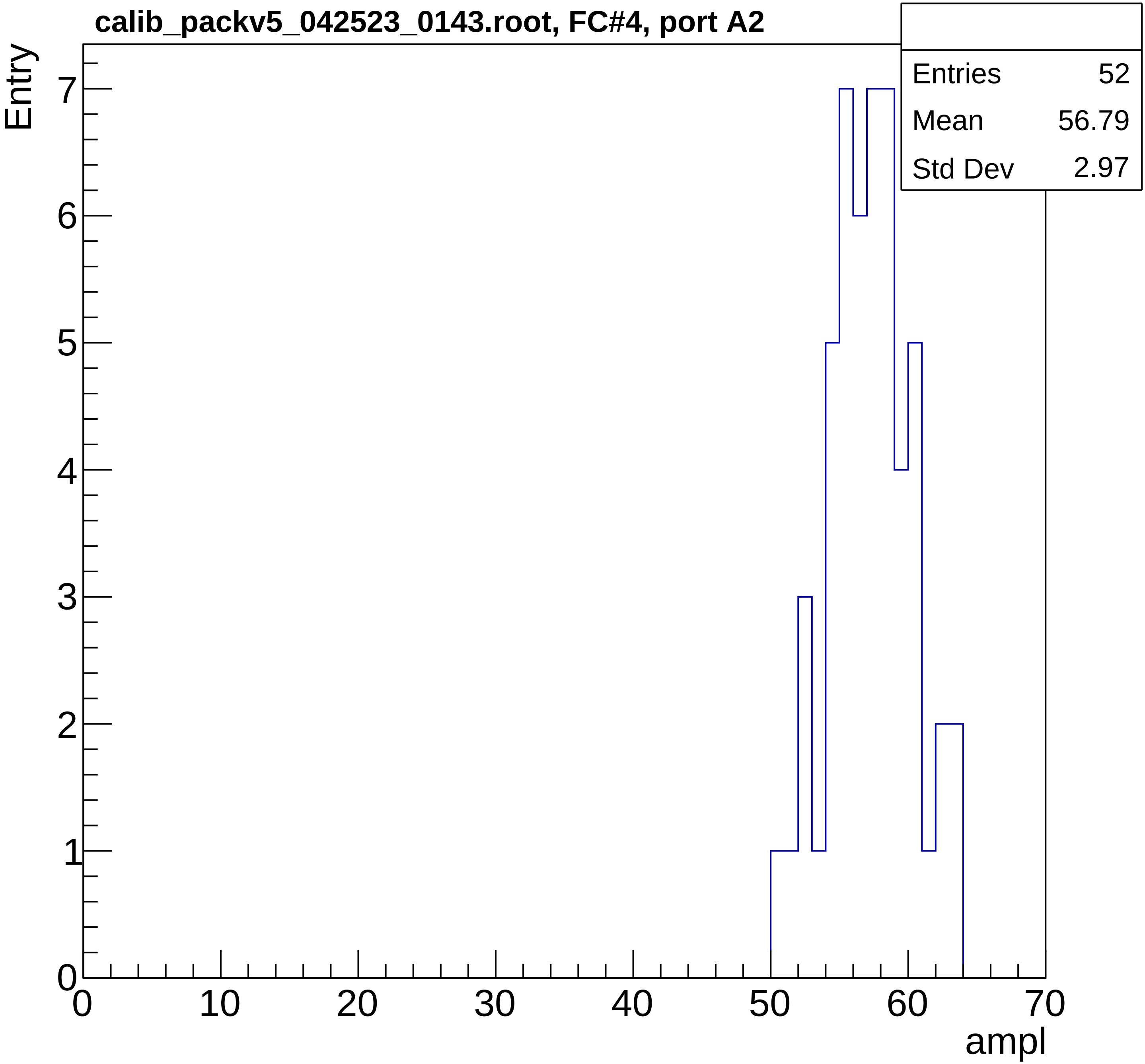


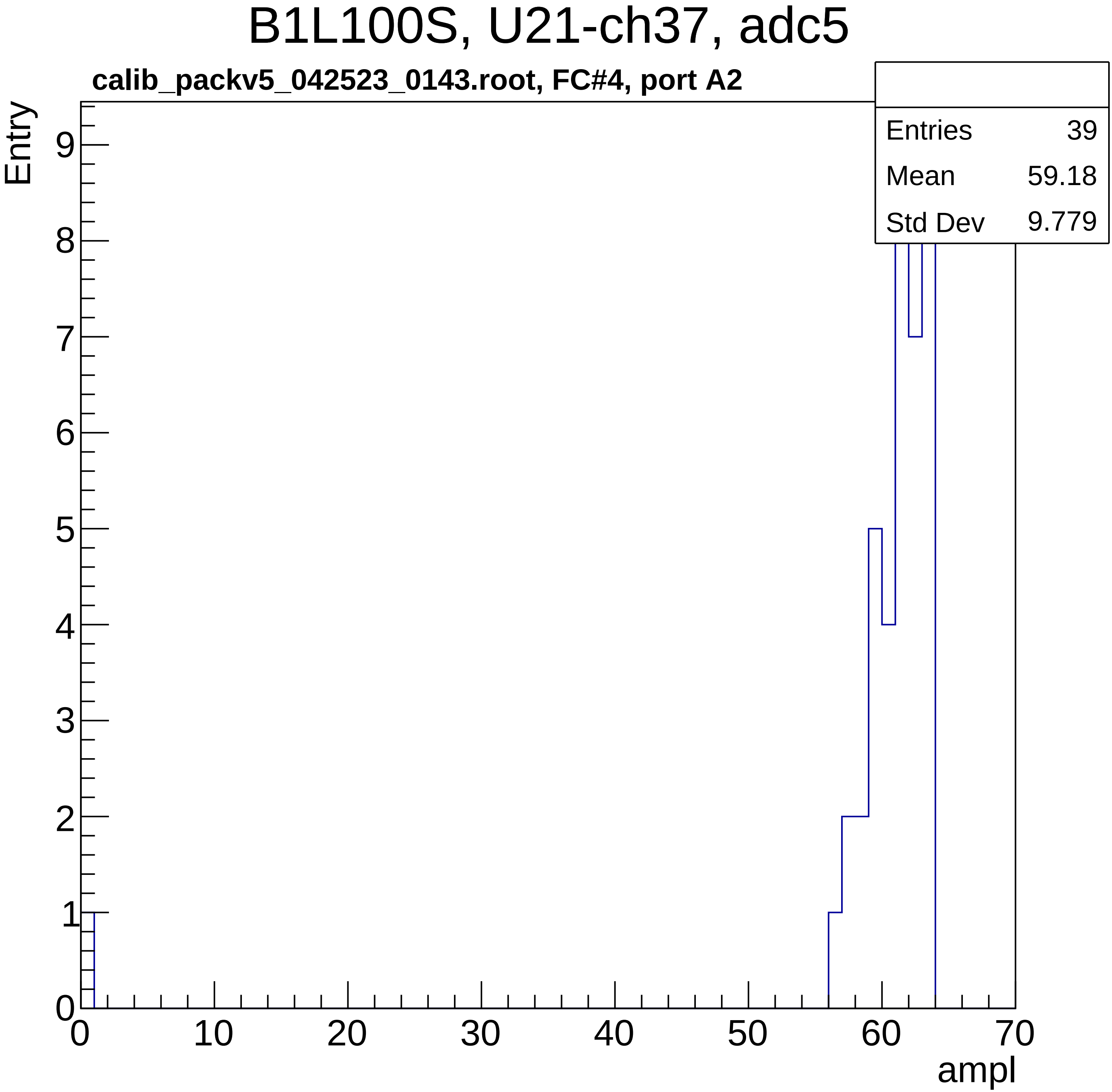

B1L100S, U21-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 67
Mean 44.52
Std Dev 3.453
Gaus mean : 44.9780
Gaus Width: 3.8526
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

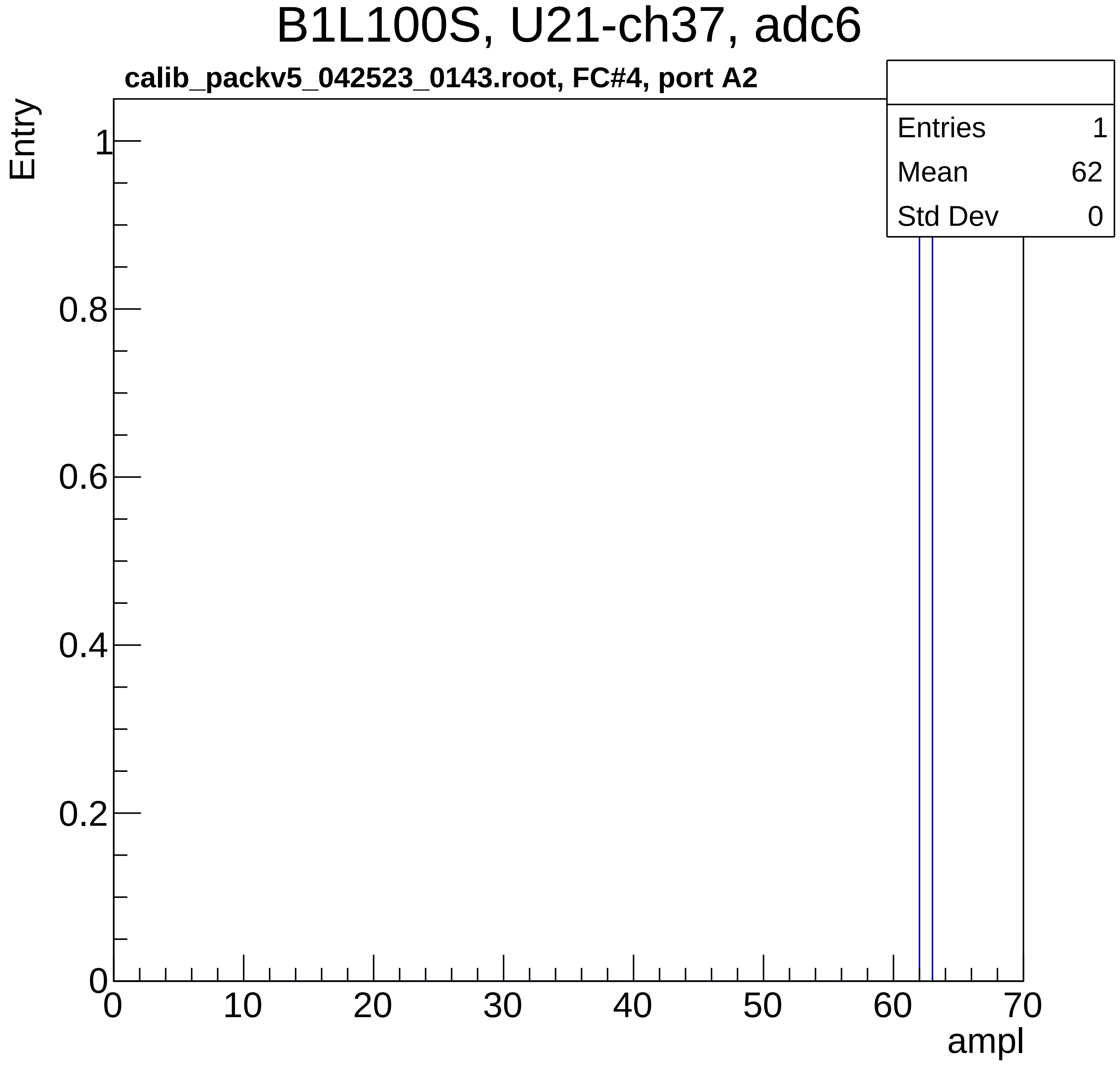

B1L100S, U21-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.95
Std Dev 3.747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

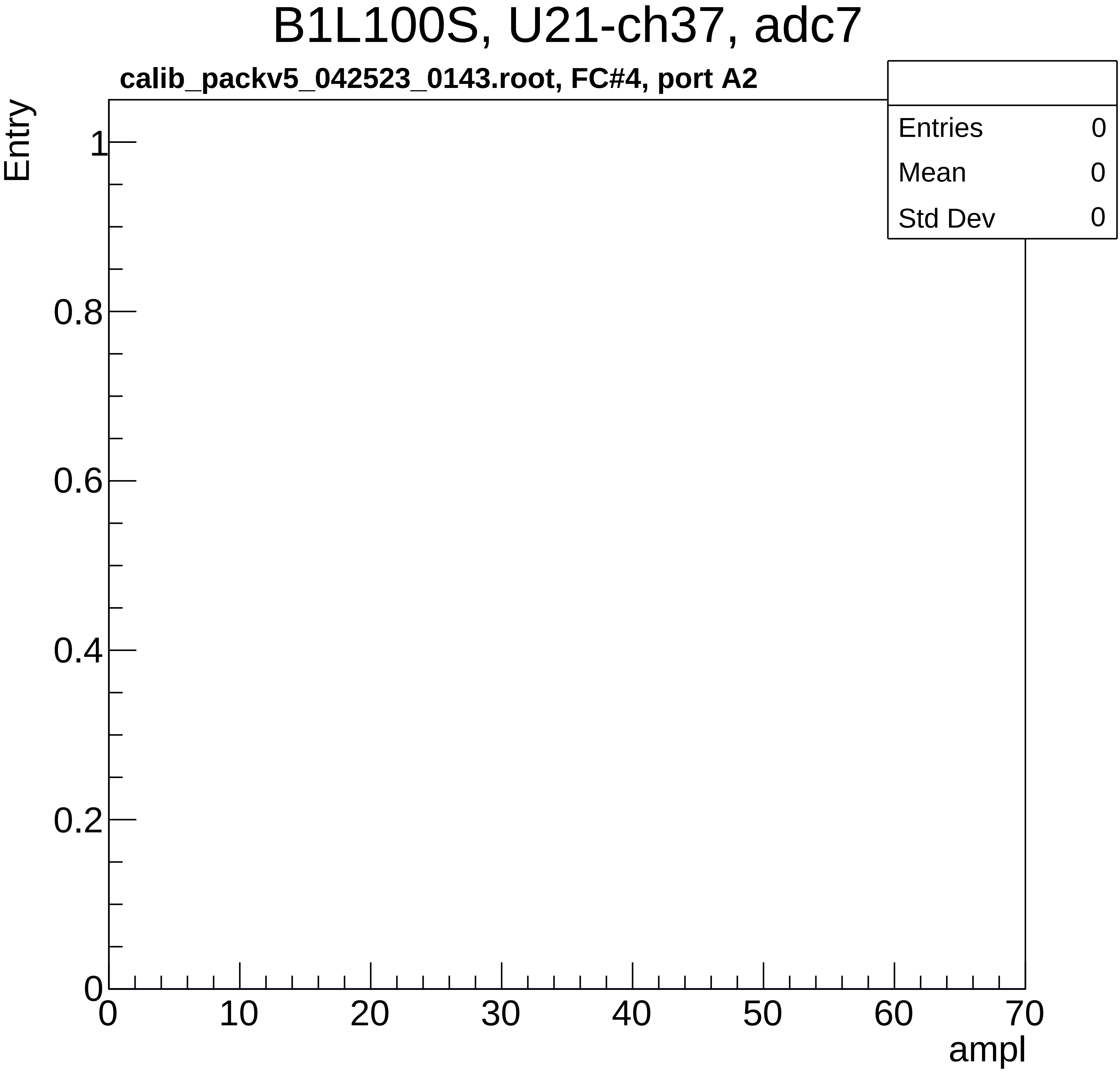
B1L100S, U21-ch37, adc4

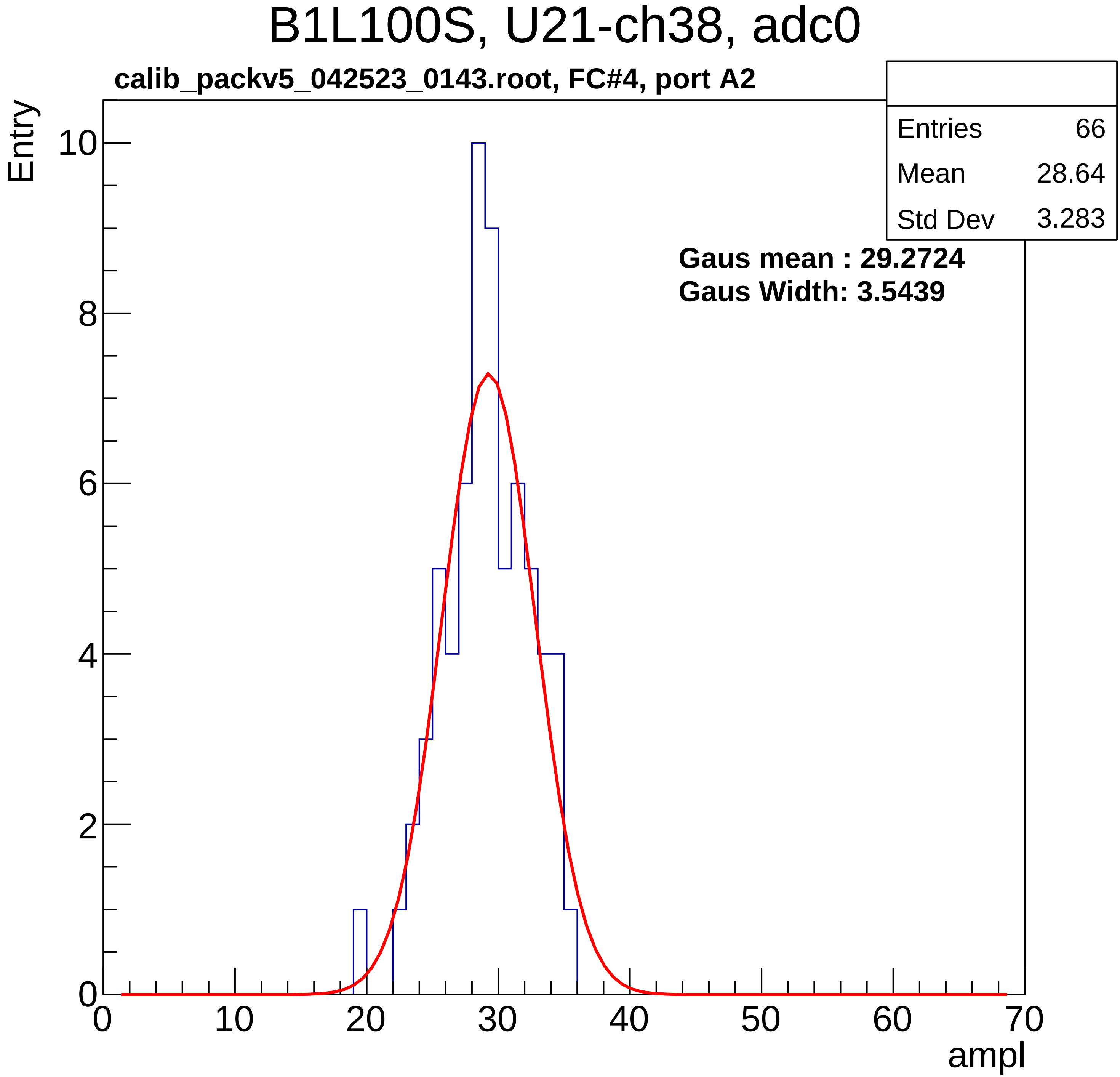
calib_packv5_042523_0143.root, FC#4, port A2

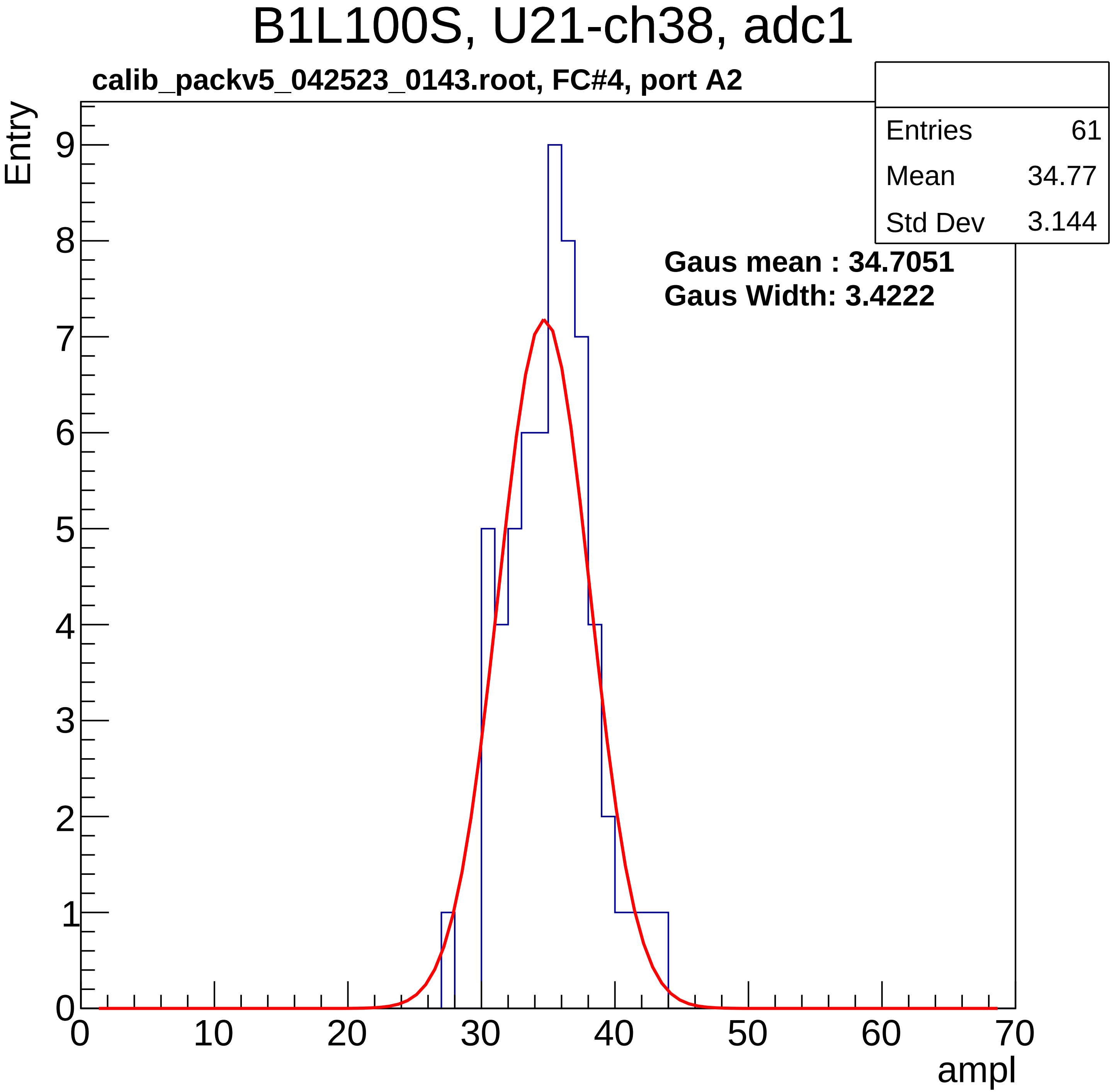


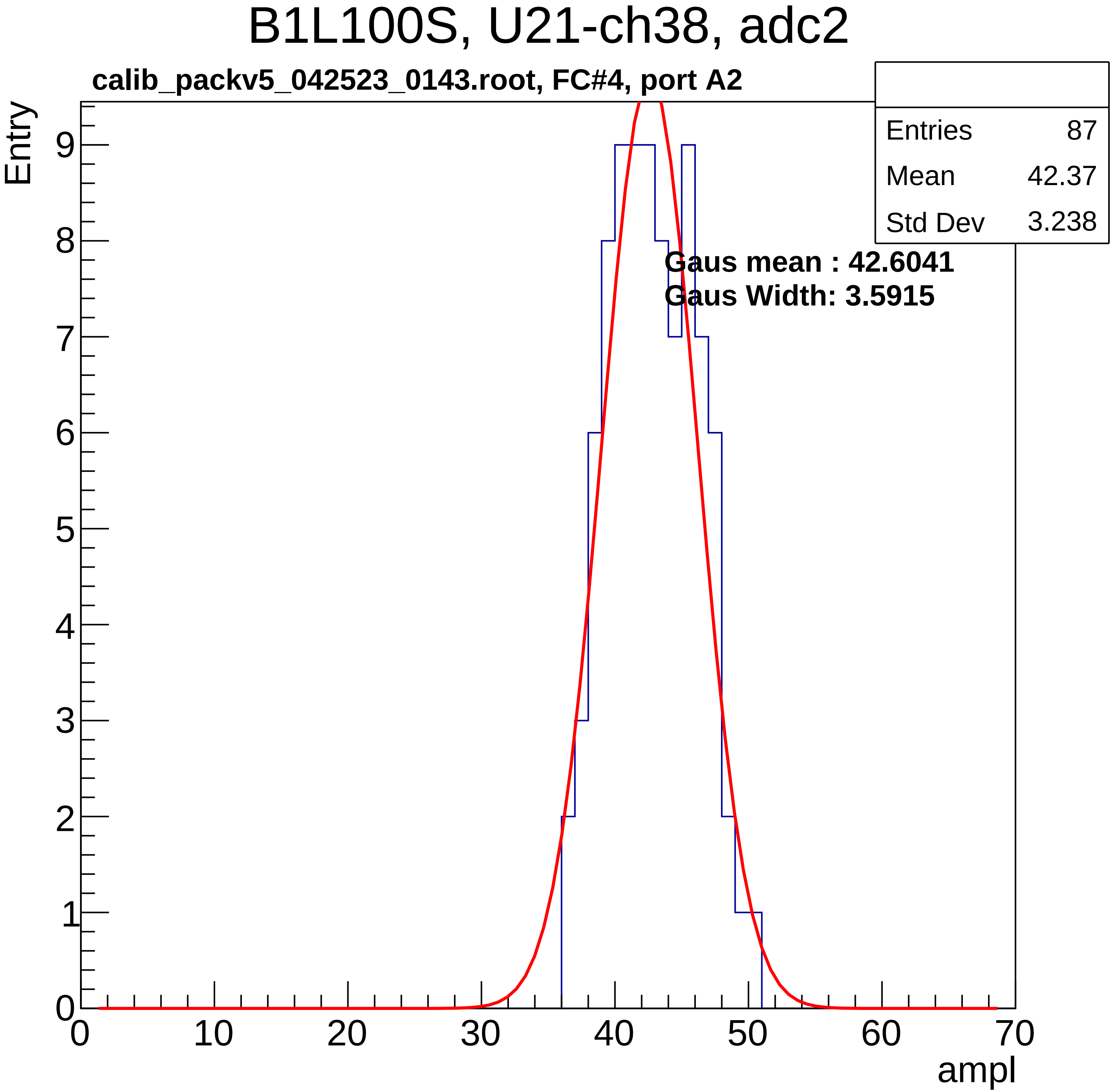

B1L100S, U21-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.18
Std Dev 9.779
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

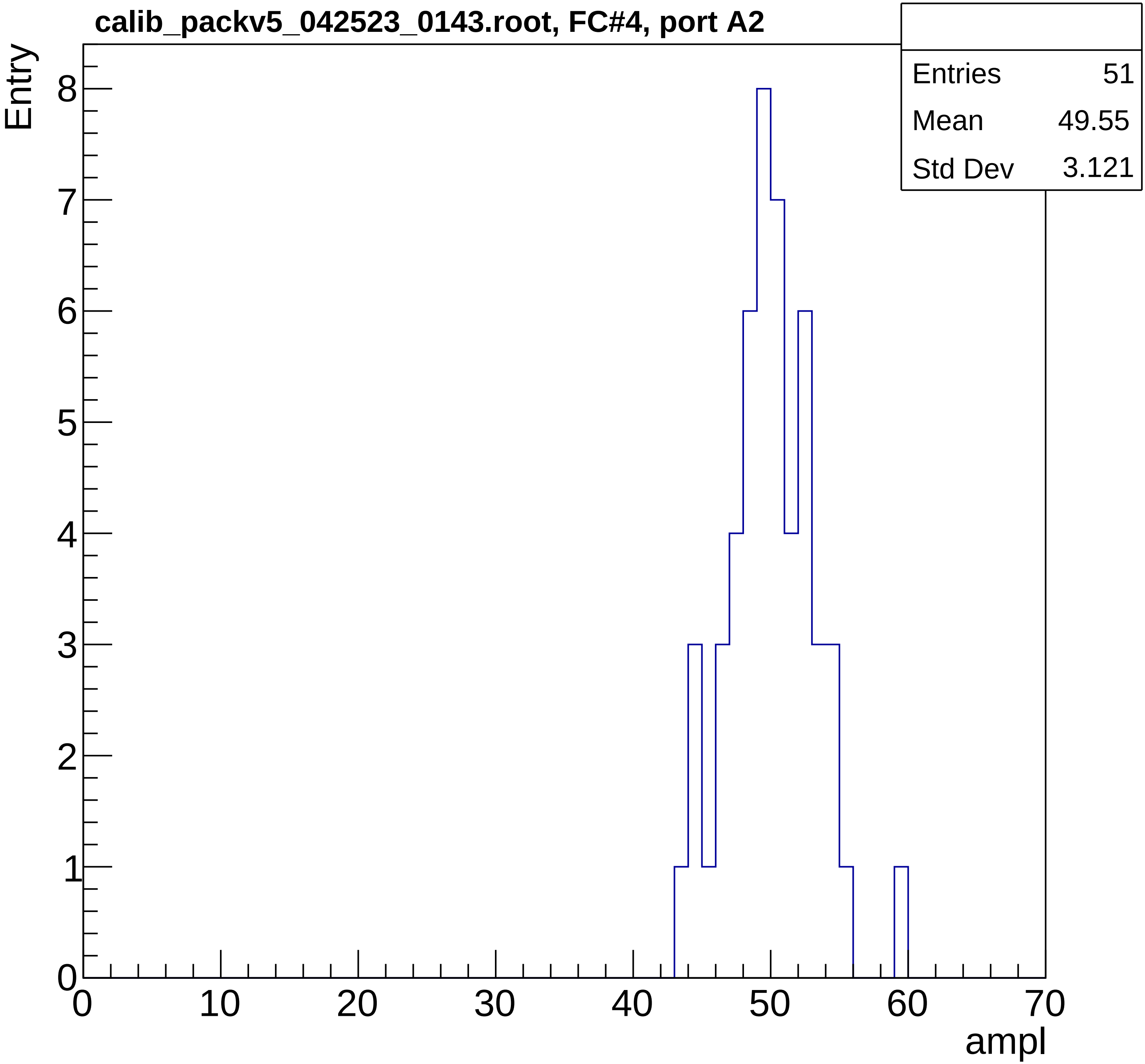

B1L100S, U21-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 0
Mean 0
Std Dev 0
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.64
Std Dev 3.283
Gaus mean : 29.2724
Gaus Width: 3.5439
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

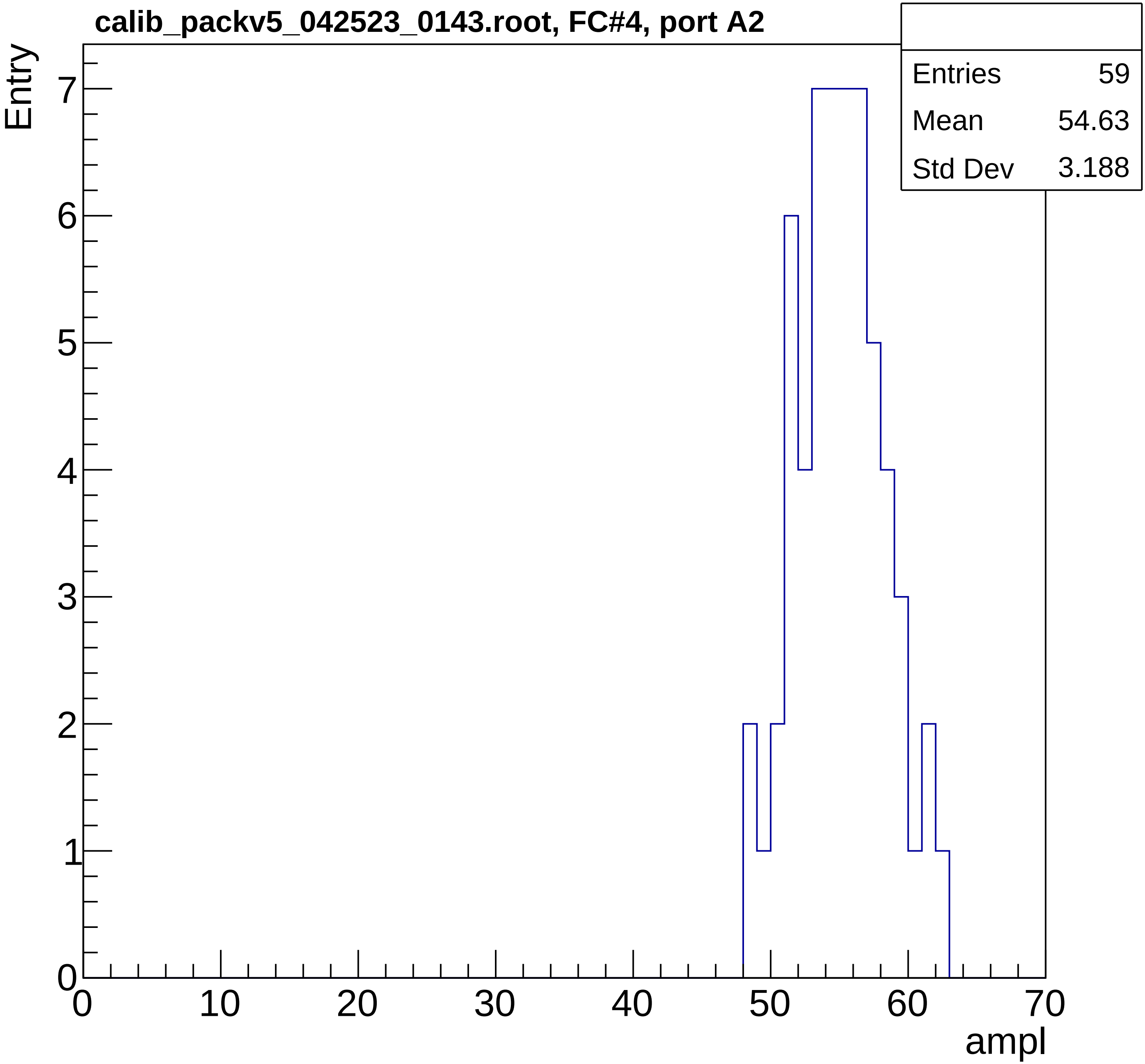

B1L100S, U21-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 34.77
Std Dev 3.144
Gaus mean : 34.7051
Gaus Width: 3.4222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

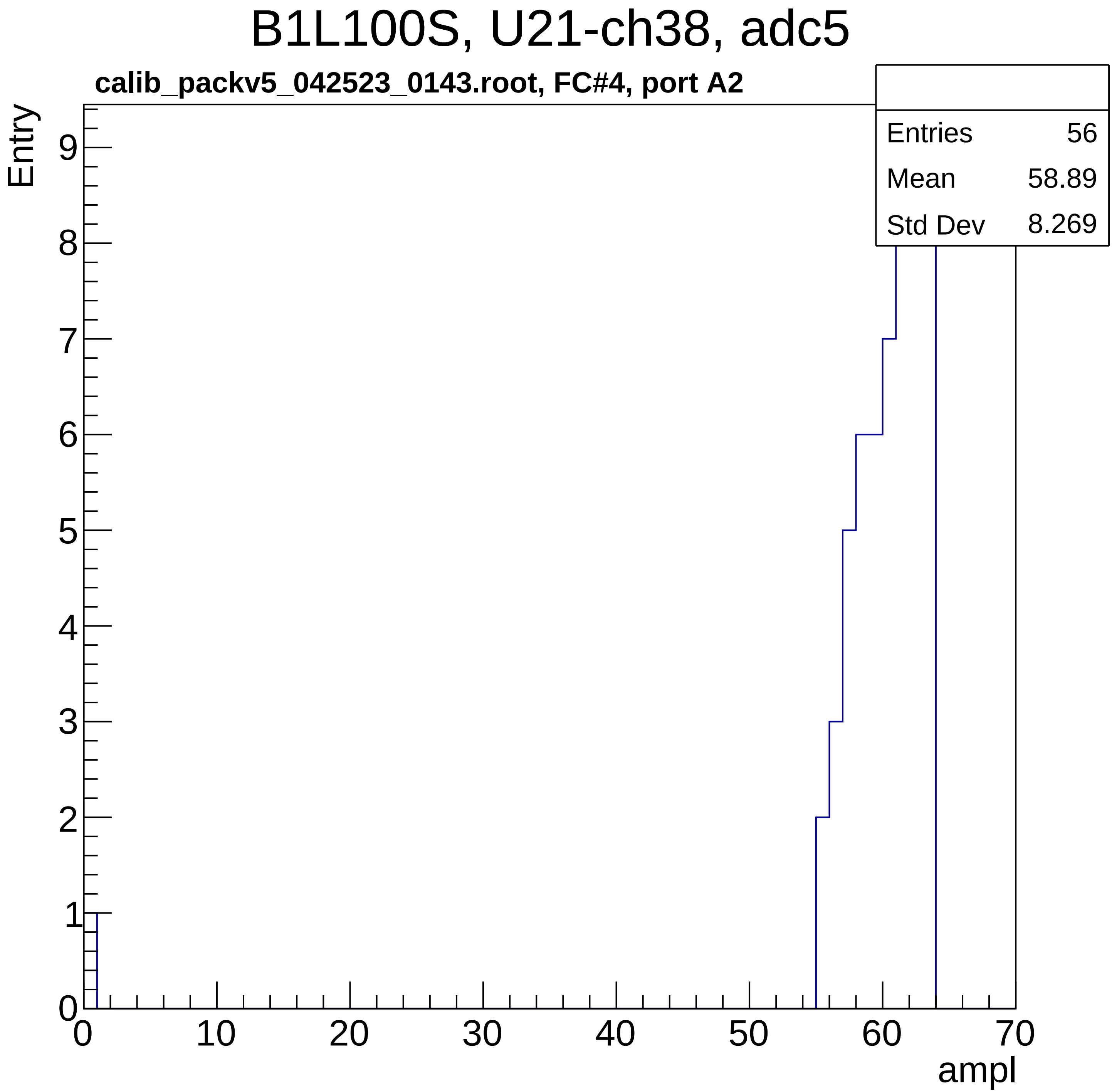

B1L100S, U21-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 42.37
Std Dev 3.238
Gaus mean : 42.6041
Gaus Width: 3.5915
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch38, adc3

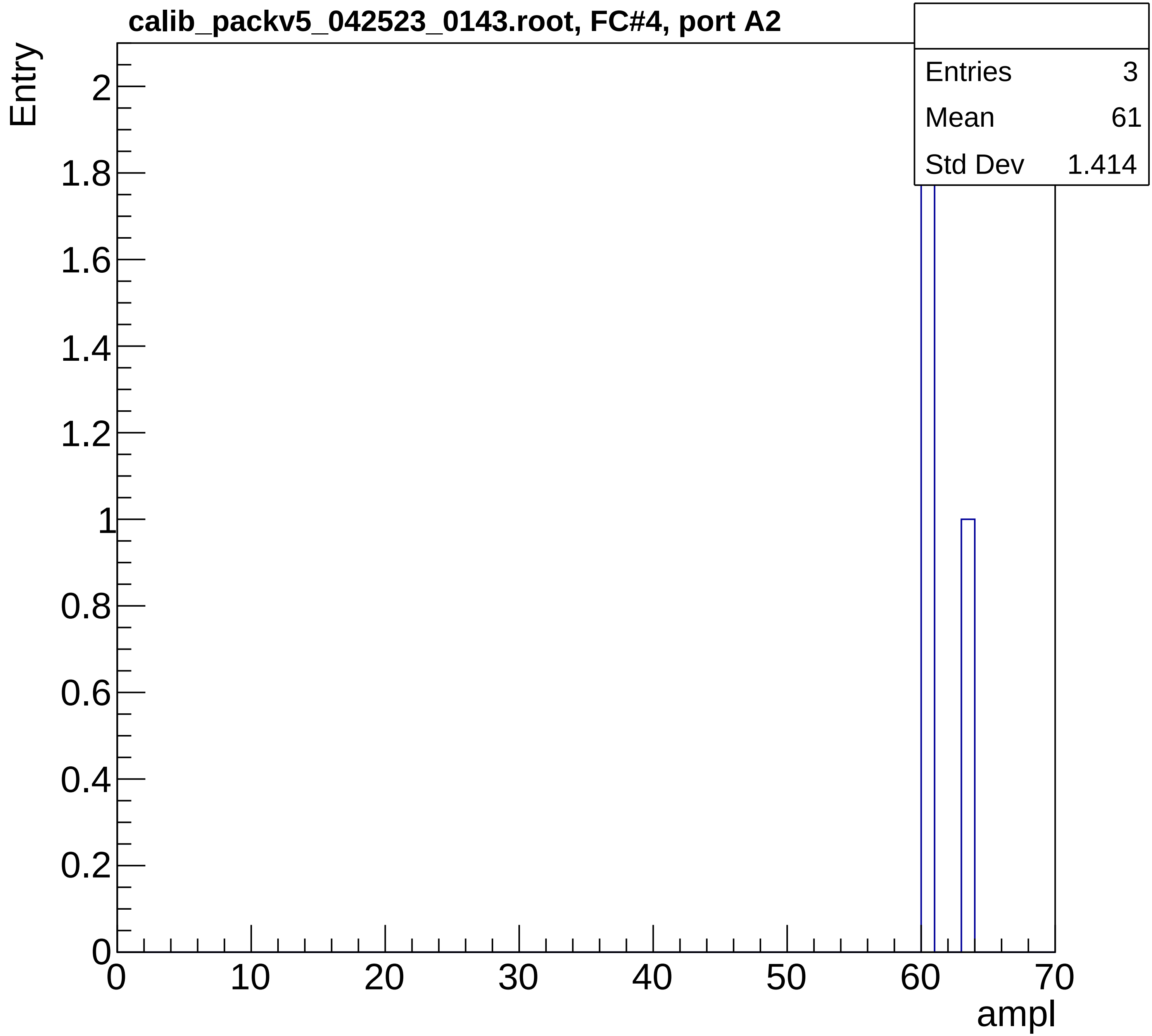


B1L100S, U21-ch38, adc4

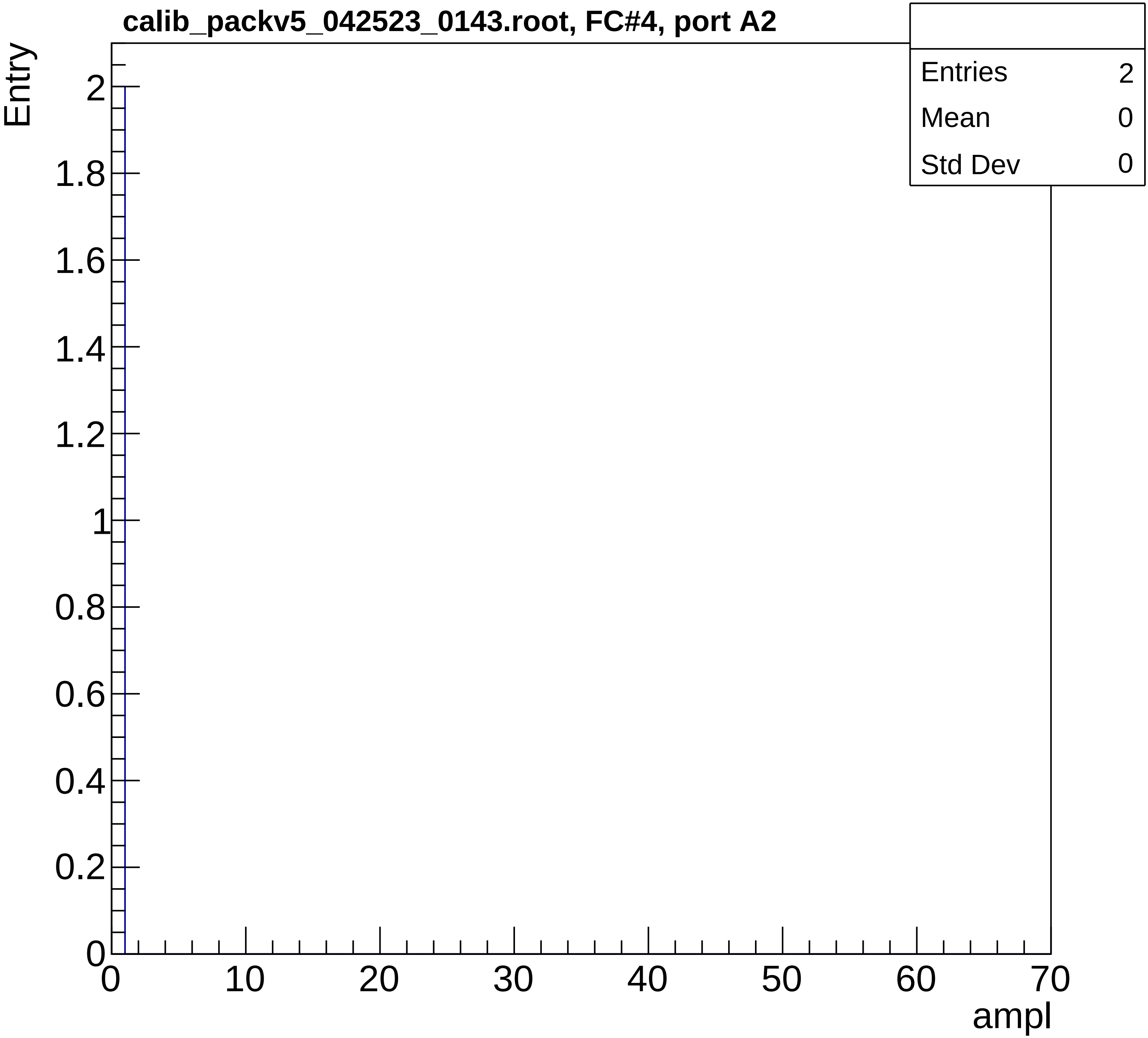


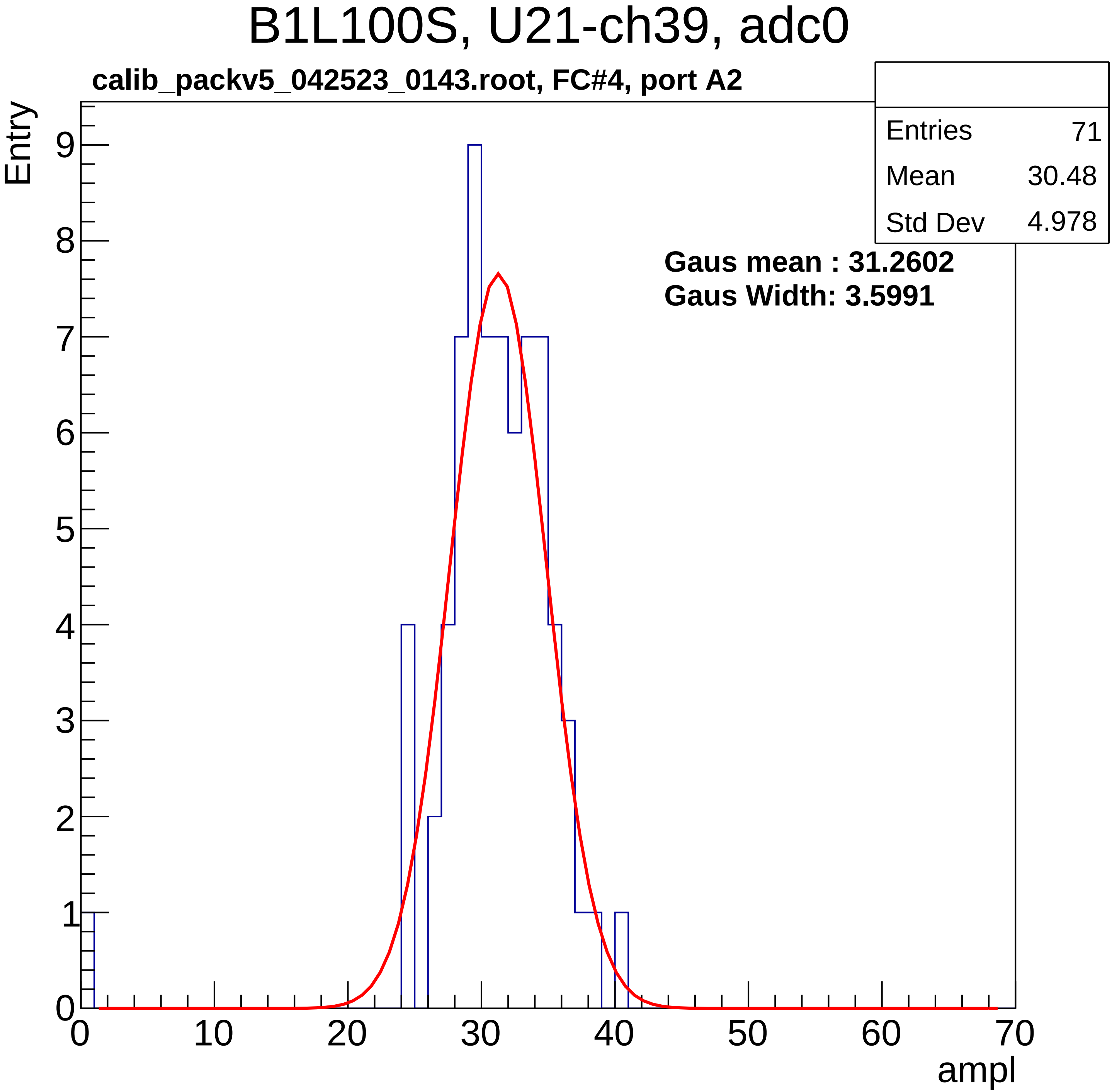

B1L100S, U21-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.89
Std Dev 8.269
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

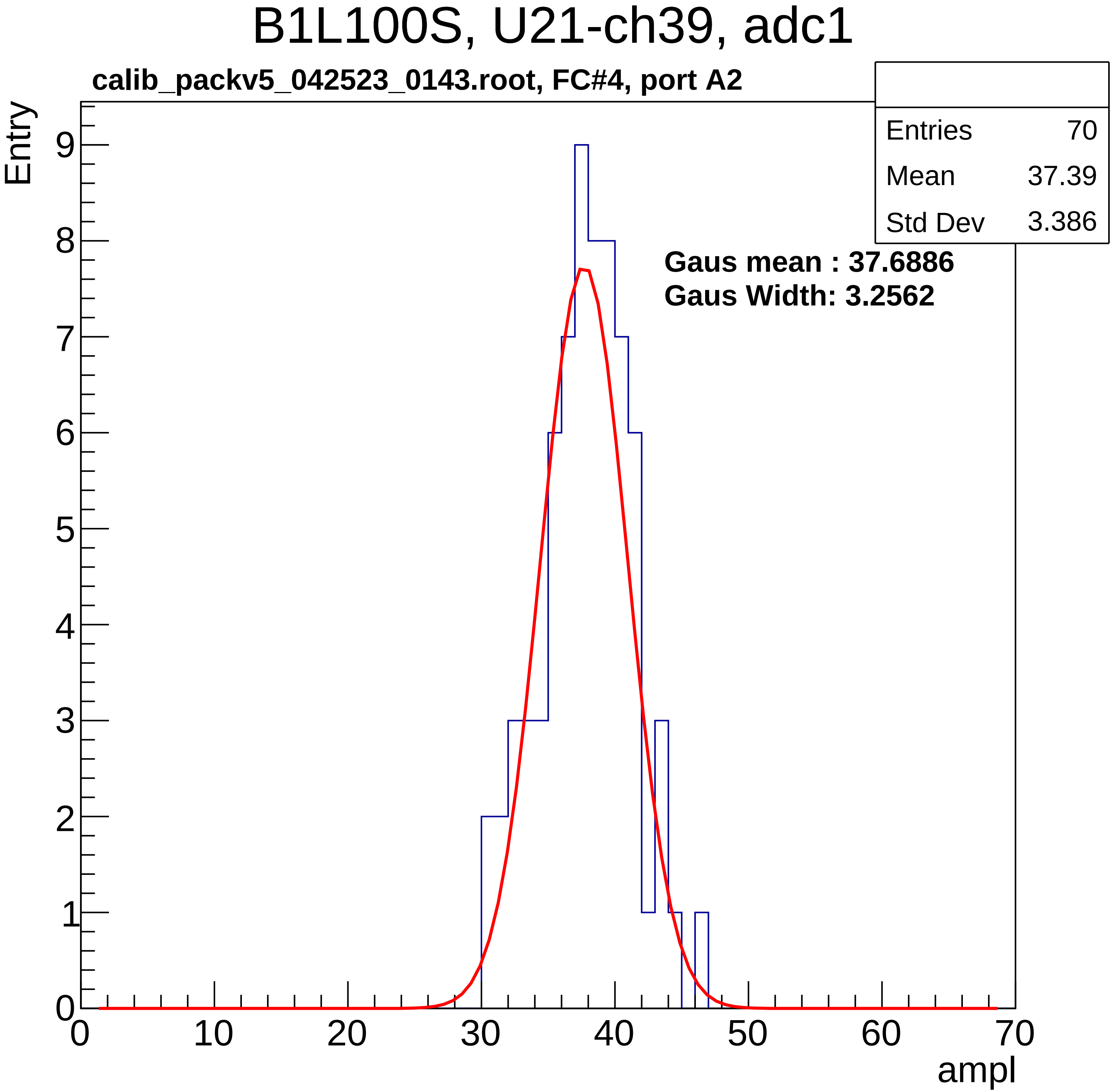
B1L100S, U21-ch38, adc6



B1L100S, U21-ch38, adc7




B1L100S, U21-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 30.48
Std Dev 4.978
Gaus mean : 31.2602
Gaus Width: 3.5991
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.39
Std Dev 3.386
Gaus mean : 37.6886
Gaus Width: 3.2562
Entry
ampl
0
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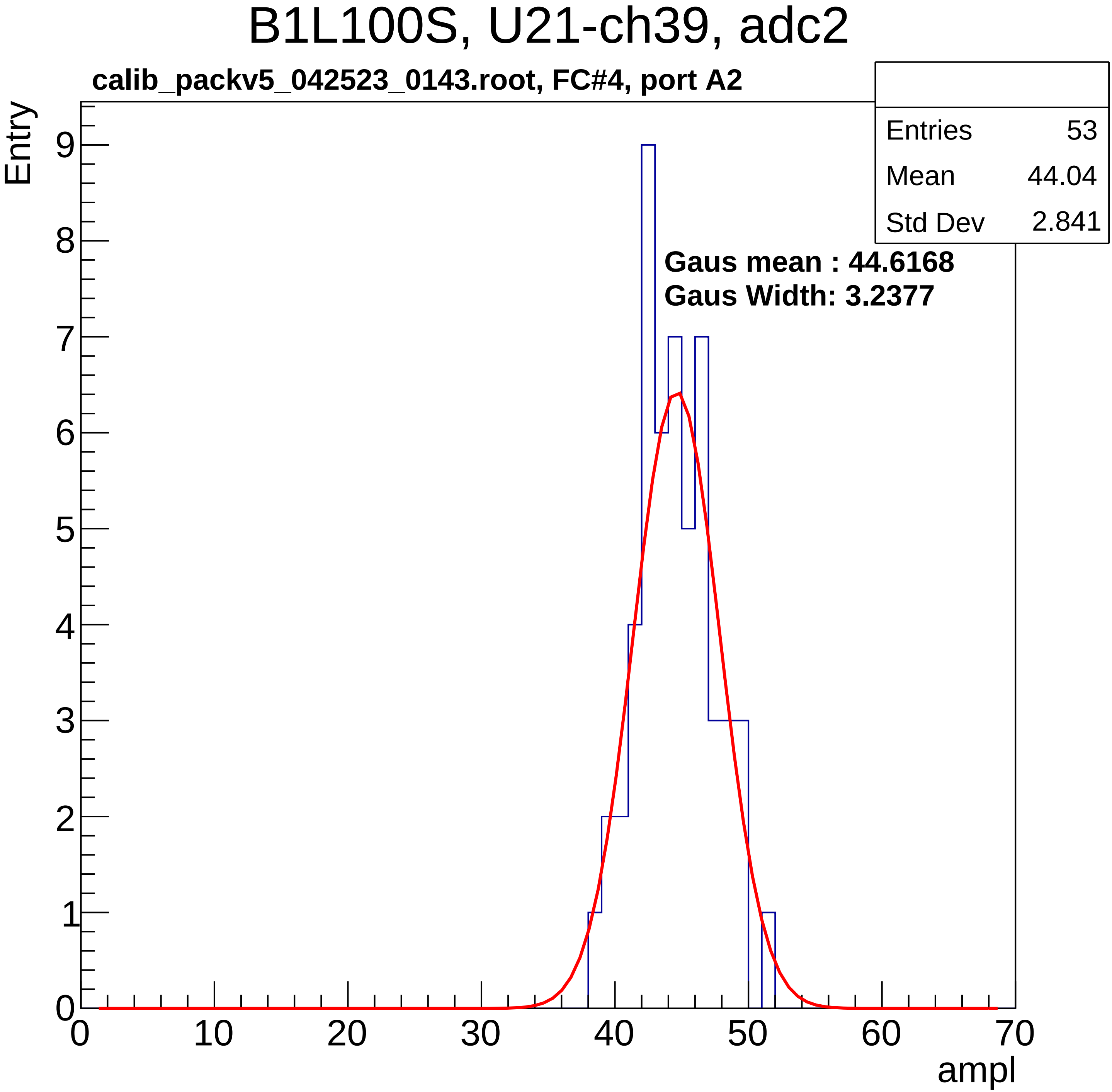
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 53
Mean 44.04
Std Dev 2.841
Gaus mean : 44.6168
Gaus Width: 3.2377
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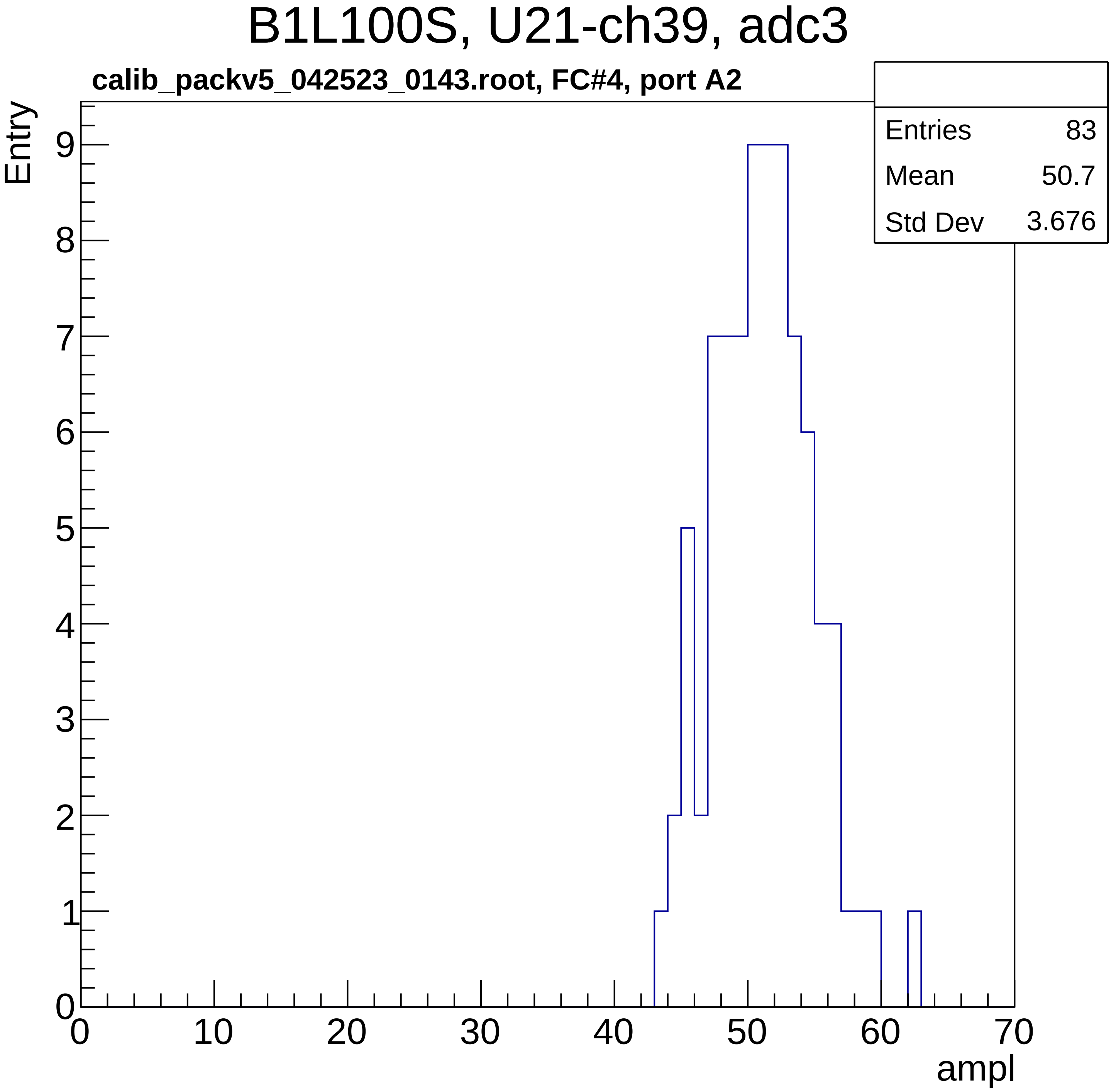
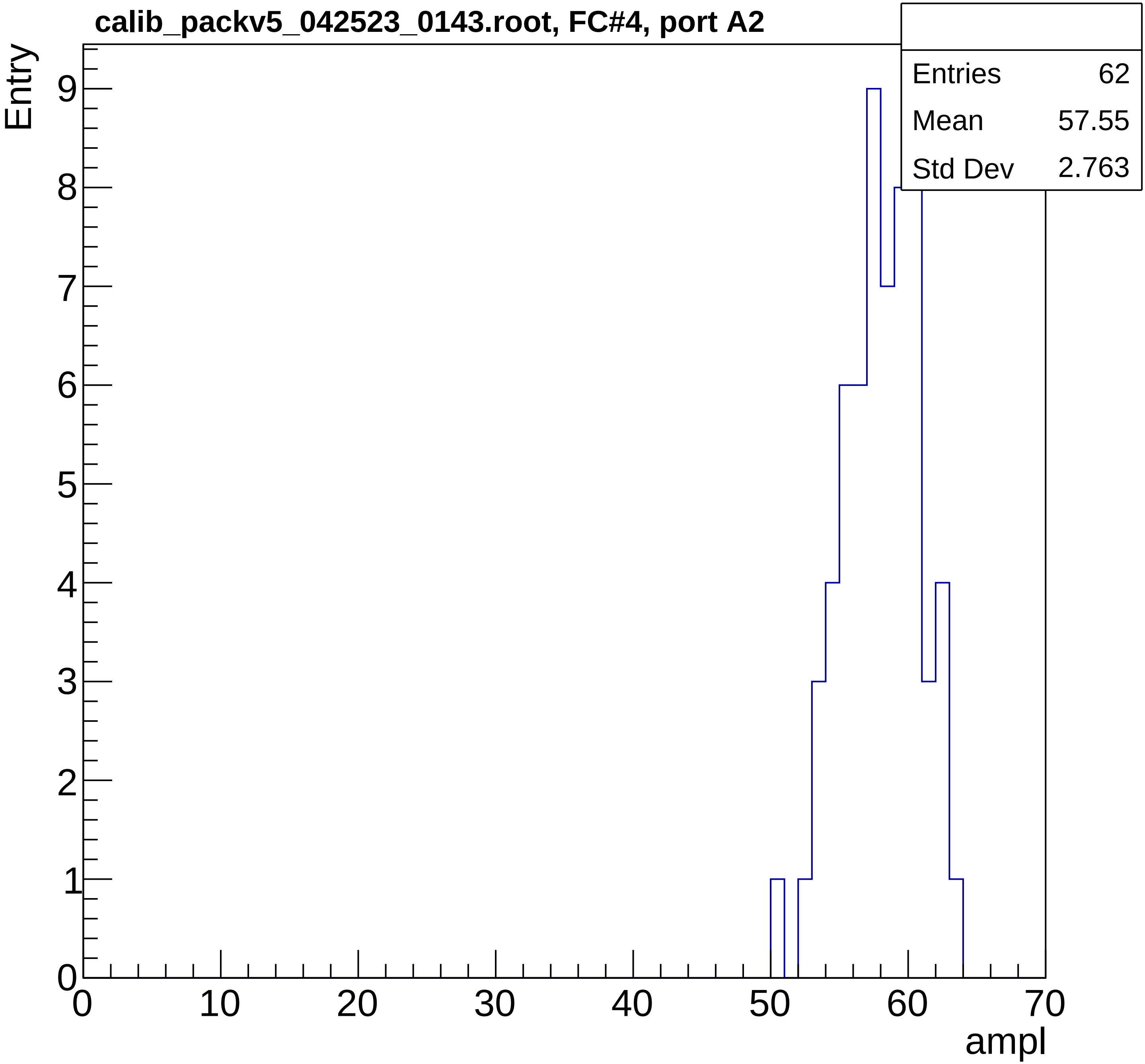
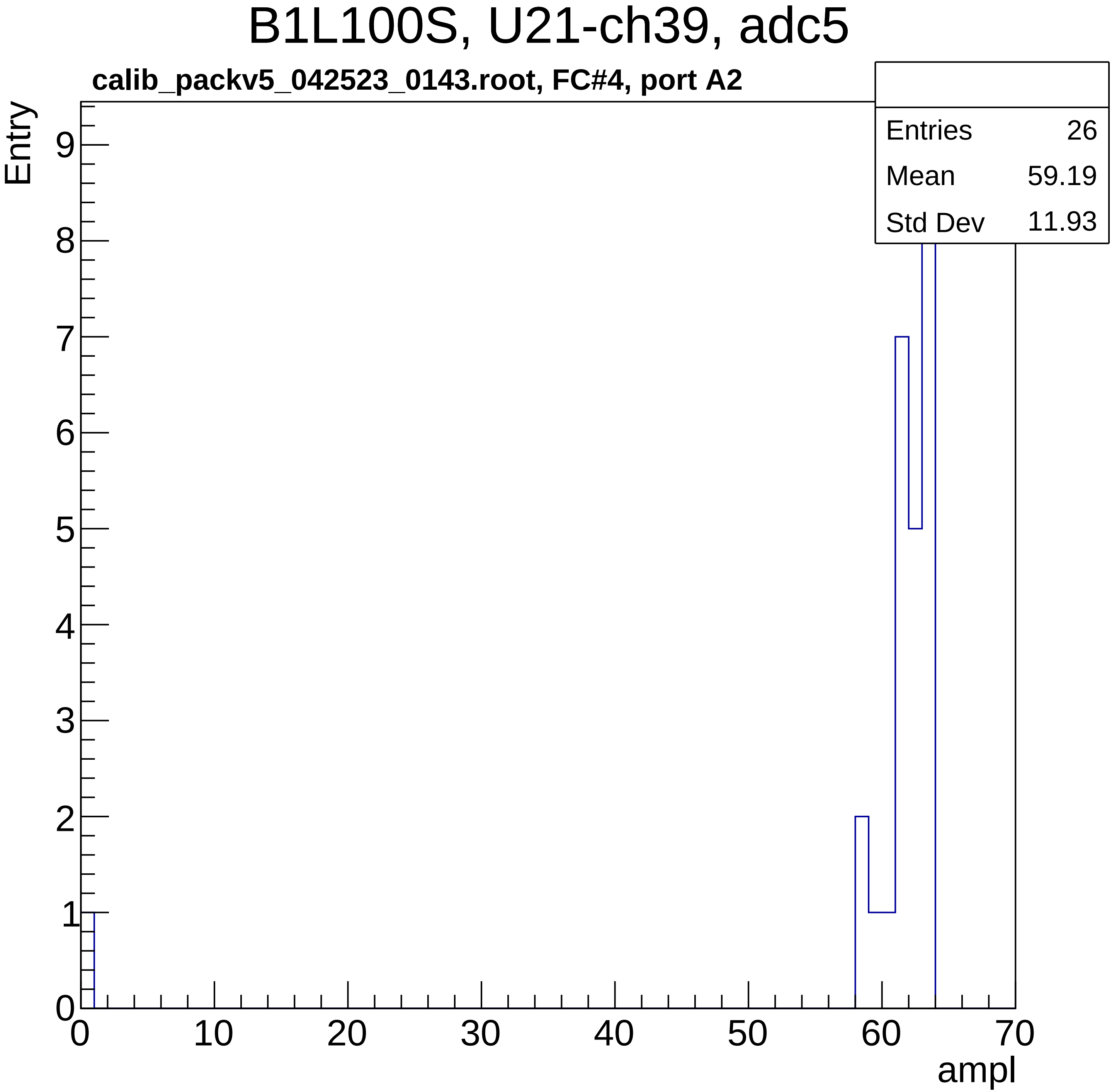
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 83
Mean 50.7
Std Dev 3.676
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2




B1L100S, U21-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 26
Mean 59.19
Std Dev 11.93
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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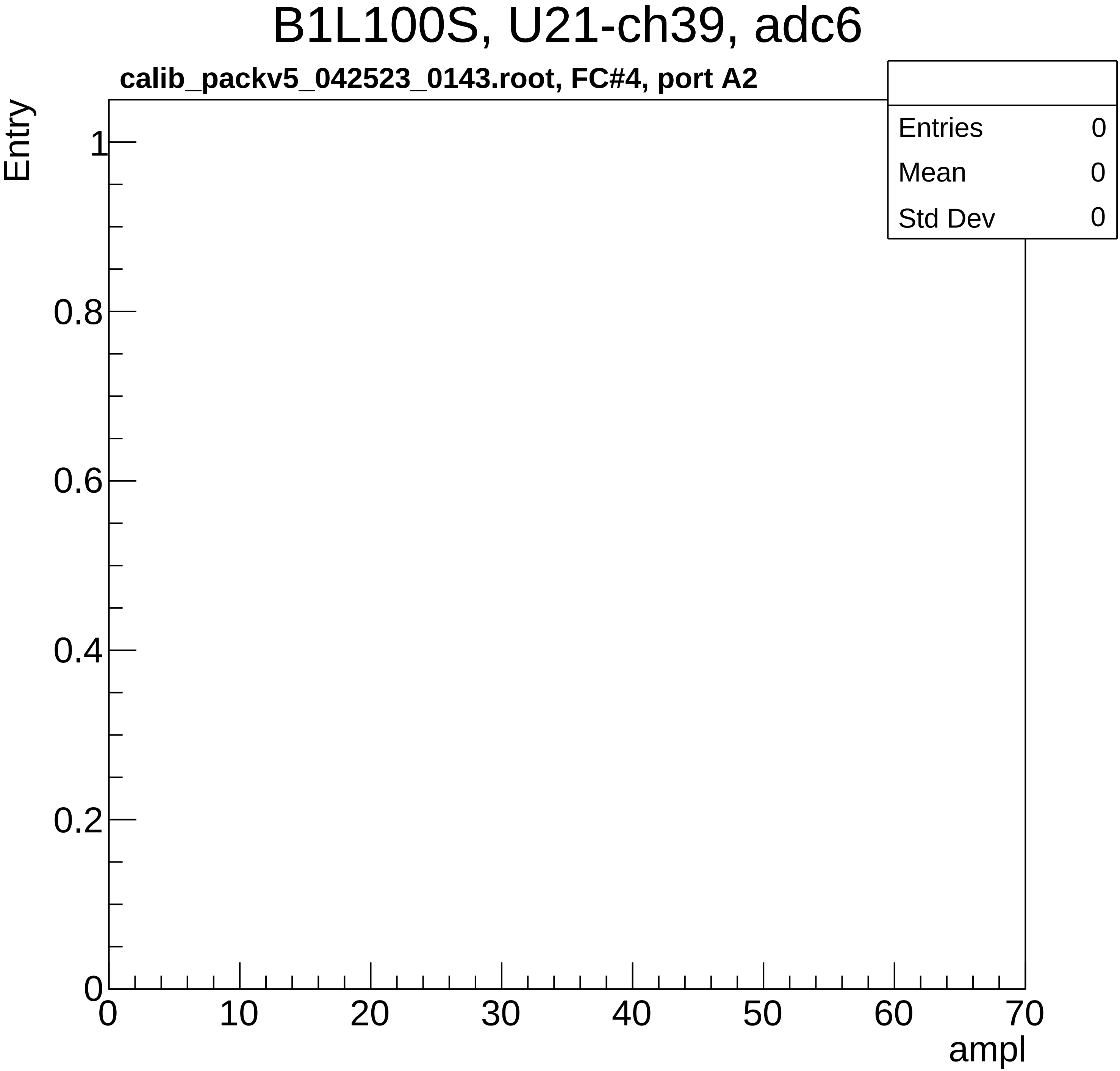
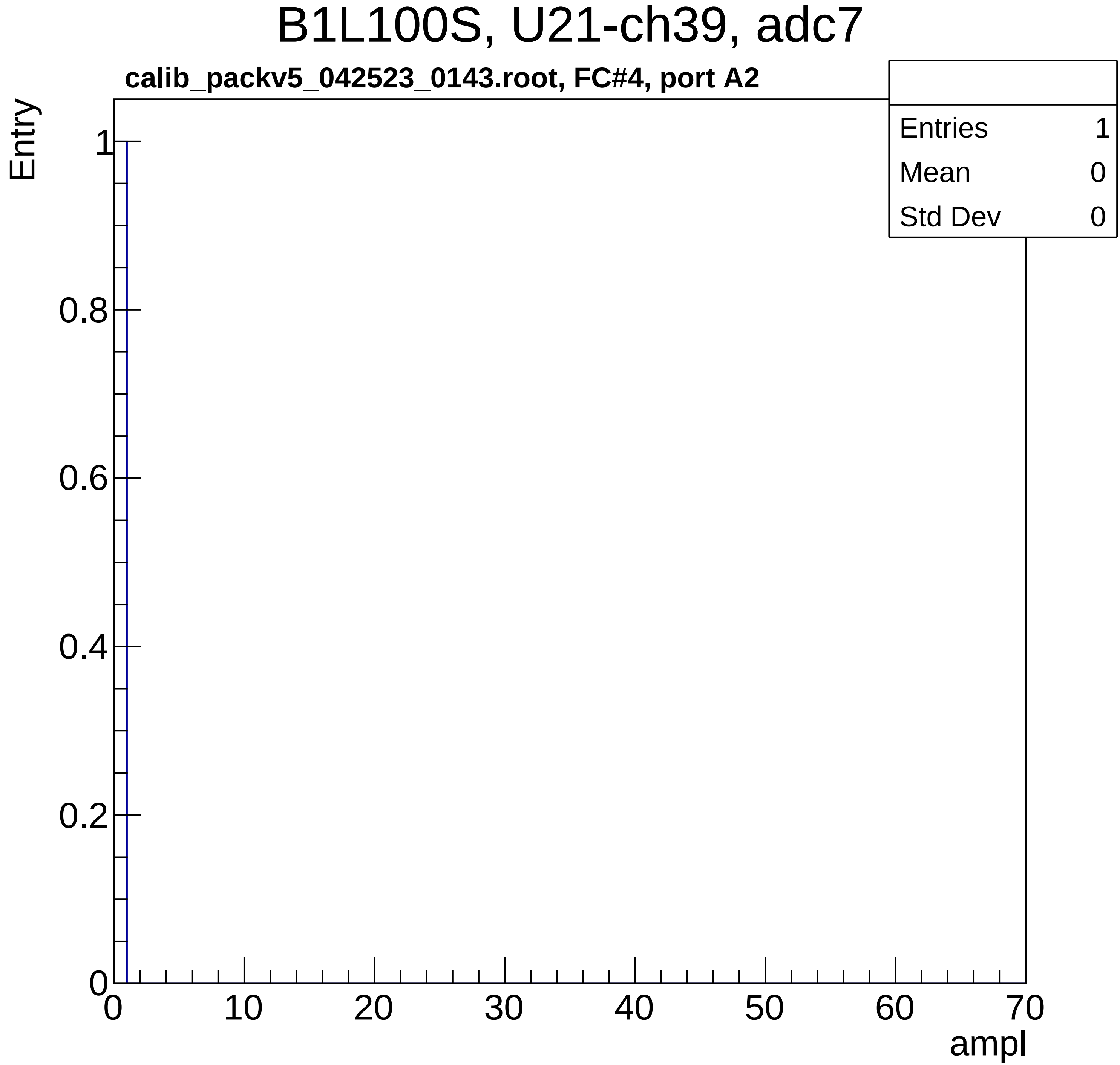
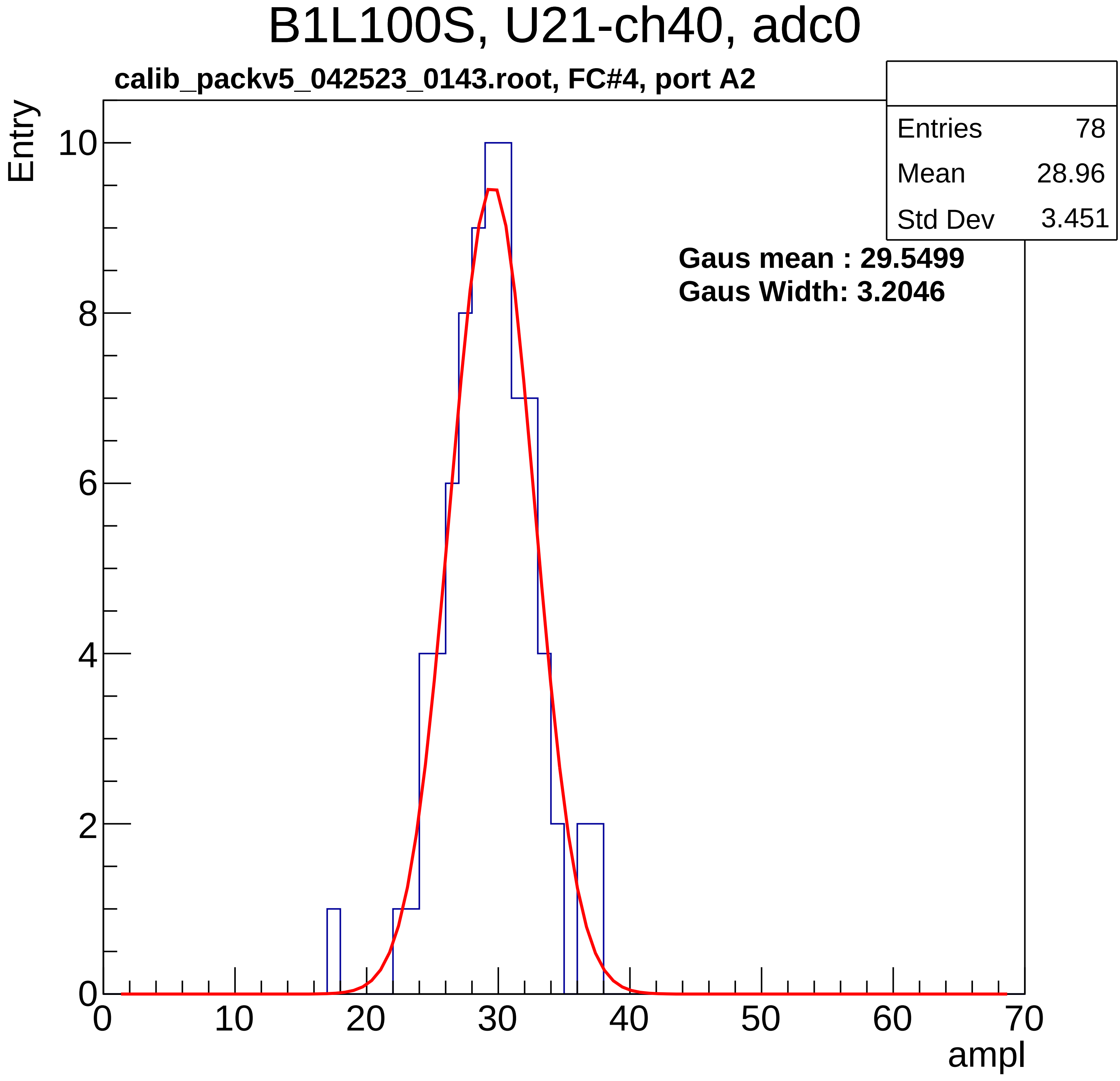
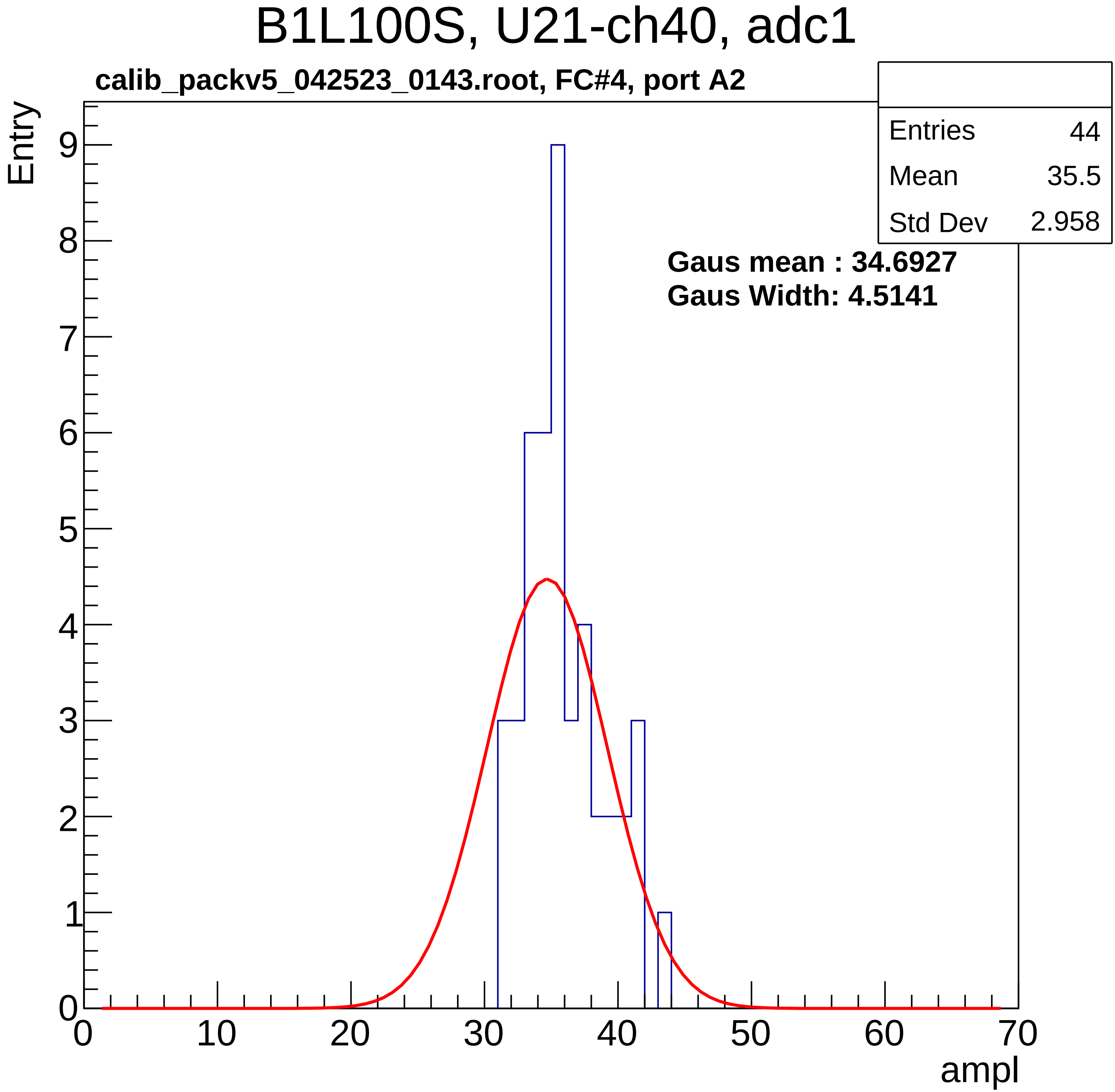
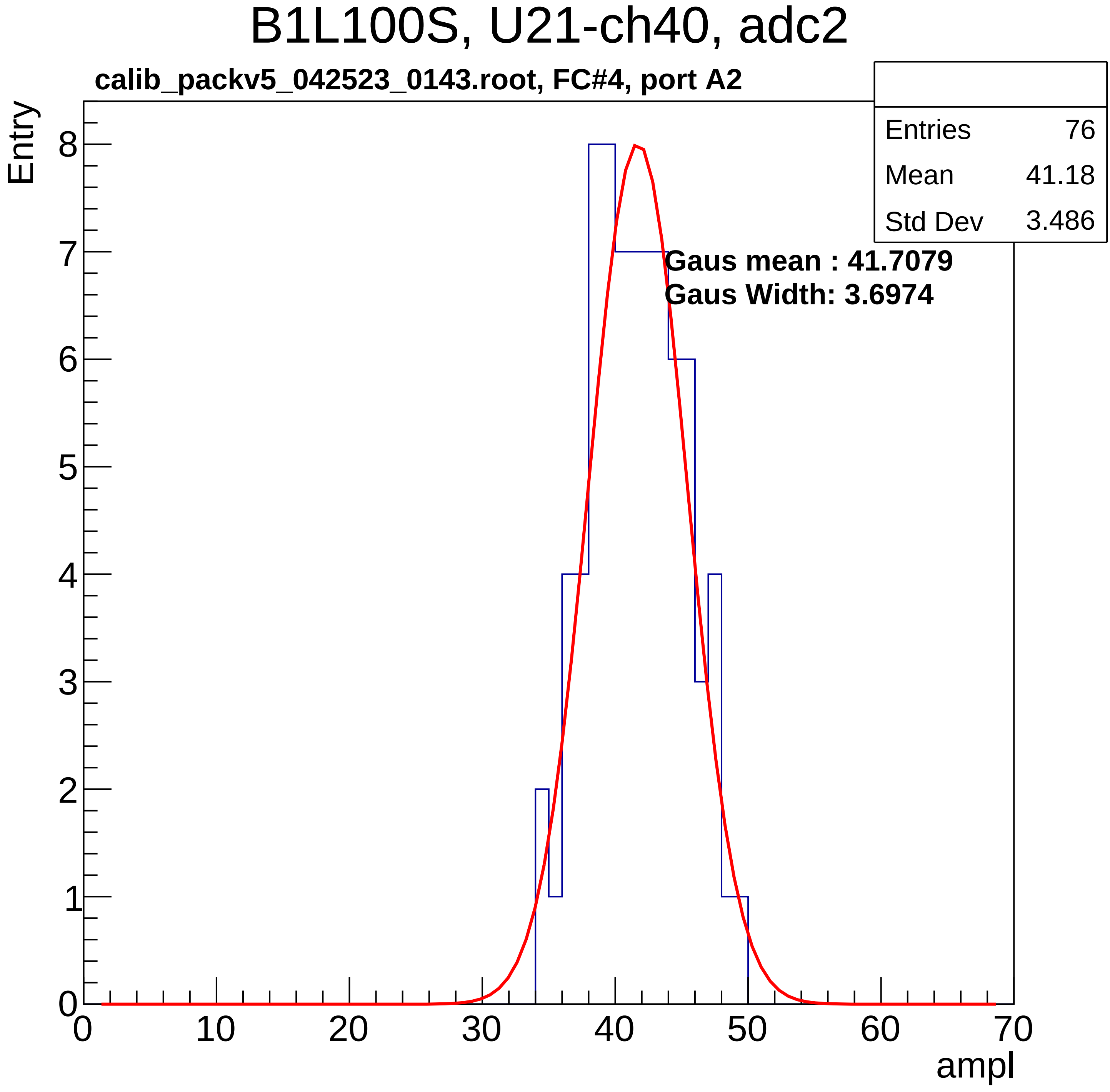

B1L100S, U21-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

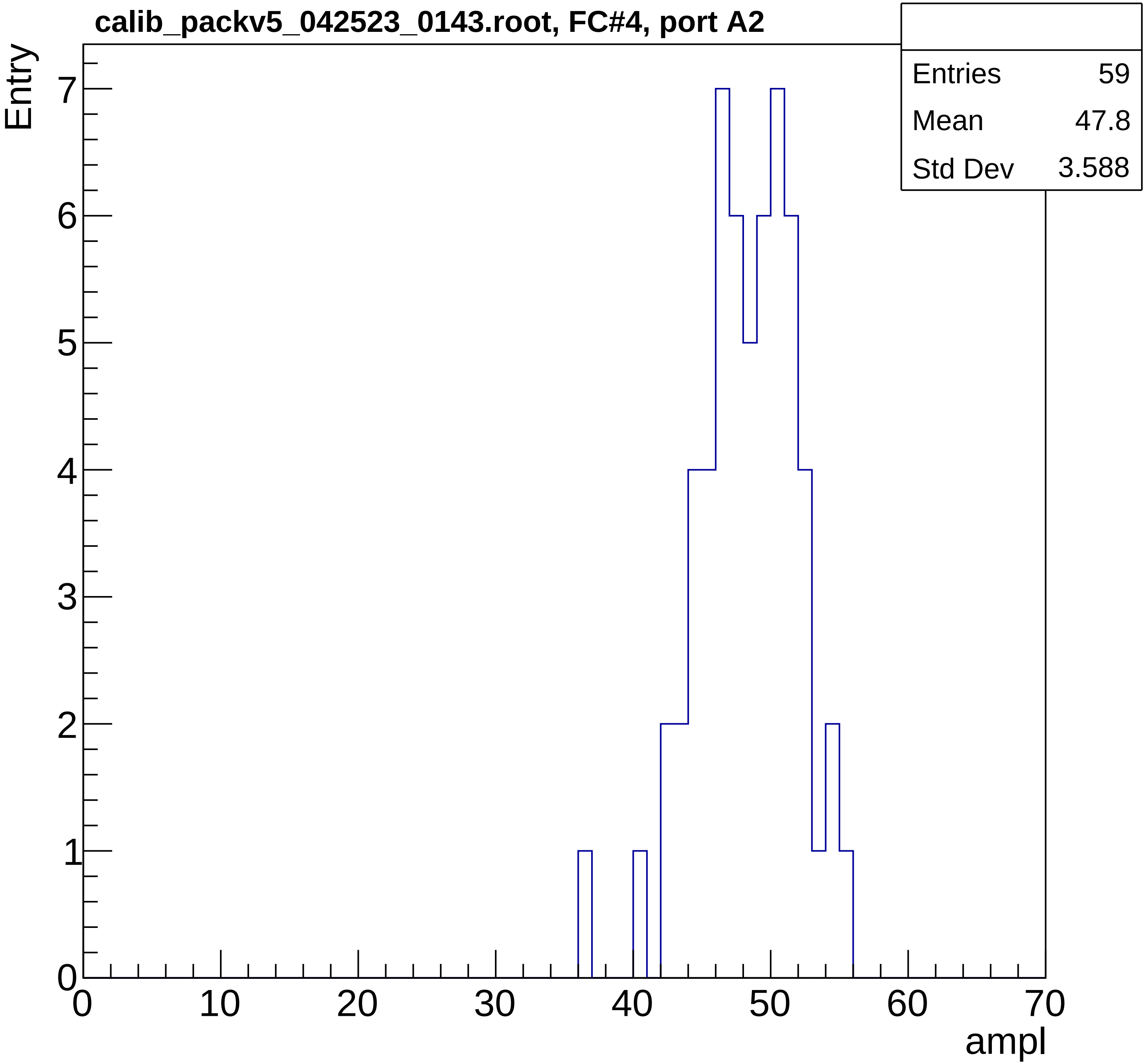

B1L100S, U21-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

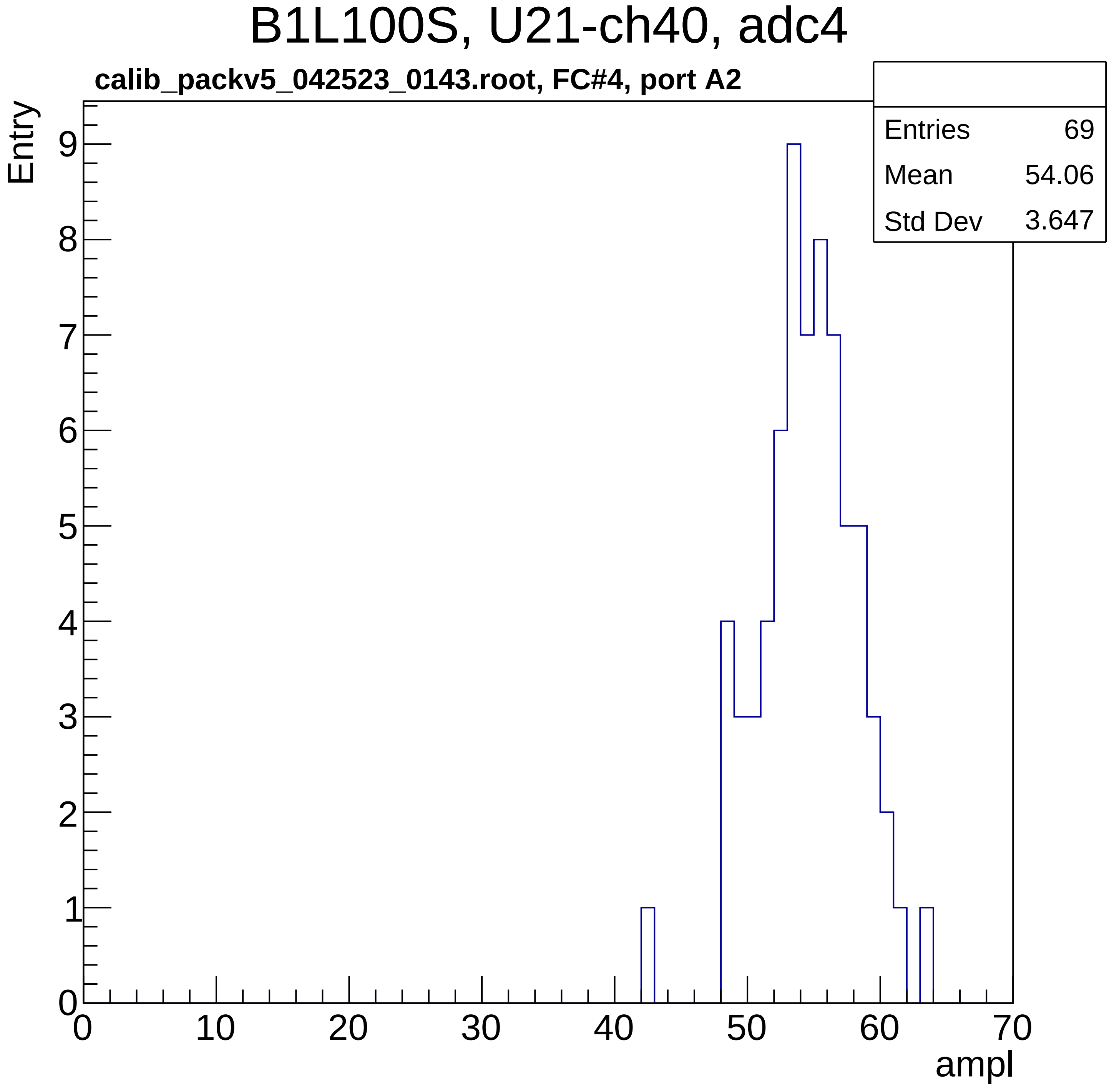

B1L100S, U21-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 28.96
Std Dev 3.451
Gaus mean : 29.5499
Gaus Width: 3.2046
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 35.5
Std Dev 2.958
Gaus mean : 34.6927
Gaus Width: 4.5141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 41.18
Std Dev 3.486
Gaus mean : 41.7079
Gaus Width: 3.6974
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch40, adc3




B1L100S, U21-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 54.06
Std Dev 3.647
Entry
0
1
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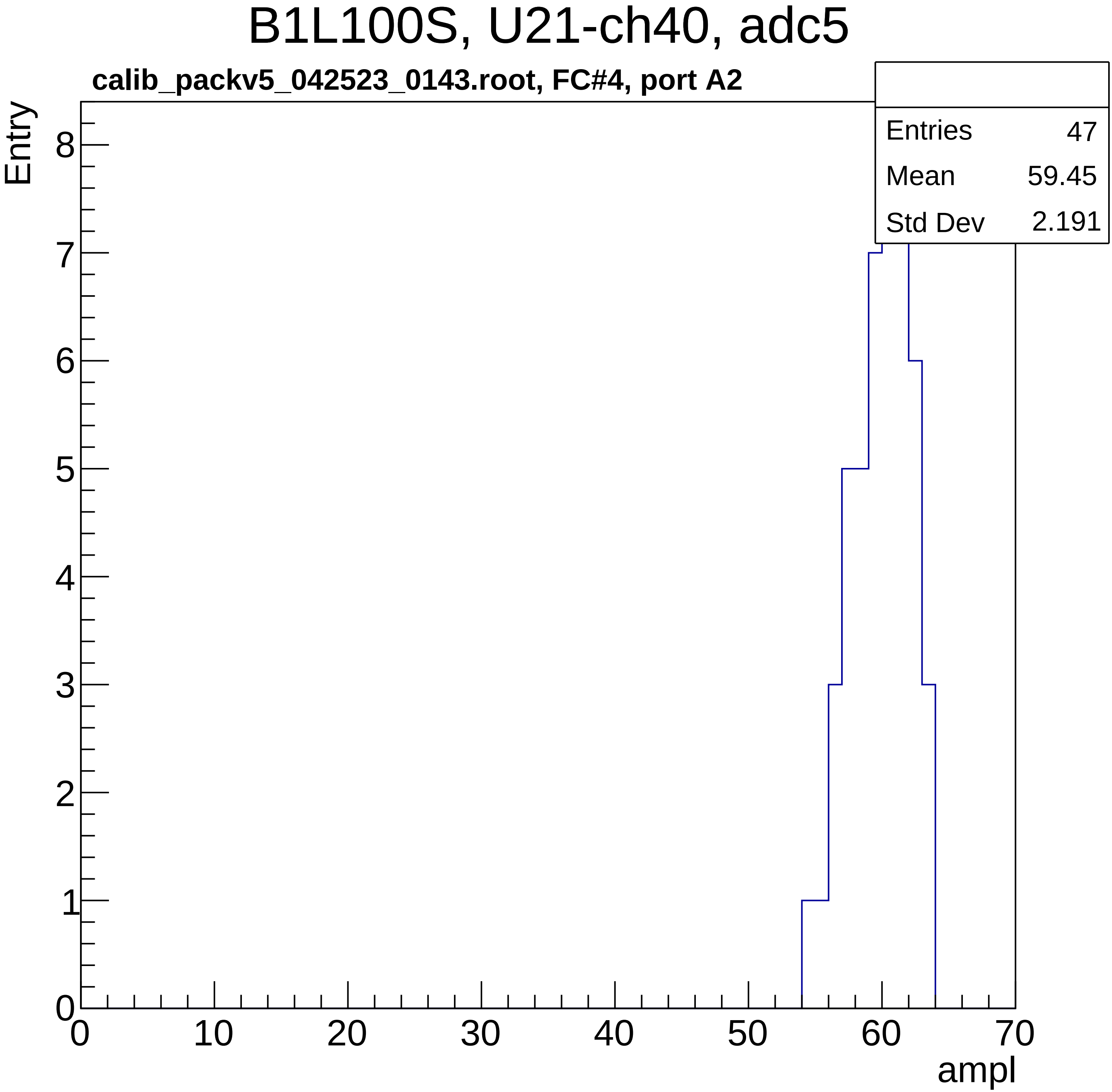
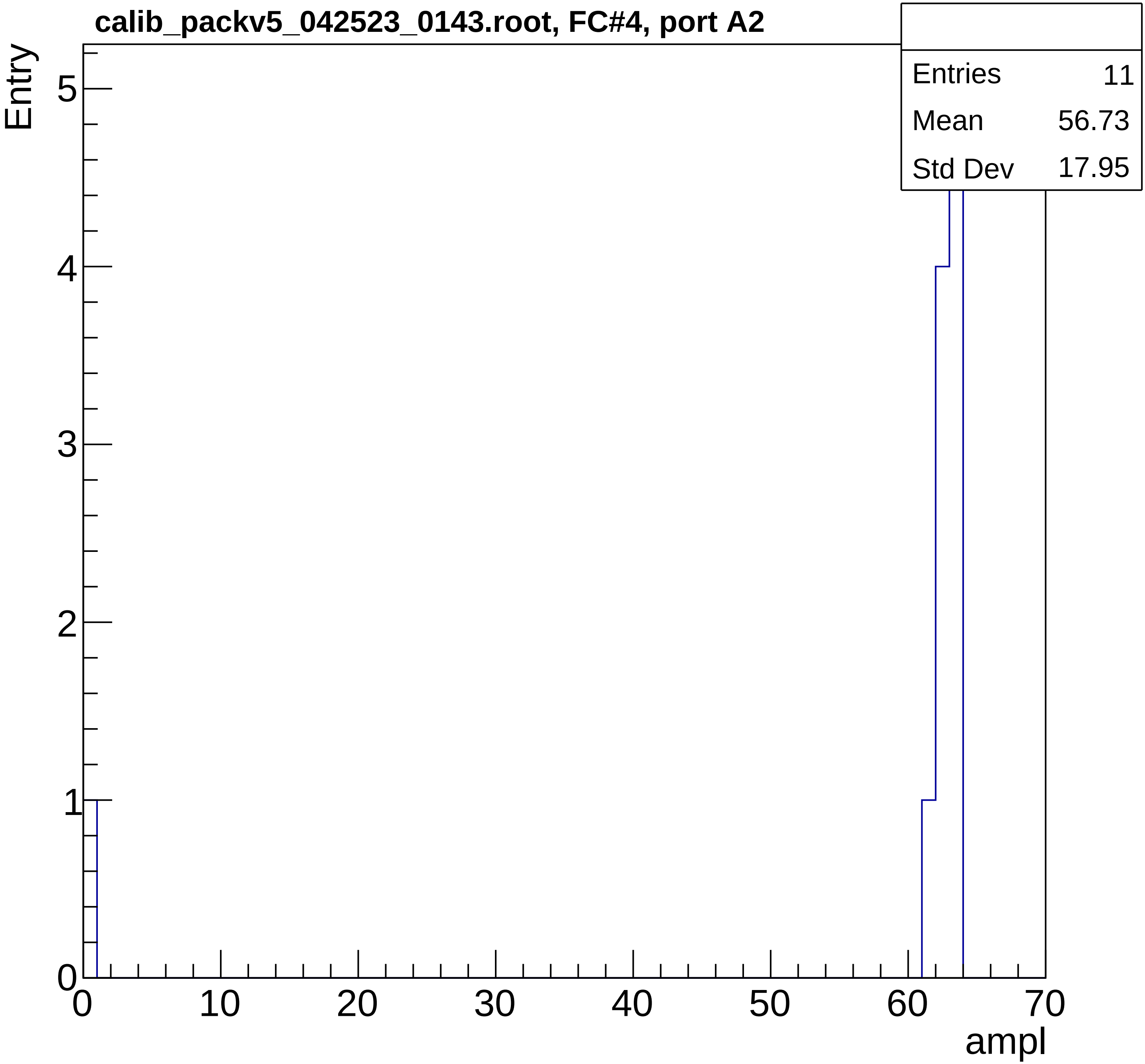
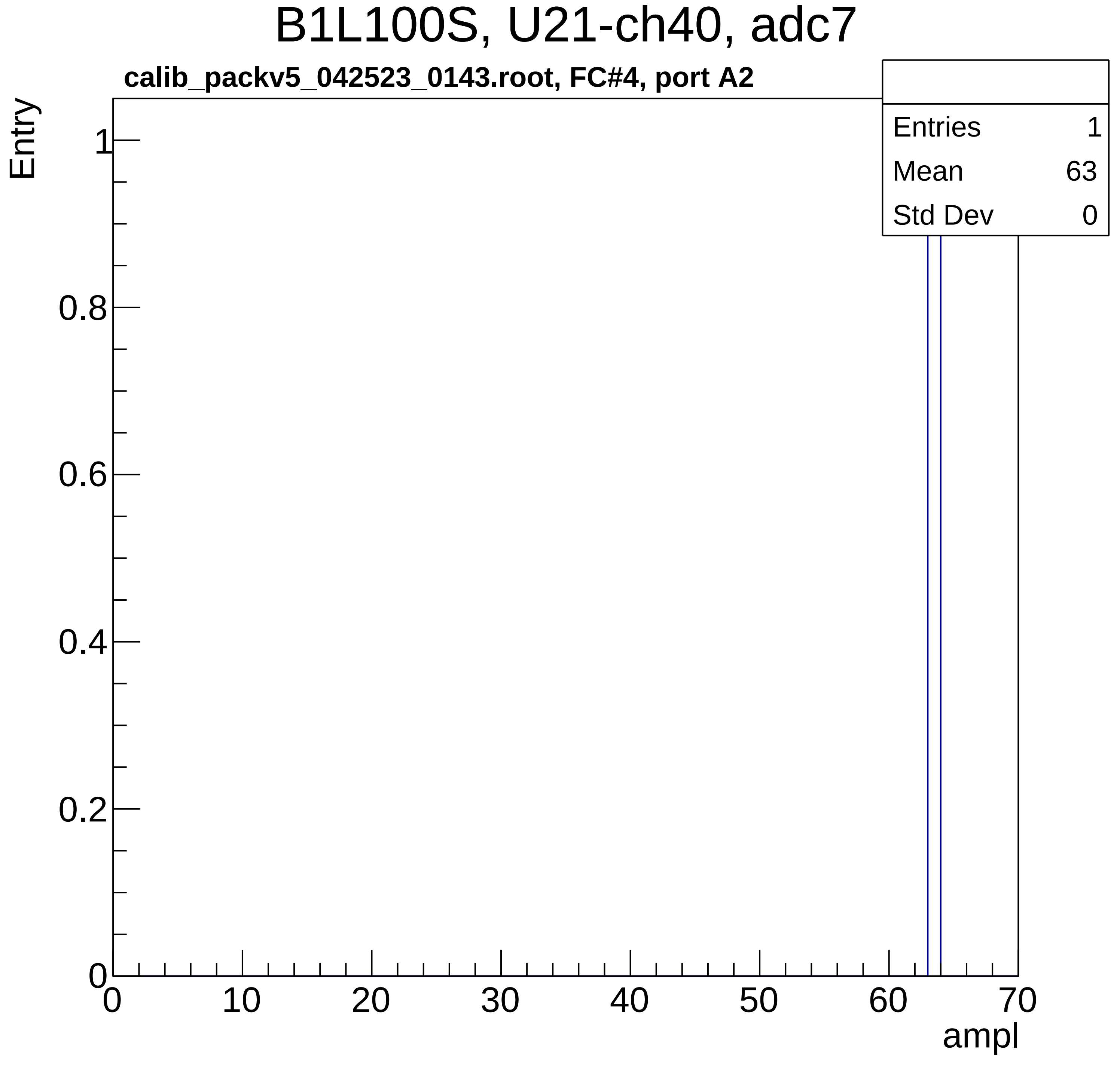
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
Entries 47
Mean 59.45
Std Dev 2.191

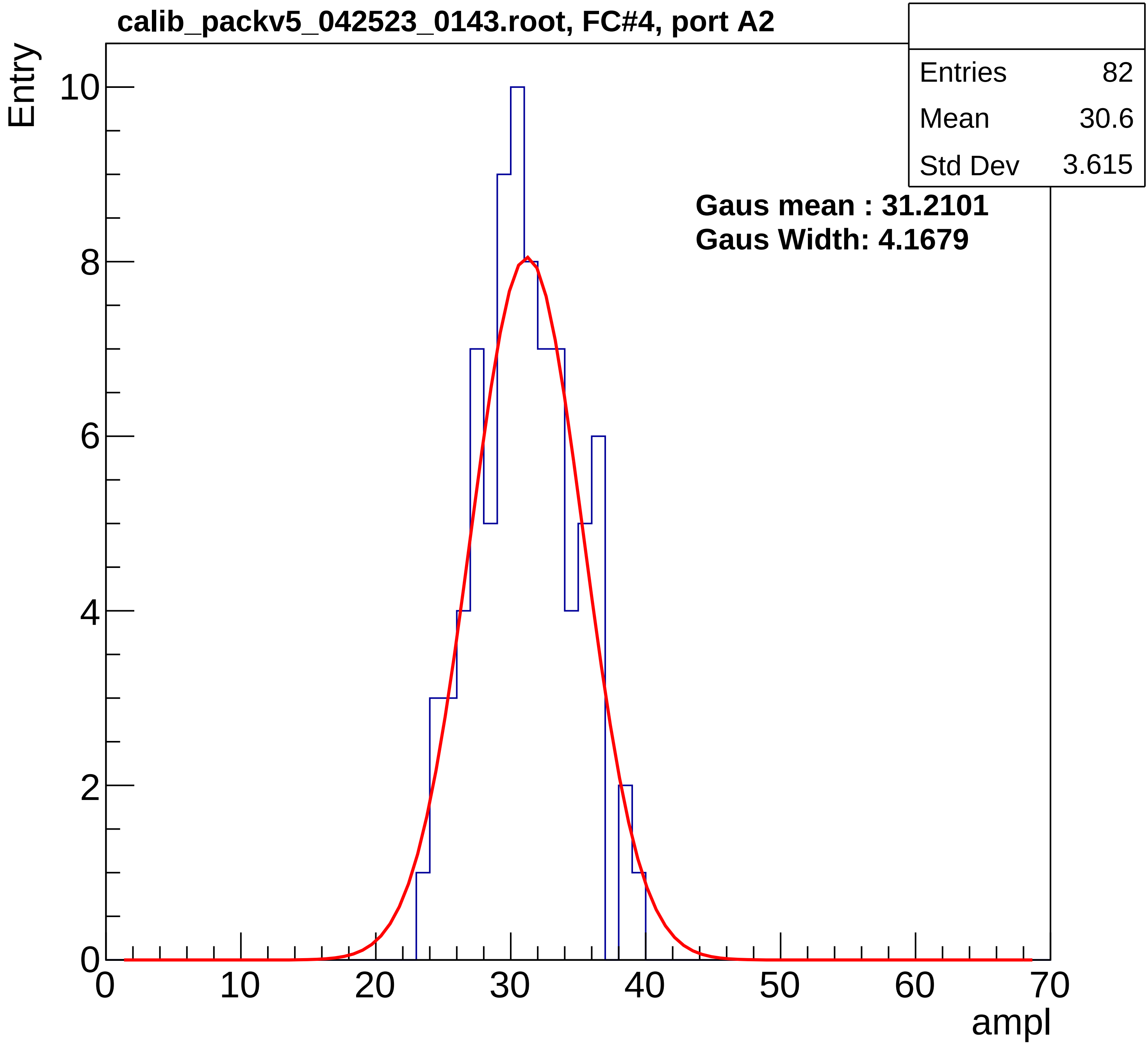
B1L100S, U21-ch40, adc6

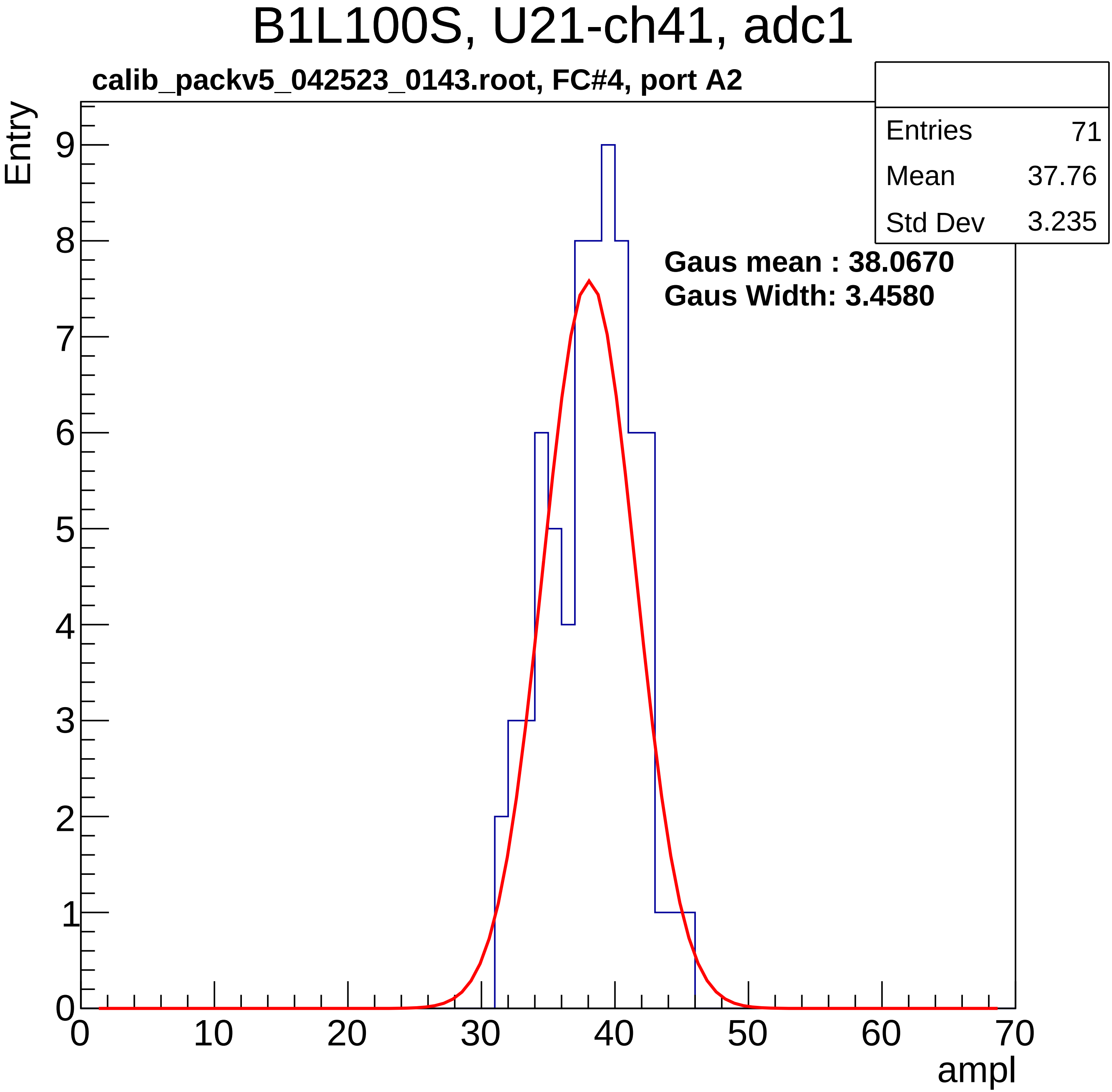
calib_packv5_042523_0143.root, FC#4, port A2

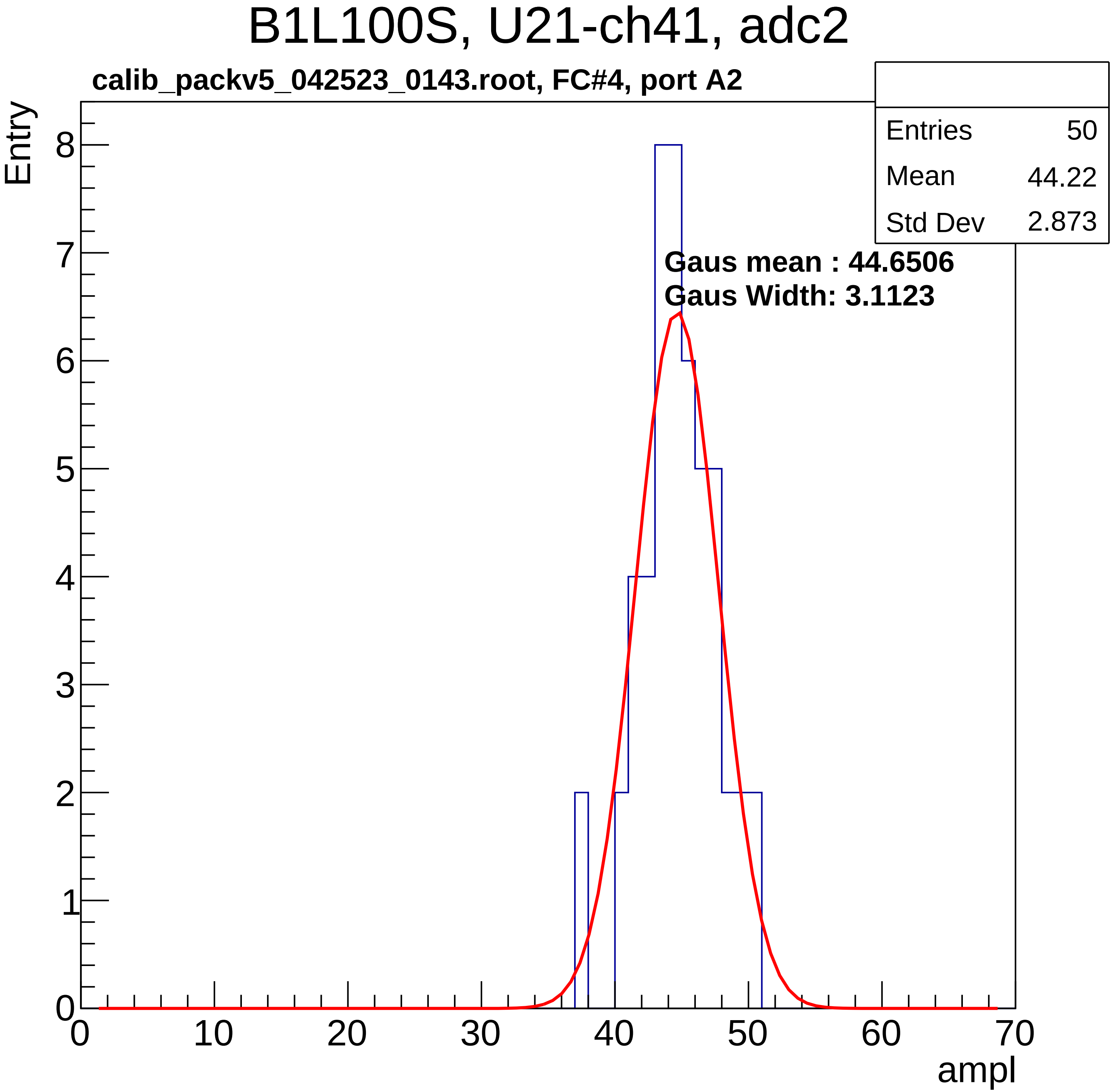


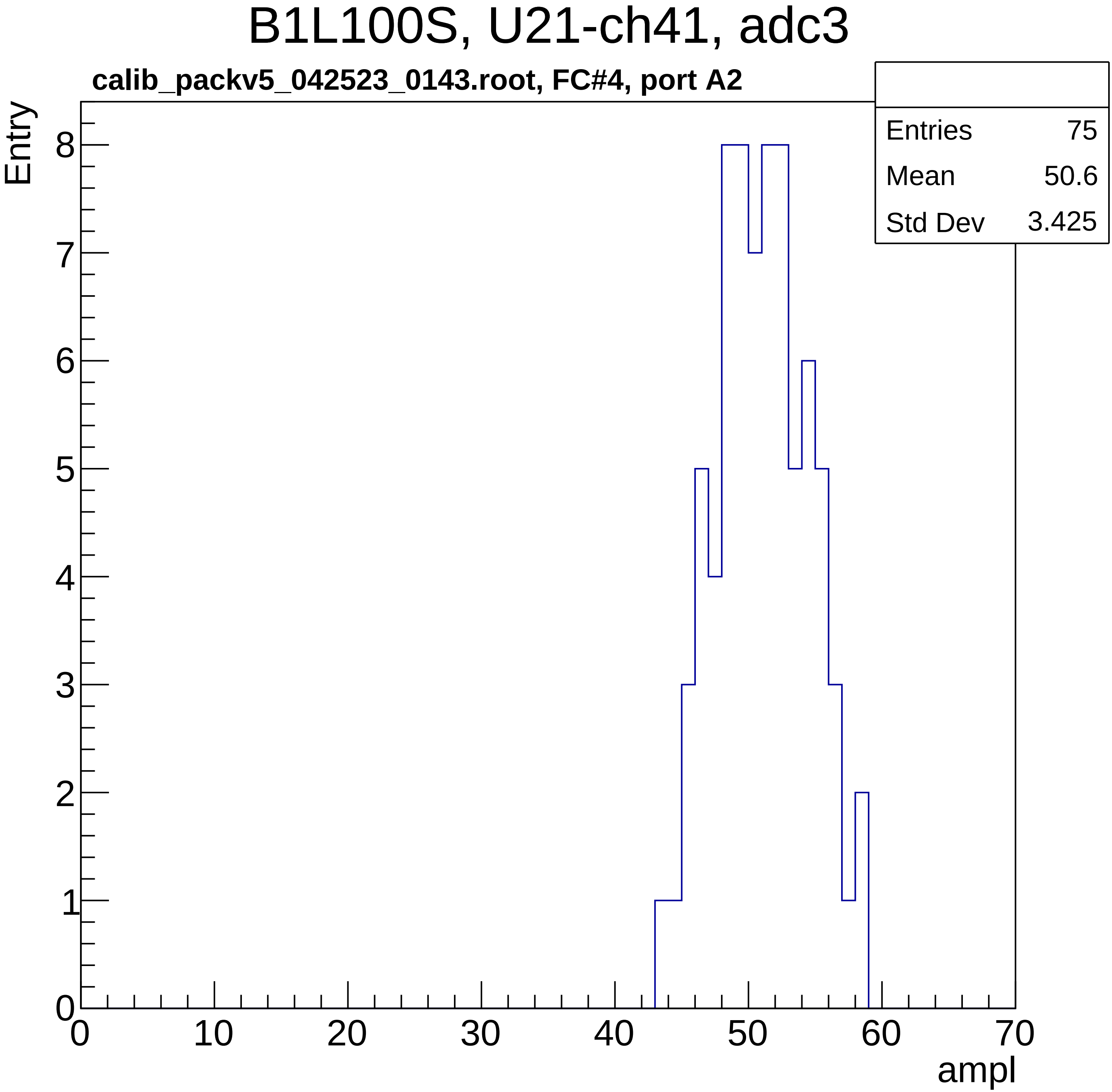

B1L100S, U21-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch41, adc0



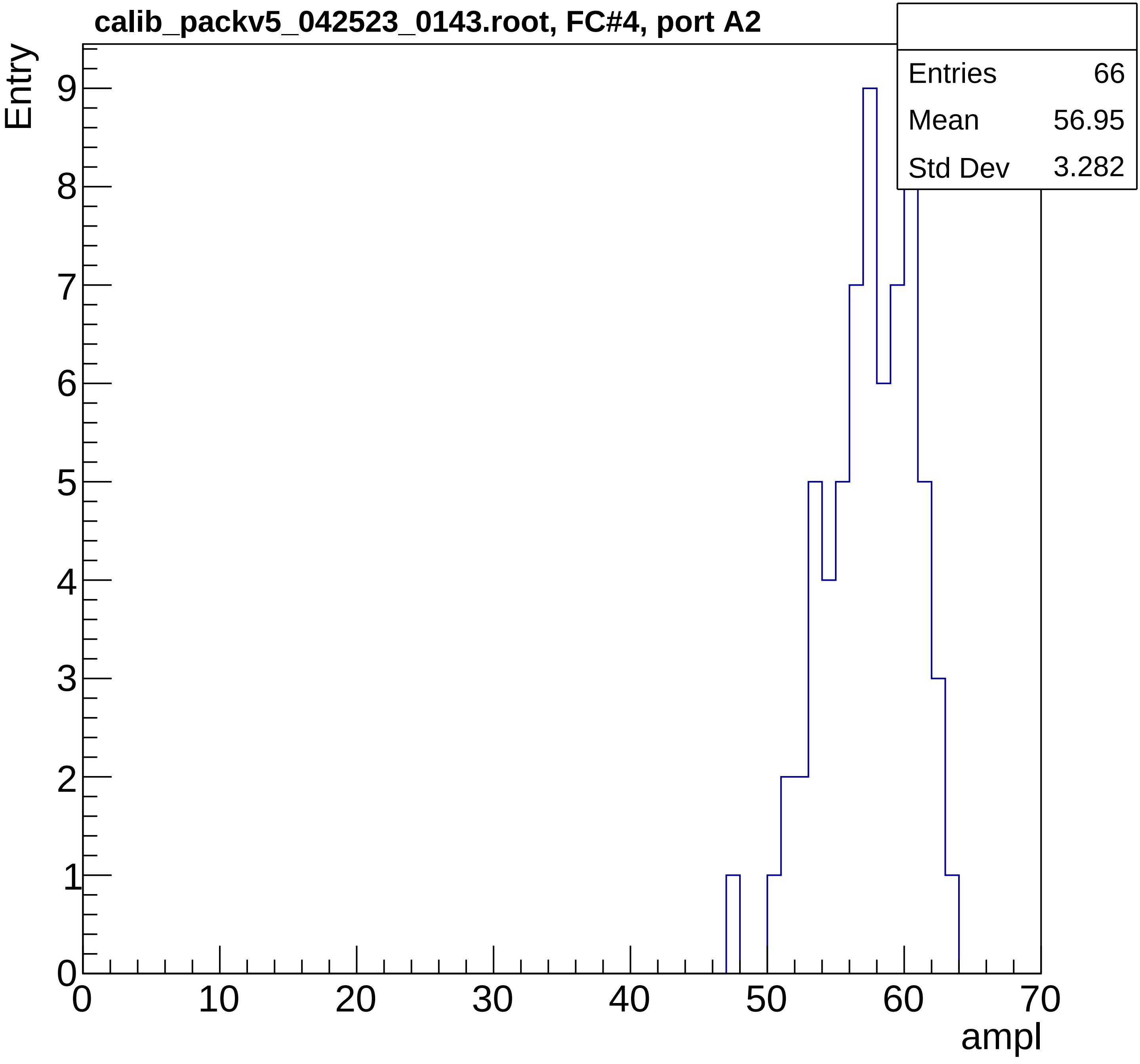

B1L100S, U21-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.76
Std Dev 3.235
Gaus mean : 38.0670
Gaus Width: 3.4580
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

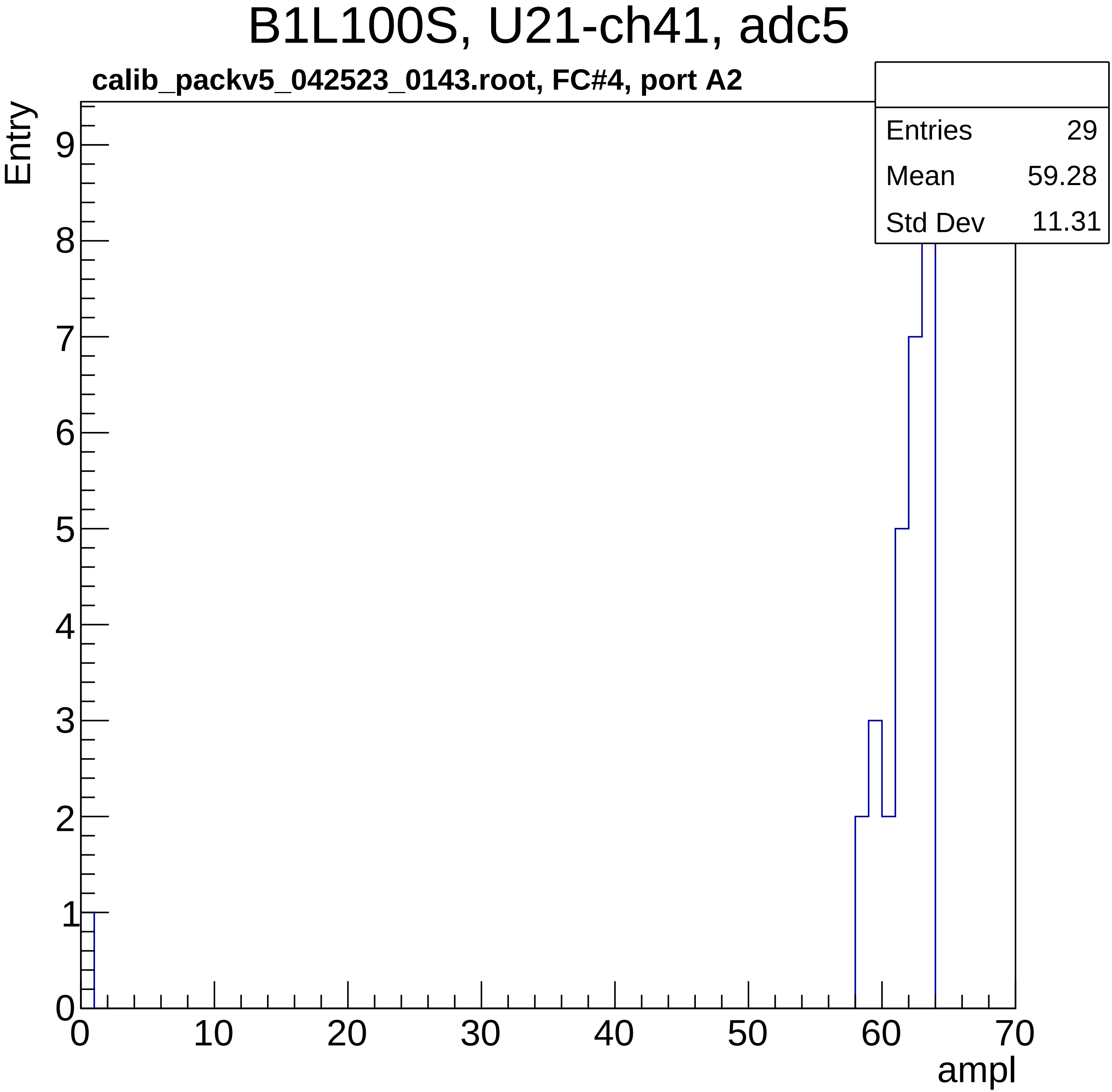

B1L100S, U21-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 50
Mean 44.22
Std Dev 2.873
Gaus mean : 44.6506
Gaus Width: 3.1123
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

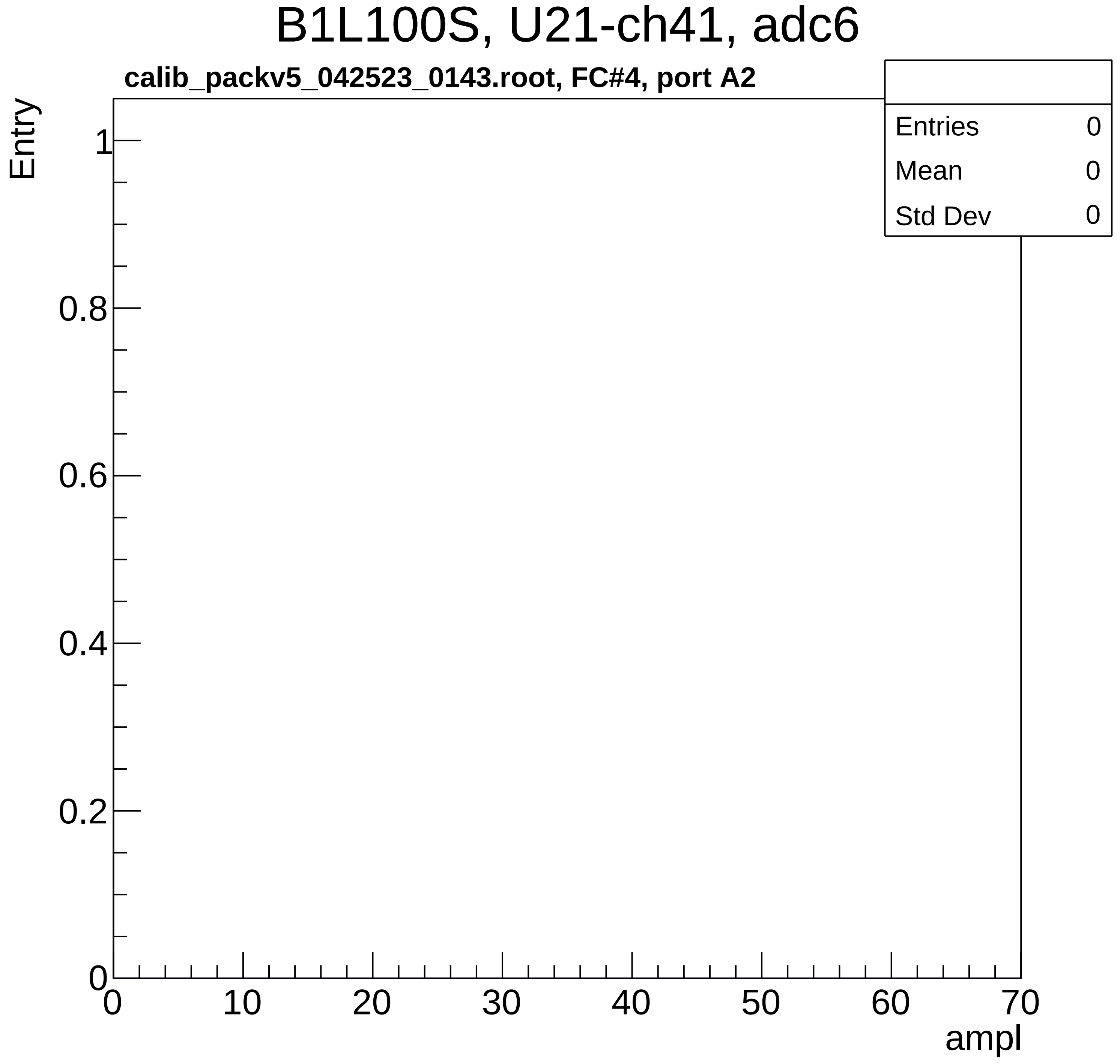

B1L100S, U21-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.6
Std Dev 3.425
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

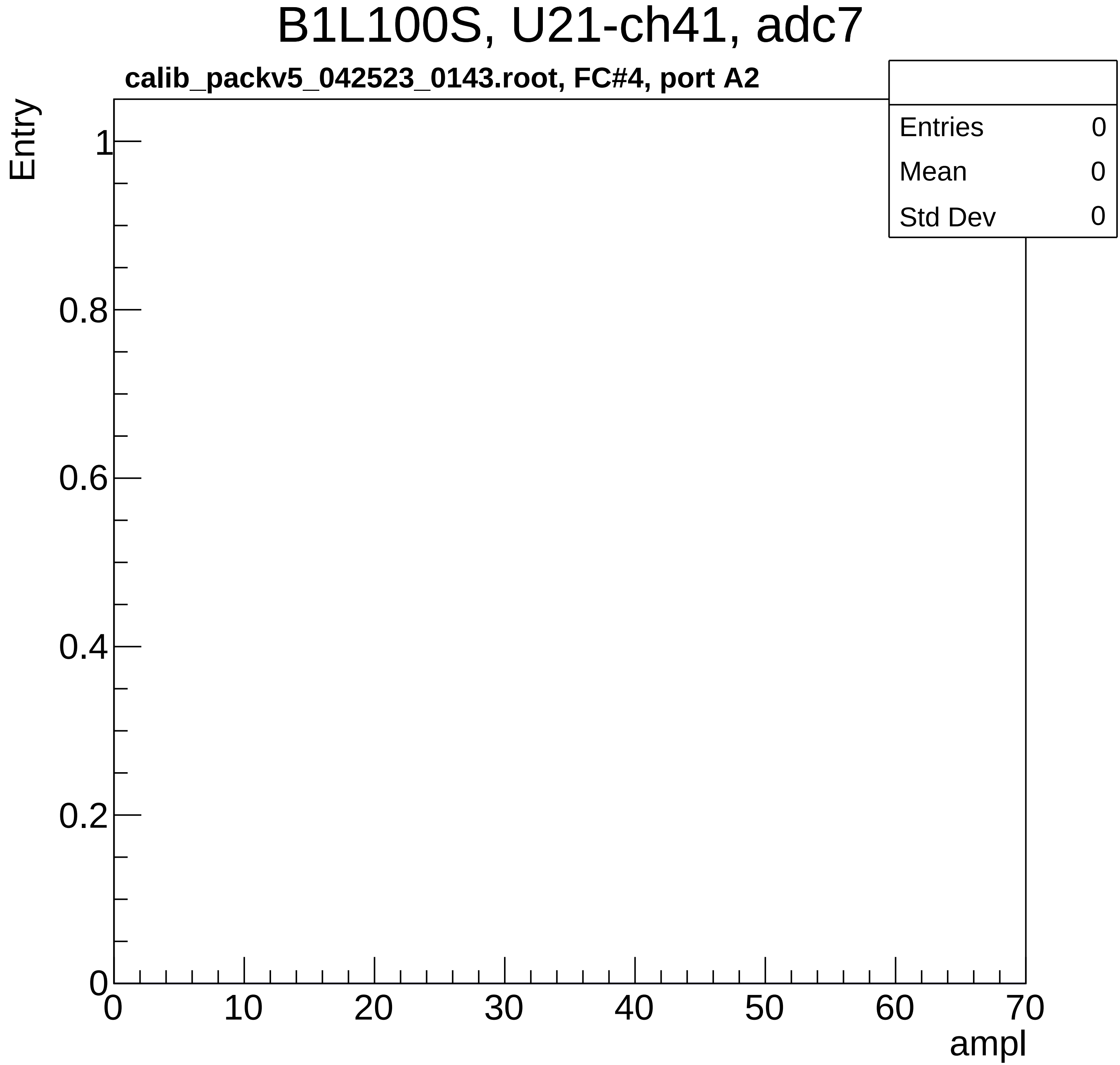
B1L100S, U21-ch41, adc4

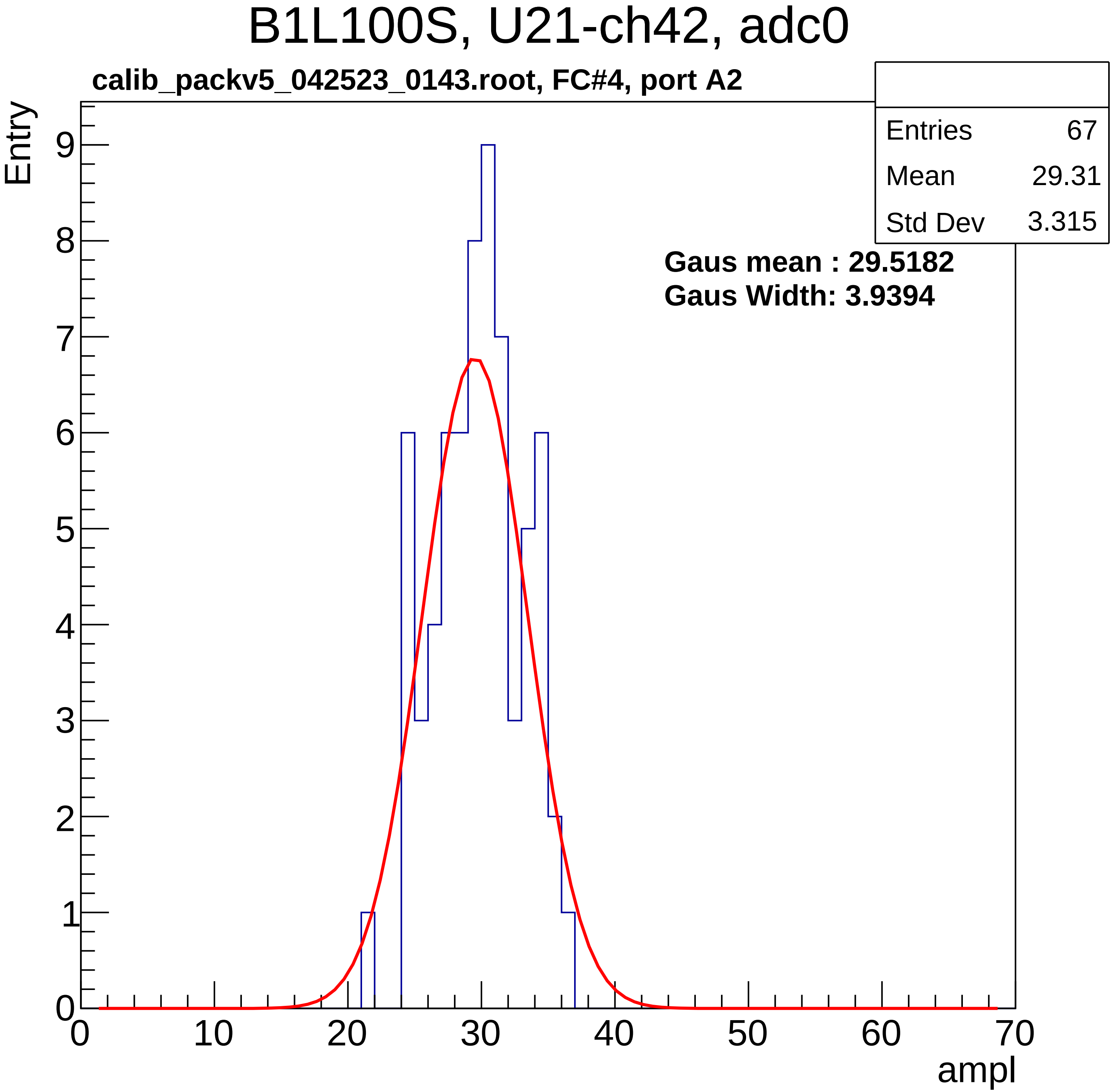
calib_packv5_042523_0143.root, FC#4, port A2




B1L100S, U21-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 59.28
Std Dev 11.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

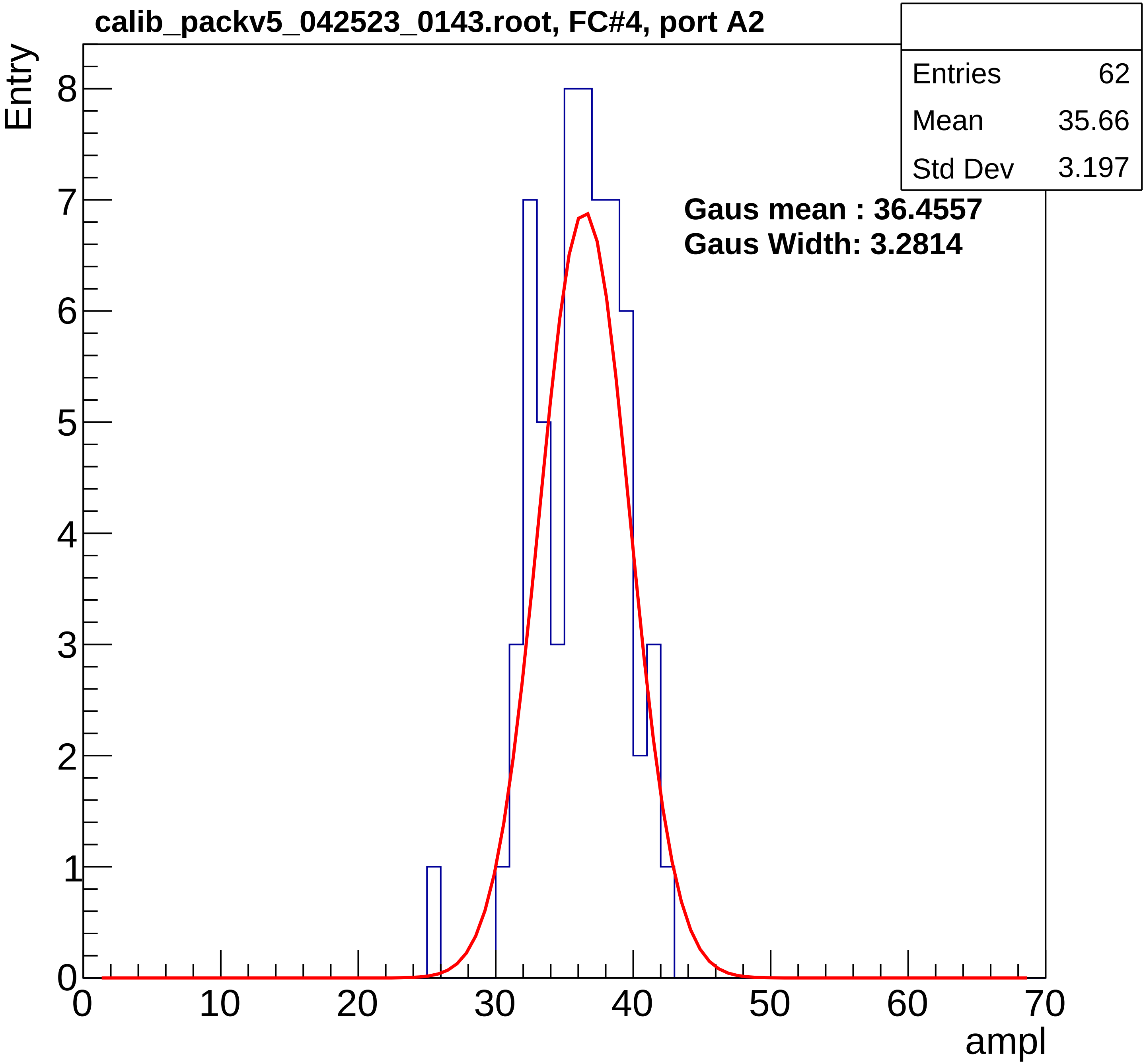

B1L100S, U21-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.31
Std Dev 3.315
Gaus mean : 29.5182
Gaus Width: 3.9394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

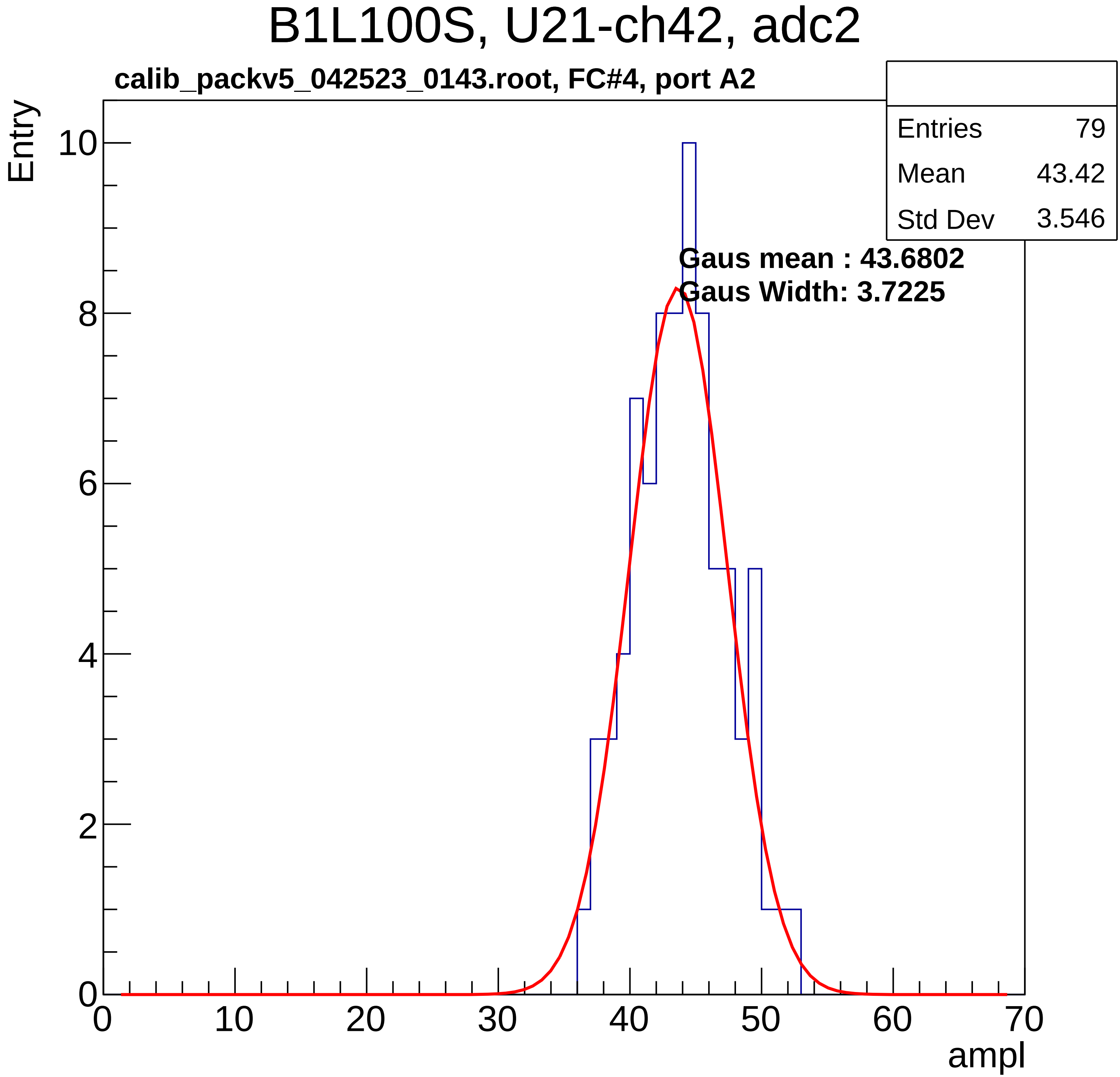
B1L100S, U21-ch42, adc1

calib_packv5_042523_0143.root, FC#4, port A2

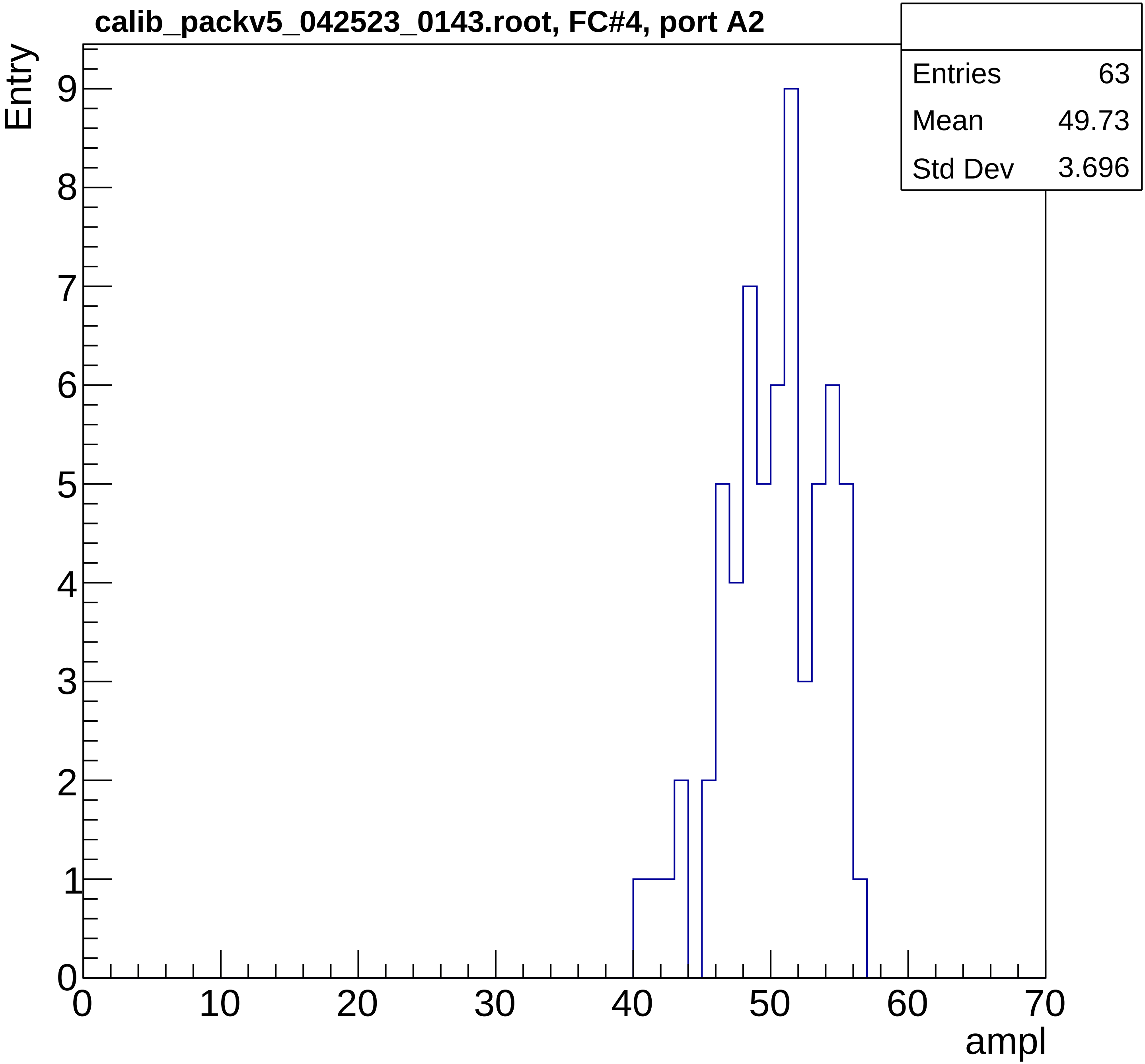


Entries	62
Mean	35.66
Std Dev	3.197

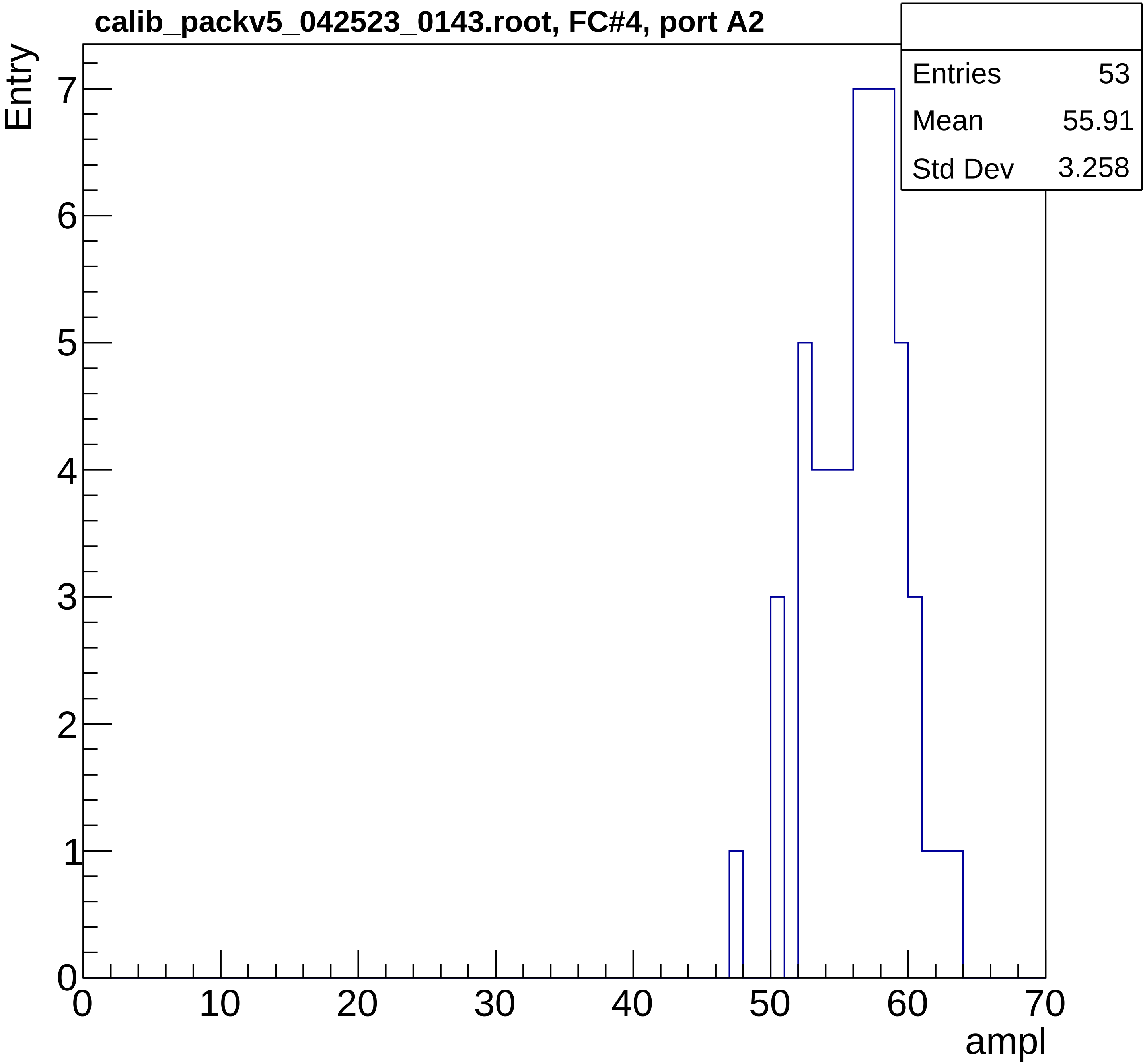
Gaus mean : 36.4557
Gaus Width: 3.2814

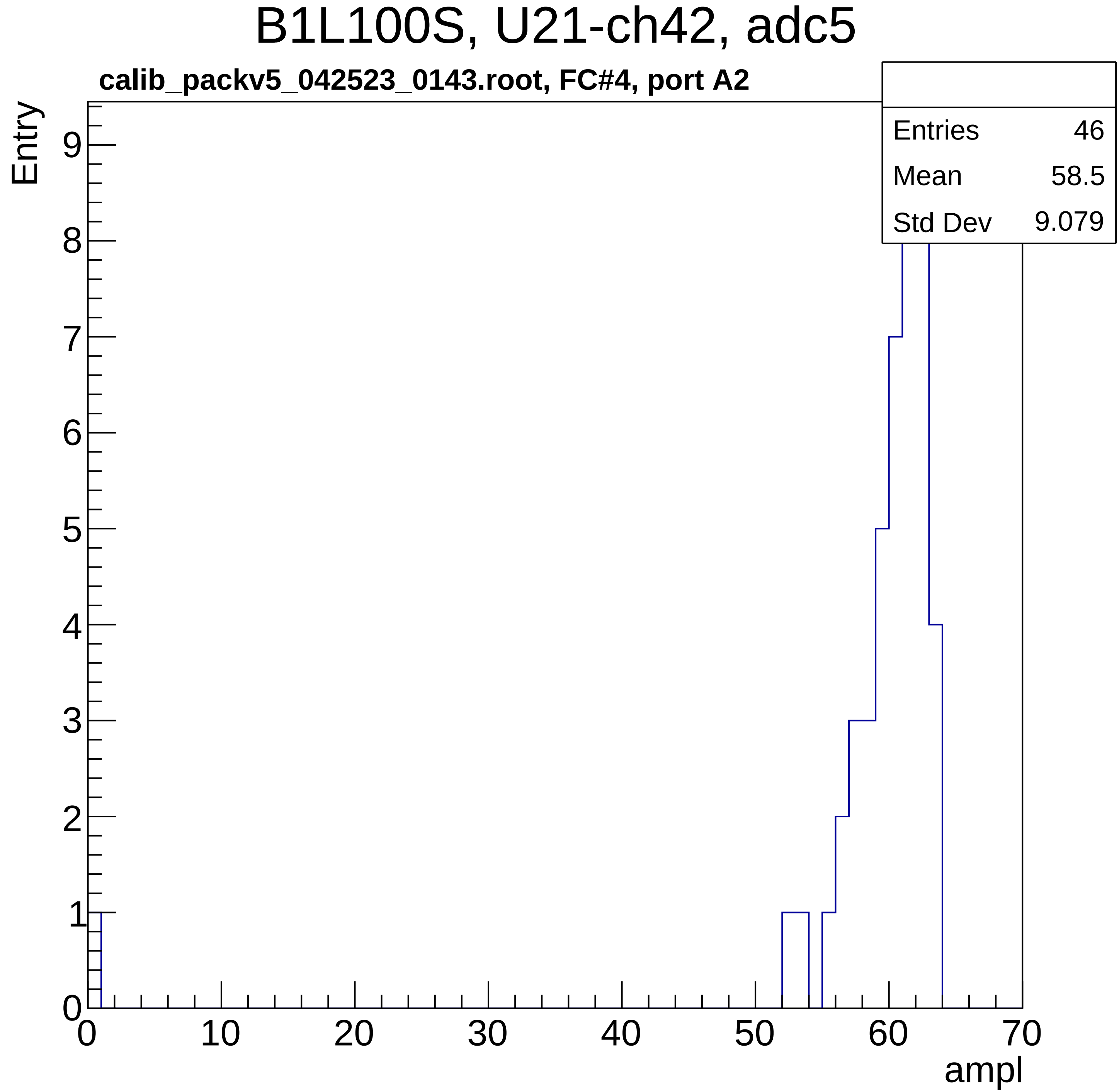

B1L100S, U21-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 43.42
Std Dev 3.546
Gaus mean : 43.6802
Gaus Width: 3.7225
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

B1L100S, U21-ch42, adc3

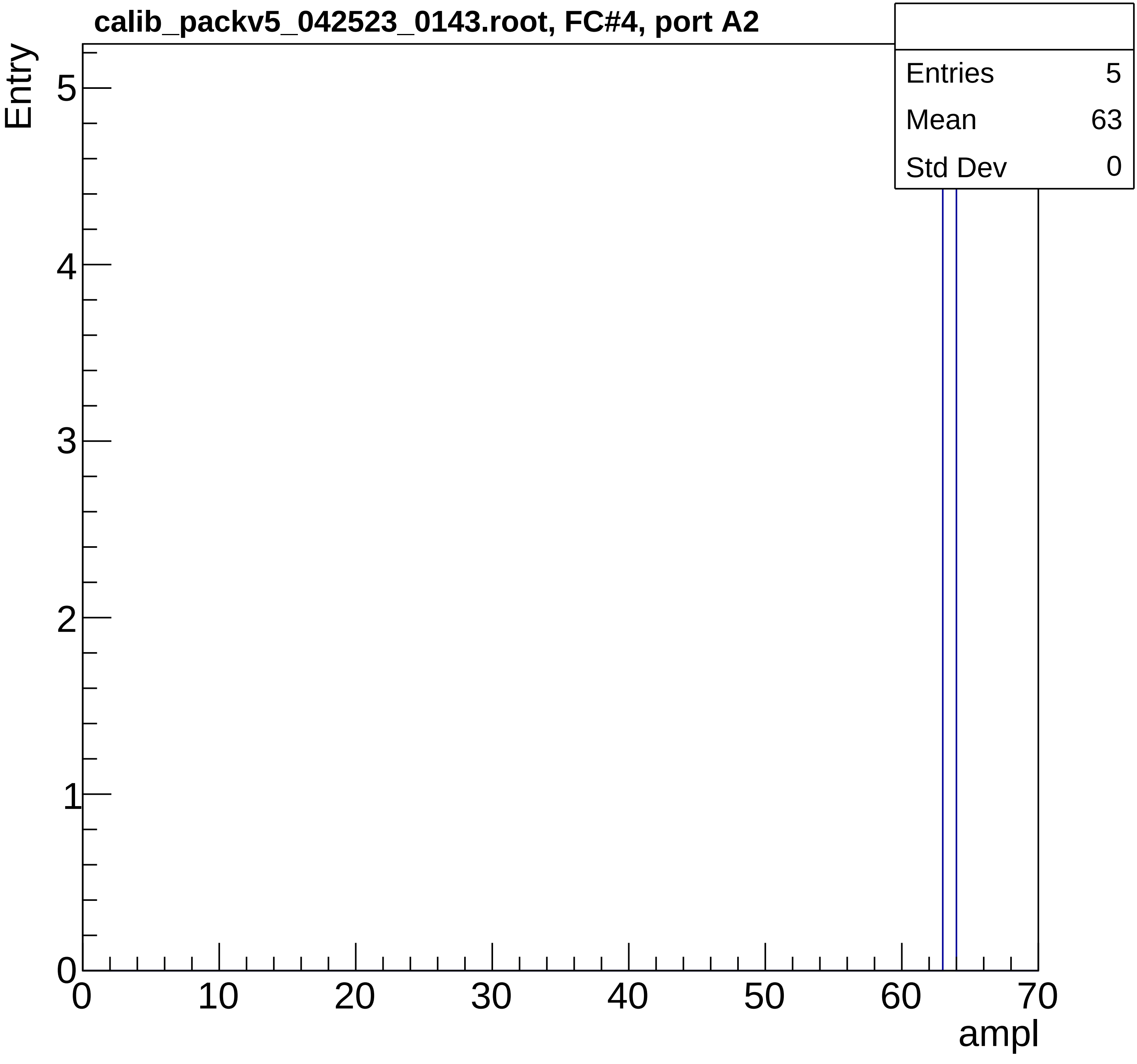


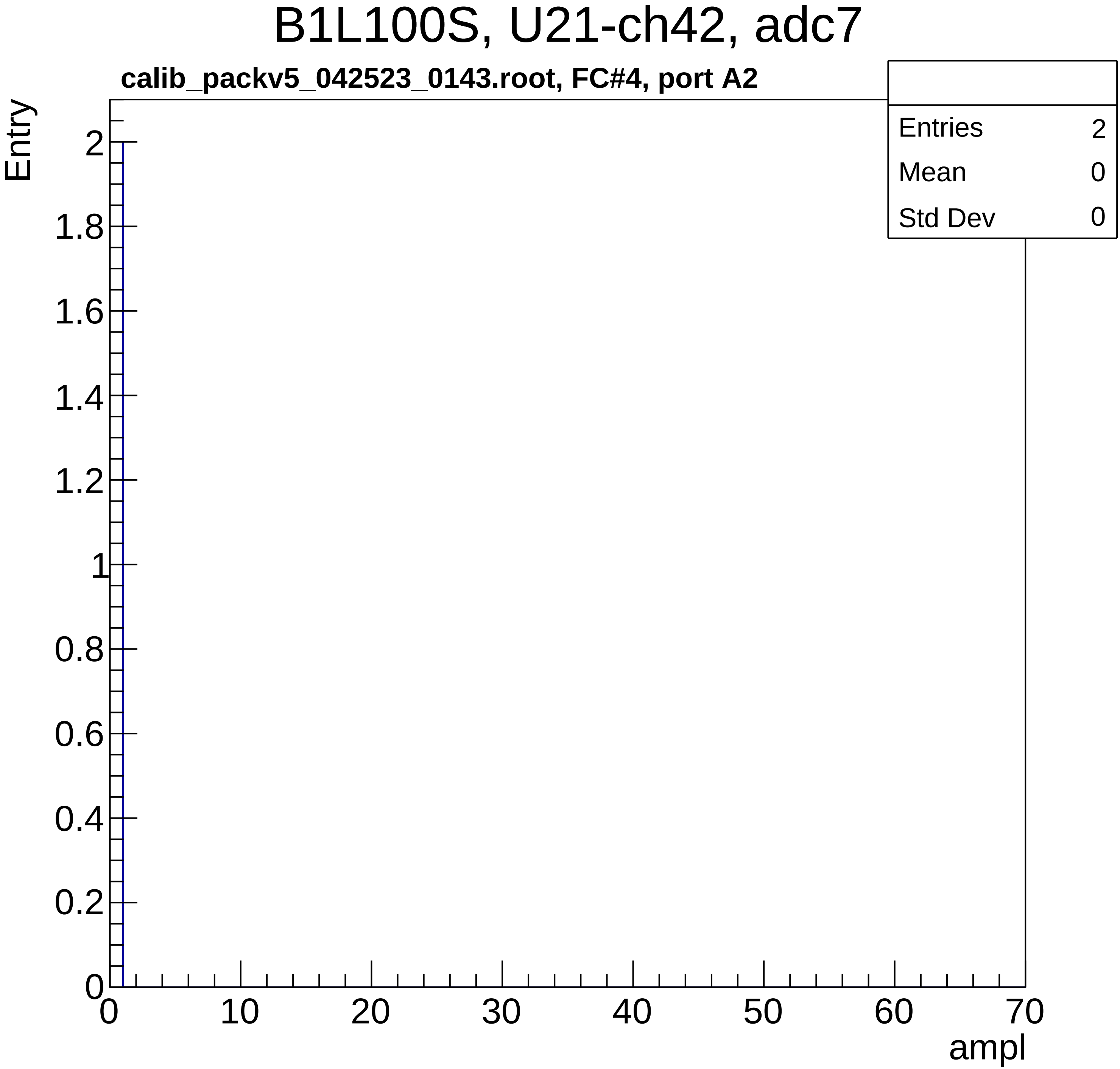
B1L100S, U21-ch42, adc4



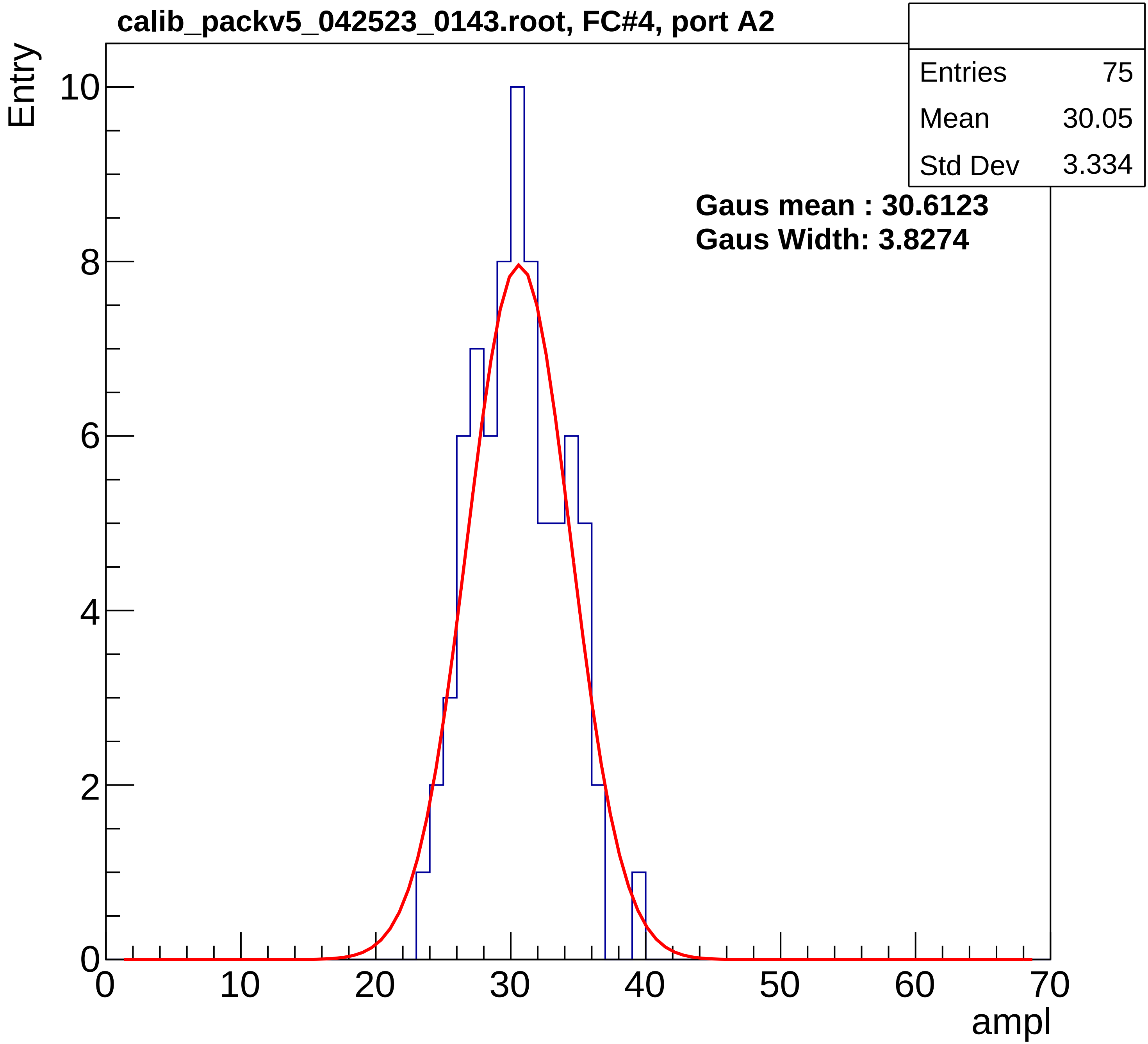

B1L100S, U21-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 46
Mean 58.5
Std Dev 9.079
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

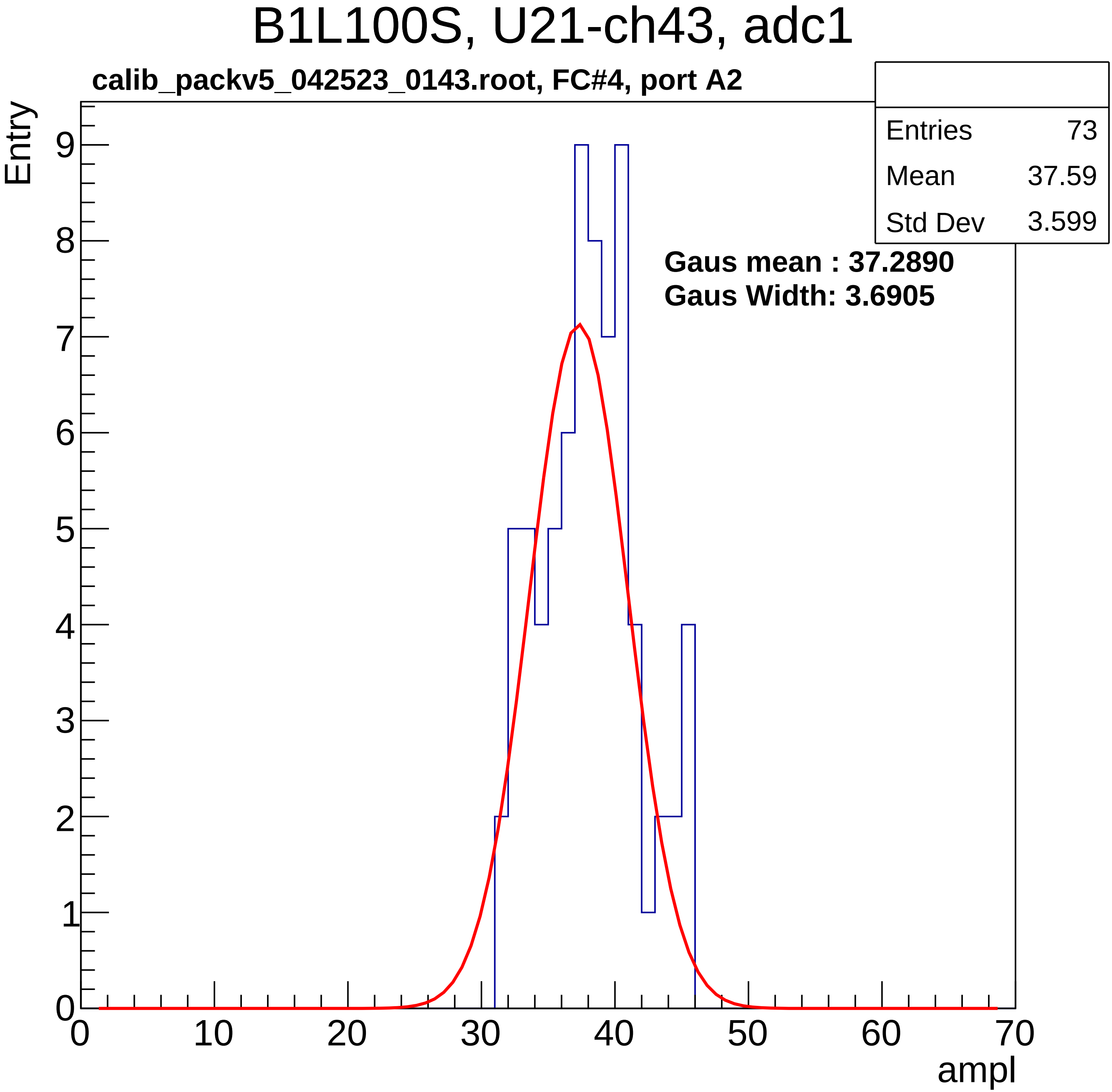
B1L100S, U21-ch42, adc6

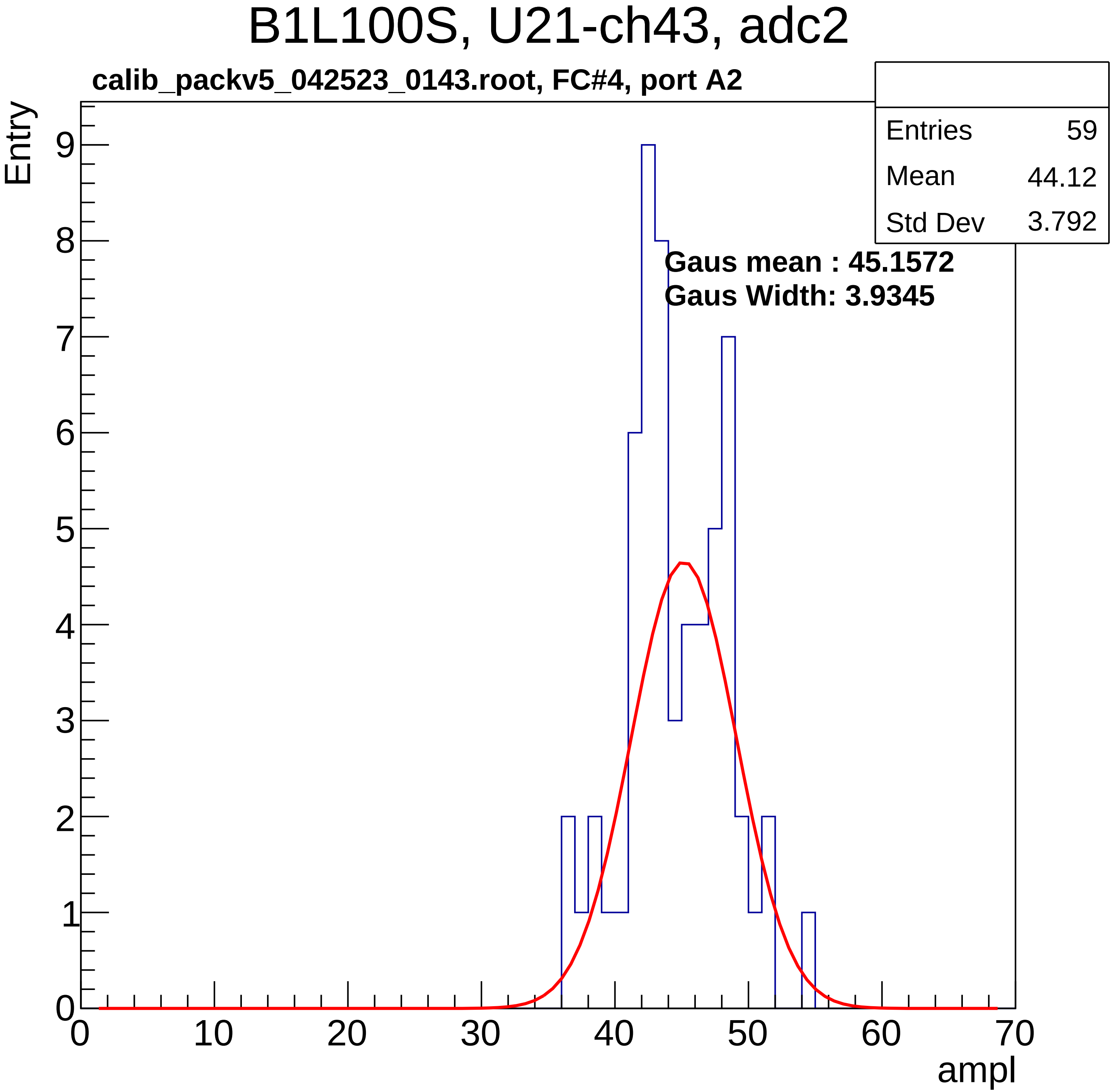


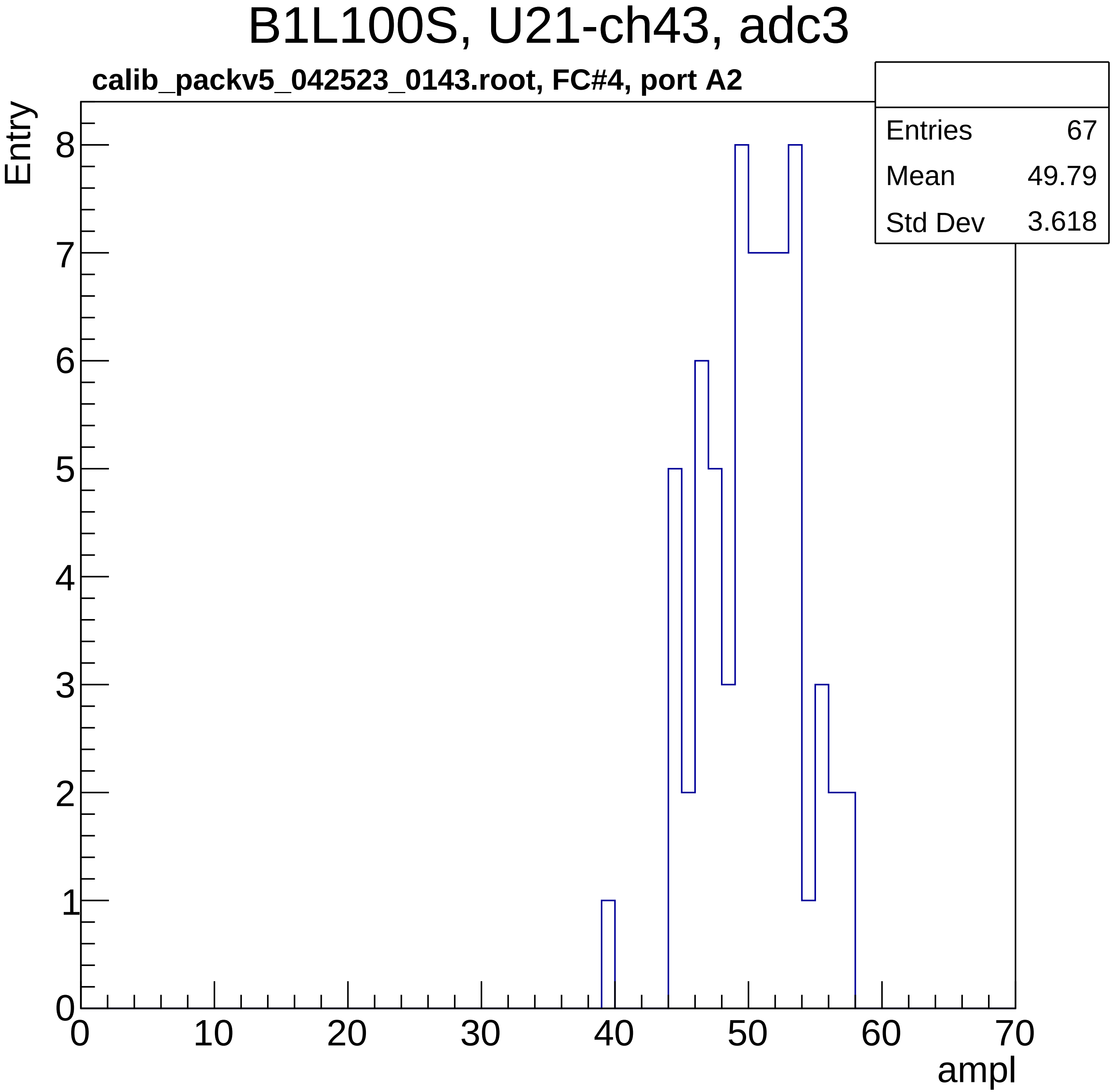

B1L100S, U21-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

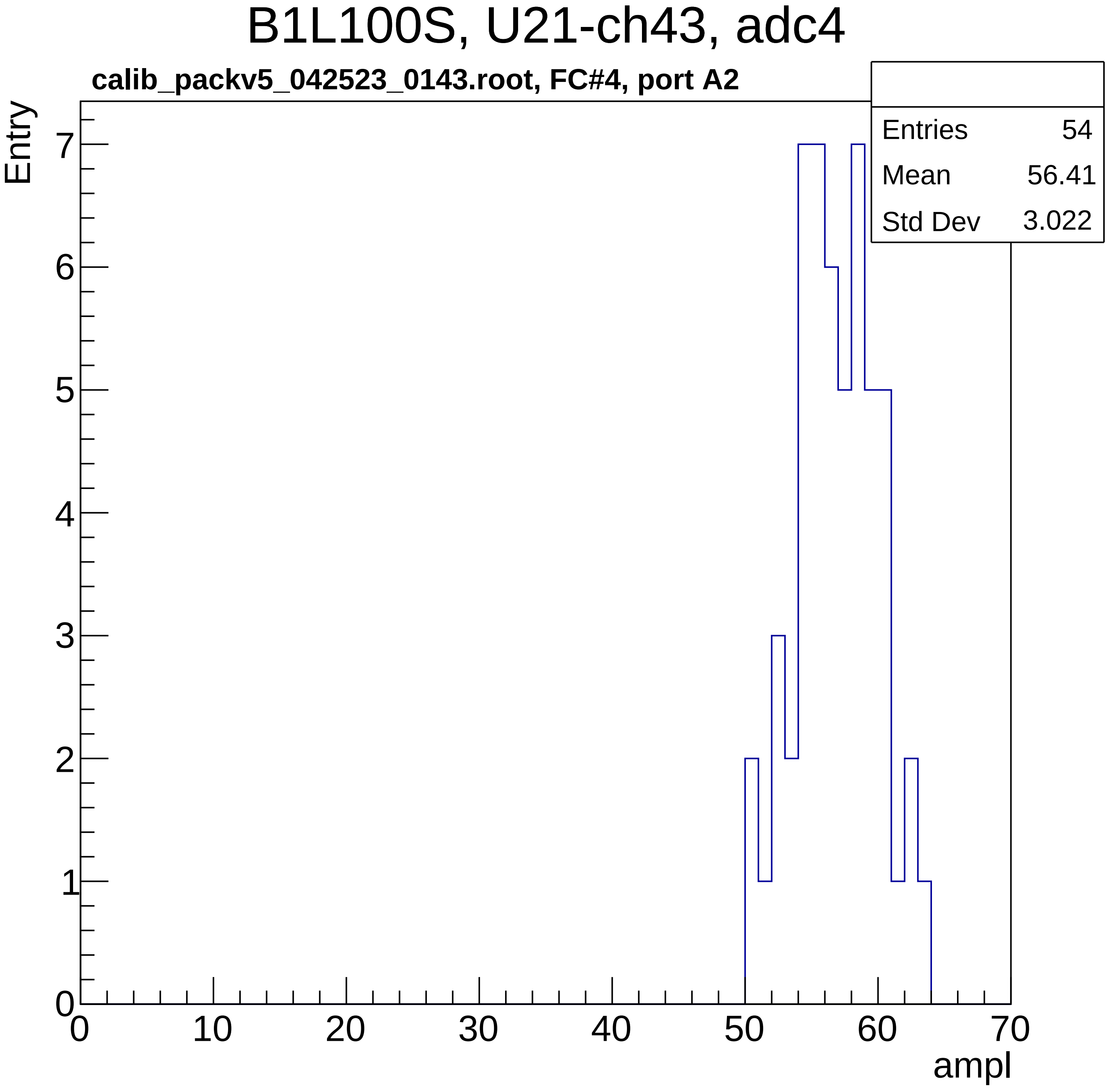
B1L100S, U21-ch43, adc0




B1L100S, U21-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.59
Std Dev 3.599
Gaus mean : 37.2890
Gaus Width: 3.6905
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

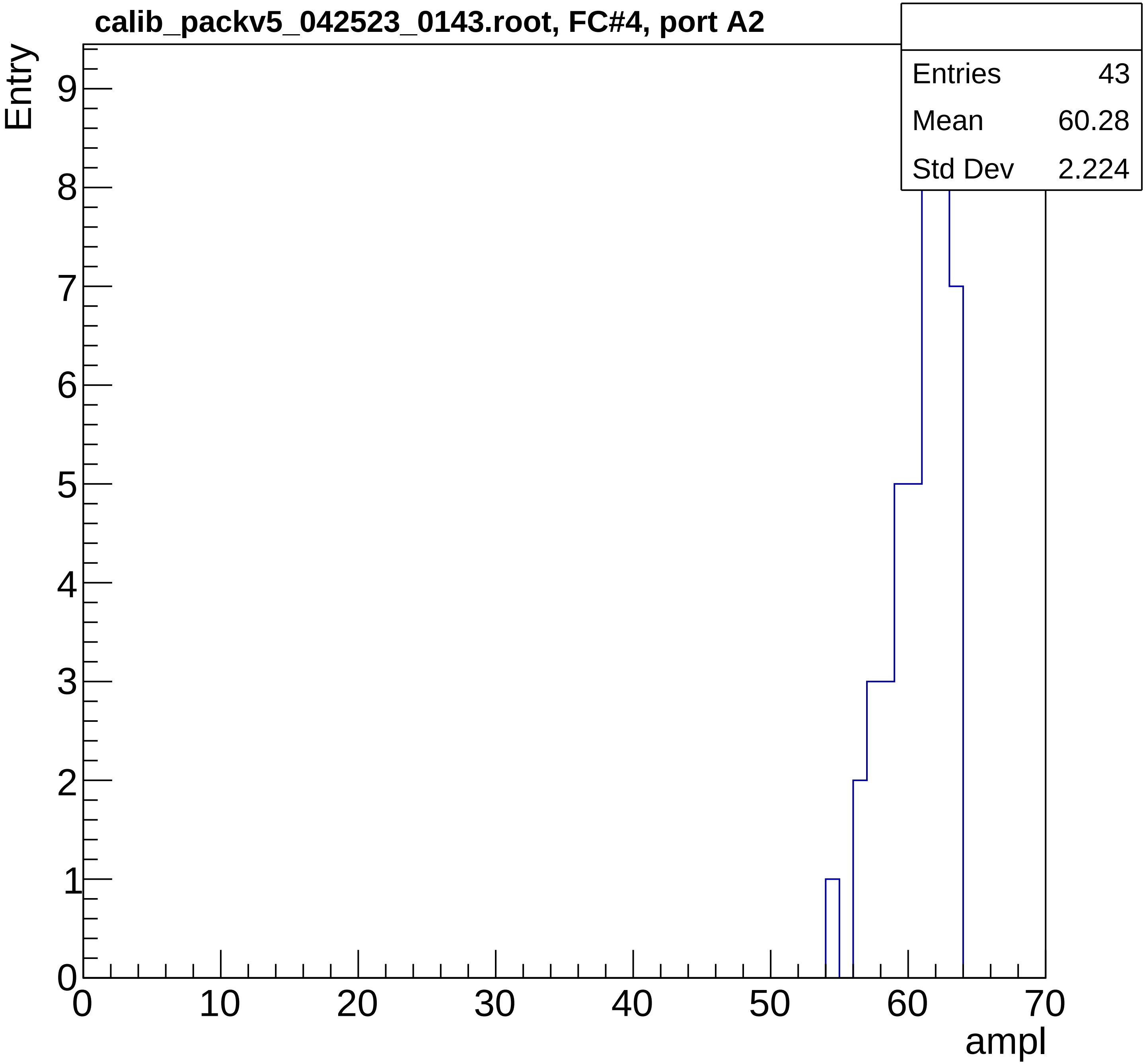

B1L100S, U21-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44.12
Std Dev 3.792
Gaus mean : 45.1572
Gaus Width: 3.9345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 67
Mean 49.79
Std Dev 3.618
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

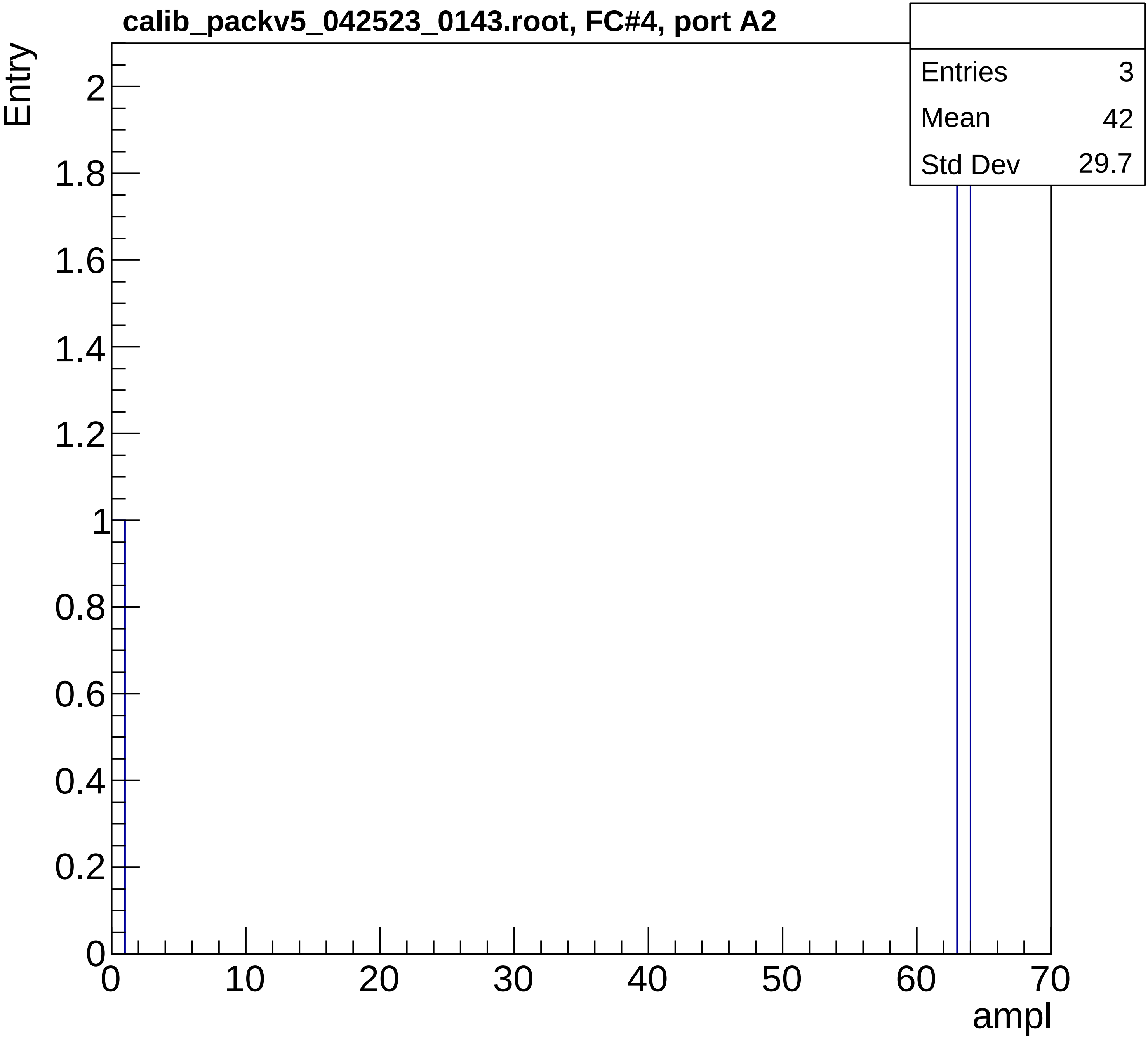

B1L100S, U21-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.41
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

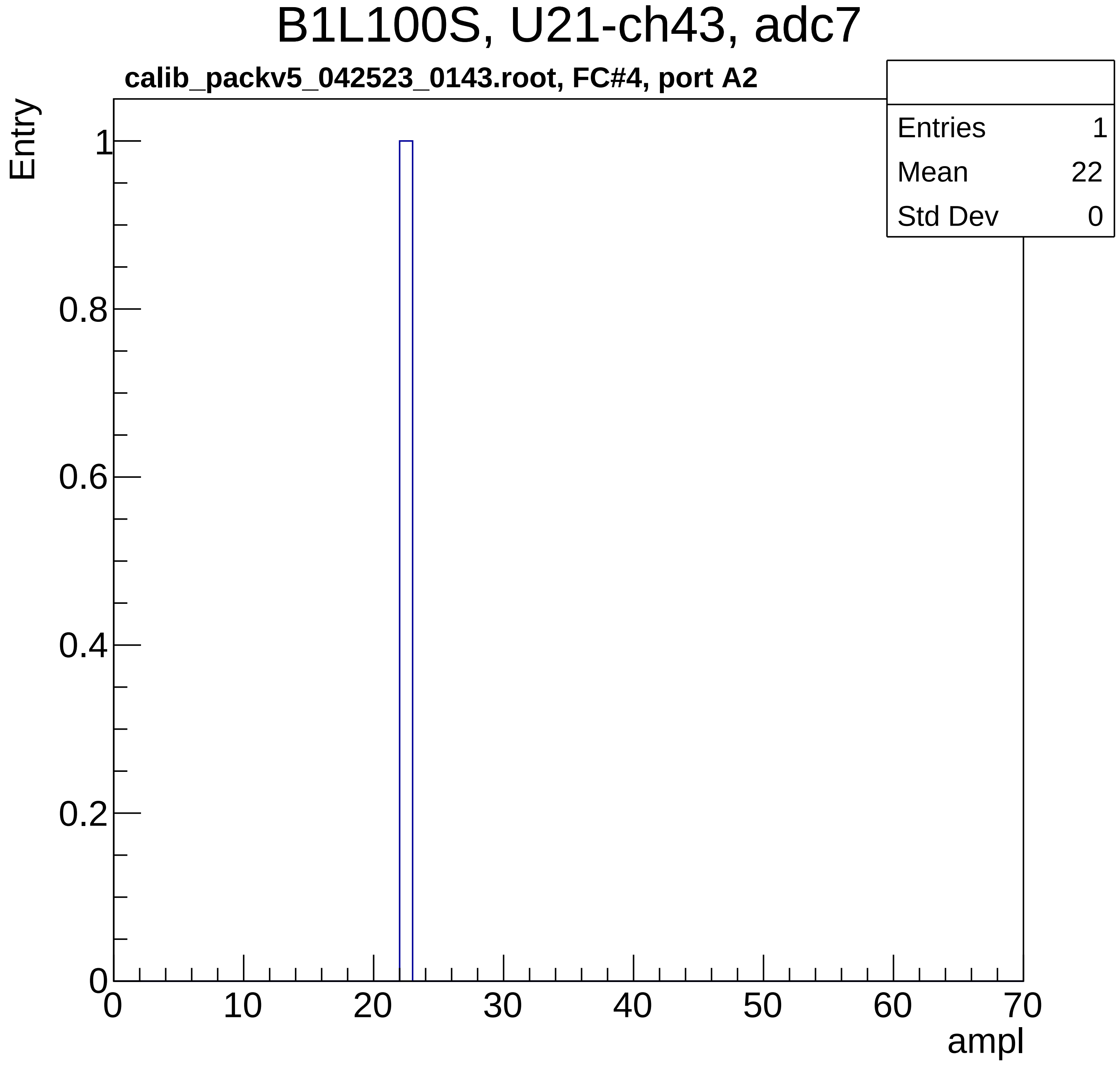
B1L100S, U21-ch43, adc5

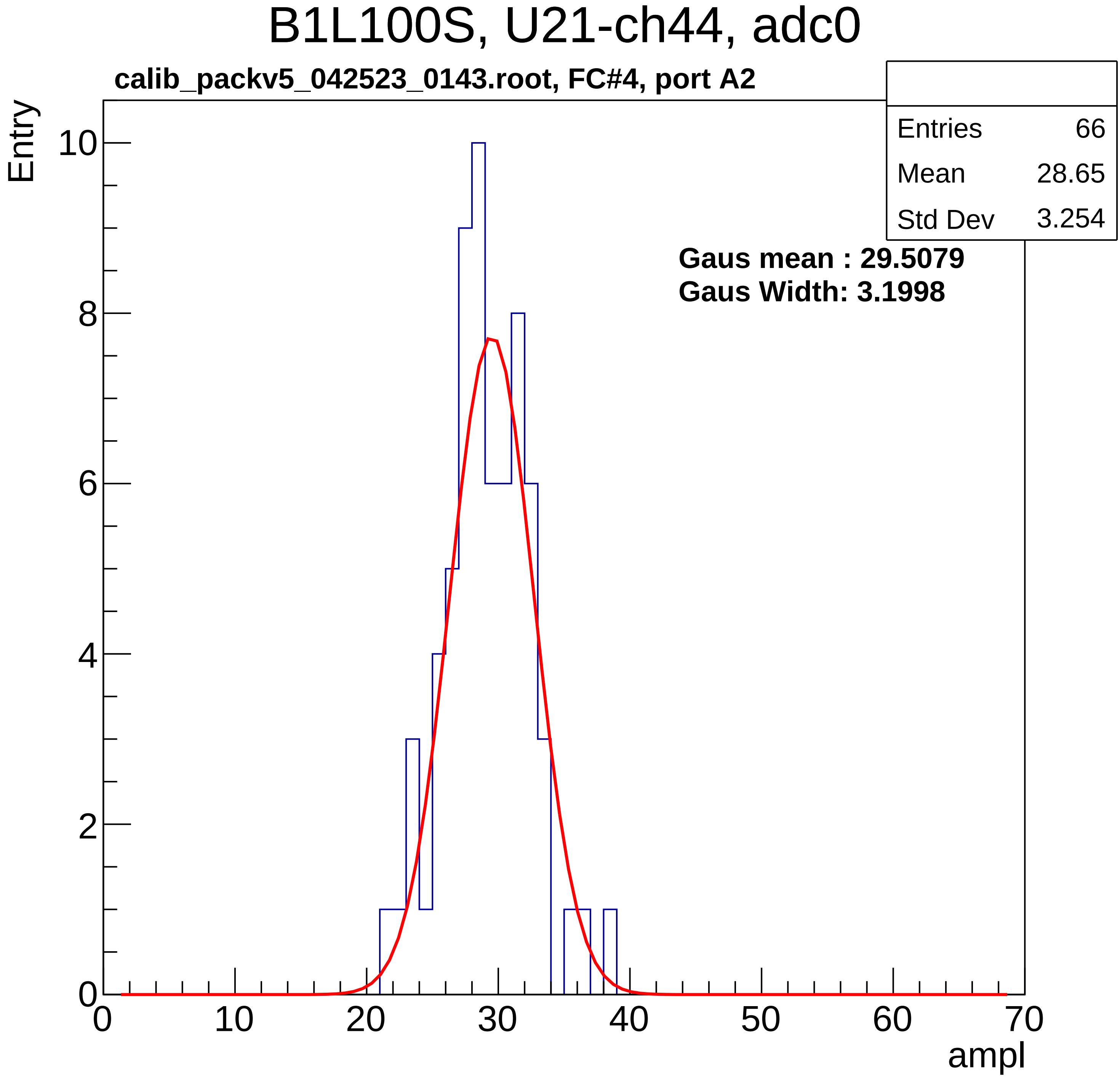
calib_packv5_042523_0143.root, FC#4, port A2

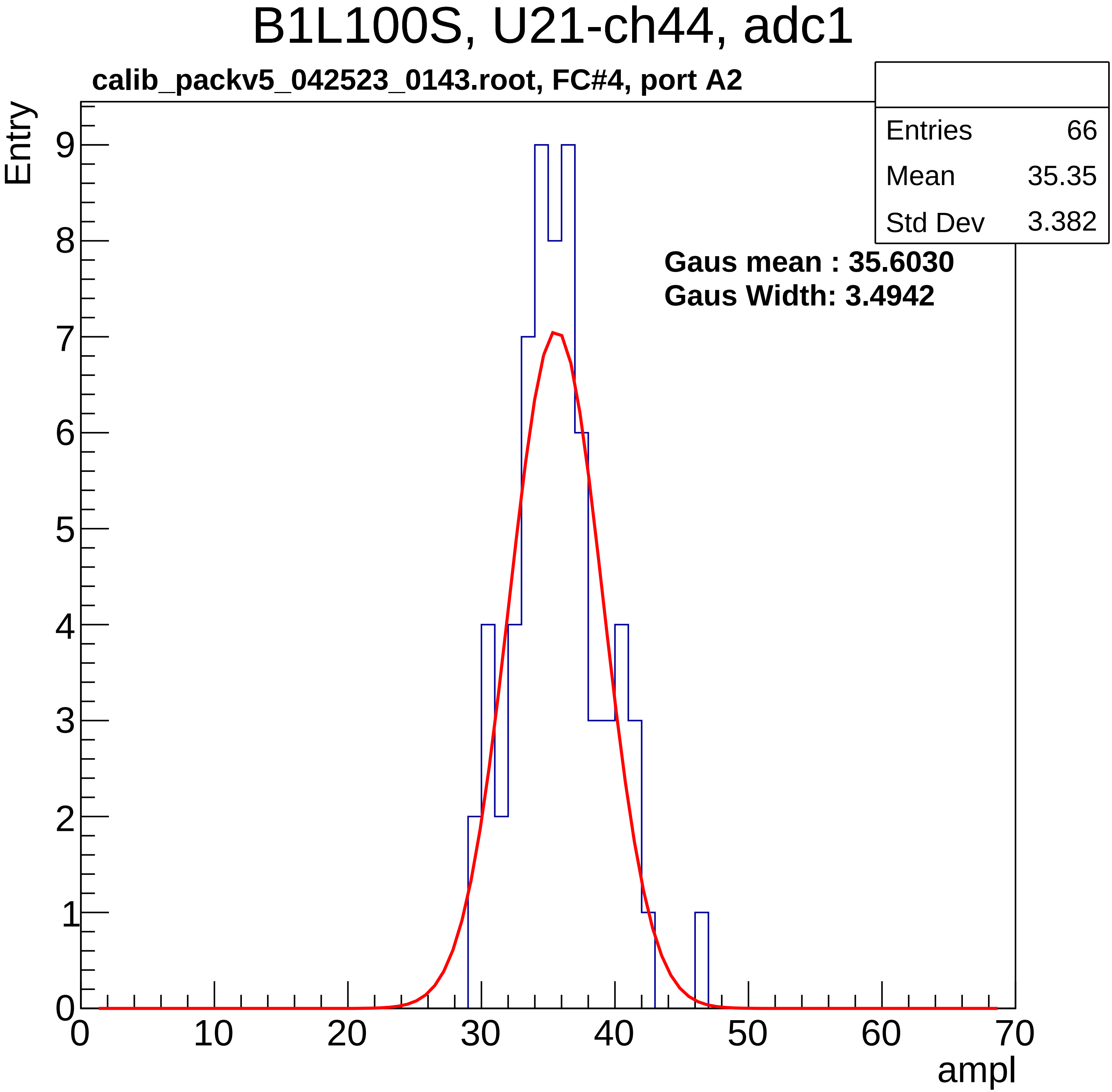


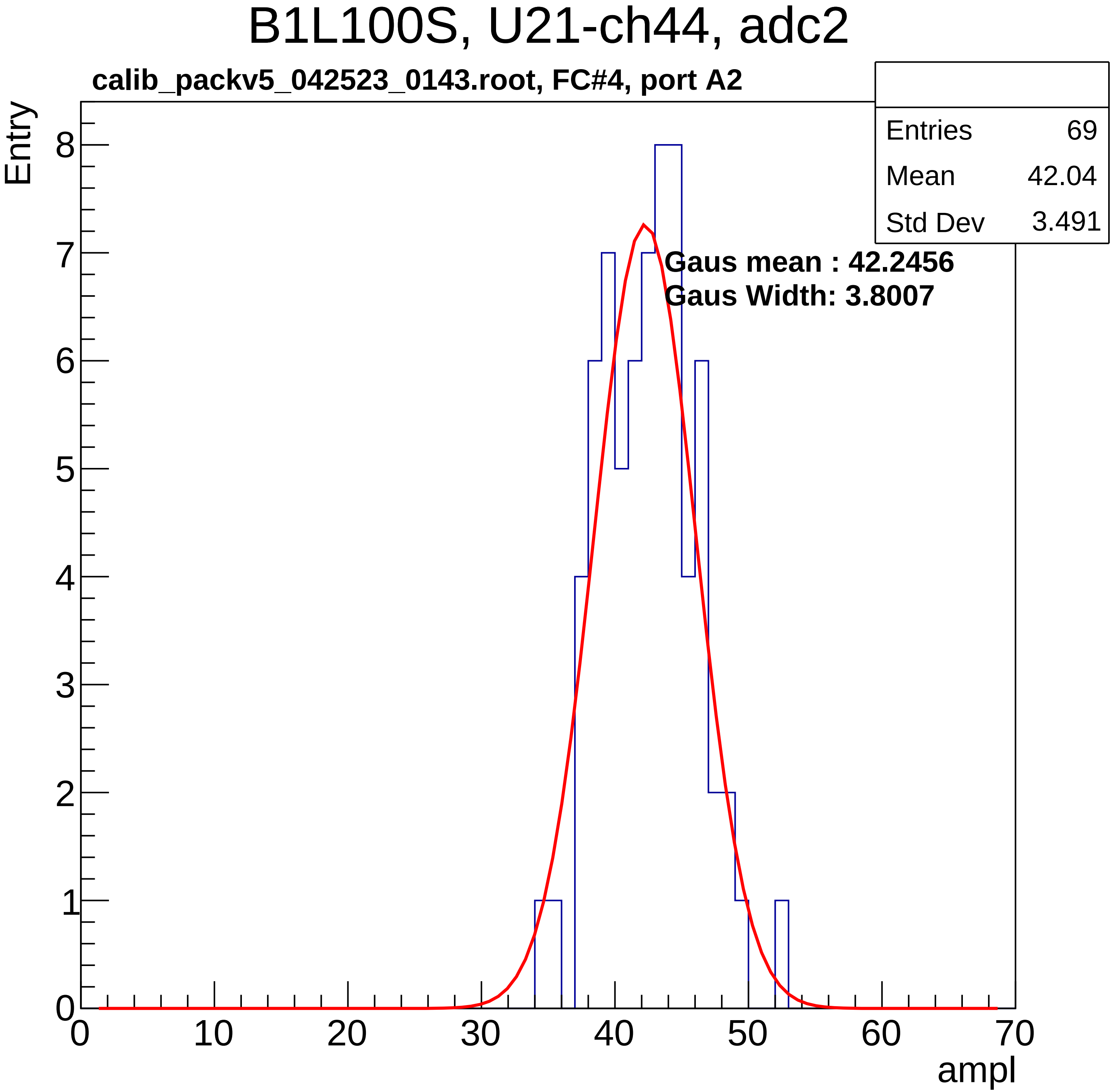
B1L100S, U21-ch43, adc6



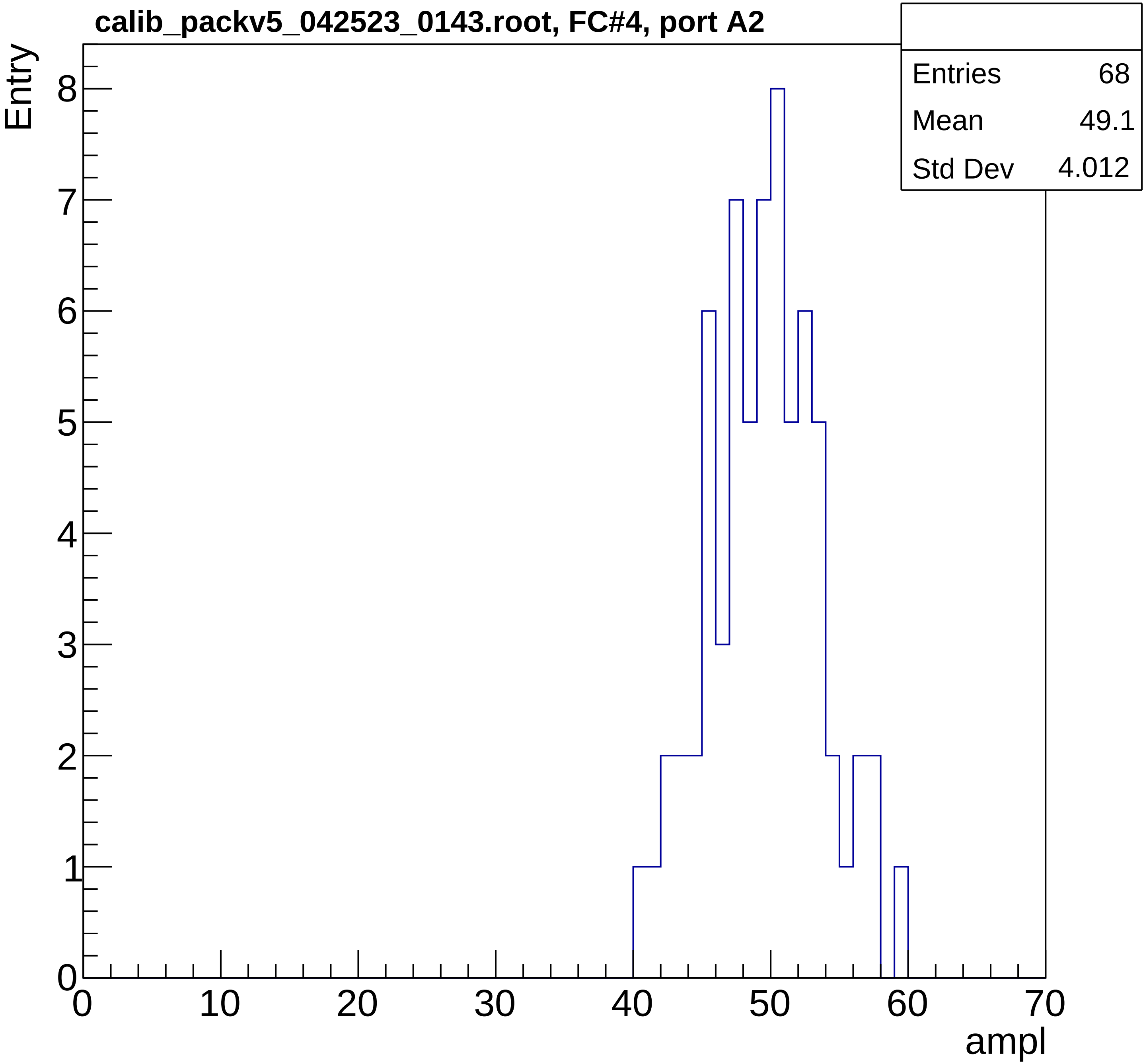

B1L100S, U21-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

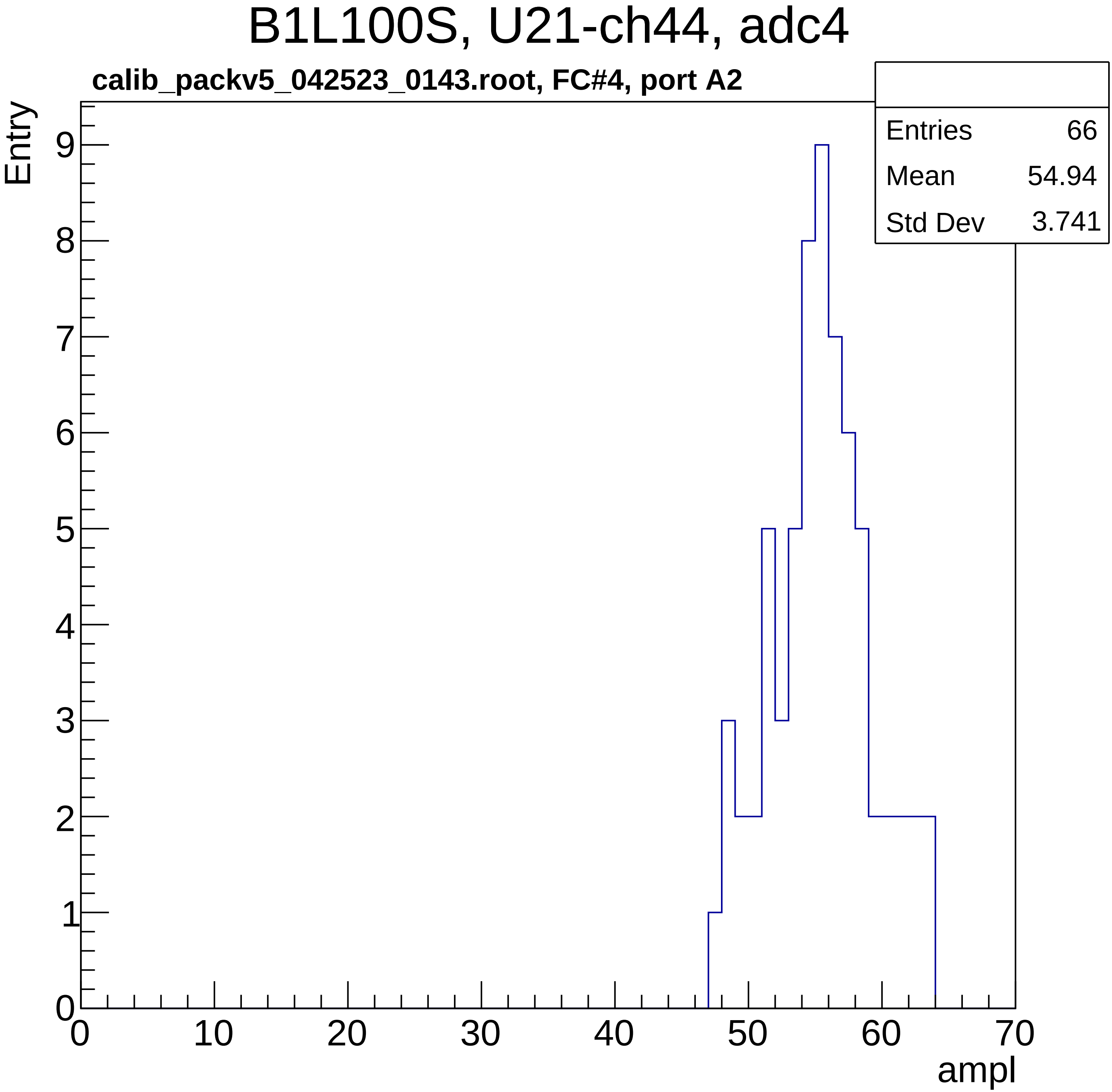

B1L100S, U21-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.65
Std Dev 3.254
Gaus mean : 29.5079
Gaus Width: 3.1998
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

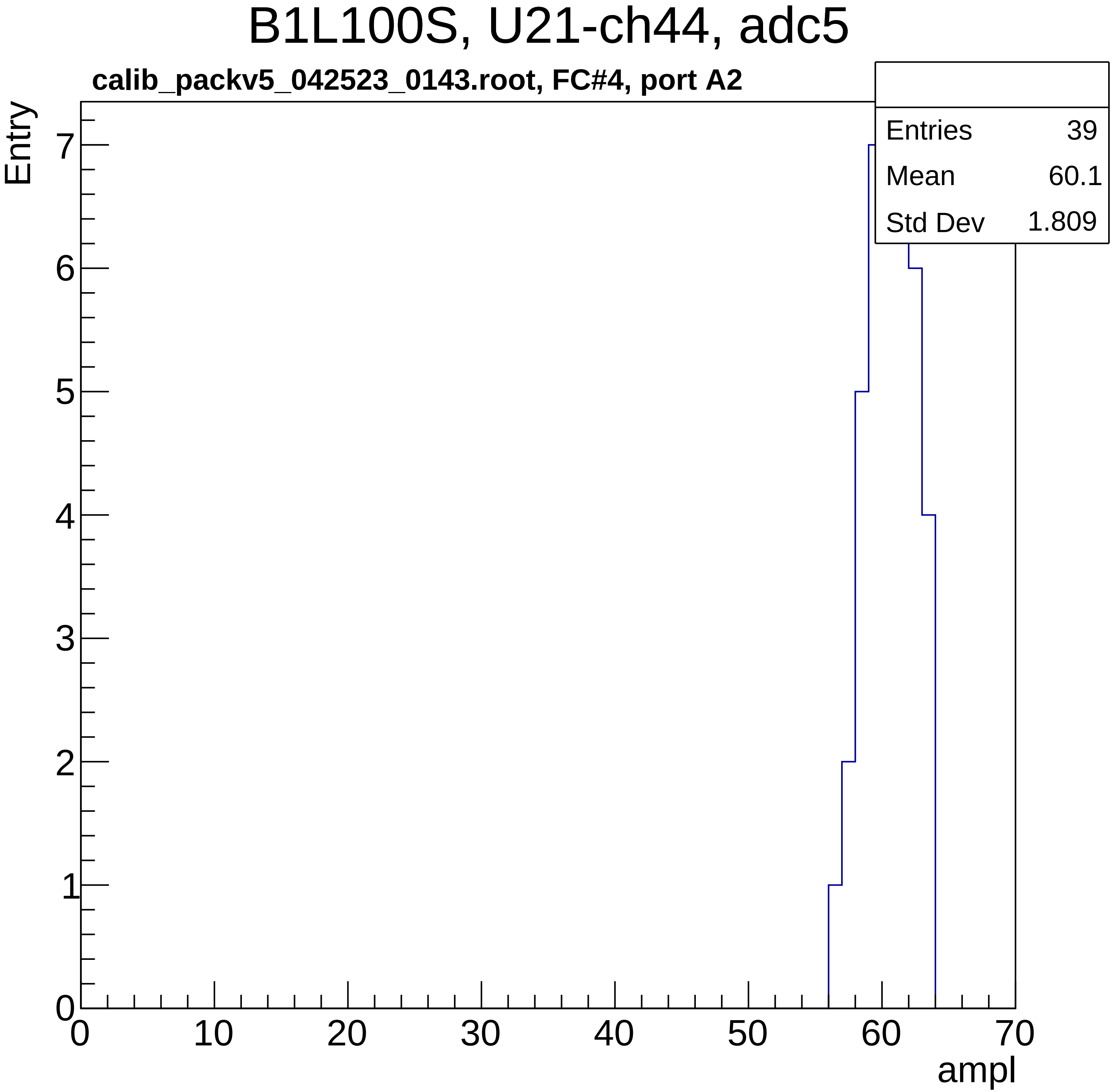

B1L100S, U21-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 66
Mean 35.35
Std Dev 3.382
Gaus mean : 35.6030
Gaus Width: 3.4942
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

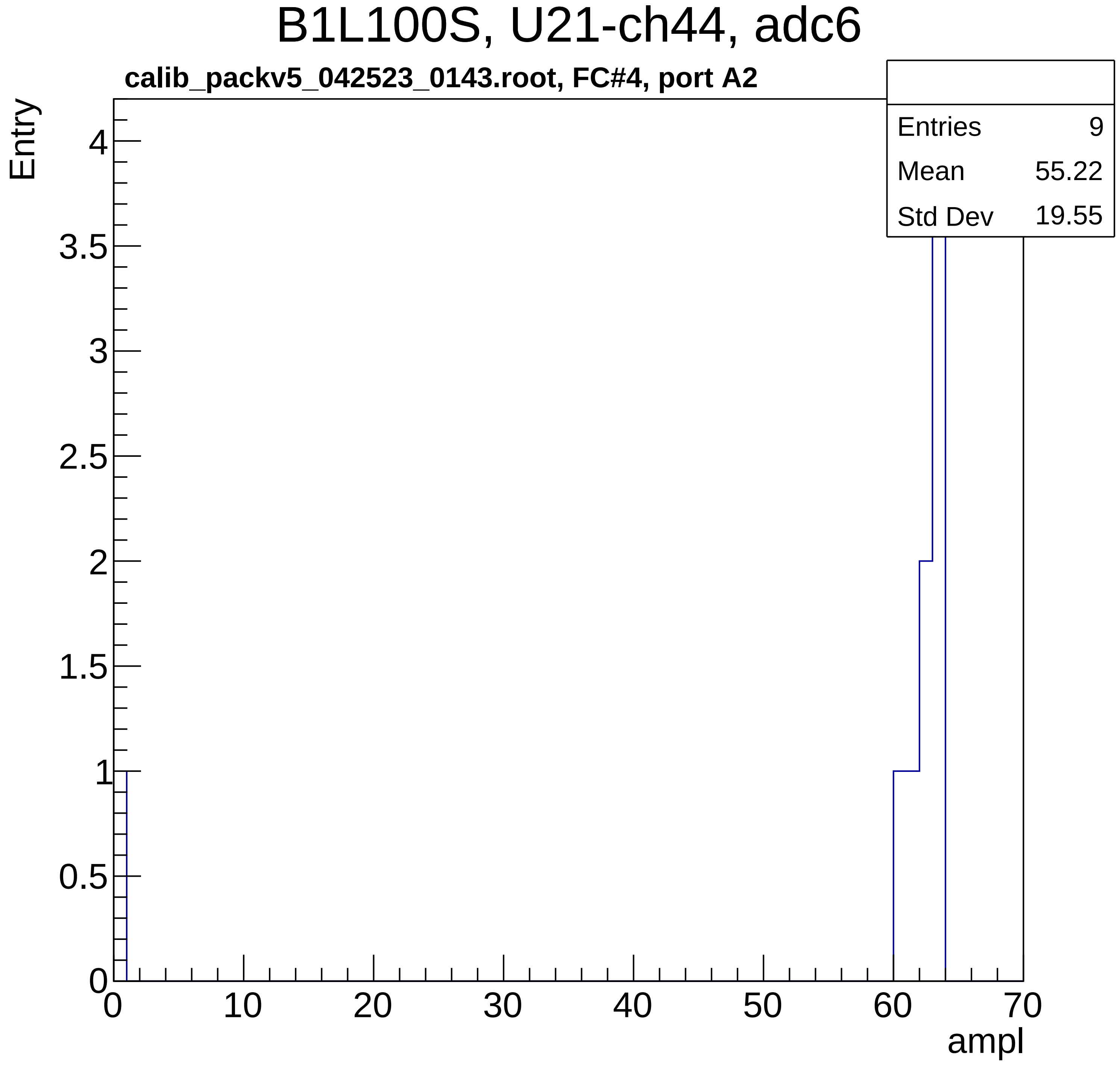

B1L100S, U21-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.04
Std Dev 3.491
Gaus mean : 42.2456
Gaus Width: 3.8007
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

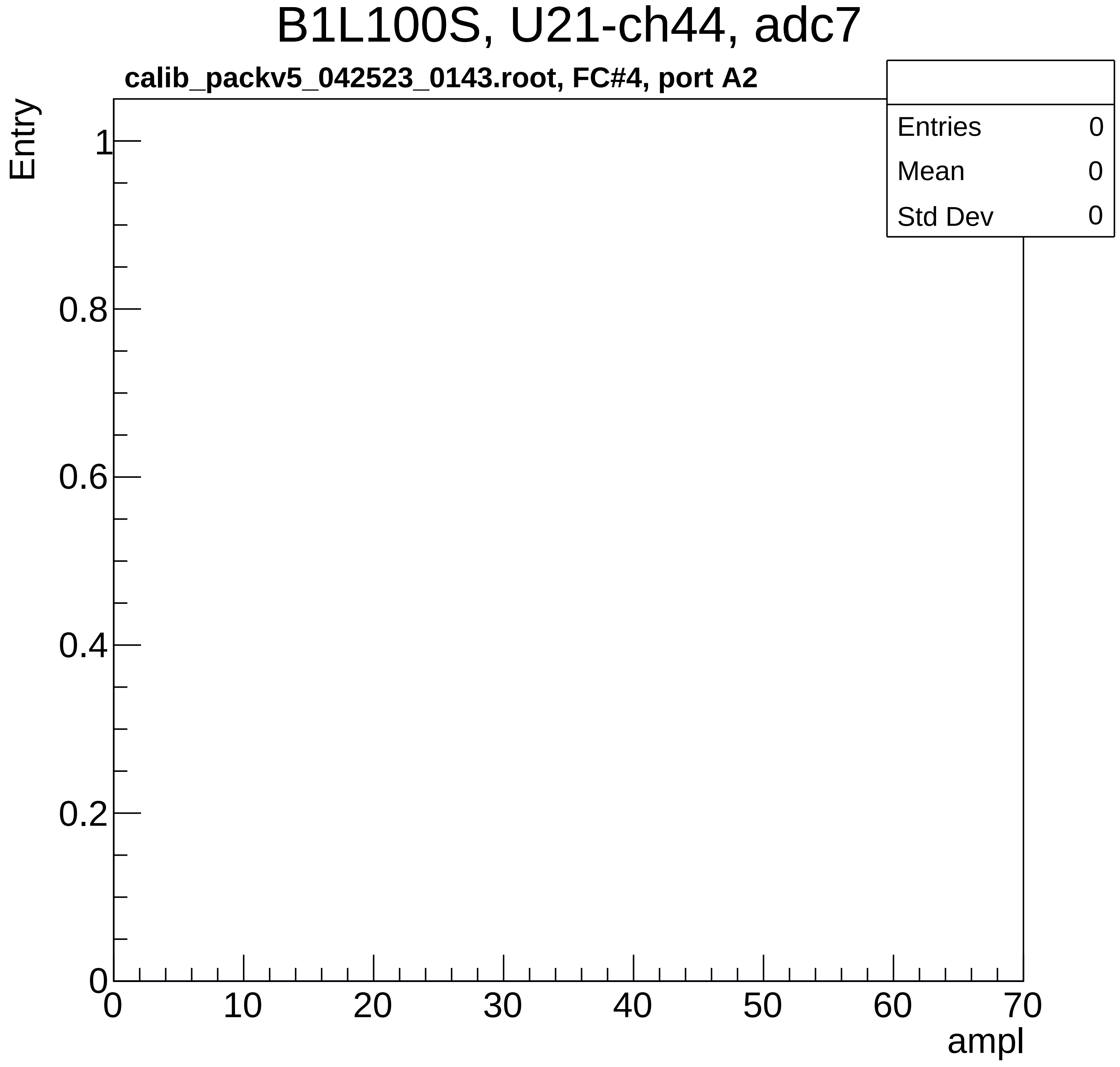
B1L100S, U21-ch44, adc3

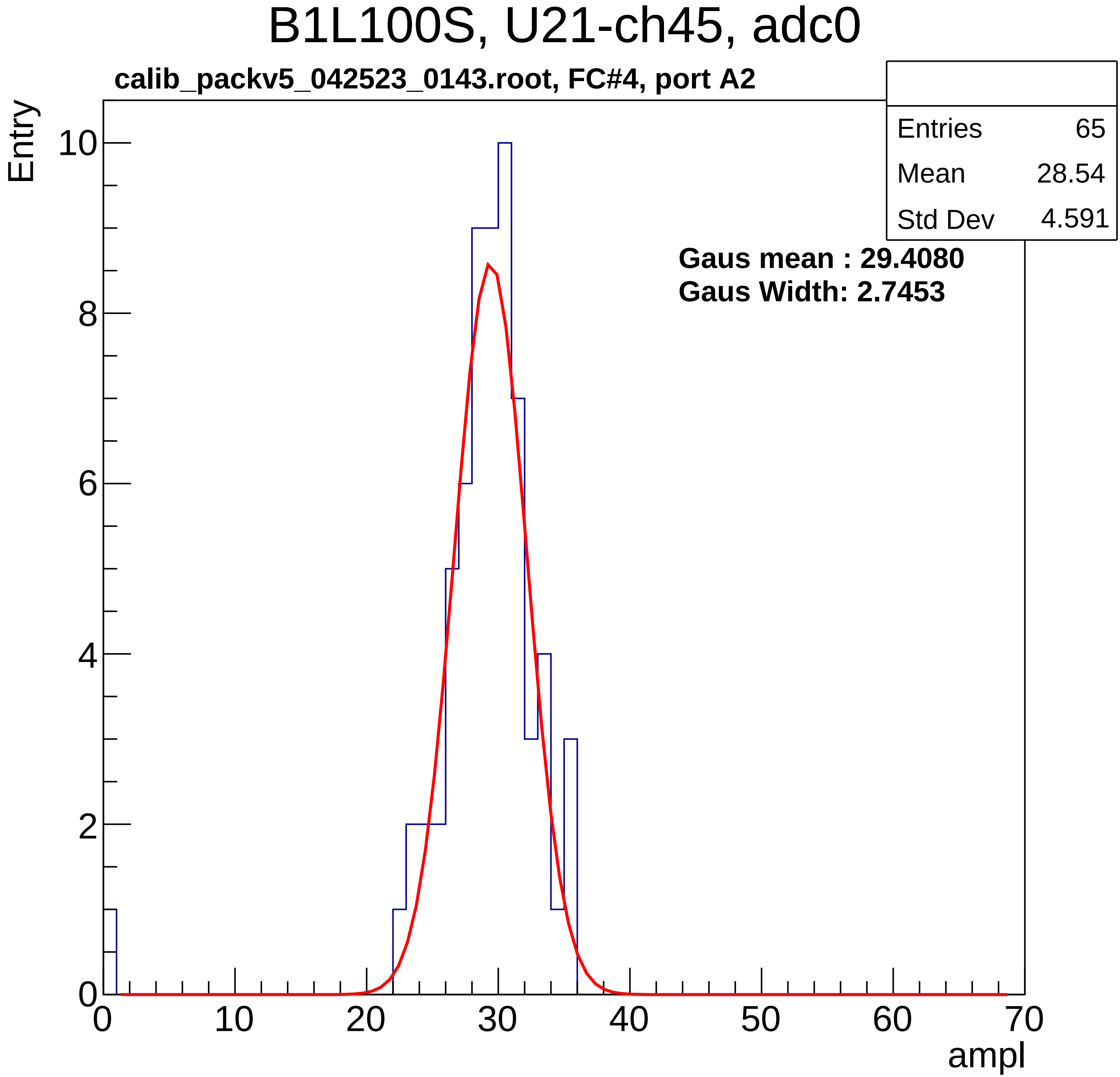



B1L100S, U21-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 54.94
Std Dev 3.741
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

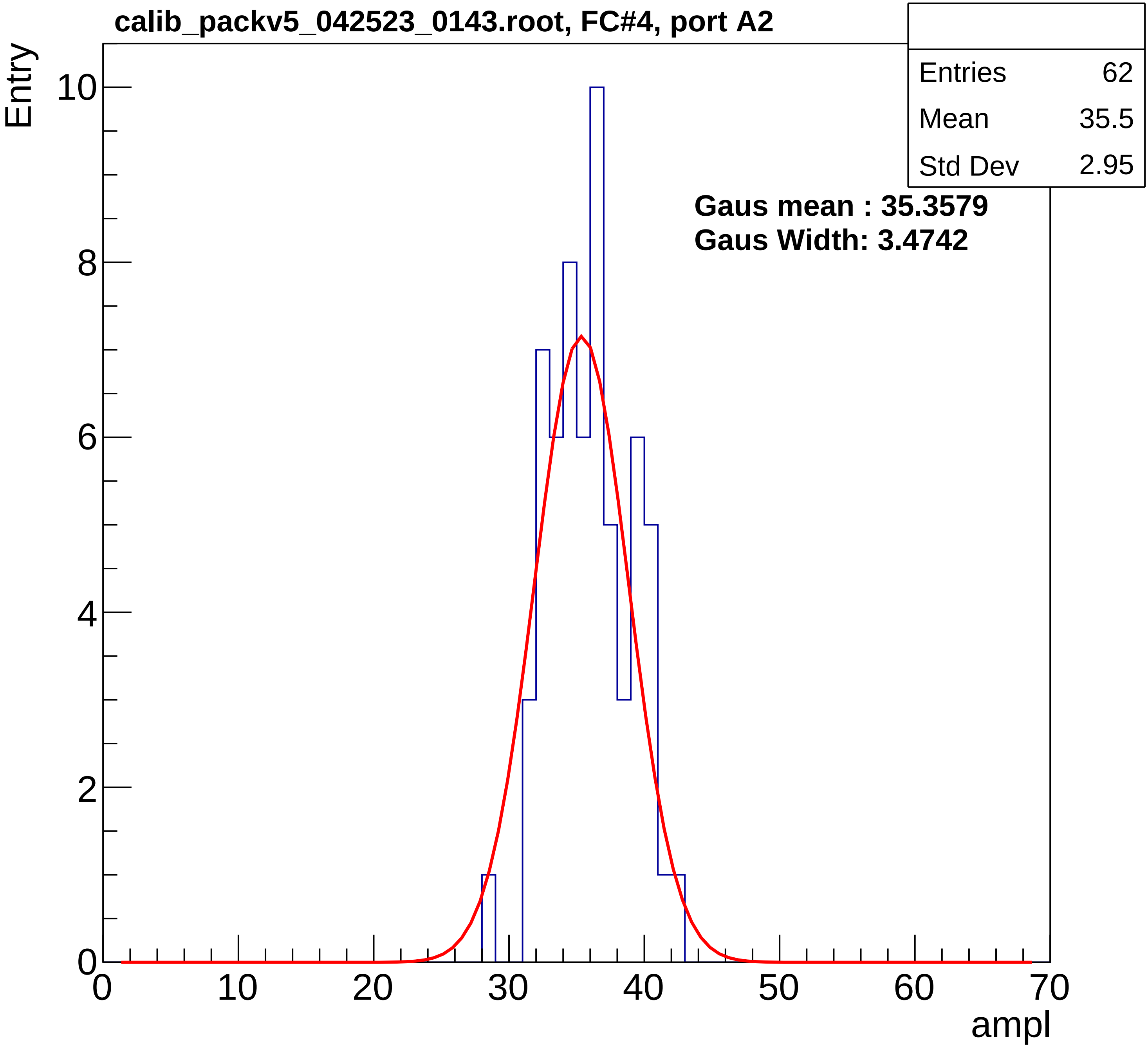

B1L100S, U21-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 39
Mean 60.1
Std Dev 1.809
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

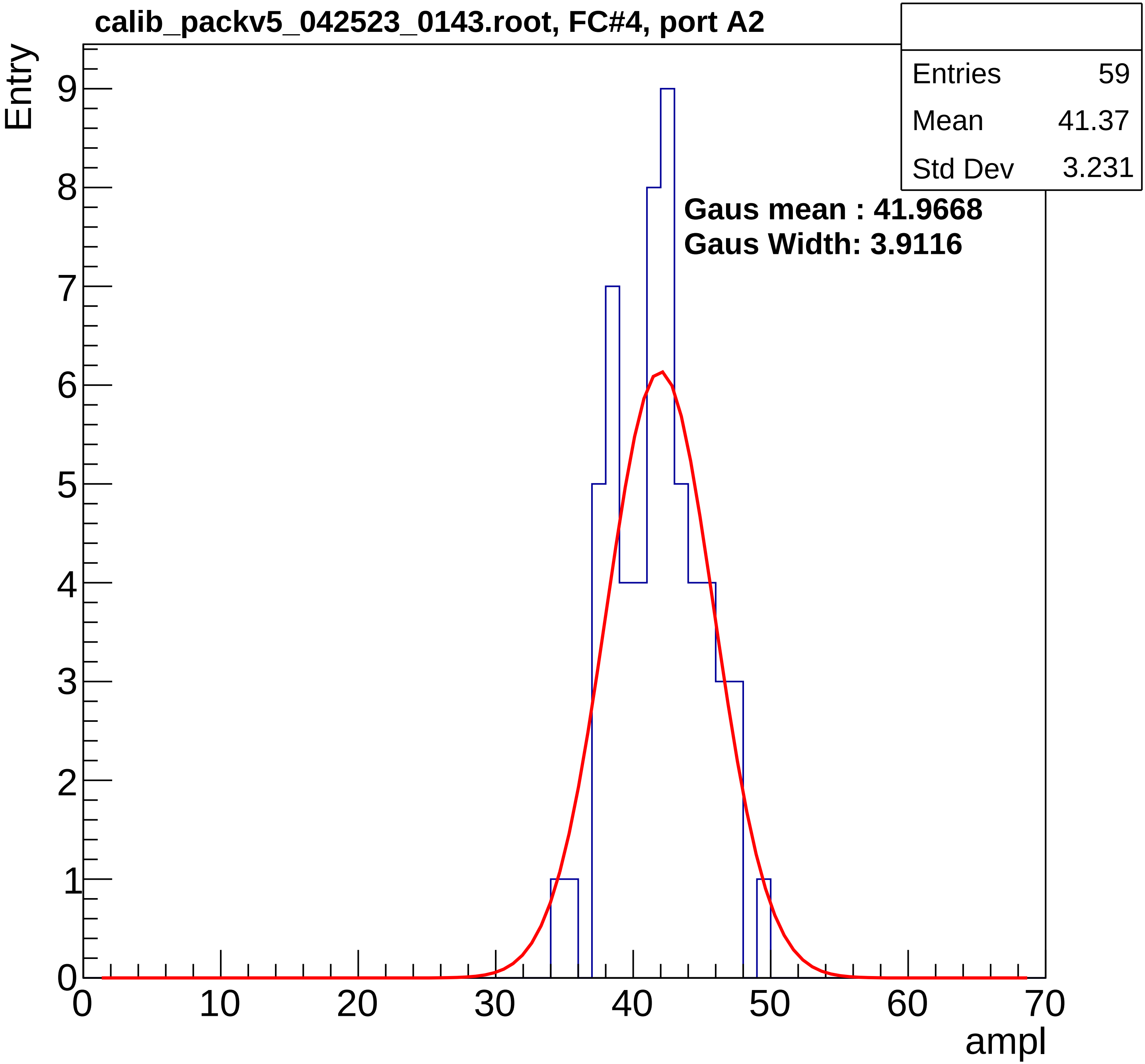

B1L100S, U21-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

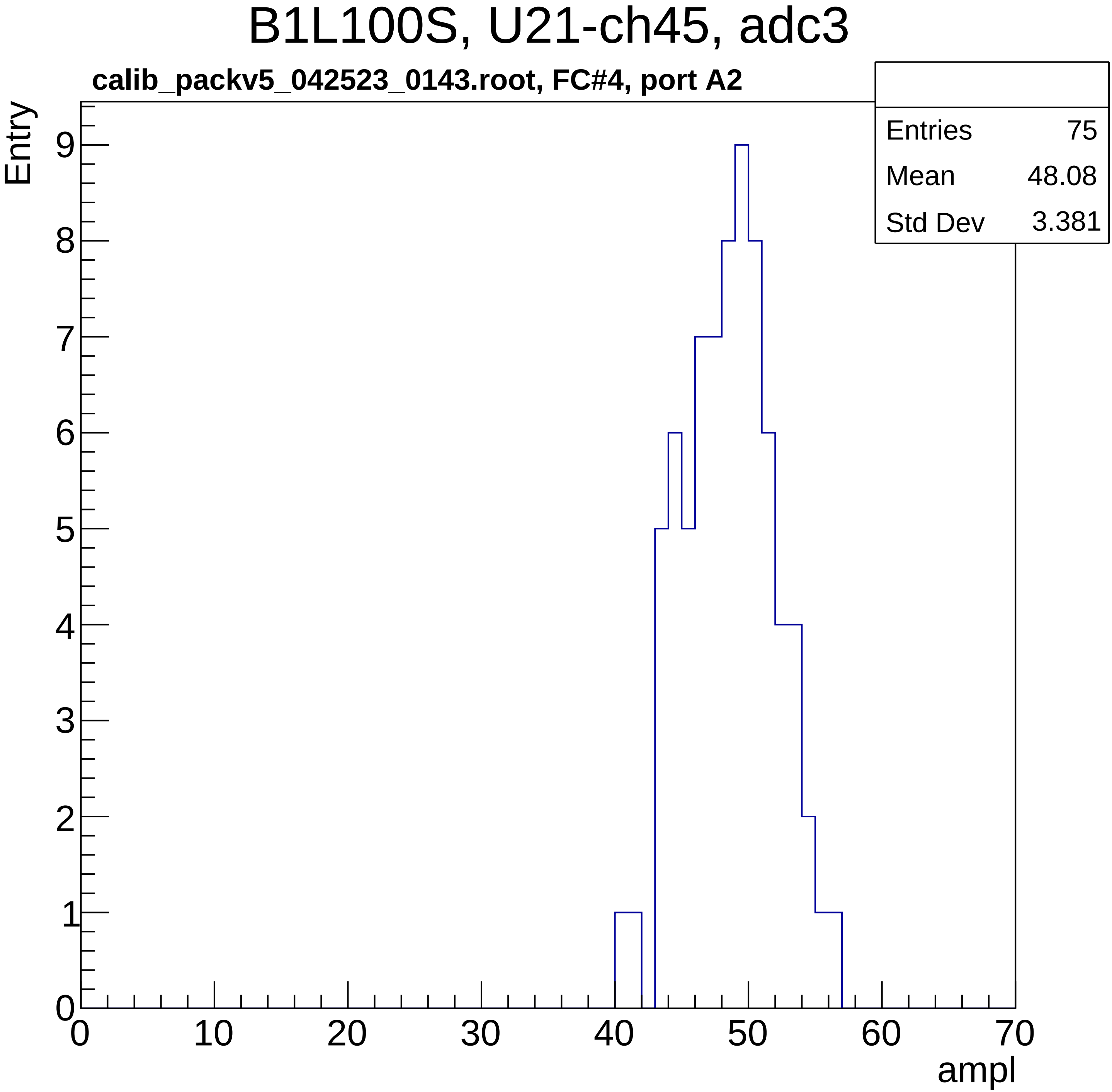

B1L100S, U21-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.54
Std Dev 4.591
Gaus mean : 29.4080
Gaus Width: 2.7453
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

B1L100S, U21-ch45, adc1



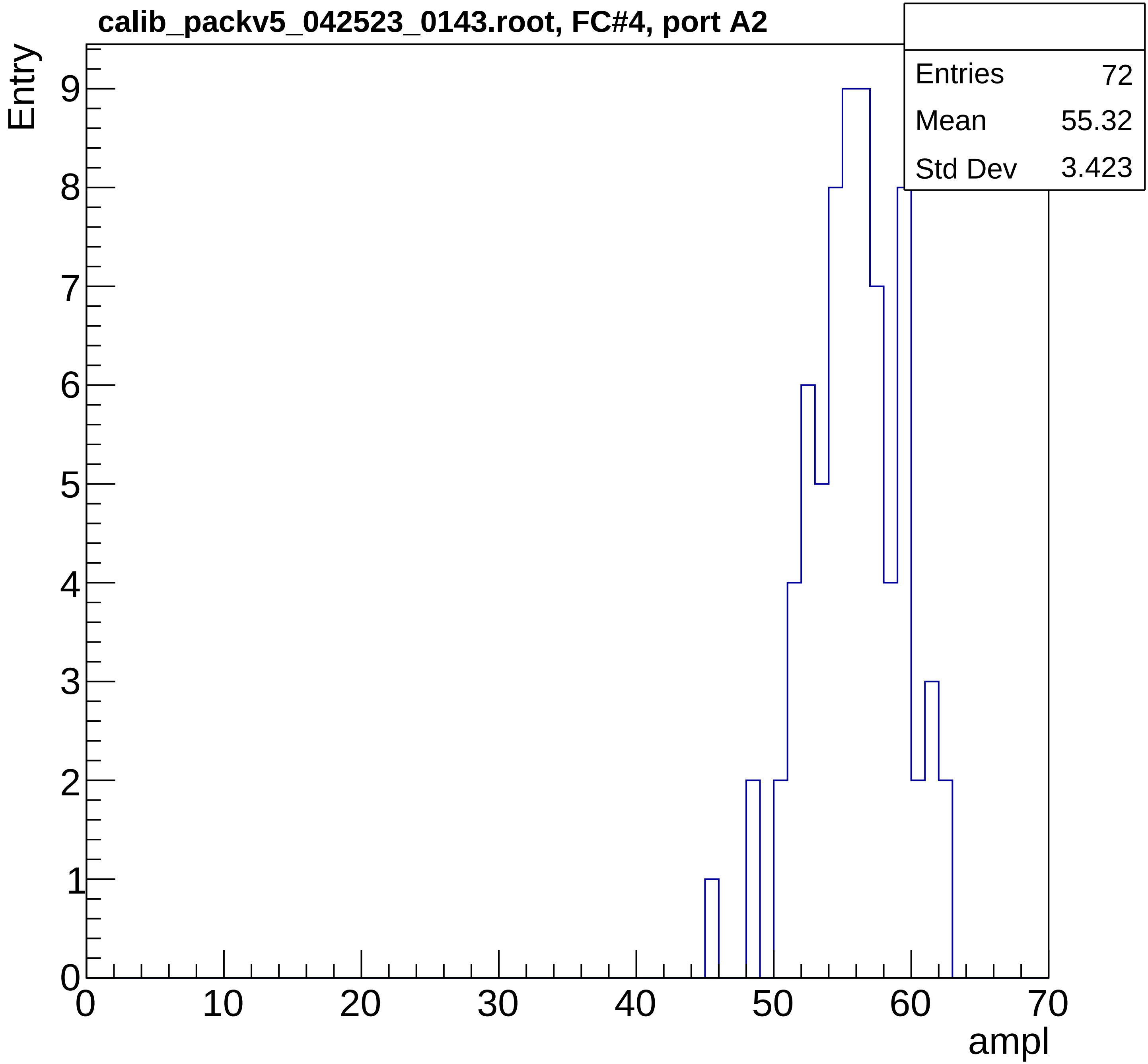
B1L100S, U21-ch45, adc2

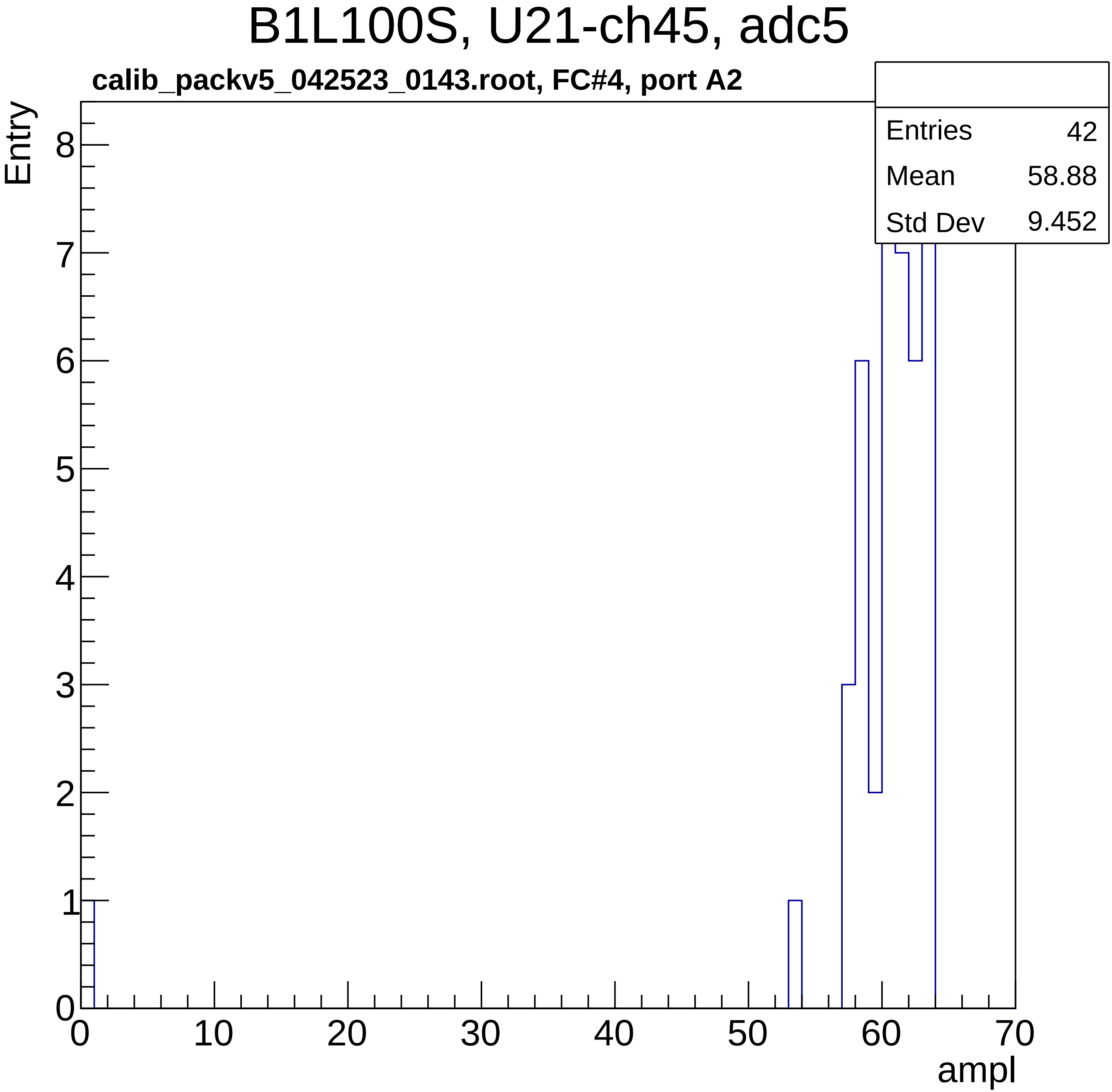



B1L100S, U21-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 75
Mean 48.08
Std Dev 3.381
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

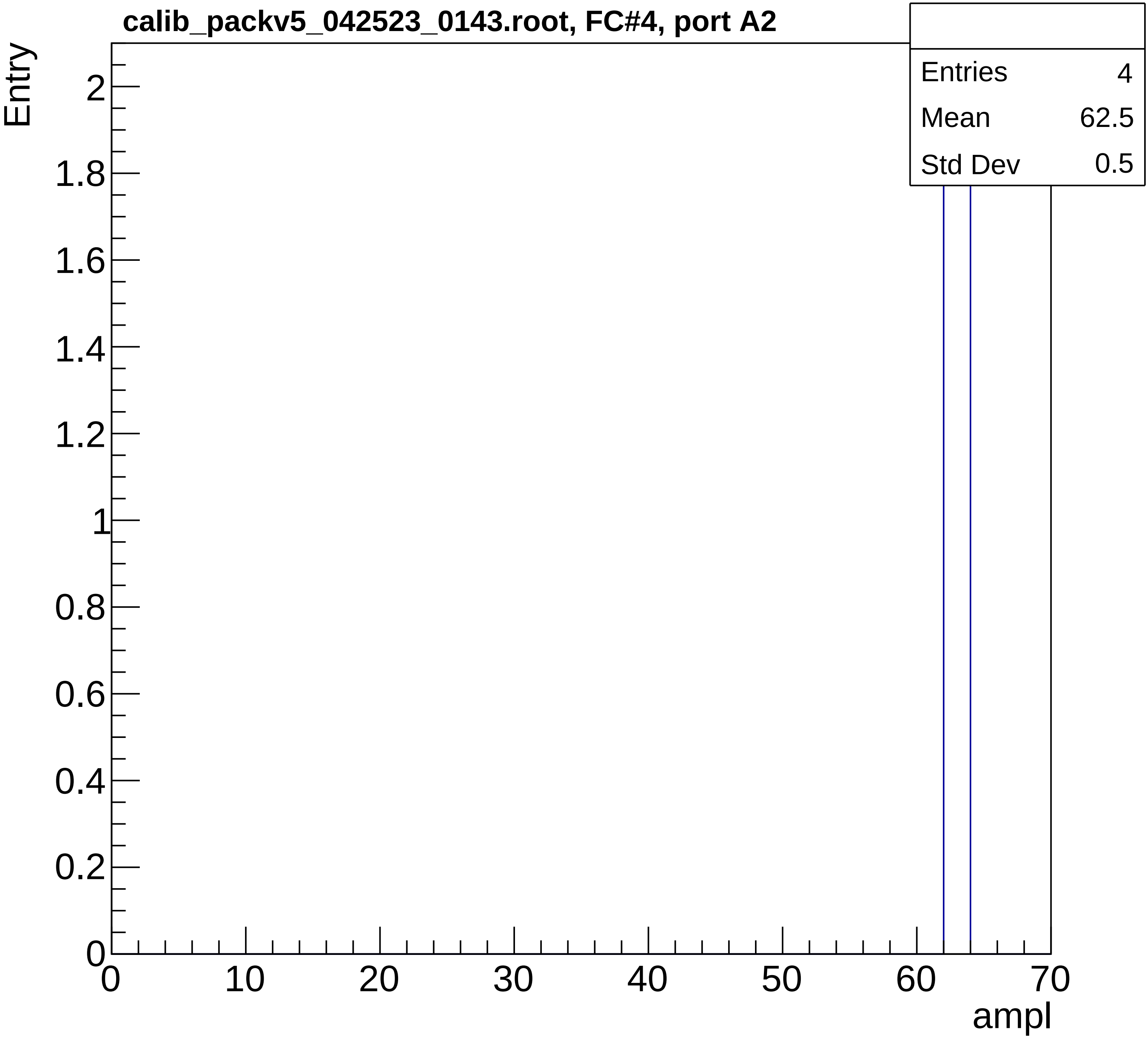
B1L100S, U21-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

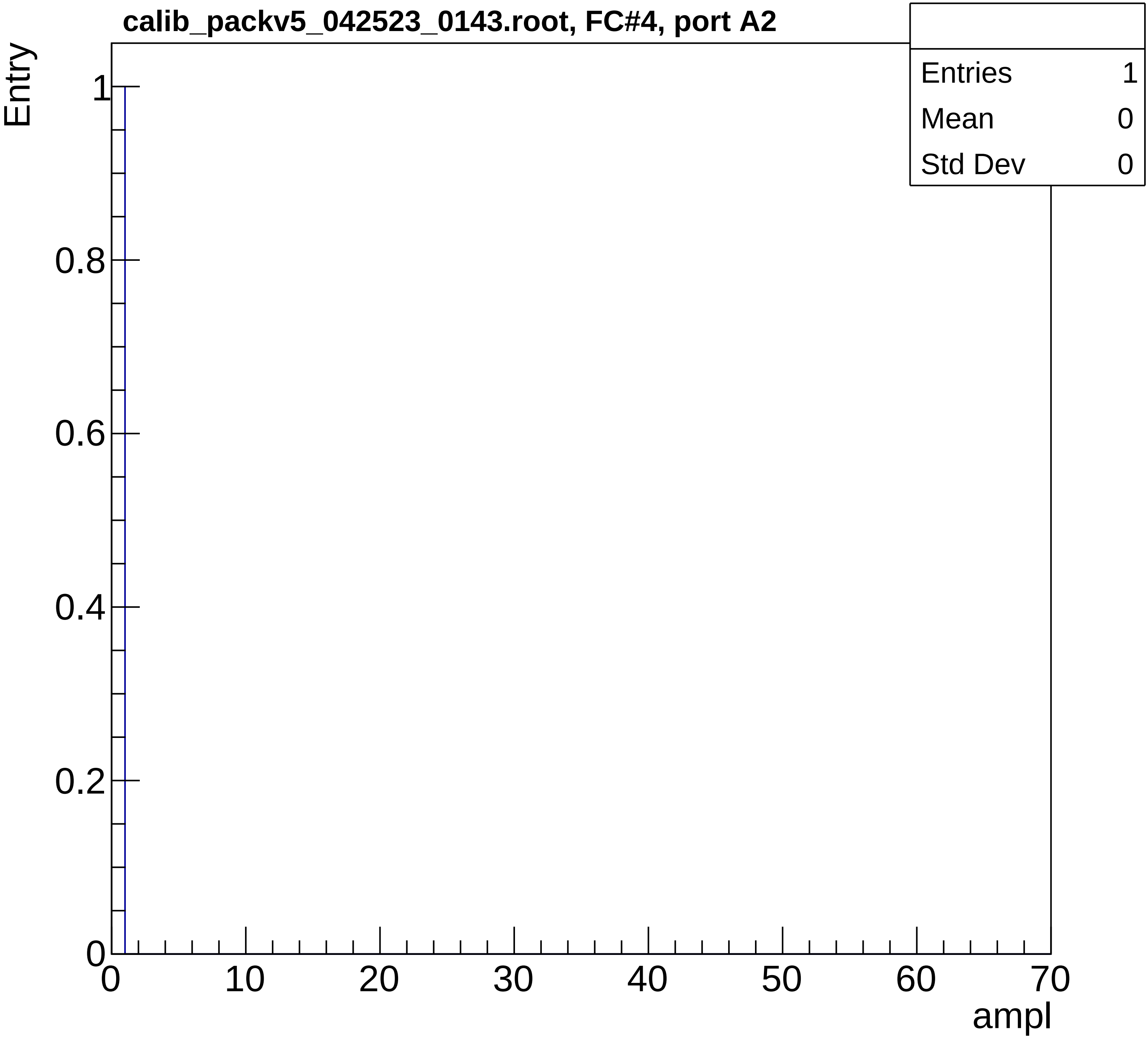


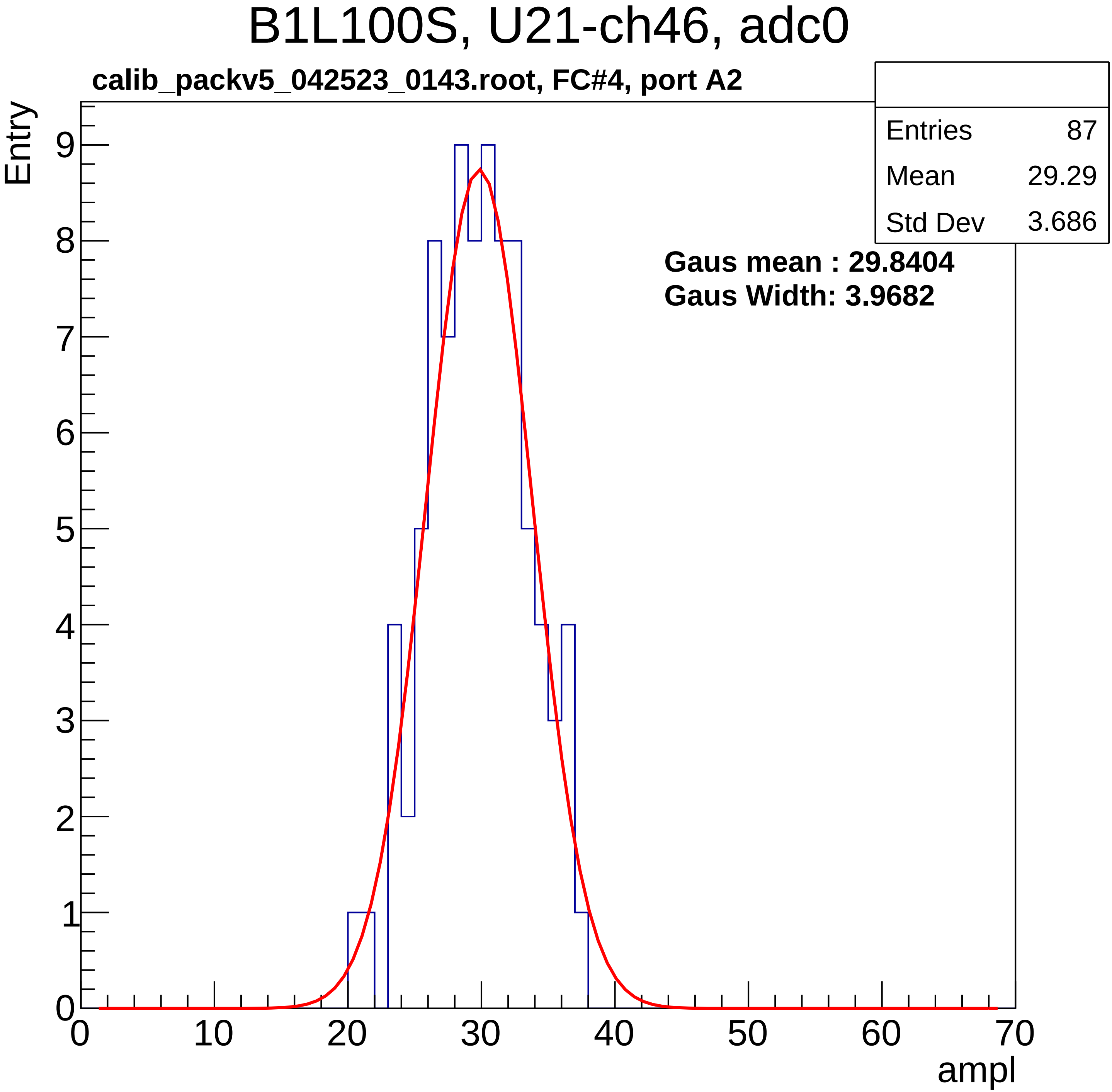

B1L100S, U21-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.88
Std Dev 9.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

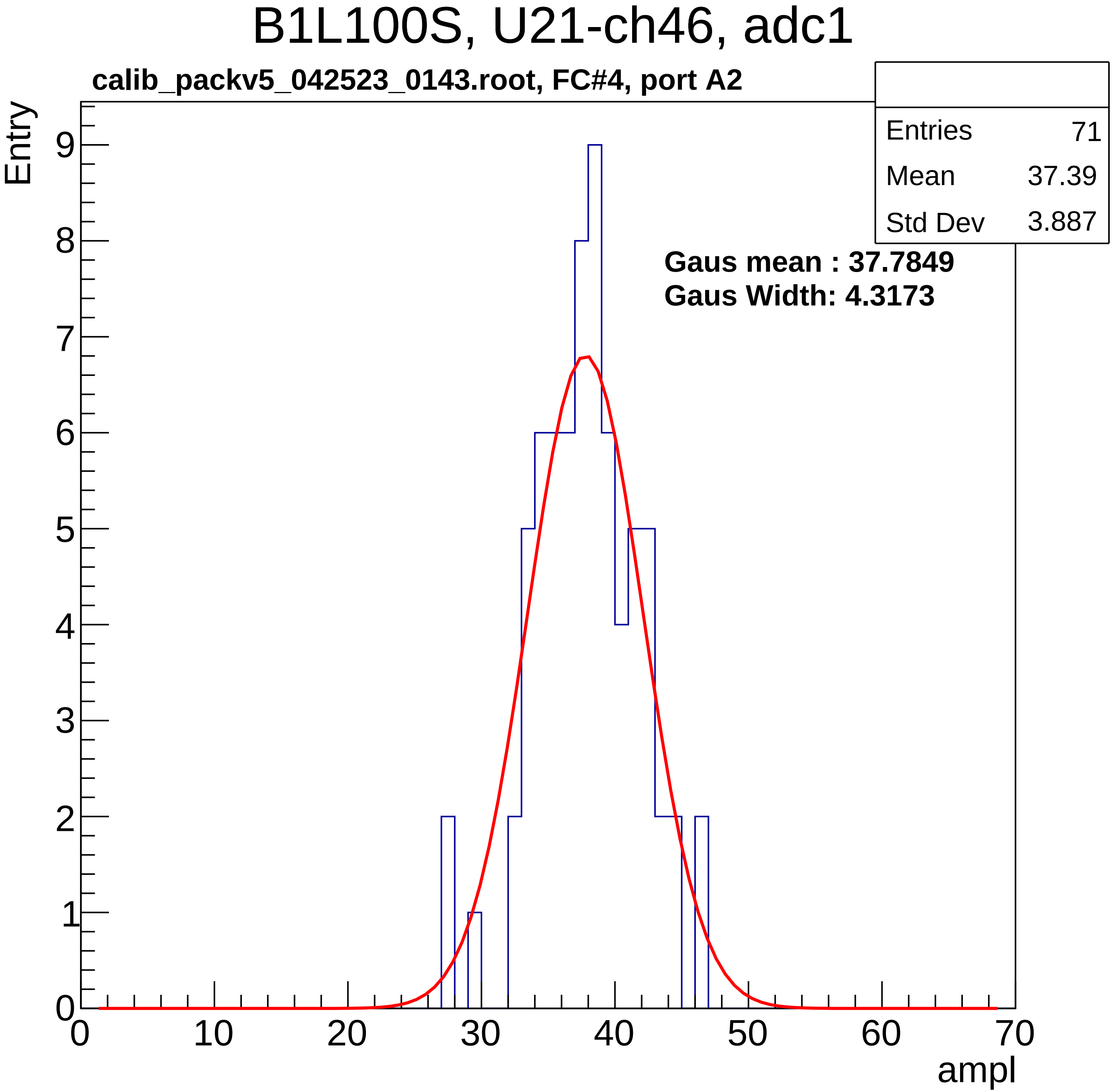
B1L100S, U21-ch45, adc6

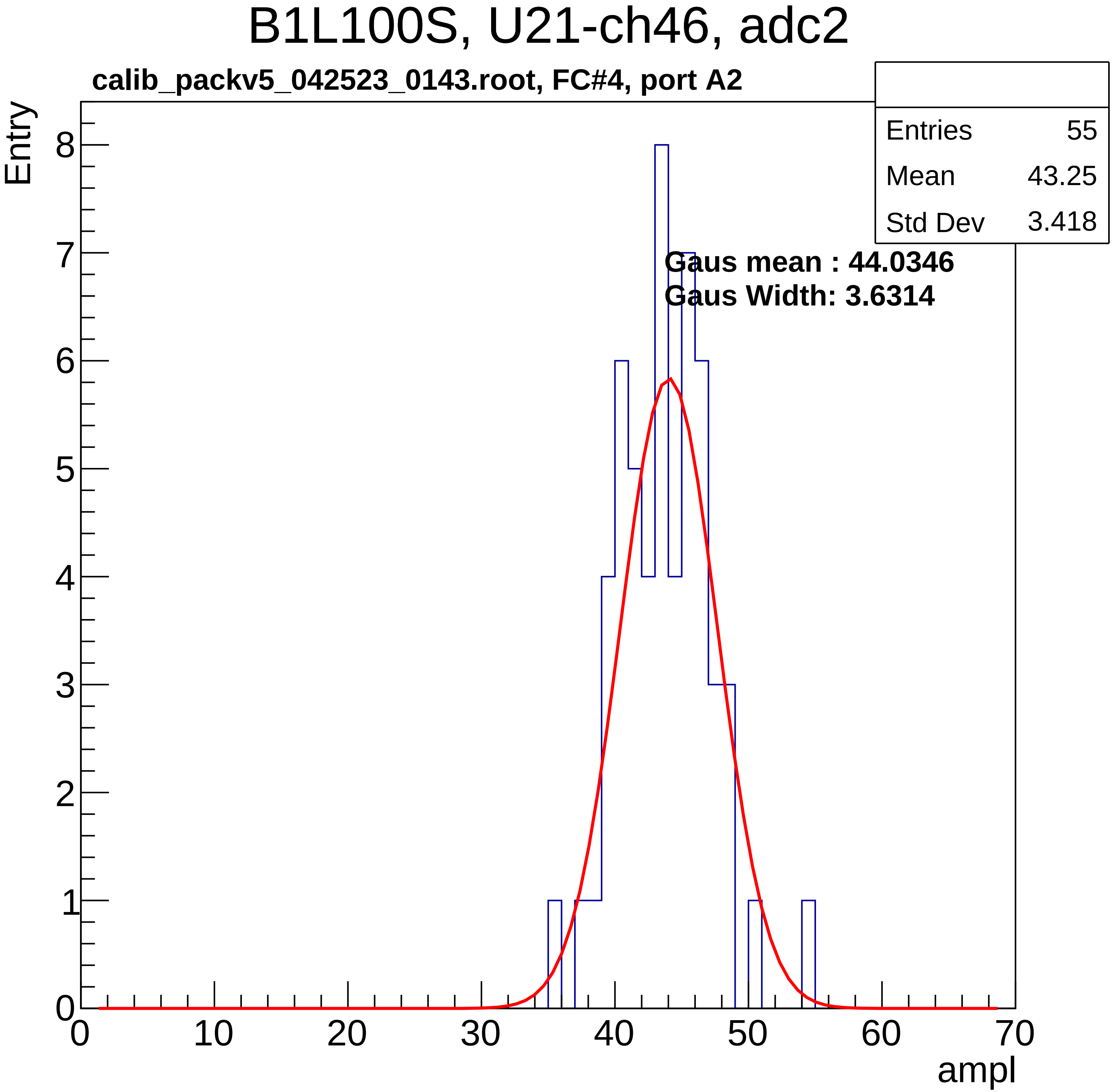


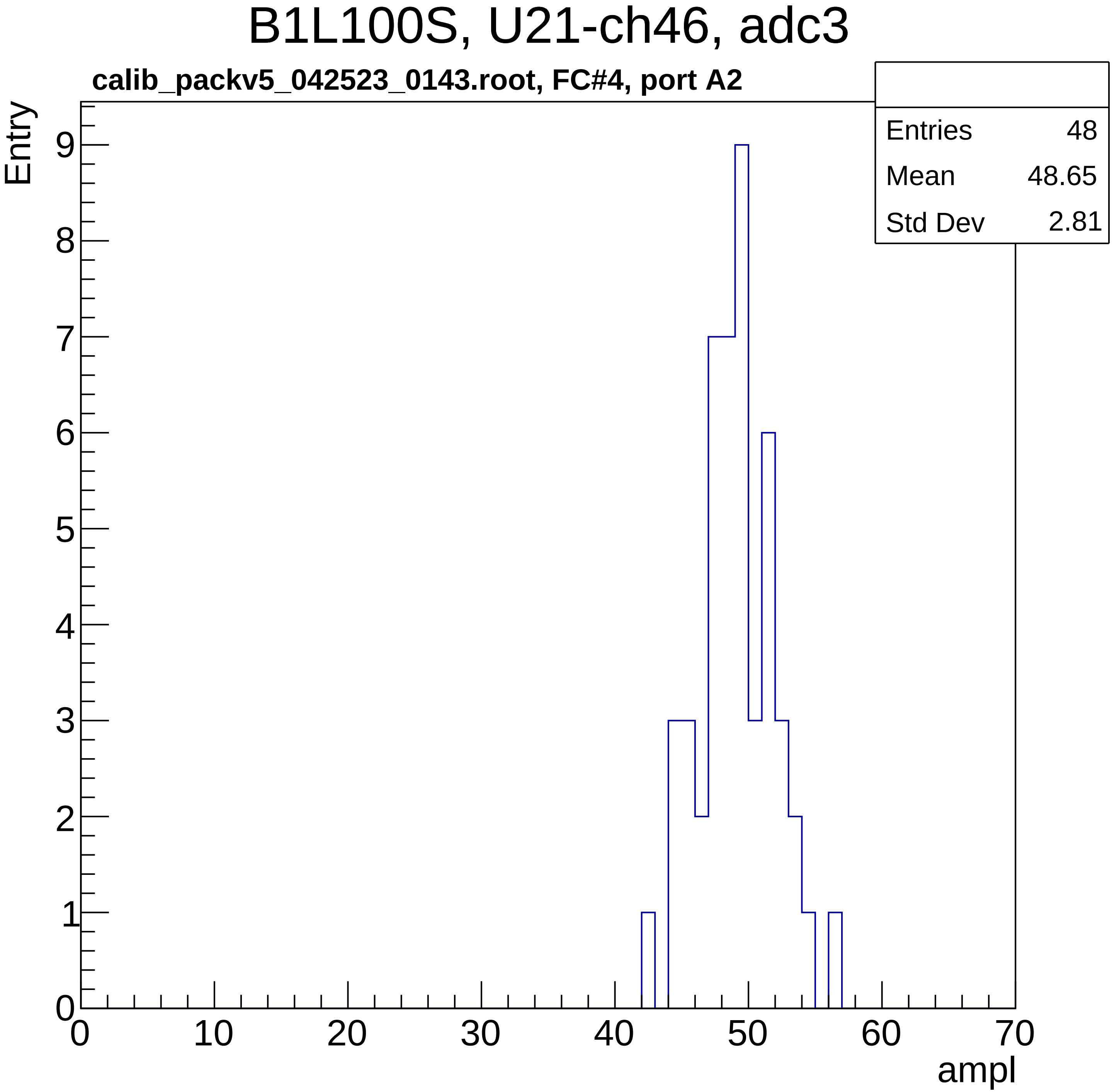
B1L100S, U21-ch45, adc7




B1L100S, U21-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 87
Mean 29.29
Std Dev 3.686
Gaus mean : 29.8404
Gaus Width: 3.9682
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

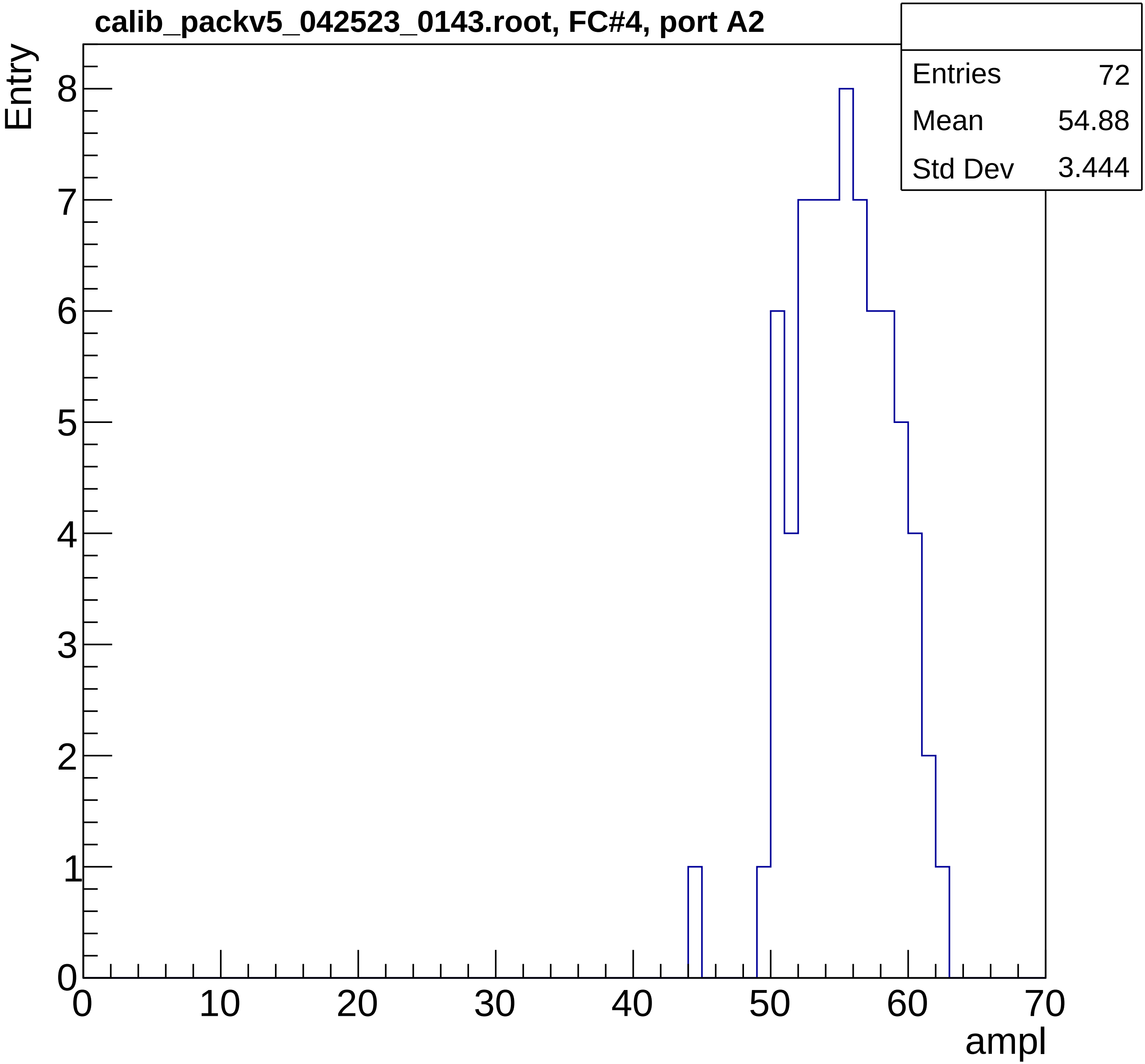

B1L100S, U21-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 37.39
Std Dev 3.887
Gaus mean : 37.7849
Gaus Width: 4.3173
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

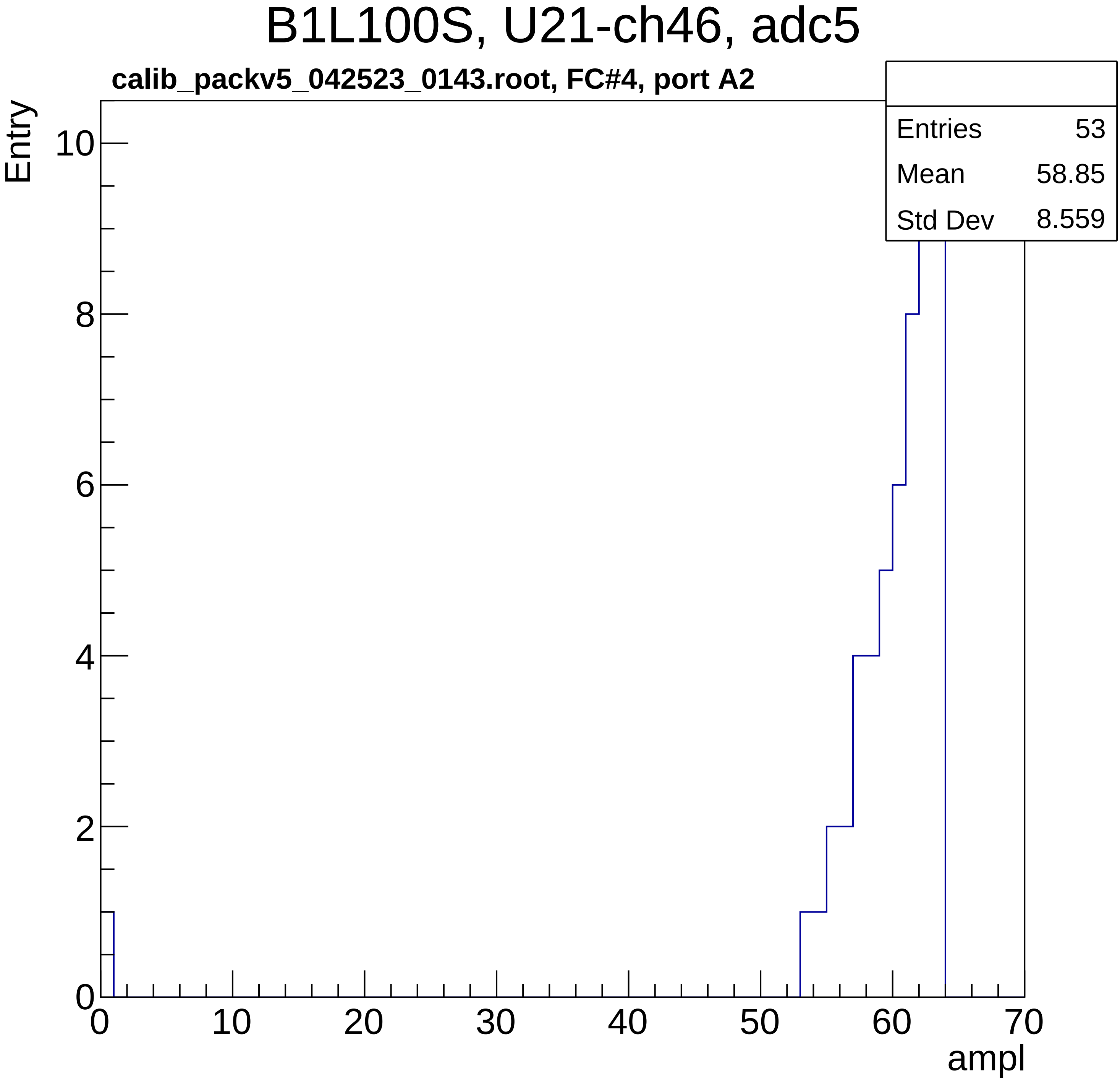

B1L100S, U21-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 43.25
Std Dev 3.418
Gaus mean : 44.0346
Gaus Width: 3.6314
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

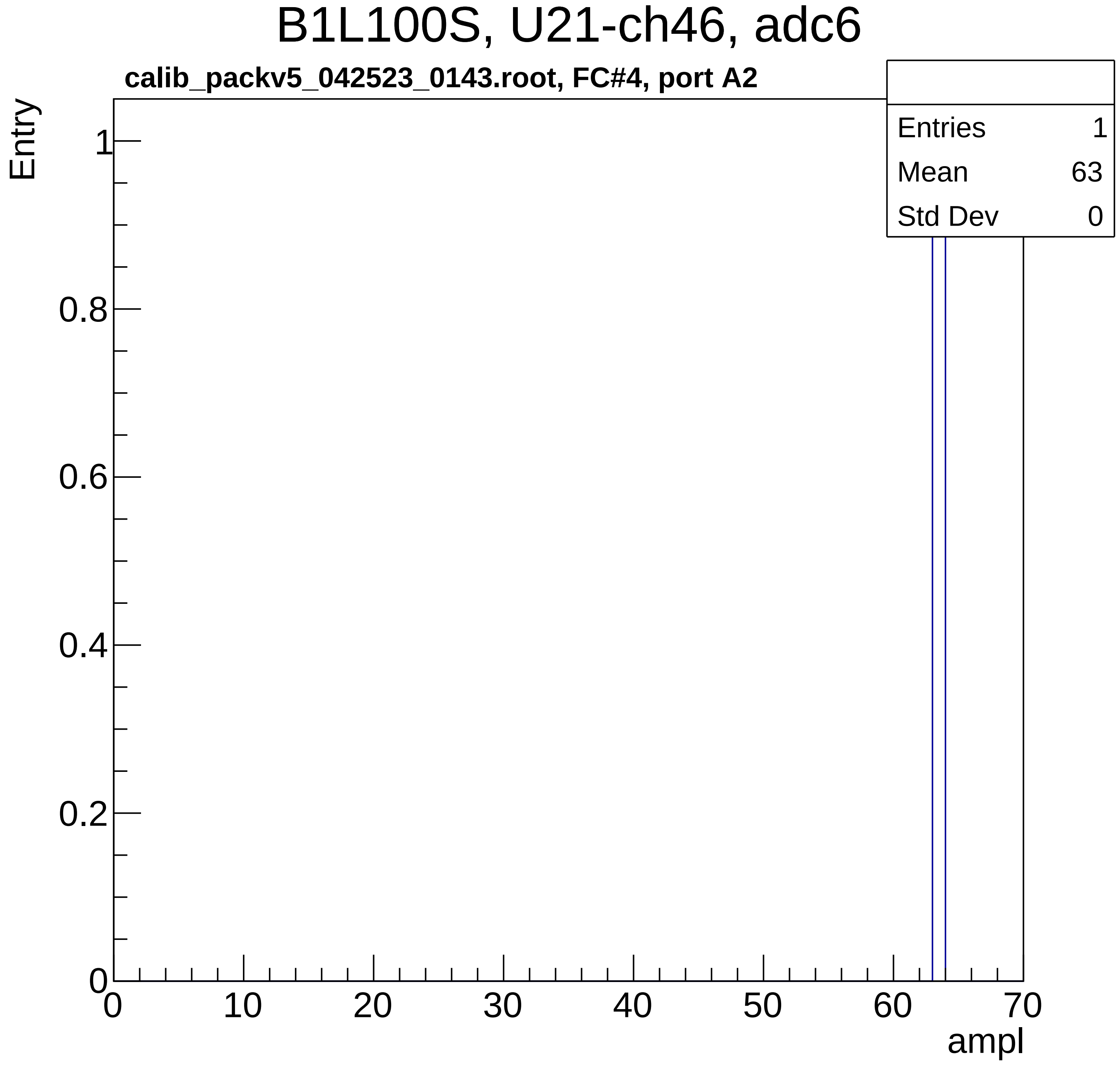

B1L100S, U21-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 48.65
Std Dev 2.81
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

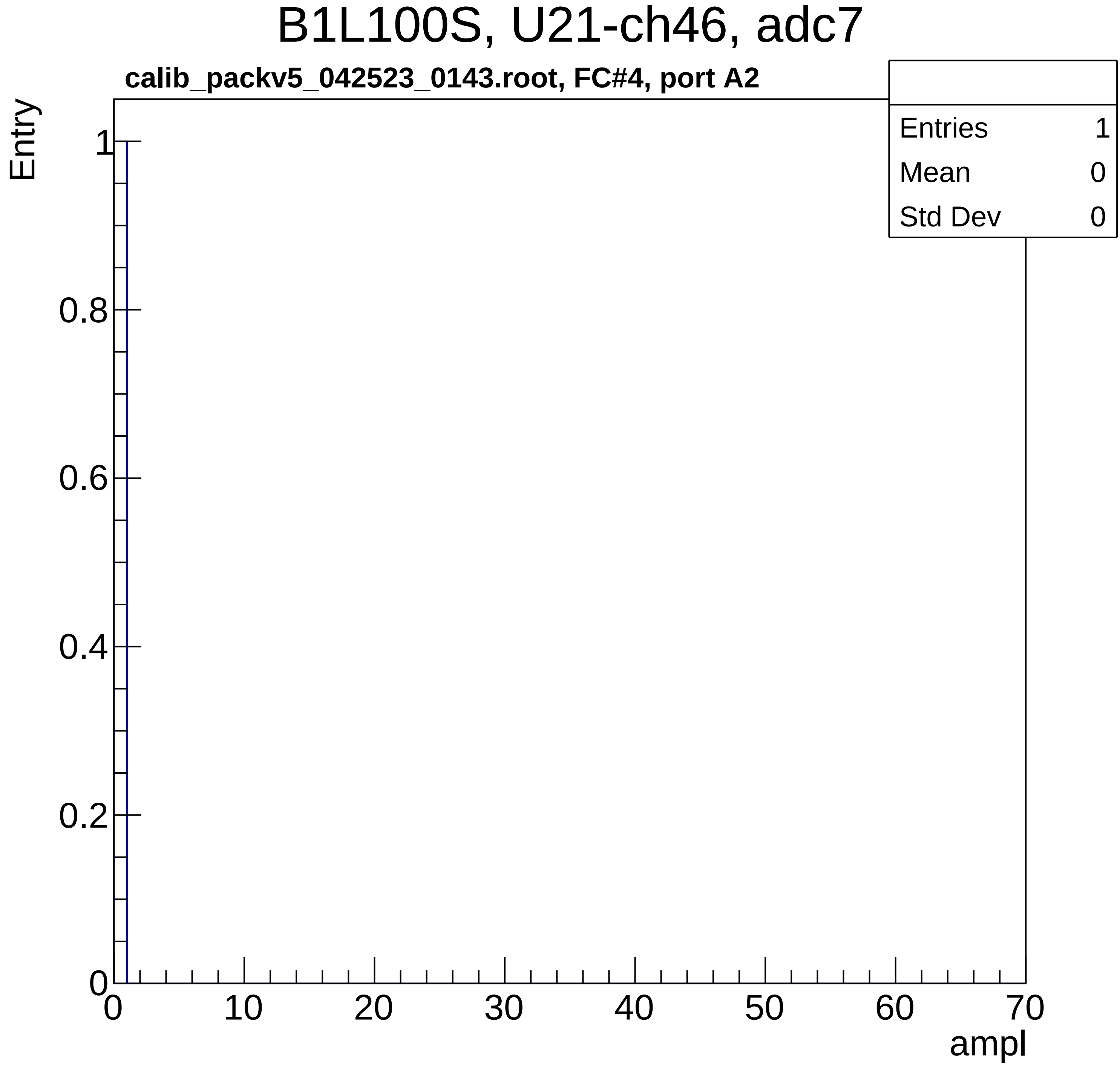
B1L100S, U21-ch46, adc4

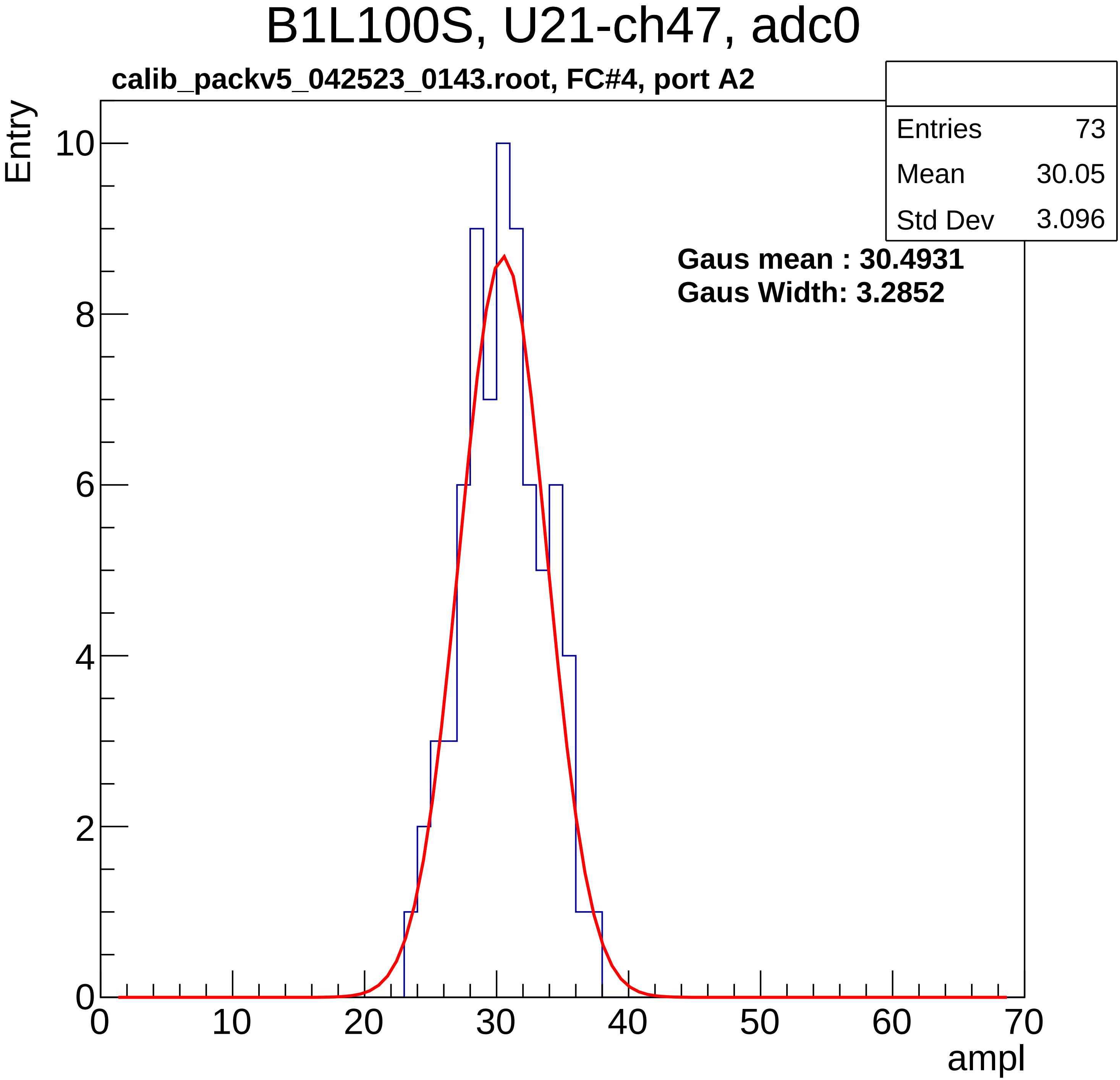
calib_packv5_042523_0143.root, FC#4, port A2

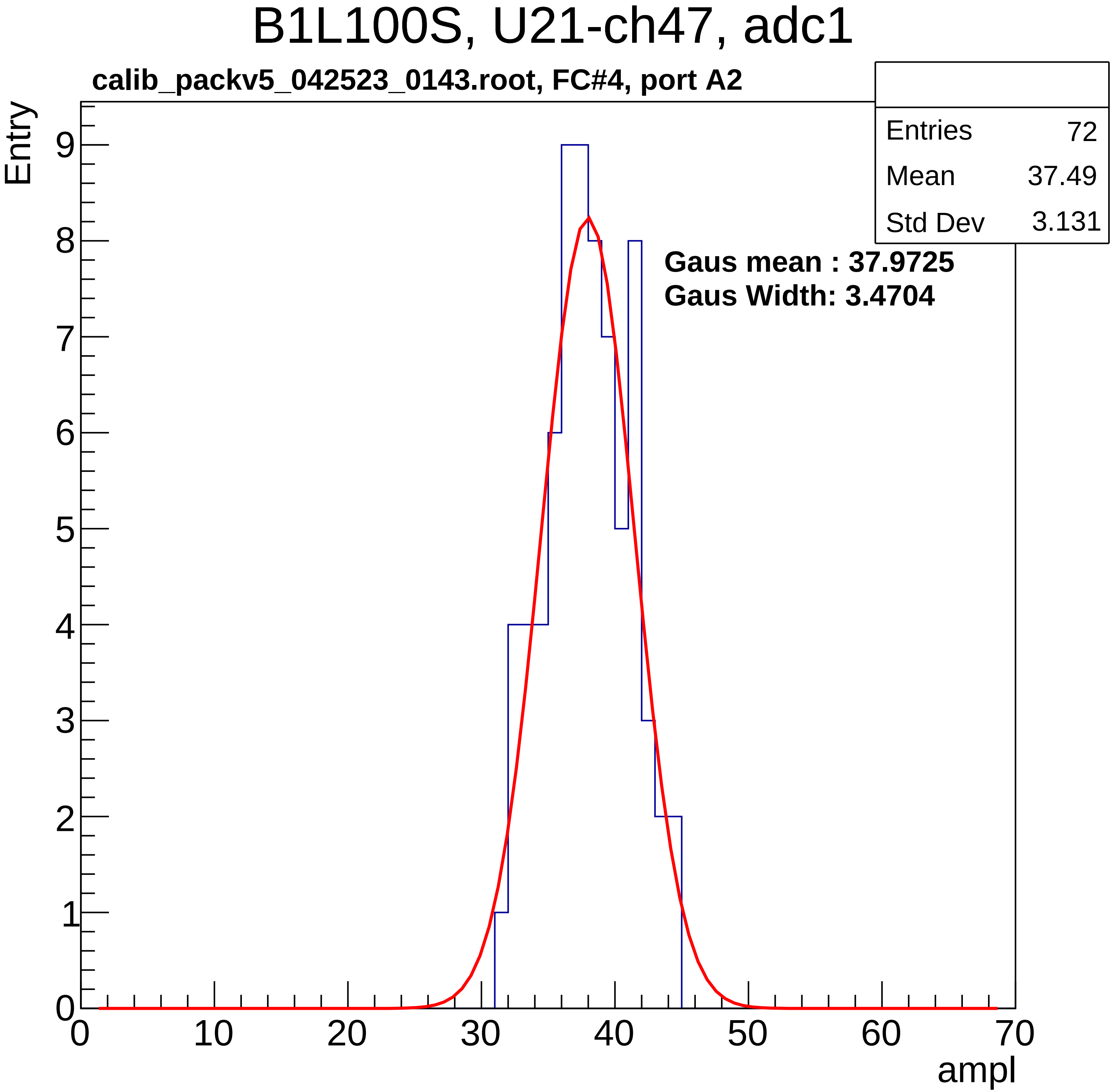


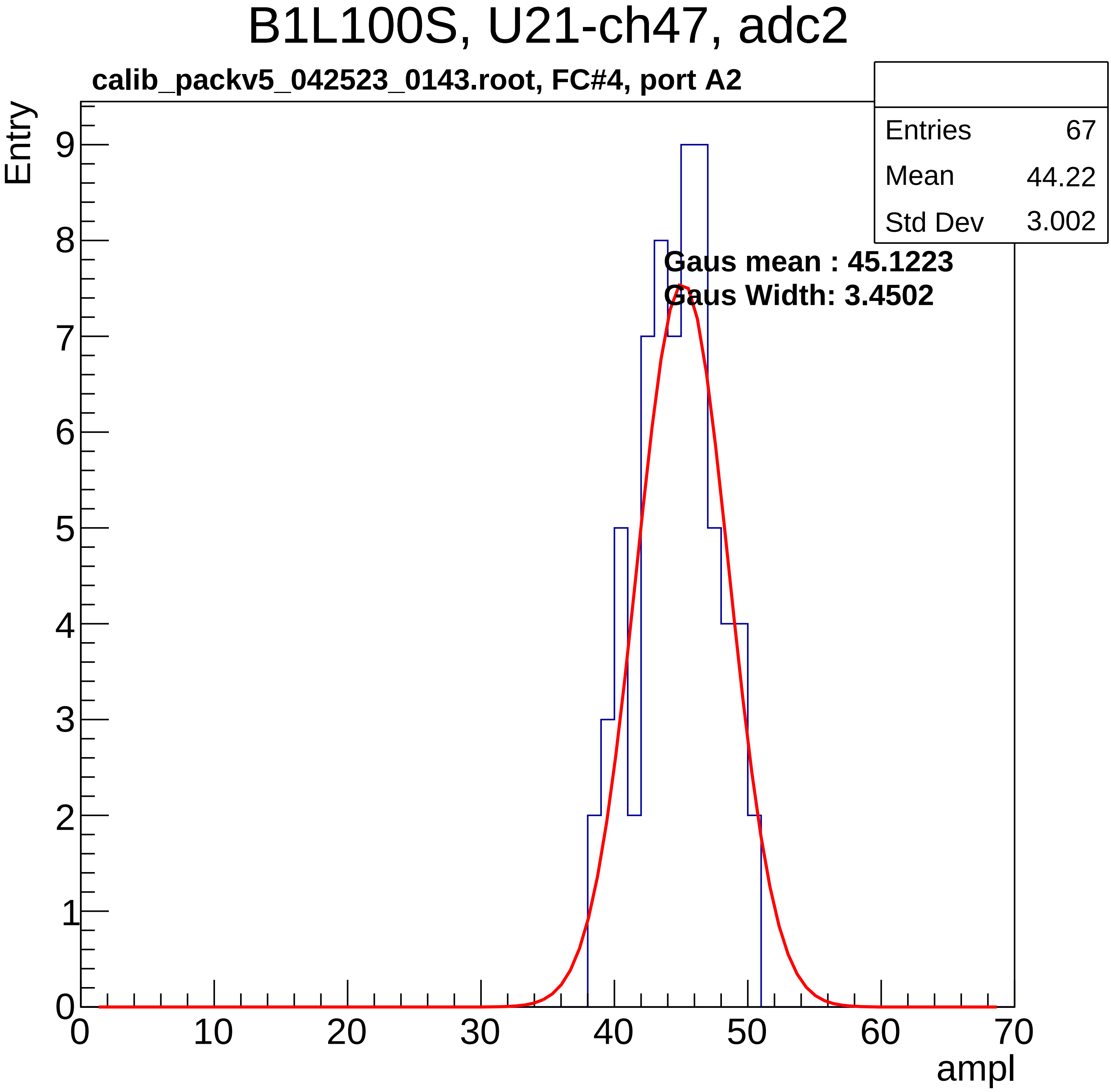

B1L100S, U21-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 53
Mean 58.85
Std Dev 8.559
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

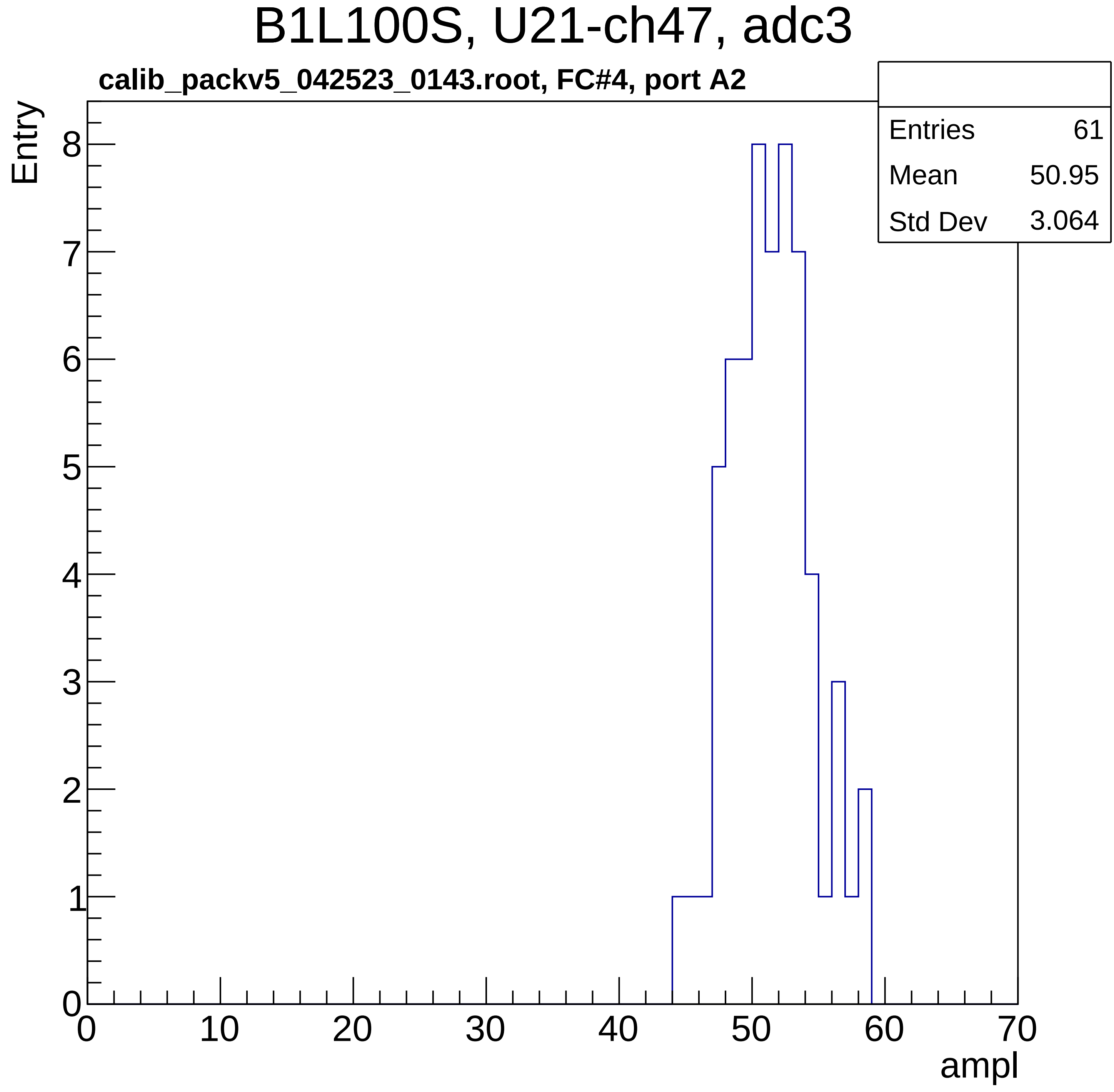

B1L100S, U21-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 1
Mean 63
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

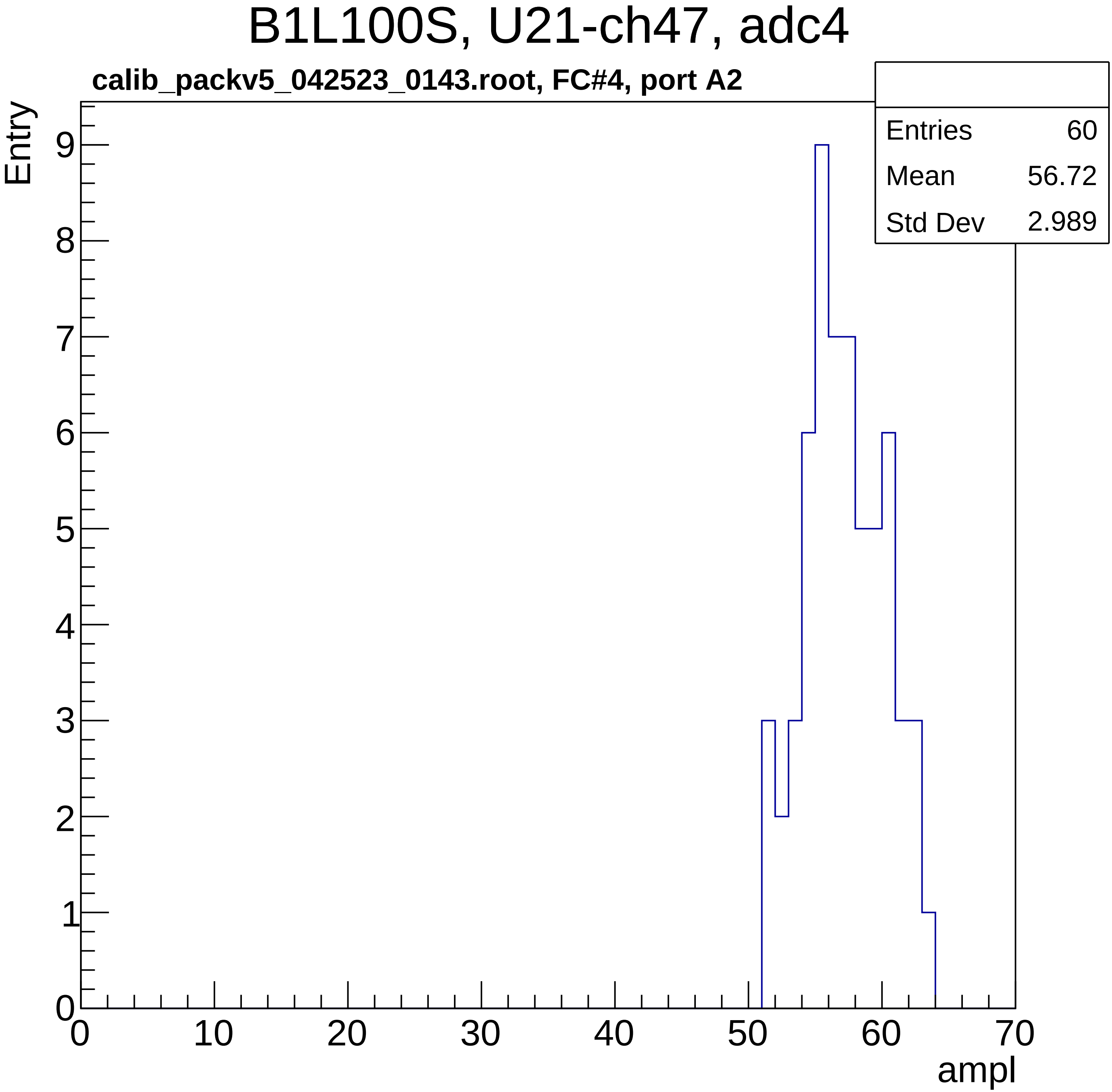

B1L100S, U21-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

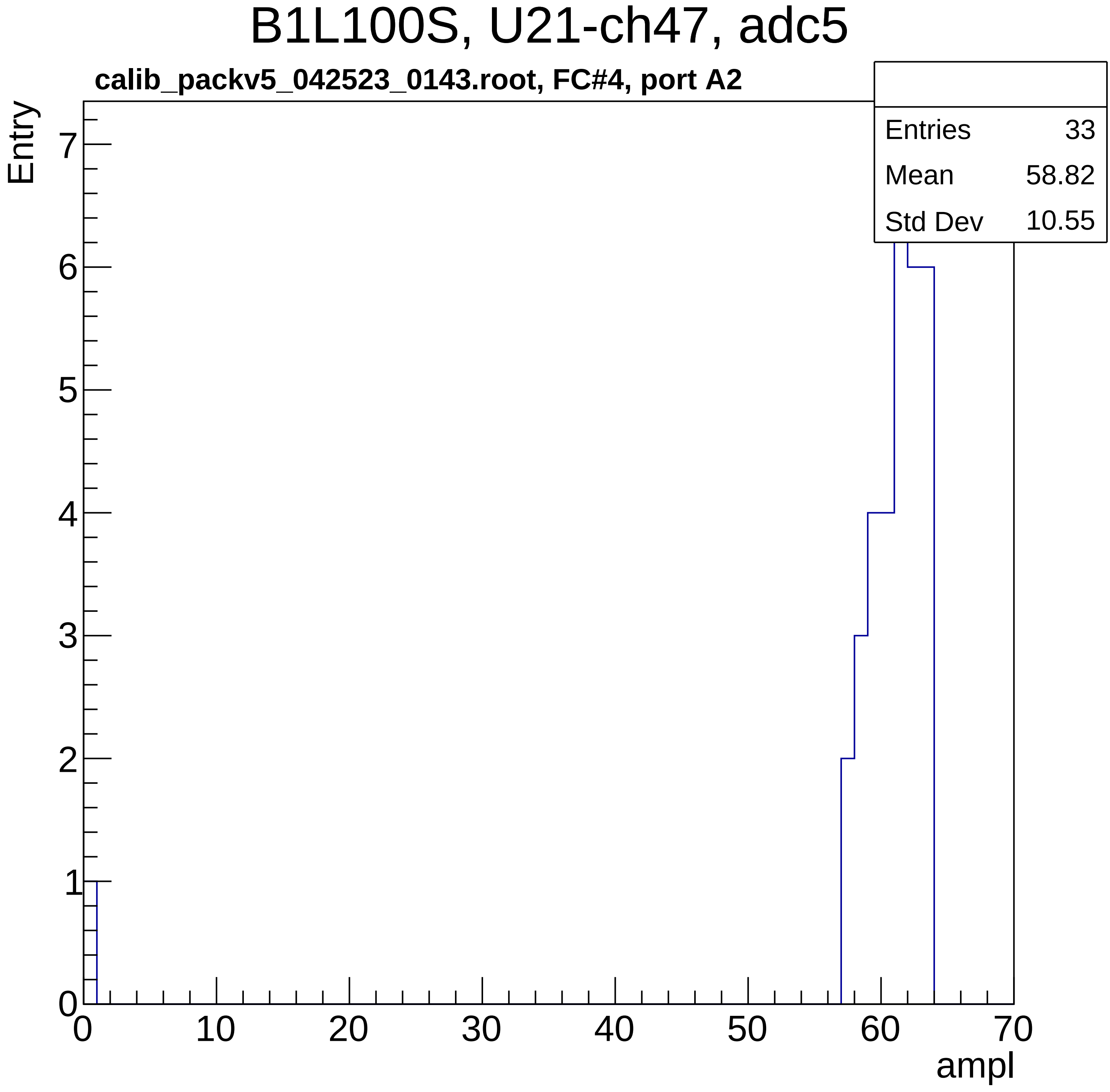

B1L100S, U21-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.05
Std Dev 3.096
Gaus mean : 30.4931
Gaus Width: 3.2852
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

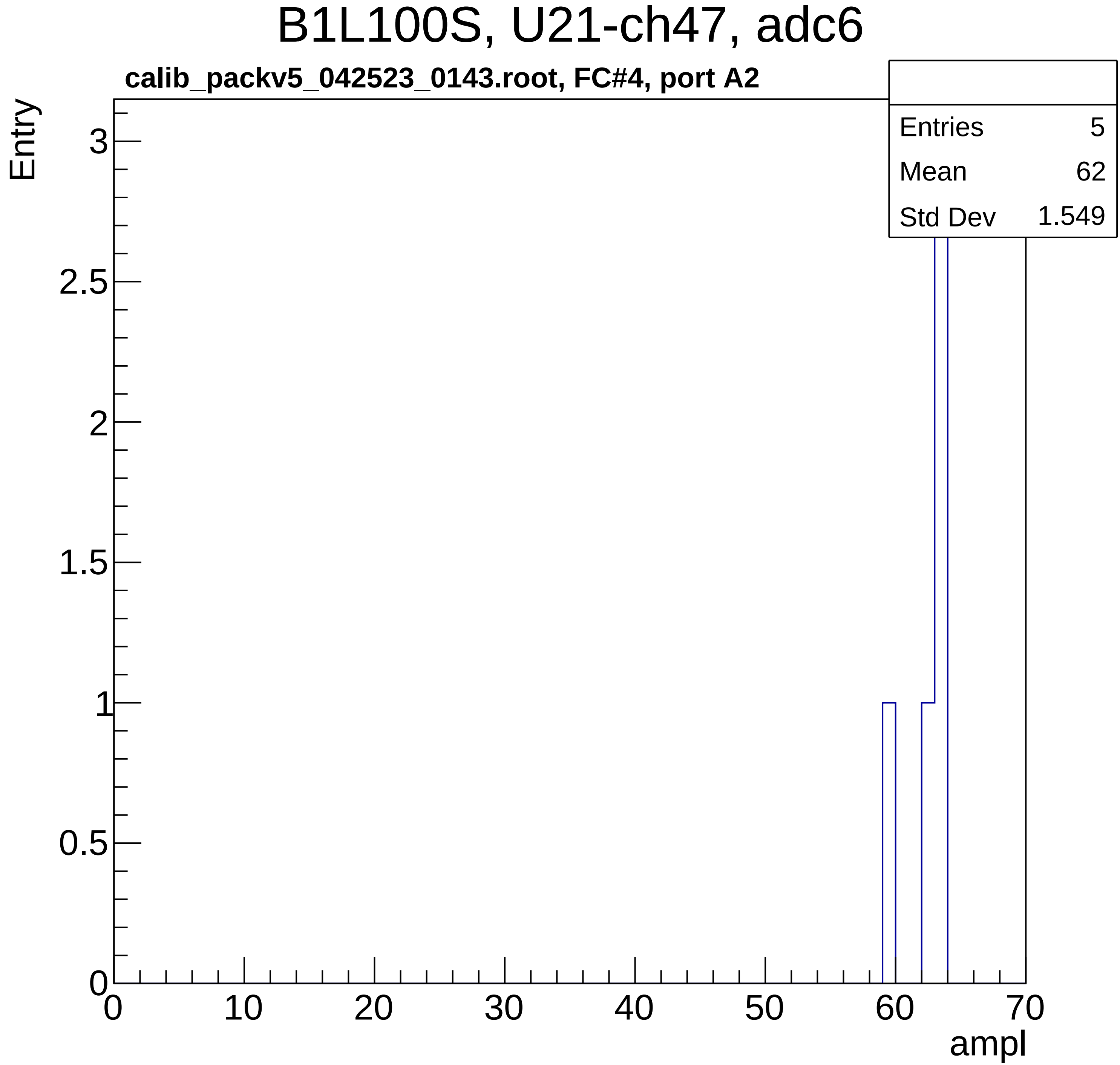

B1L100S, U21-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.49
Std Dev 3.131
Gaus mean : 37.9725
Gaus Width: 3.4704
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

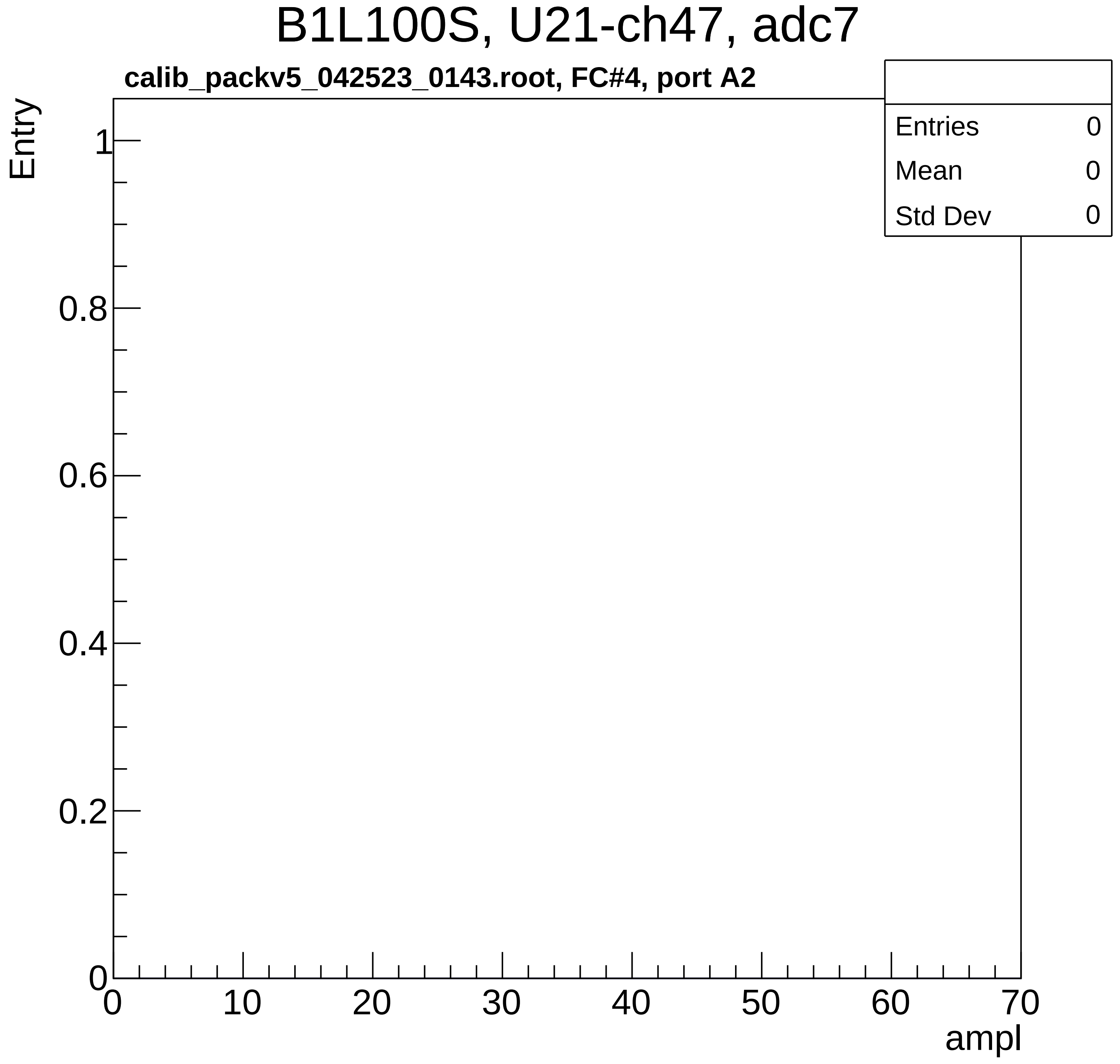

B1L100S, U21-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 67
Mean 44.22
Std Dev 3.002
Gaus mean : 45.1223
Gaus Width: 3.4502
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

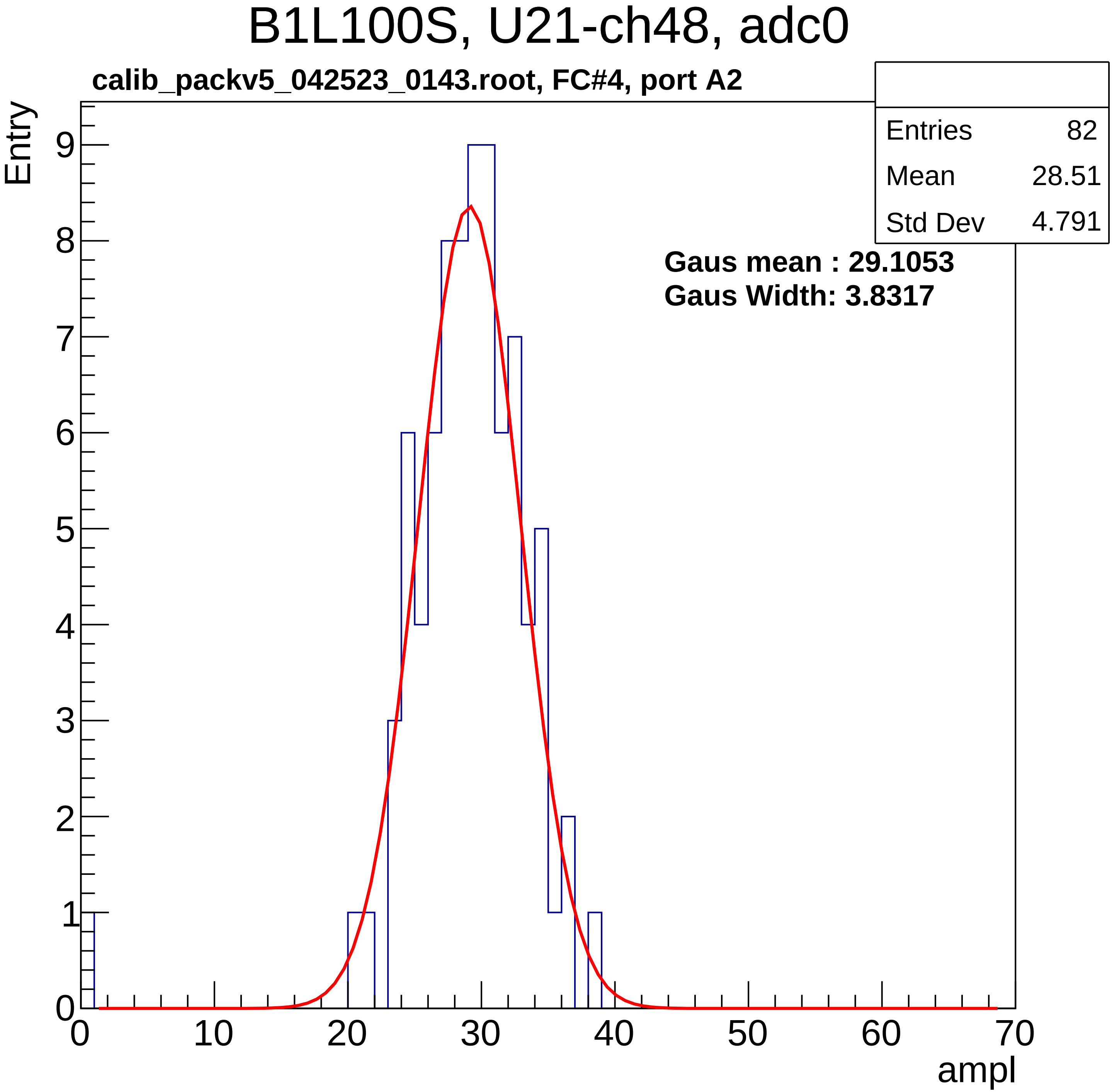

B1L100S, U21-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.95
Std Dev 3.064
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

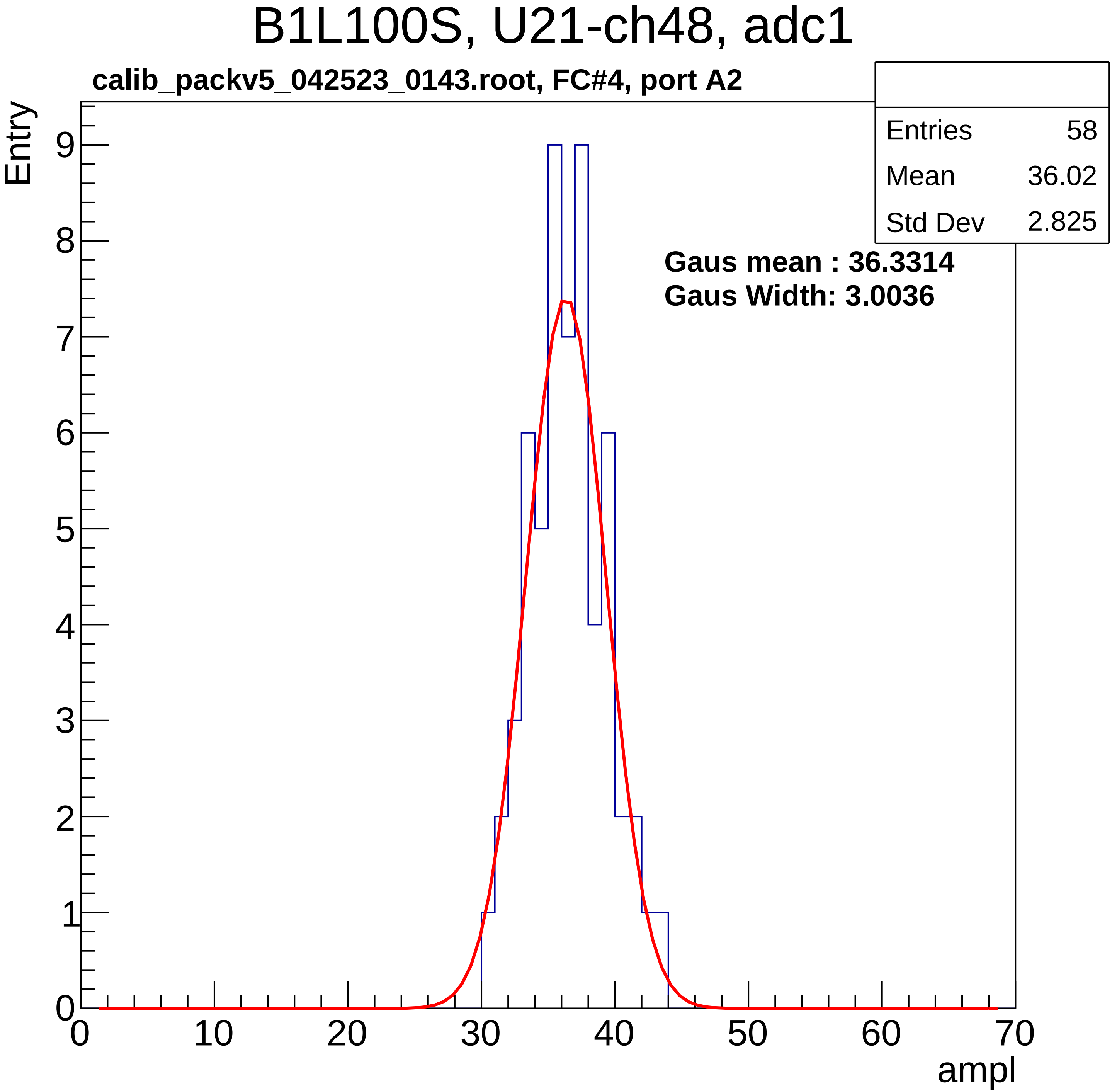

B1L100S, U21-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.72
Std Dev 2.989
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.82
Std Dev 10.55
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

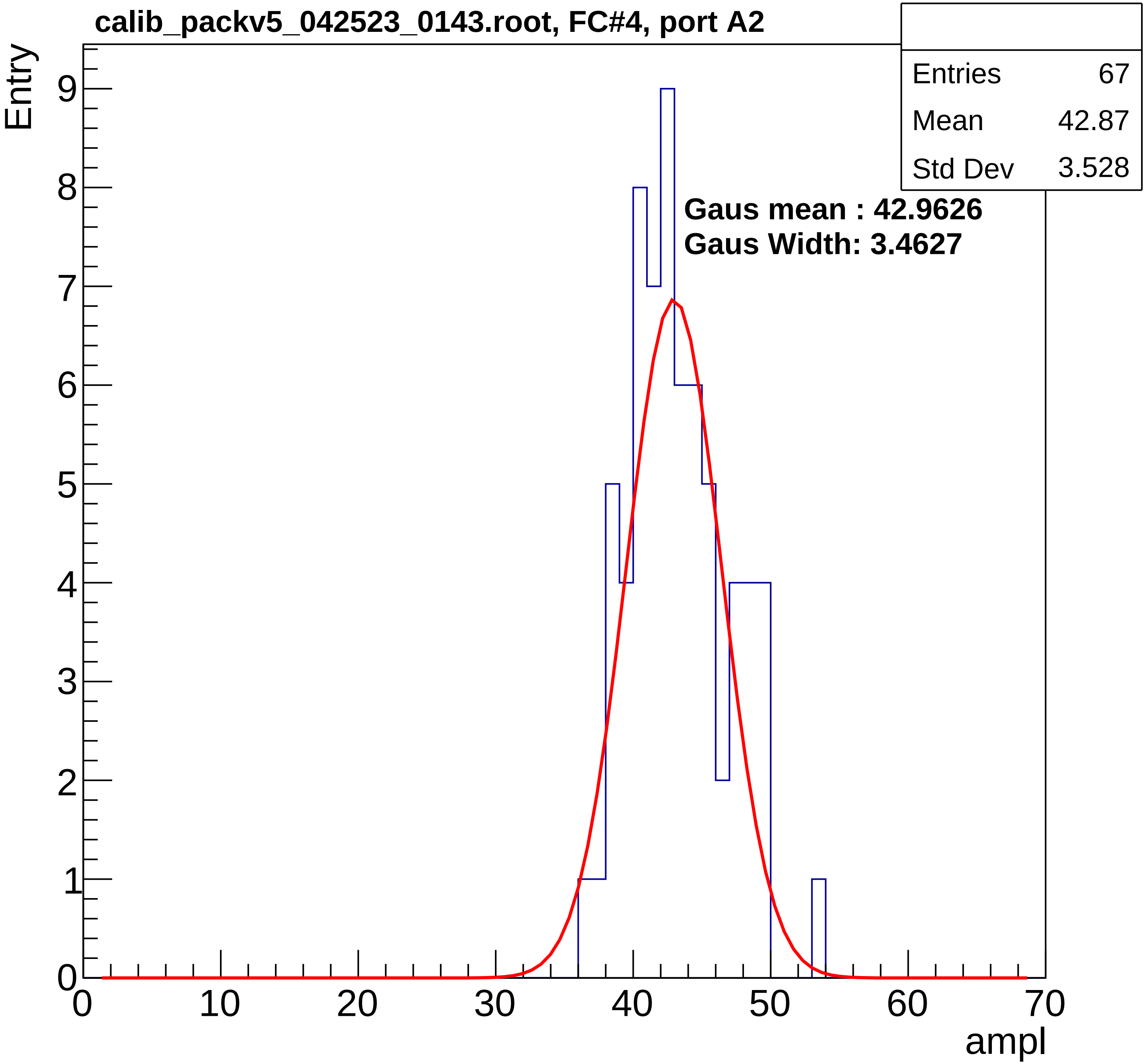

B1L100S, U21-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 5
Mean 62
Std Dev 1.549
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

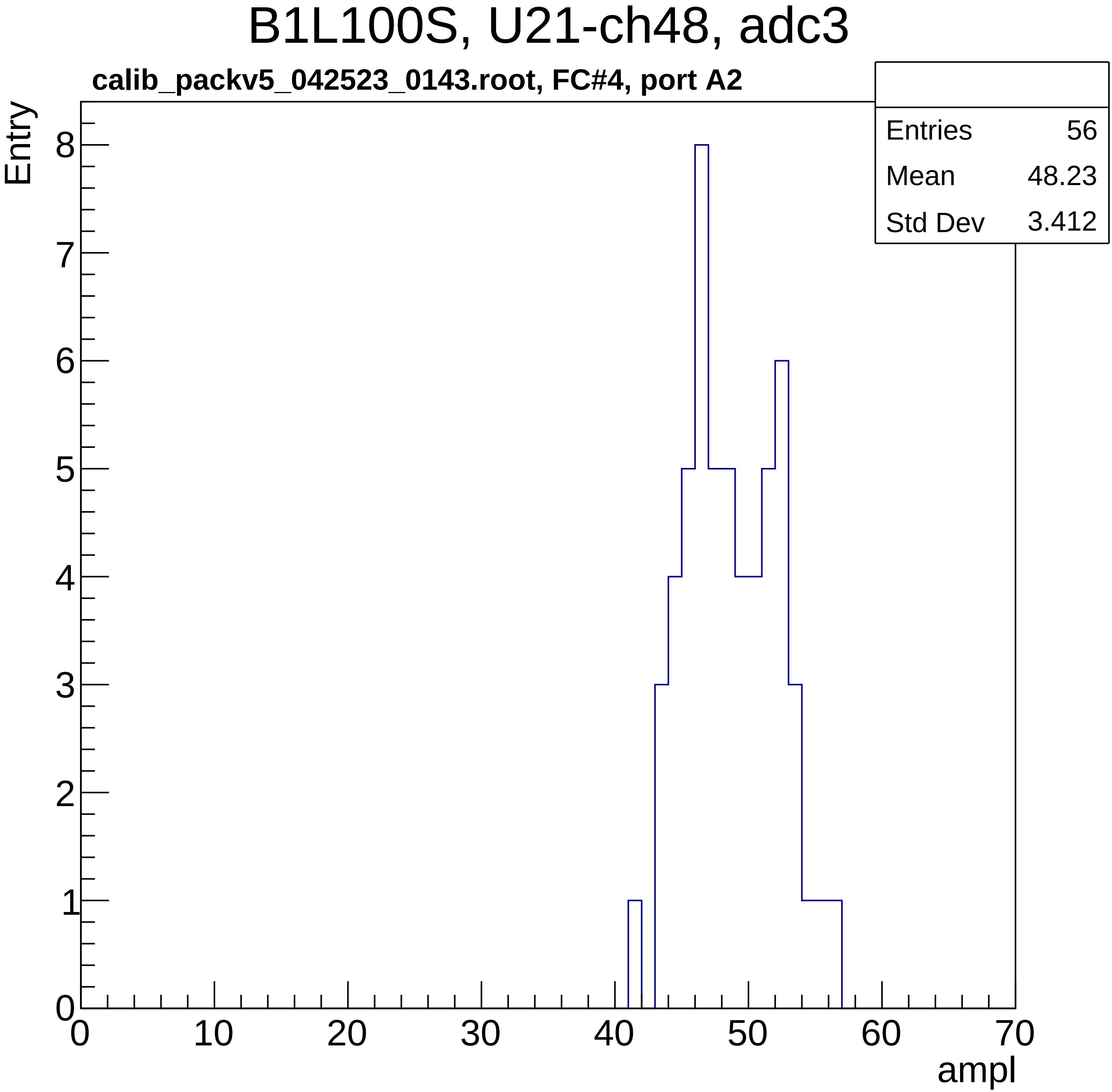

B1L100S, U21-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl
Entries 0
Mean 0
Std Dev 0

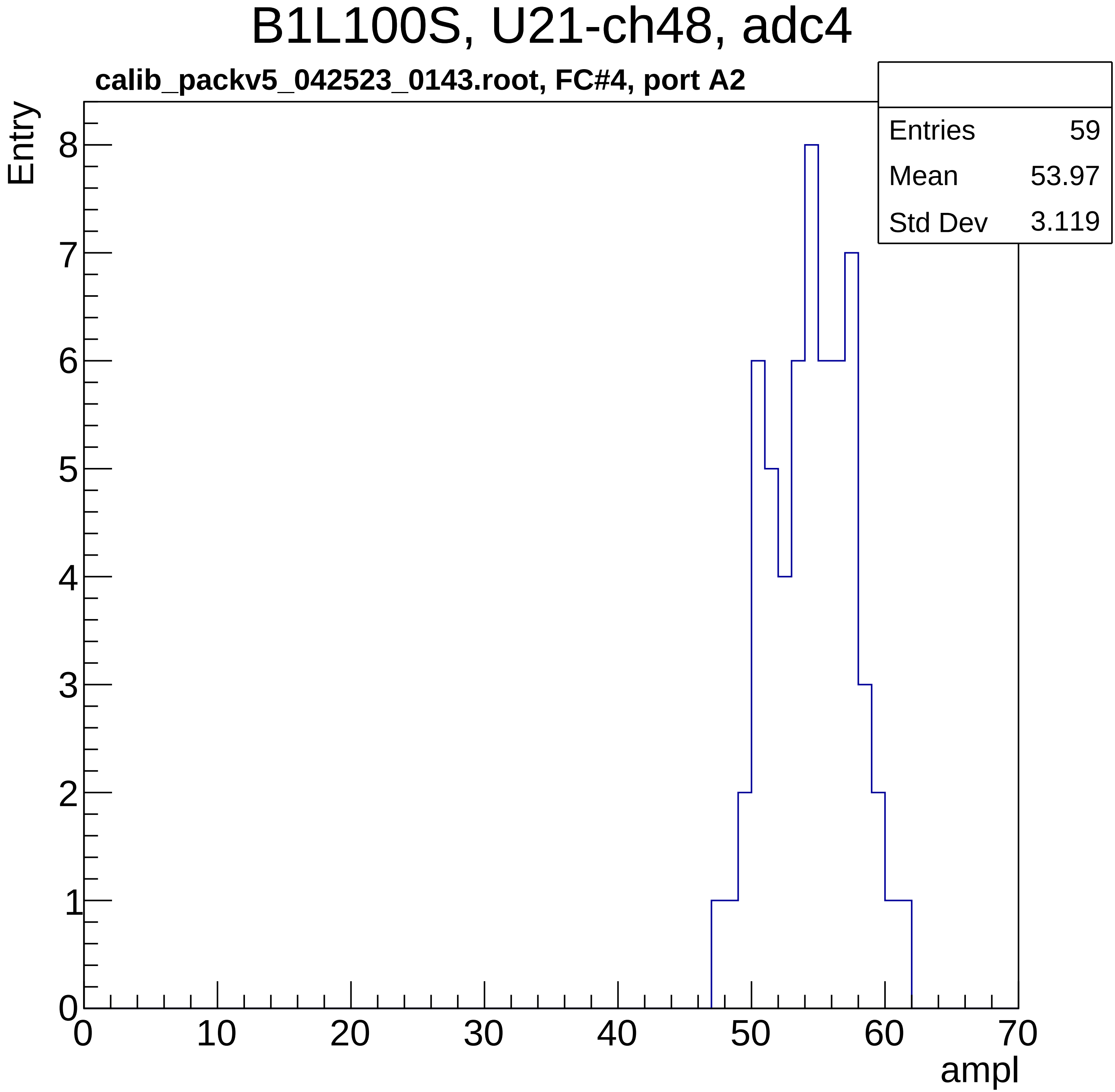

B1L100S, U21-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 28.51
Std Dev 4.791
Gaus mean : 29.1053
Gaus Width: 3.8317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

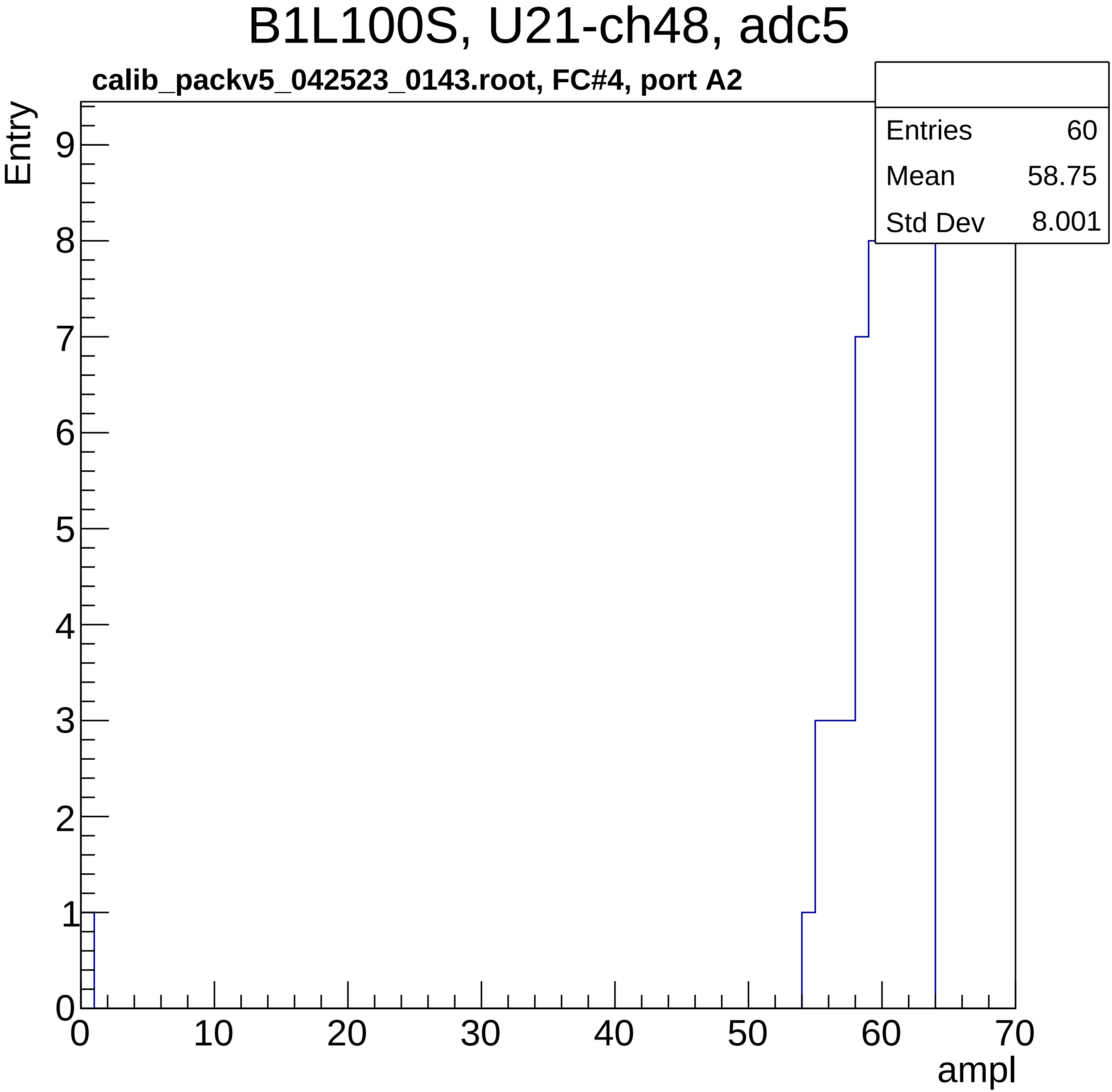

B1L100S, U21-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 36.02
Std Dev 2.825
Gaus mean : 36.3314
Gaus Width: 3.0036
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch48, adc2

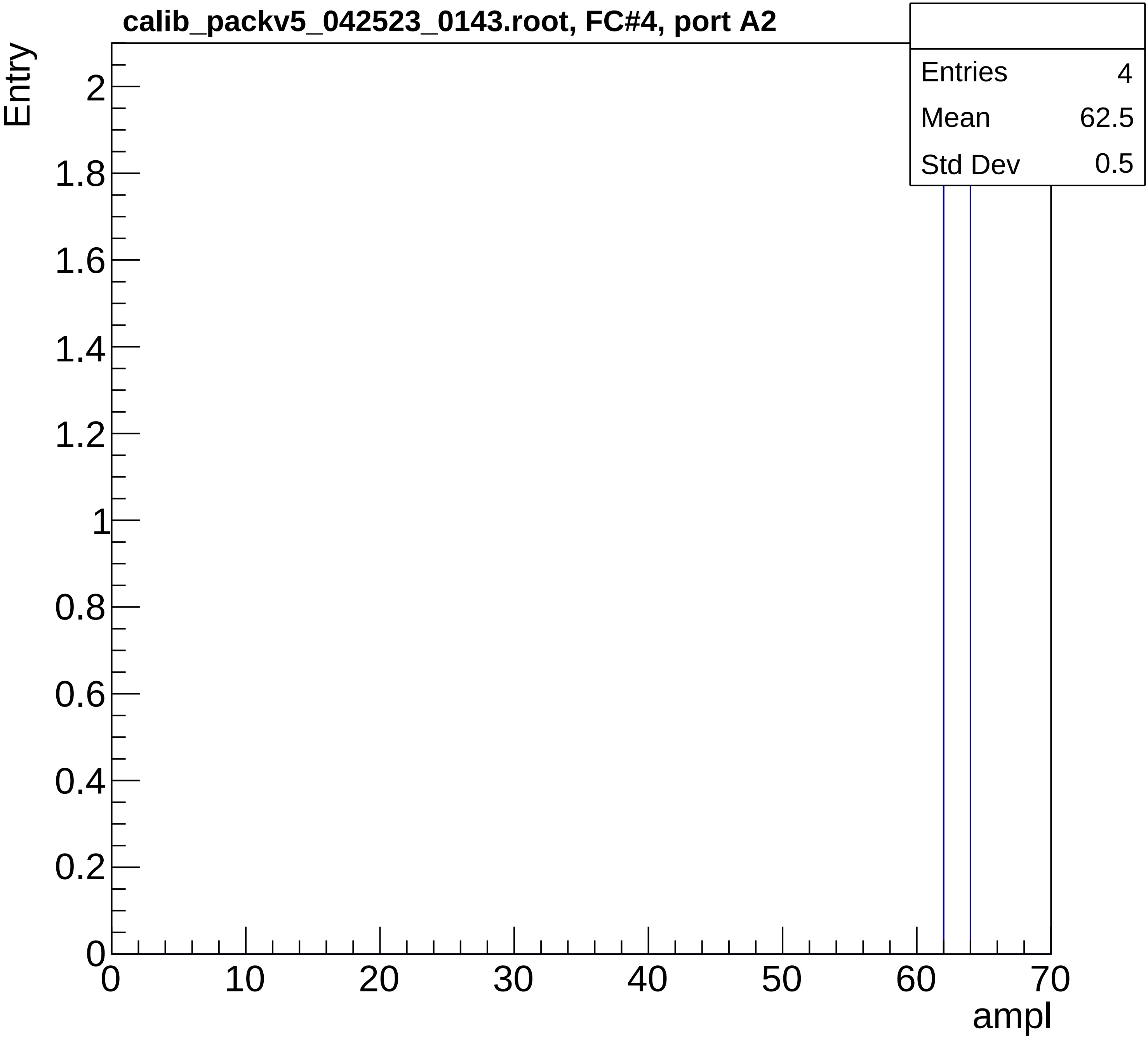



B1L100S, U21-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 56
Mean 48.23
Std Dev 3.412
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

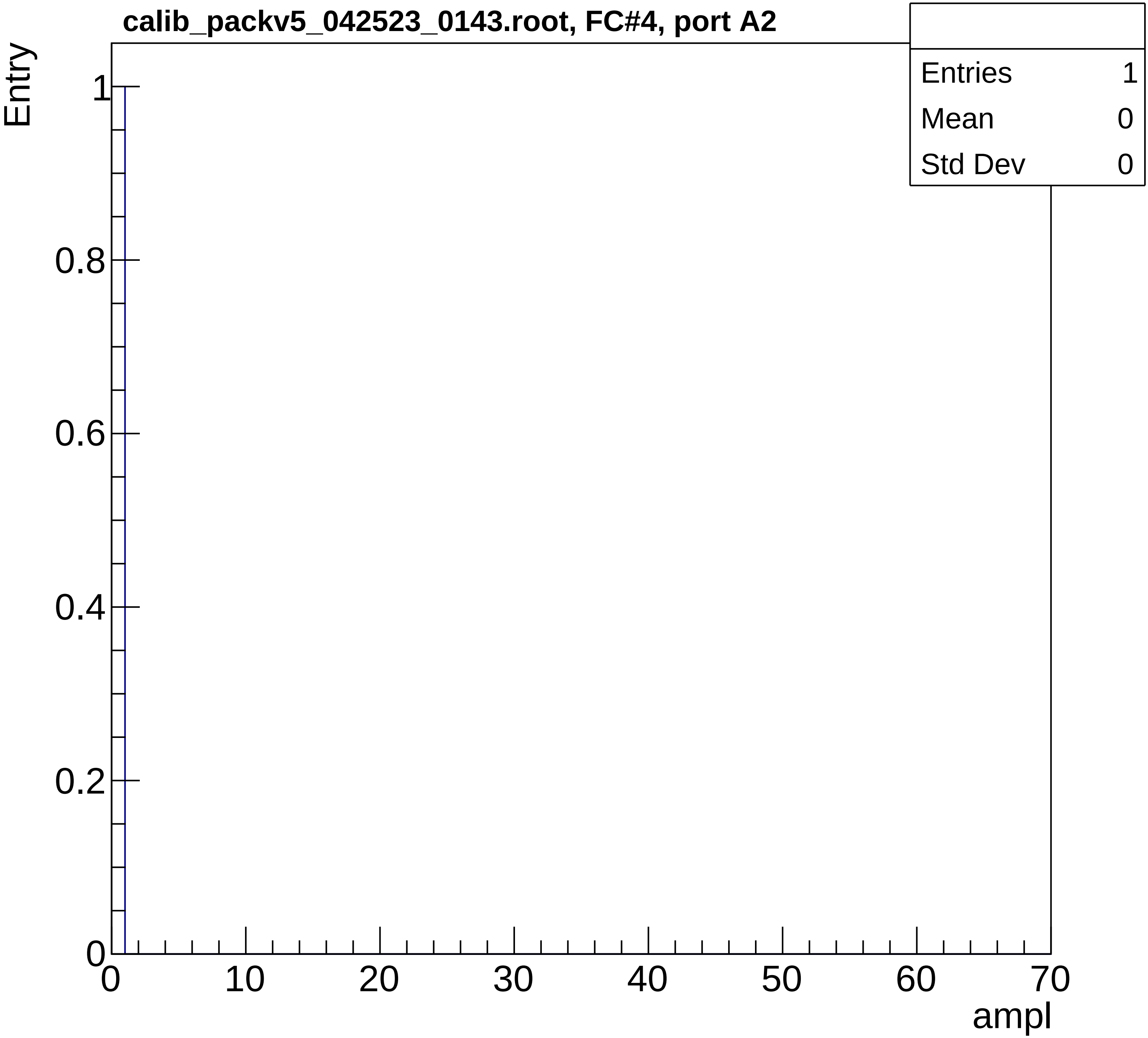

B1L100S, U21-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 53.97
Std Dev 3.119
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 58.75
Std Dev 8.001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

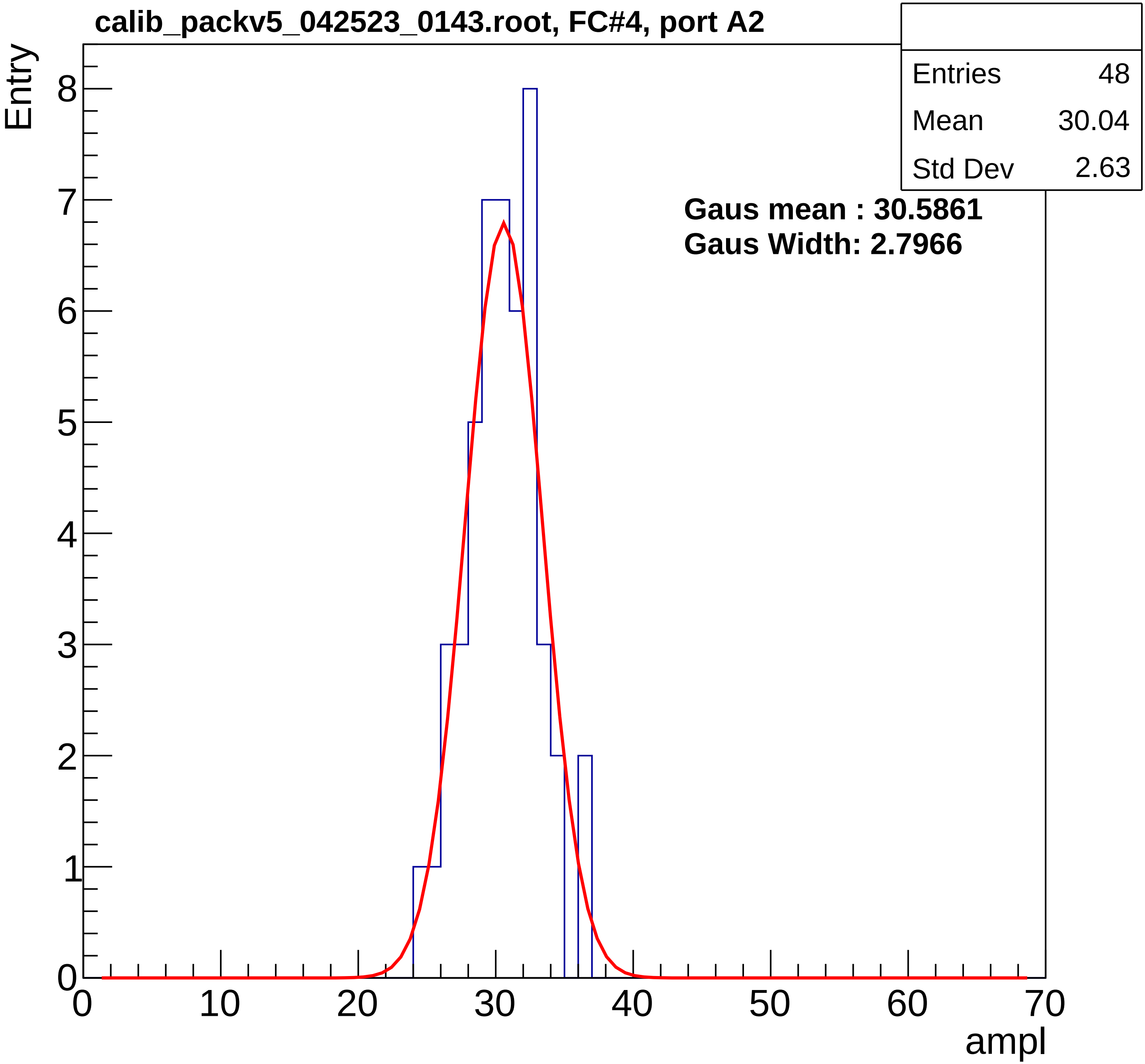
B1L100S, U21-ch48, adc6

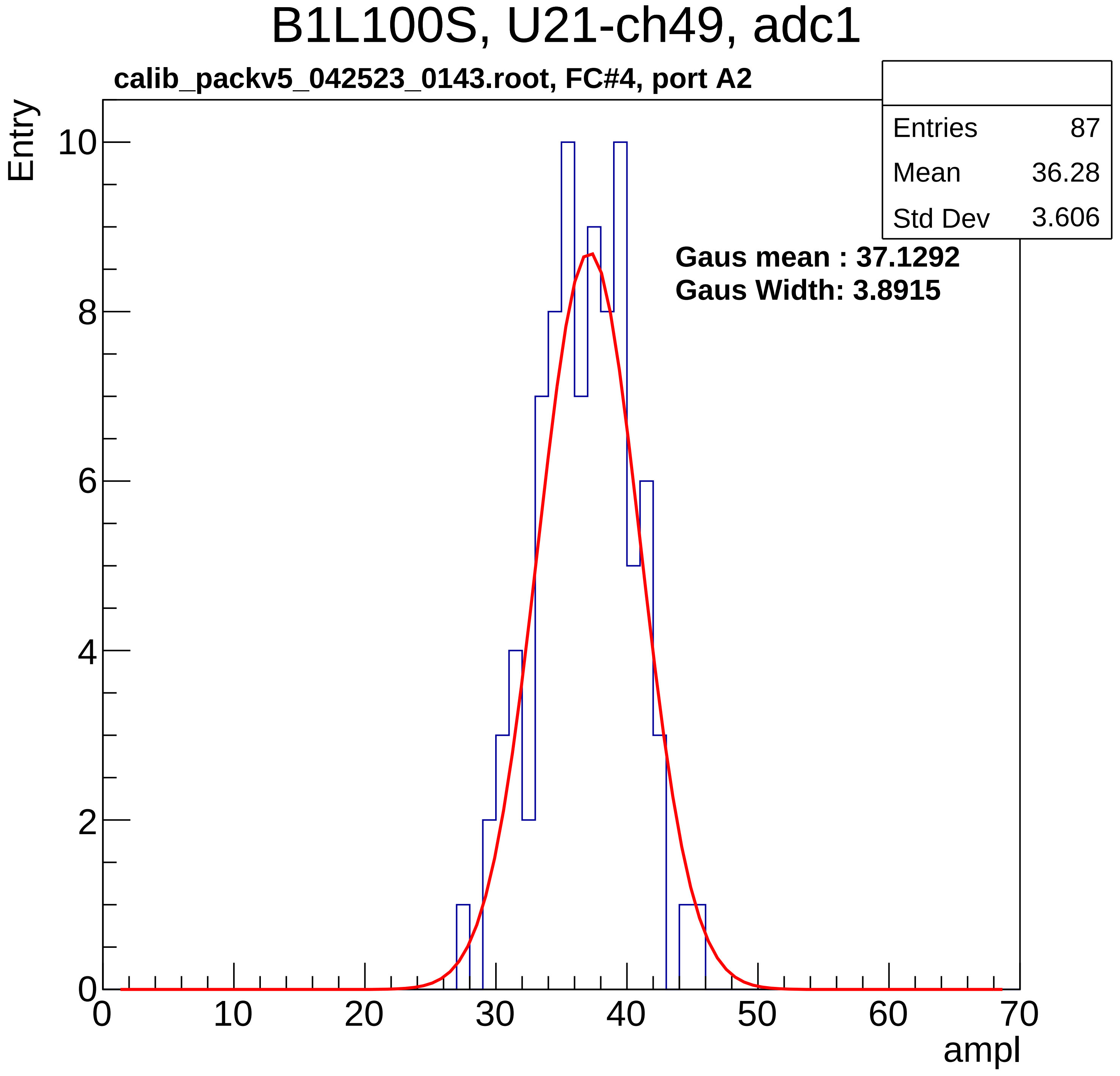


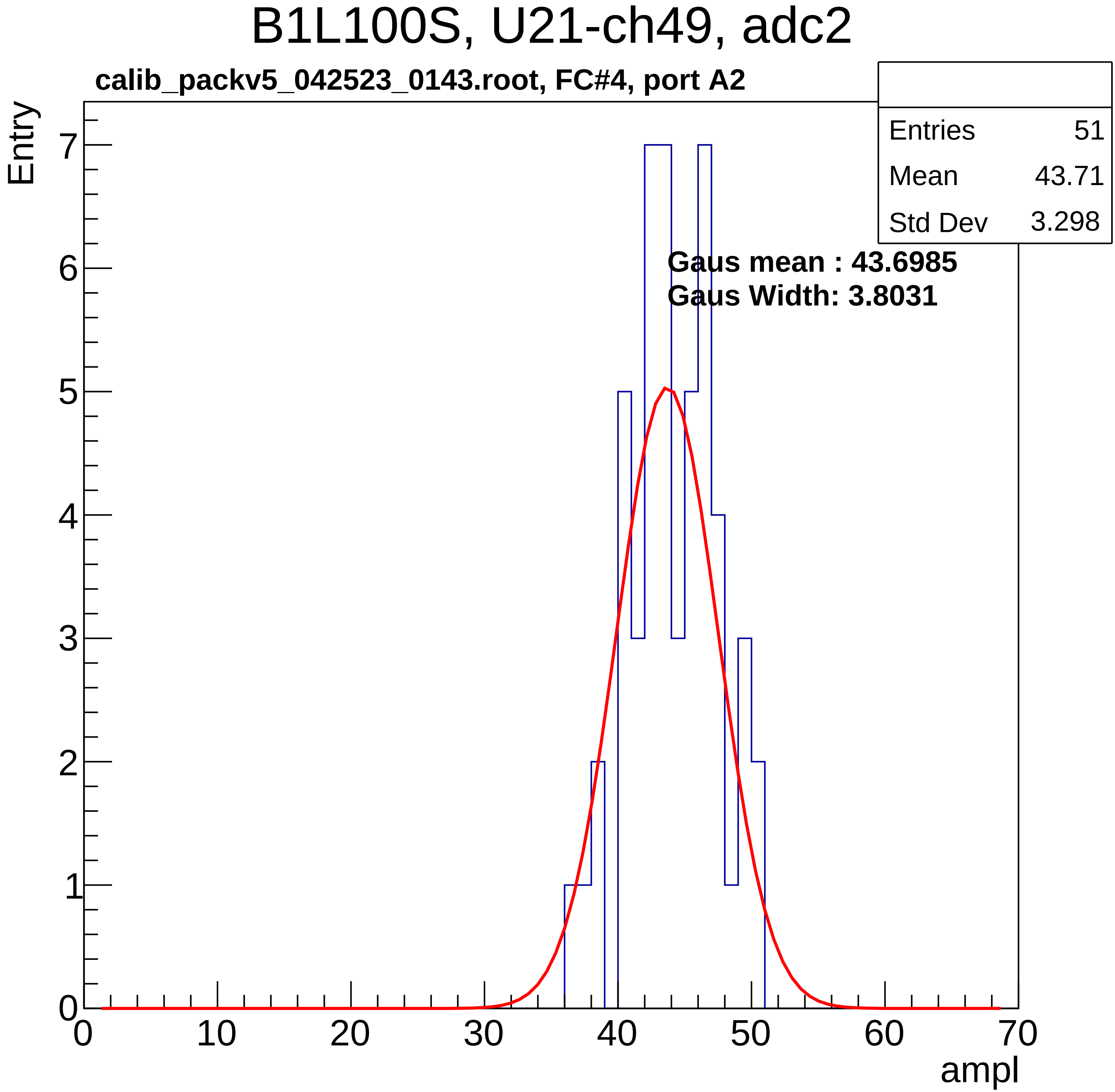
B1L100S, U21-ch48, adc7

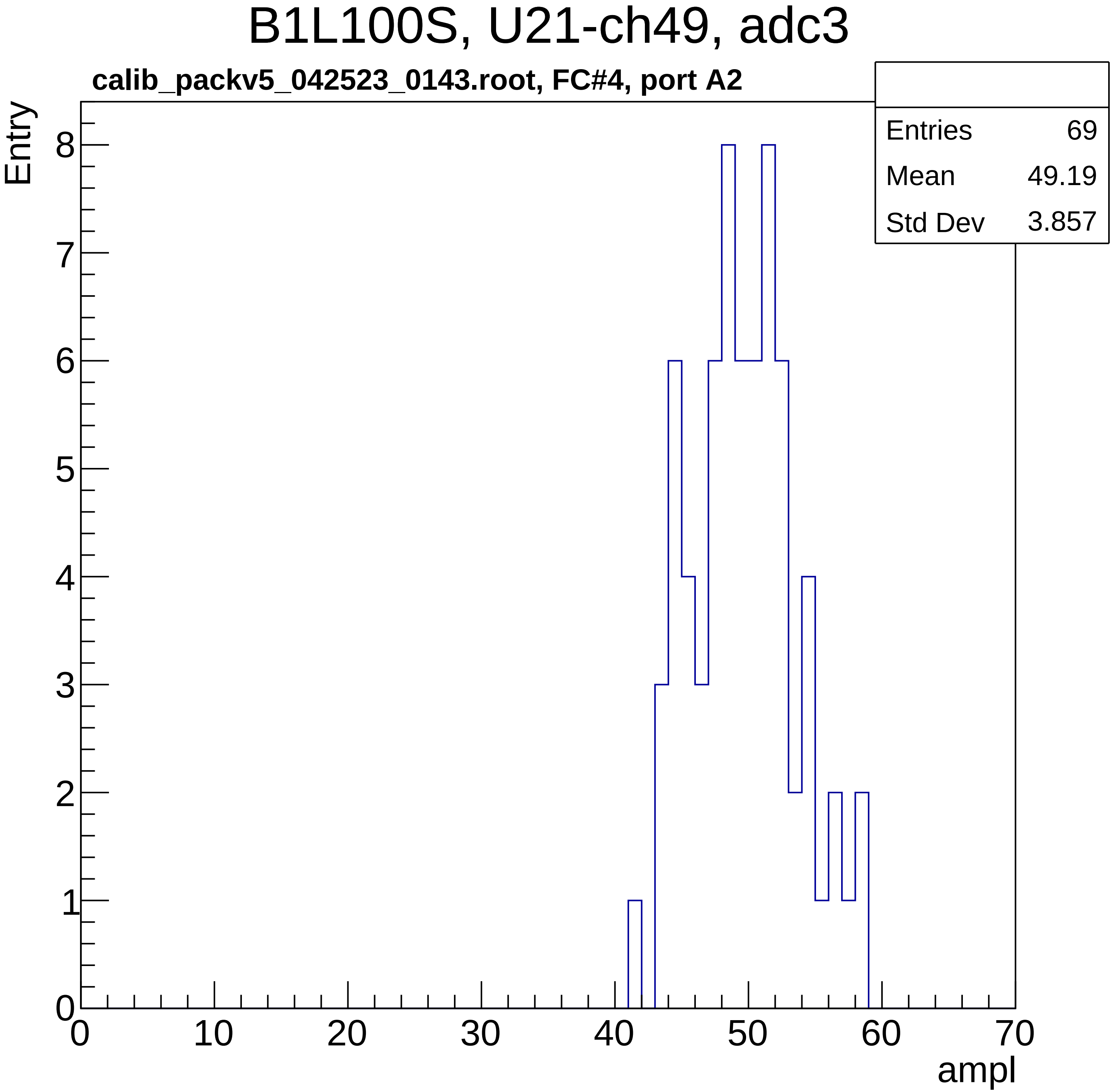


B1L100S, U21-ch49, adc0



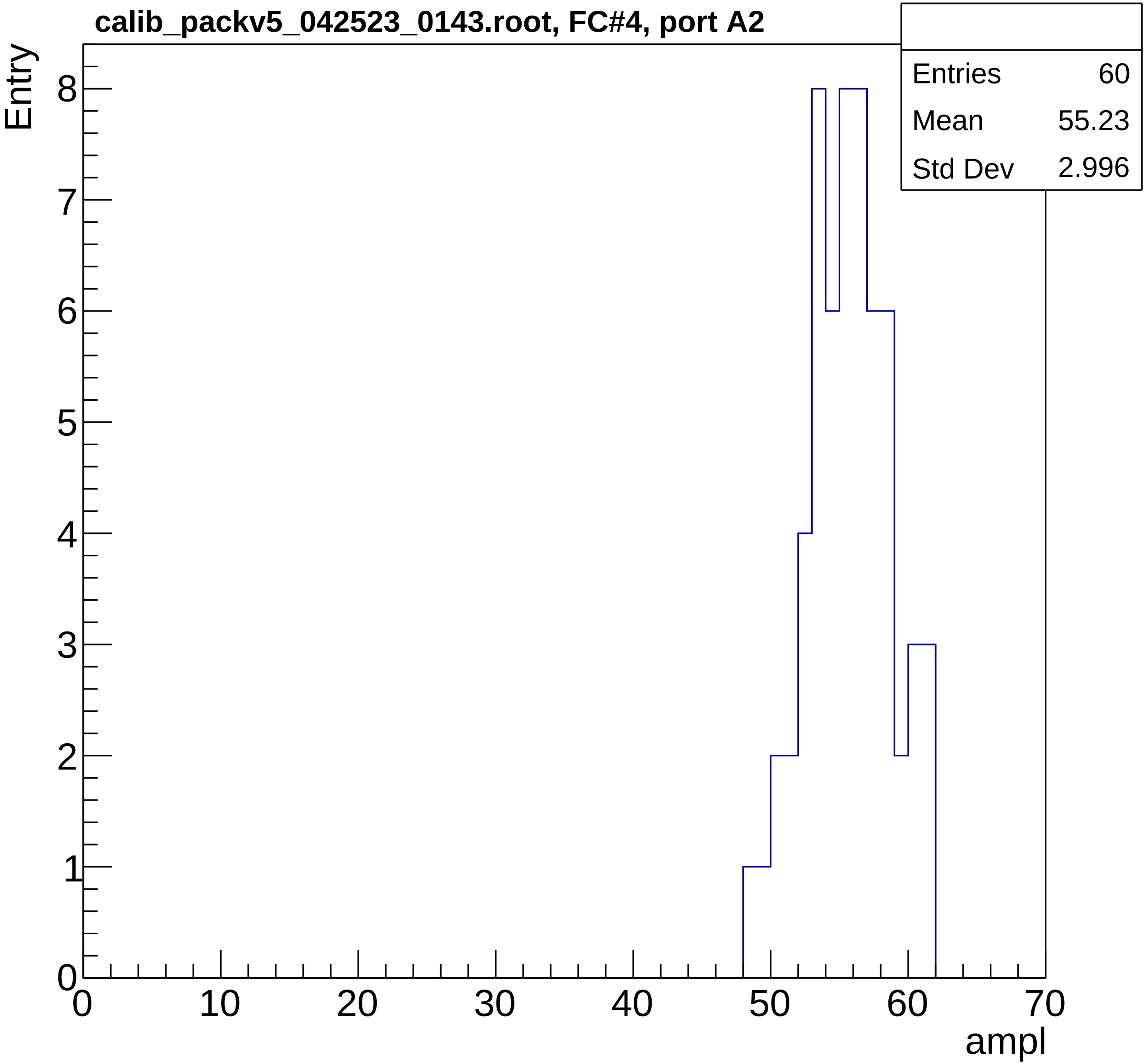

B1L100S, U21-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 36.28
Std Dev 3.606
Gaus mean : 37.1292
Gaus Width: 3.8915
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 43.71
Std Dev 3.298
Gaus mean : 43.6985
Gaus Width: 3.8031
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 69
Mean 49.19
Std Dev 3.857
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

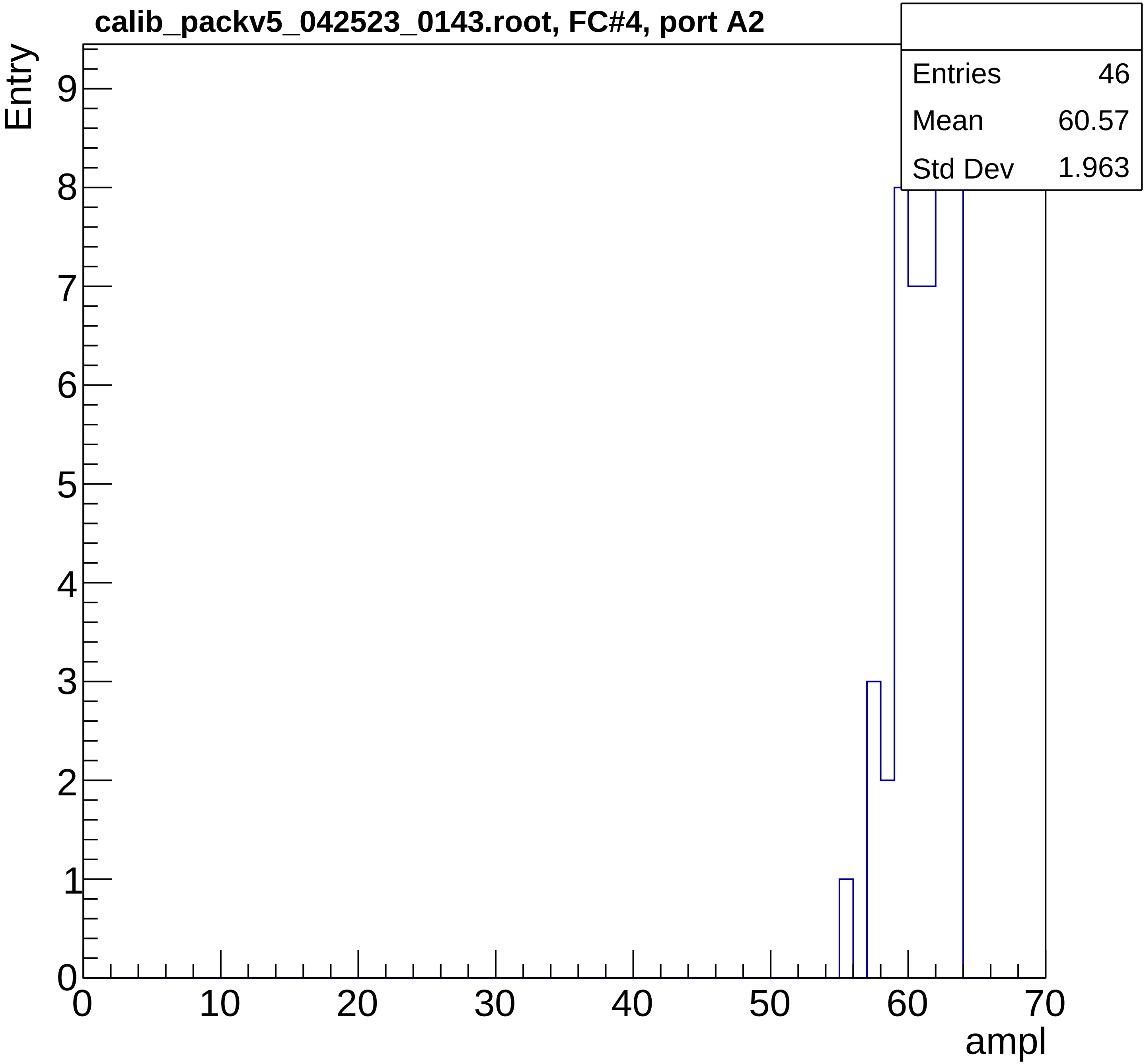
B1L100S, U21-ch49, adc4

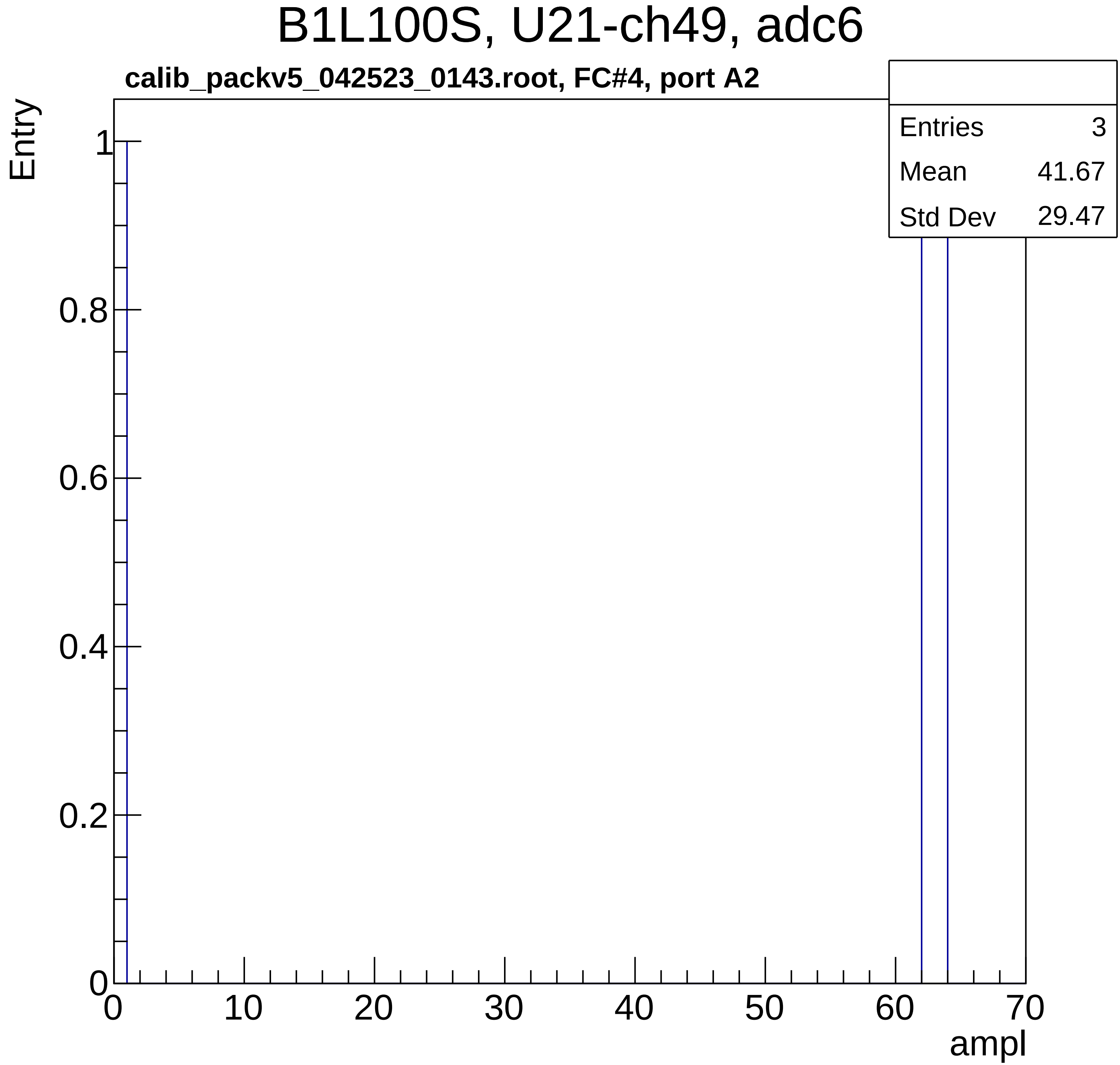
calib_packv5_042523_0143.root, FC#4, port A2

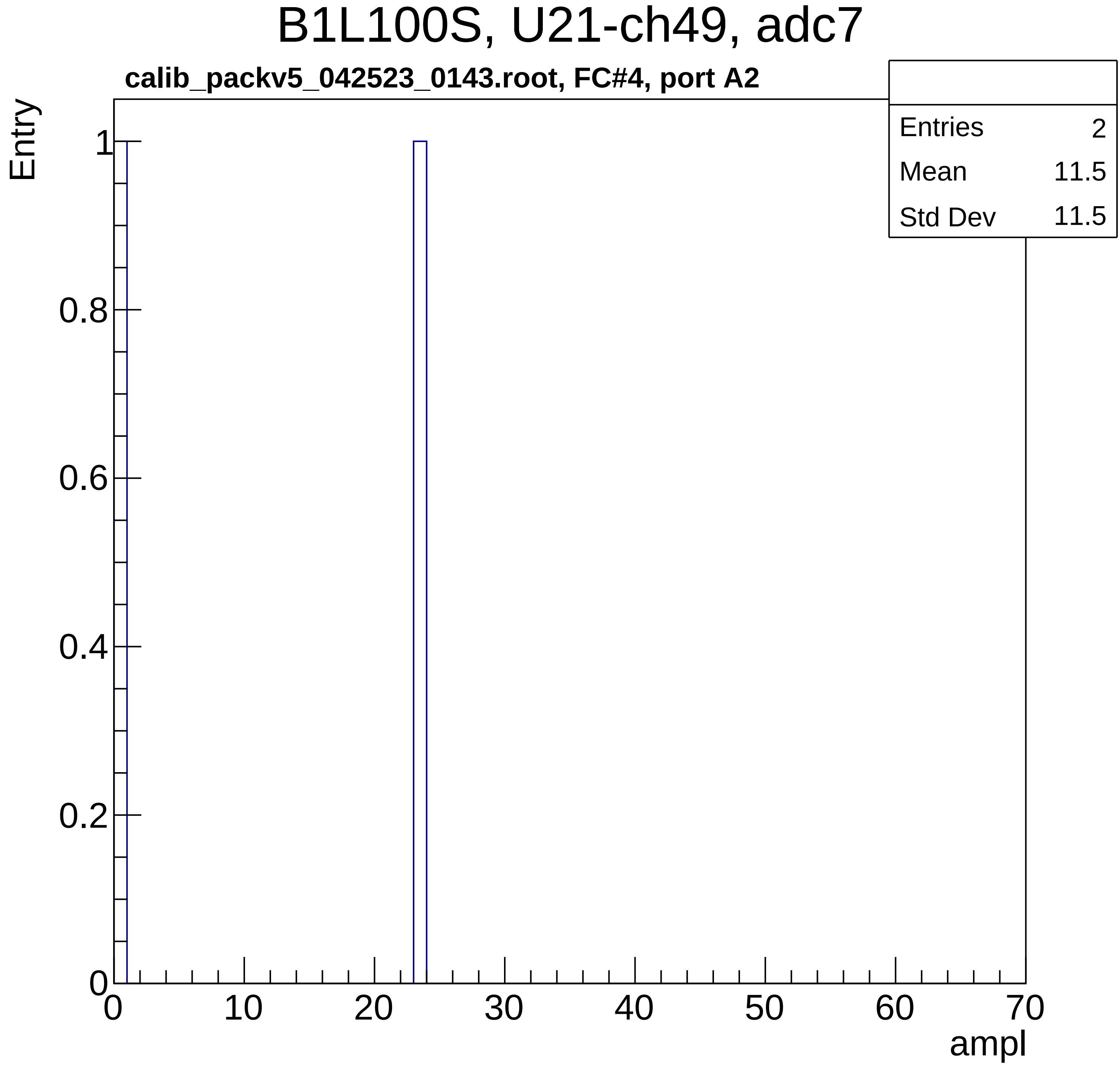


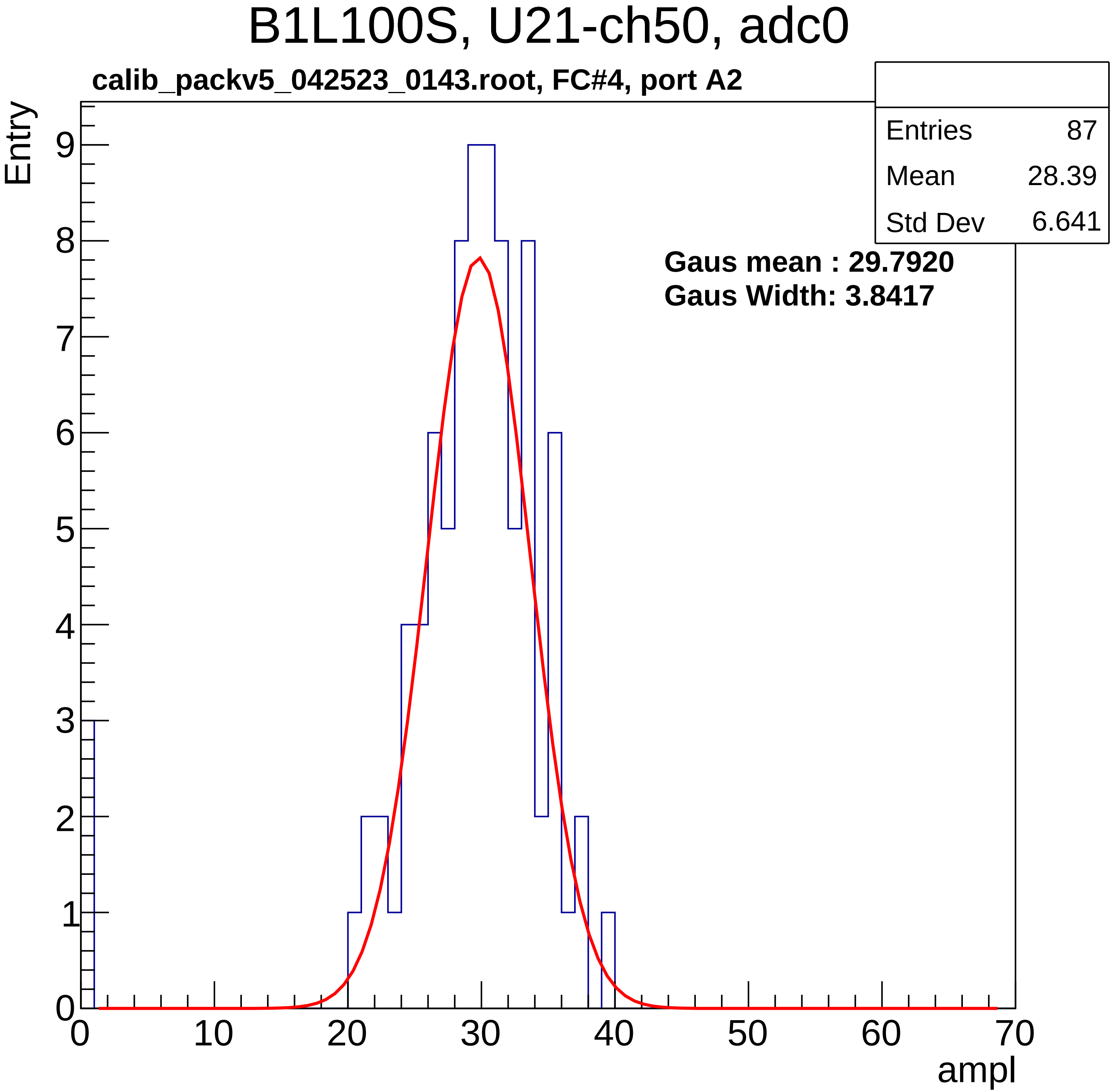
B1L100S, U21-ch49, adc5

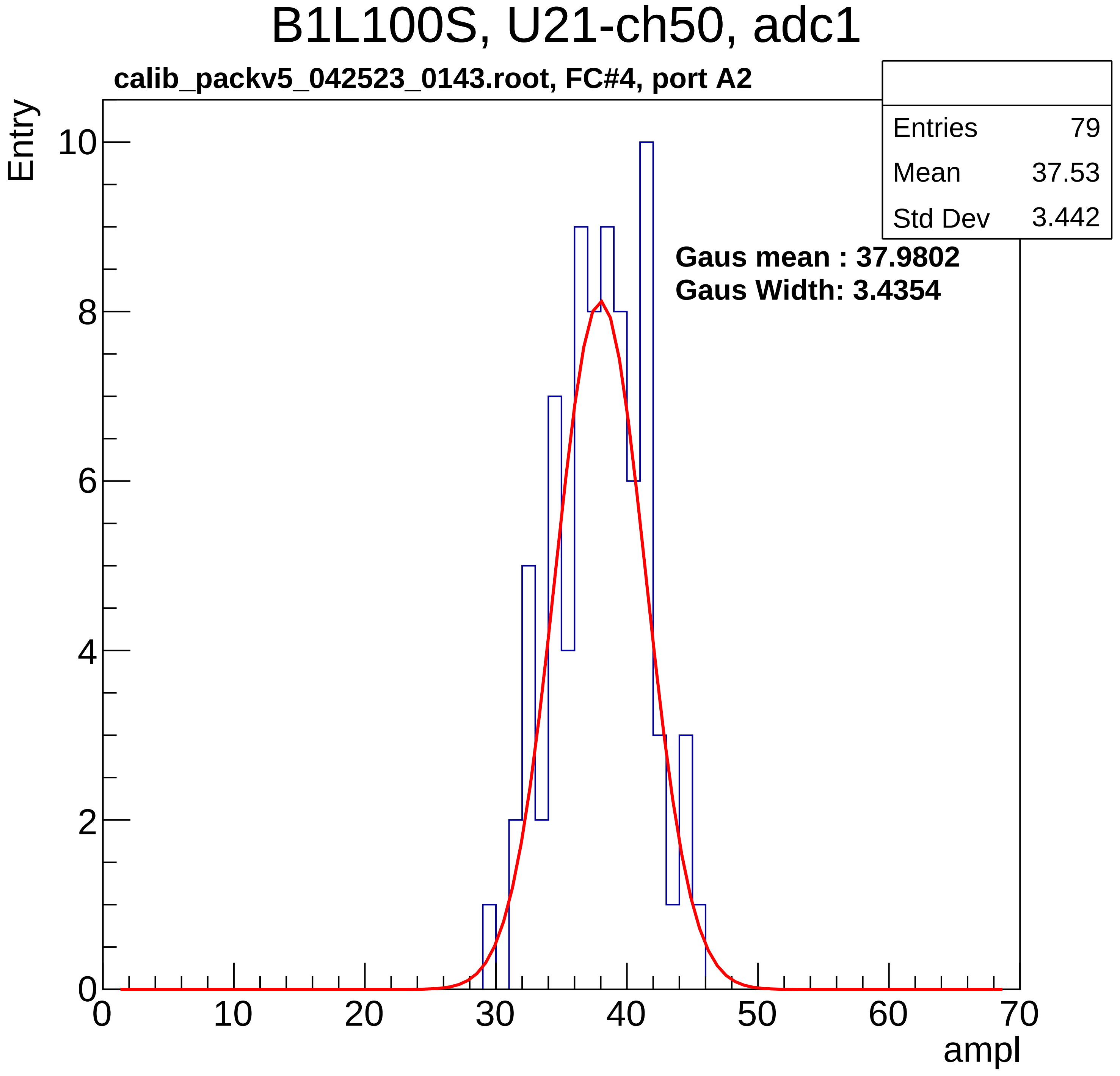
calib_packv5_042523_0143.root, FC#4, port A2

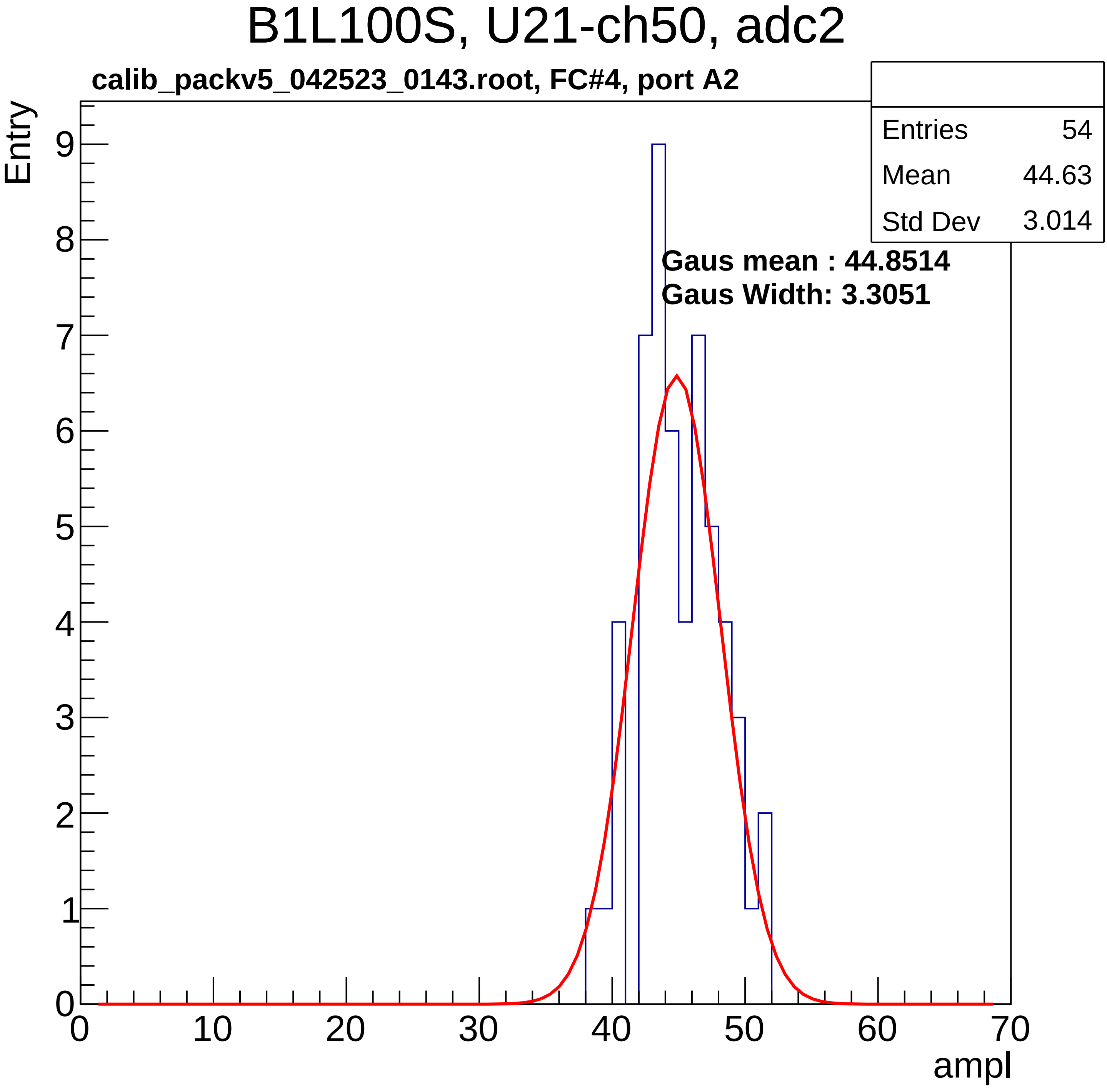


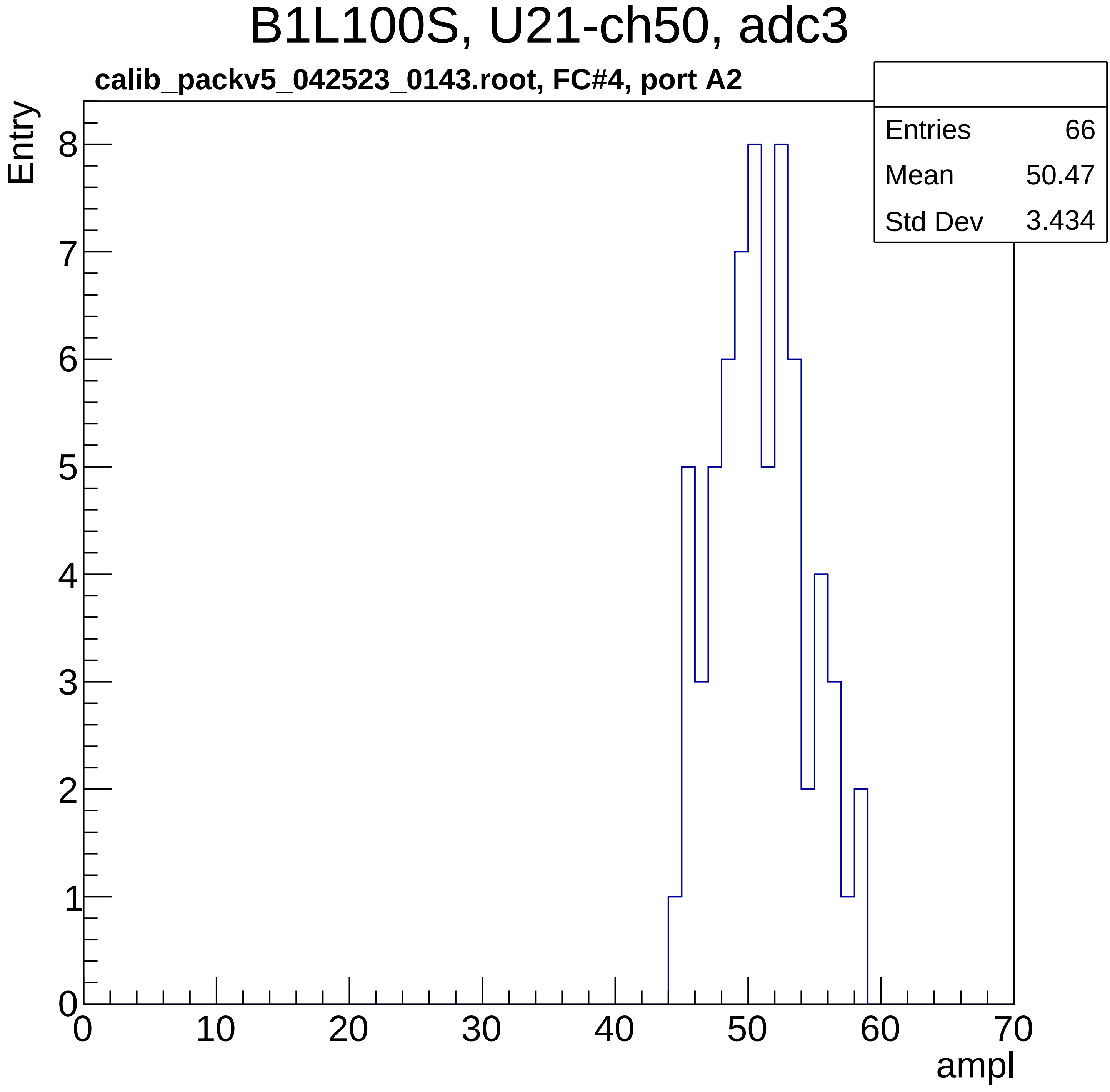

B1L100S, U21-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 3
Mean 41.67
Std Dev 29.47
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

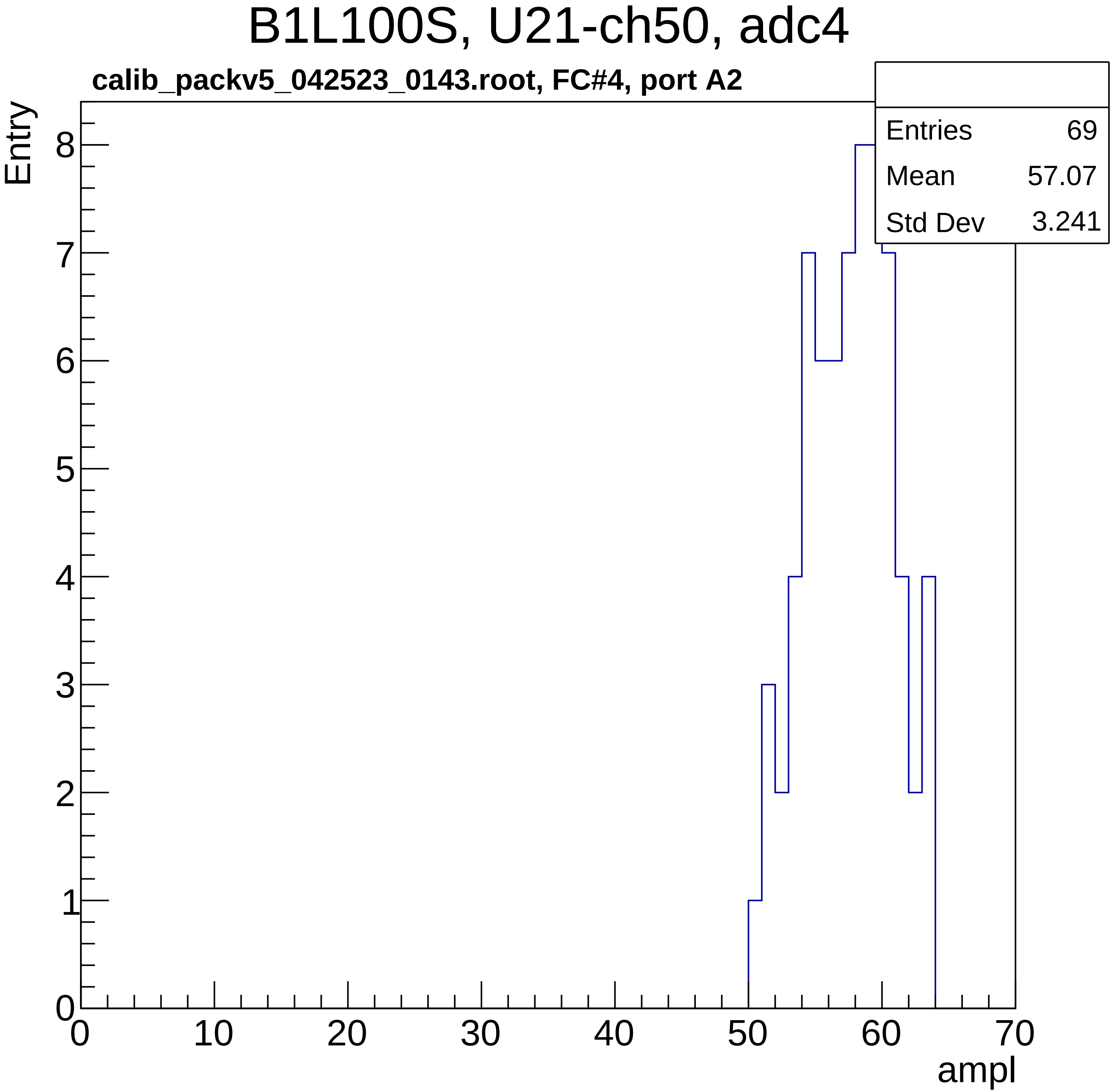

B1L100S, U21-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

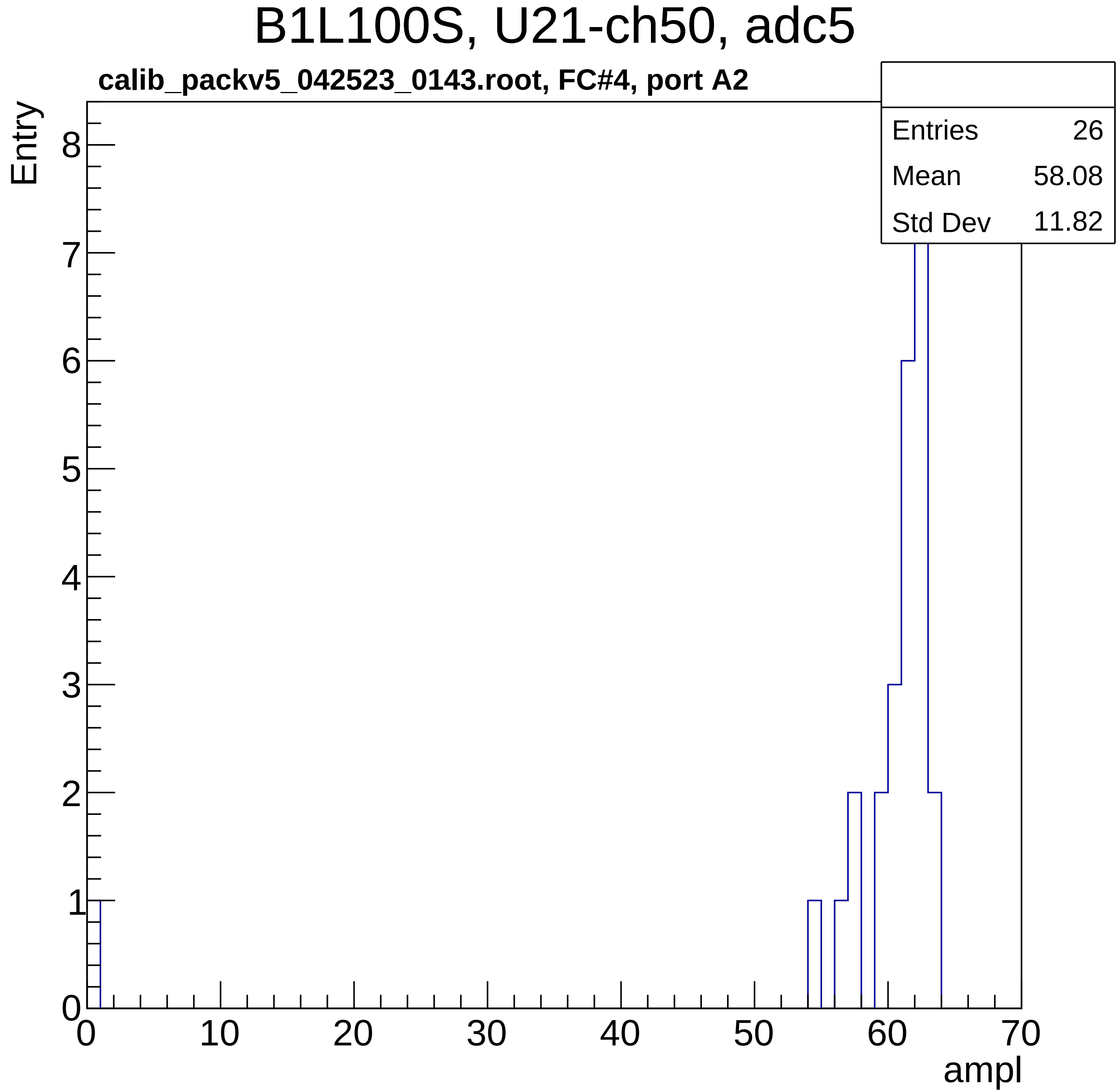

B1L100S, U21-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 28.39
Std Dev 6.641
Gaus mean : 29.7920
Gaus Width: 3.8417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

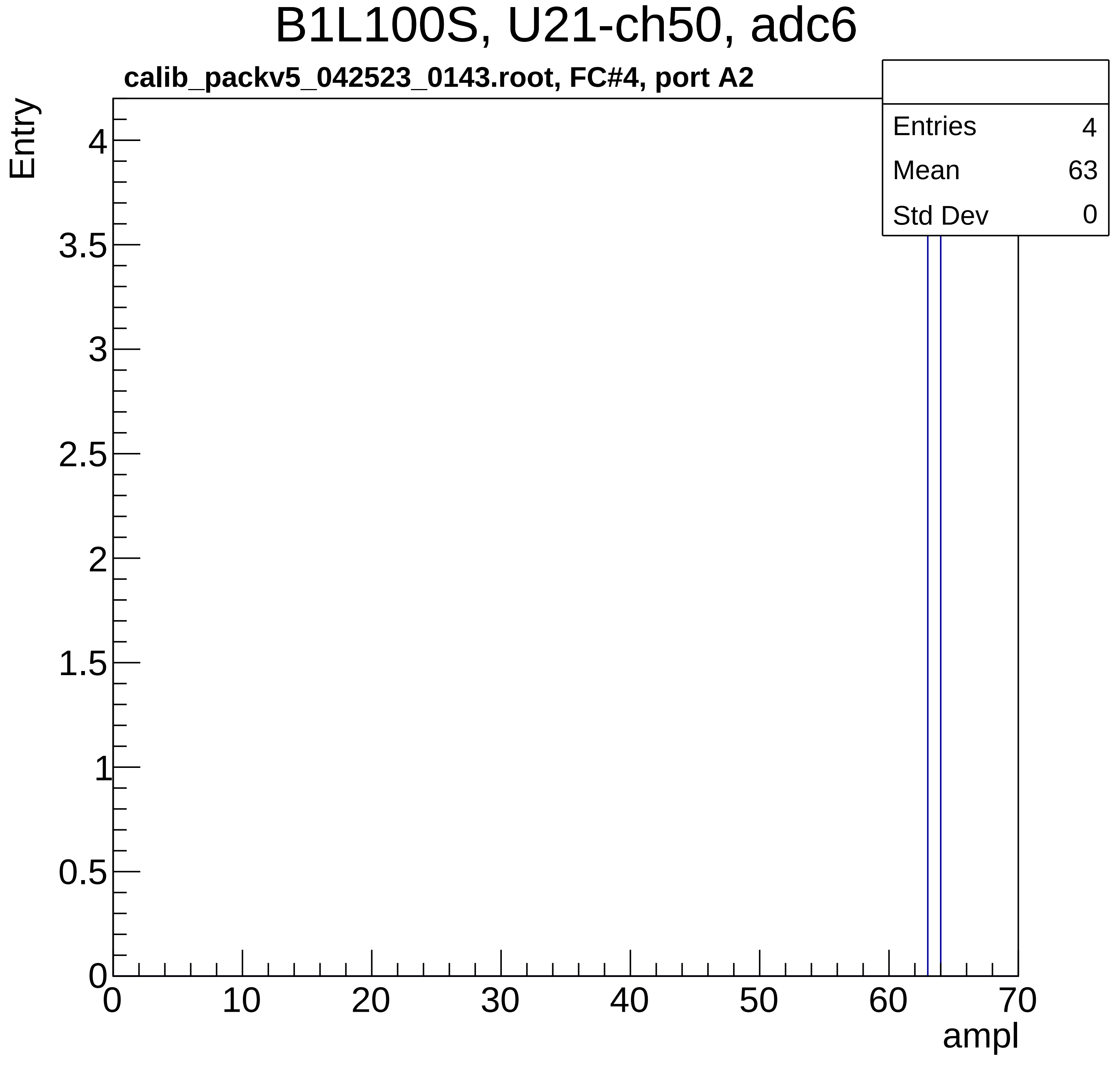

B1L100S, U21-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 37.53
Std Dev 3.442
Gaus mean : 37.9802
Gaus Width: 3.4354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

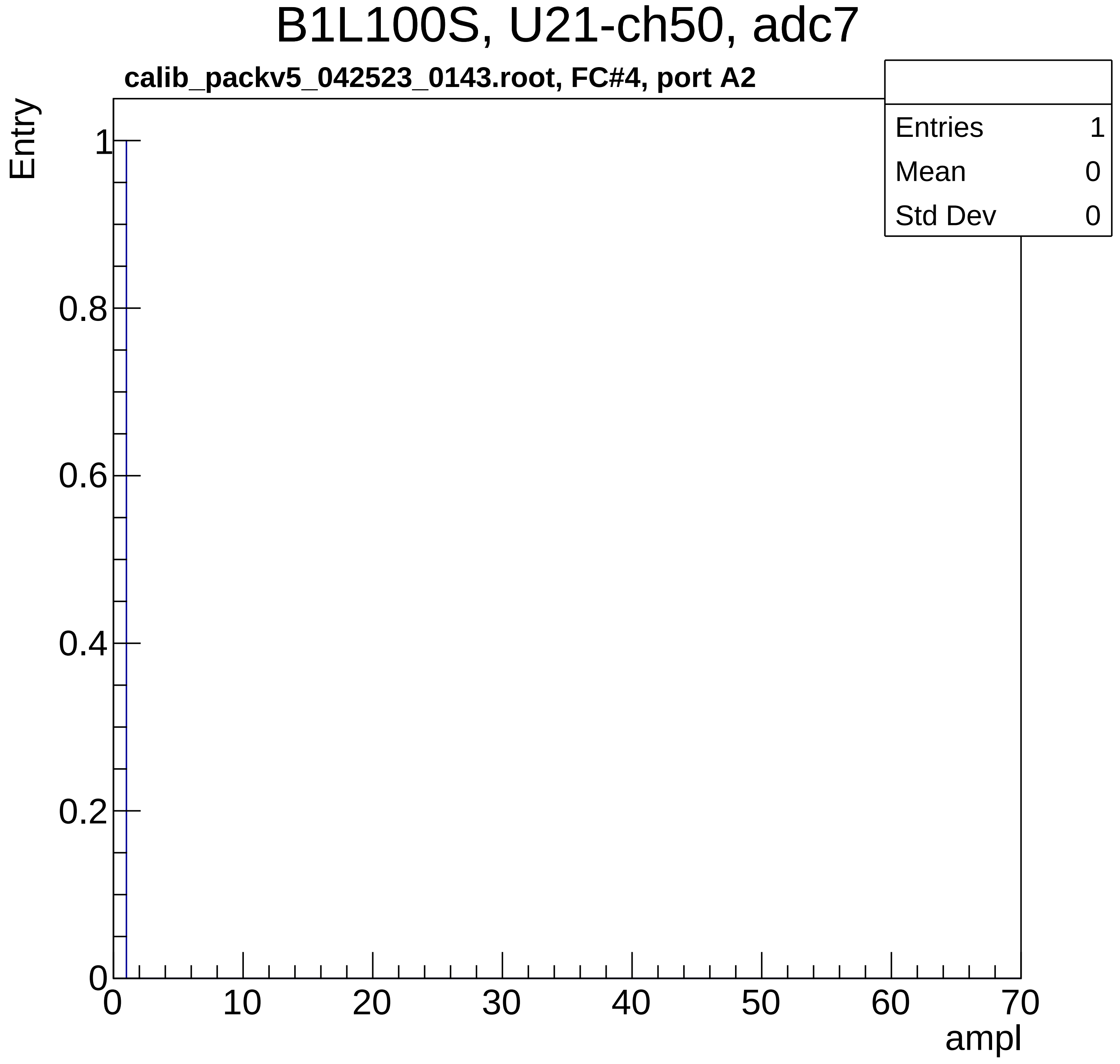

B1L100S, U21-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl
Entries 54
Mean 44.63
Std Dev 3.014
Gaus mean : 44.8514
Gaus Width: 3.3051

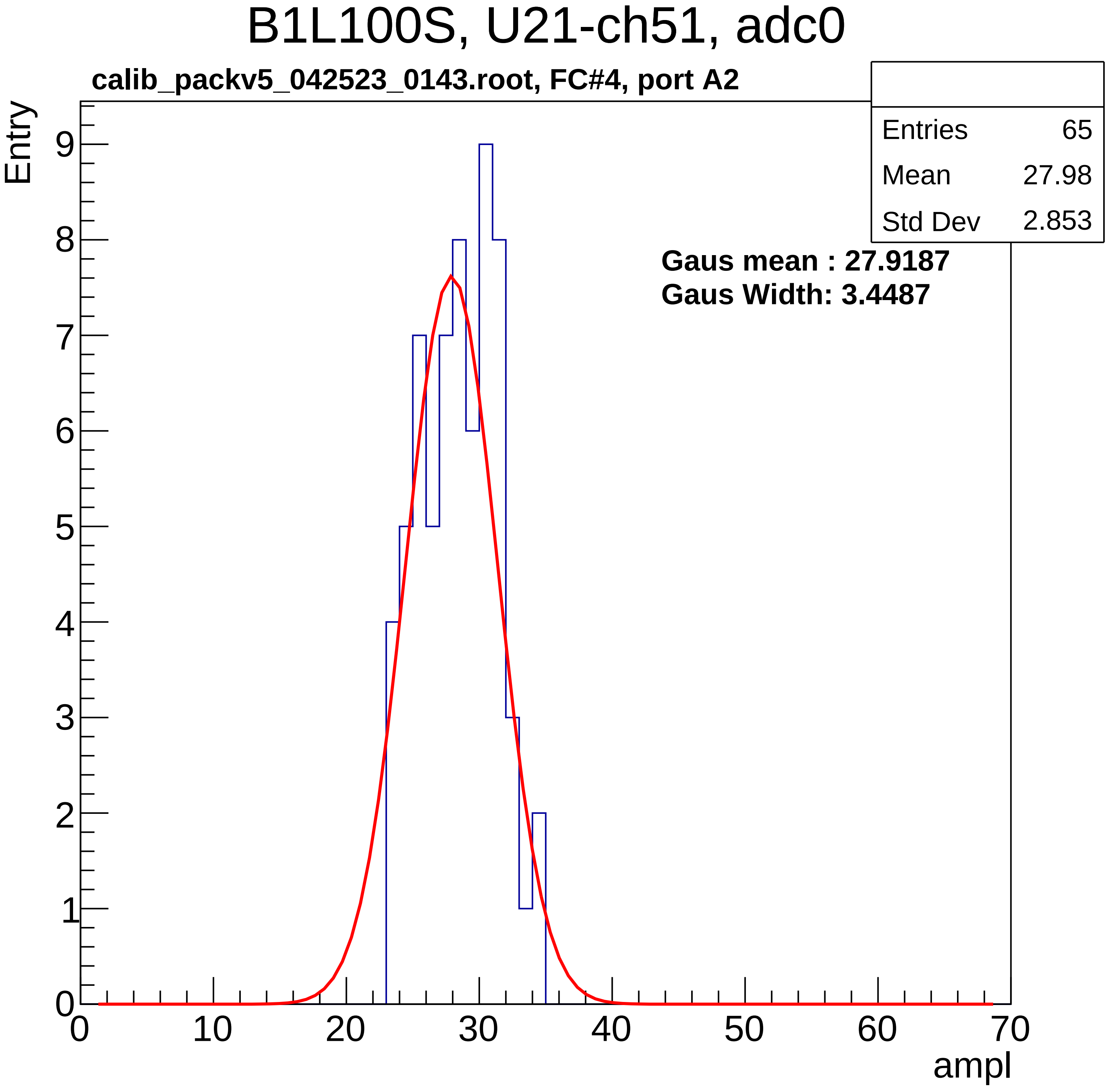

B1L100S, U21-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.47
Std Dev 3.434
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

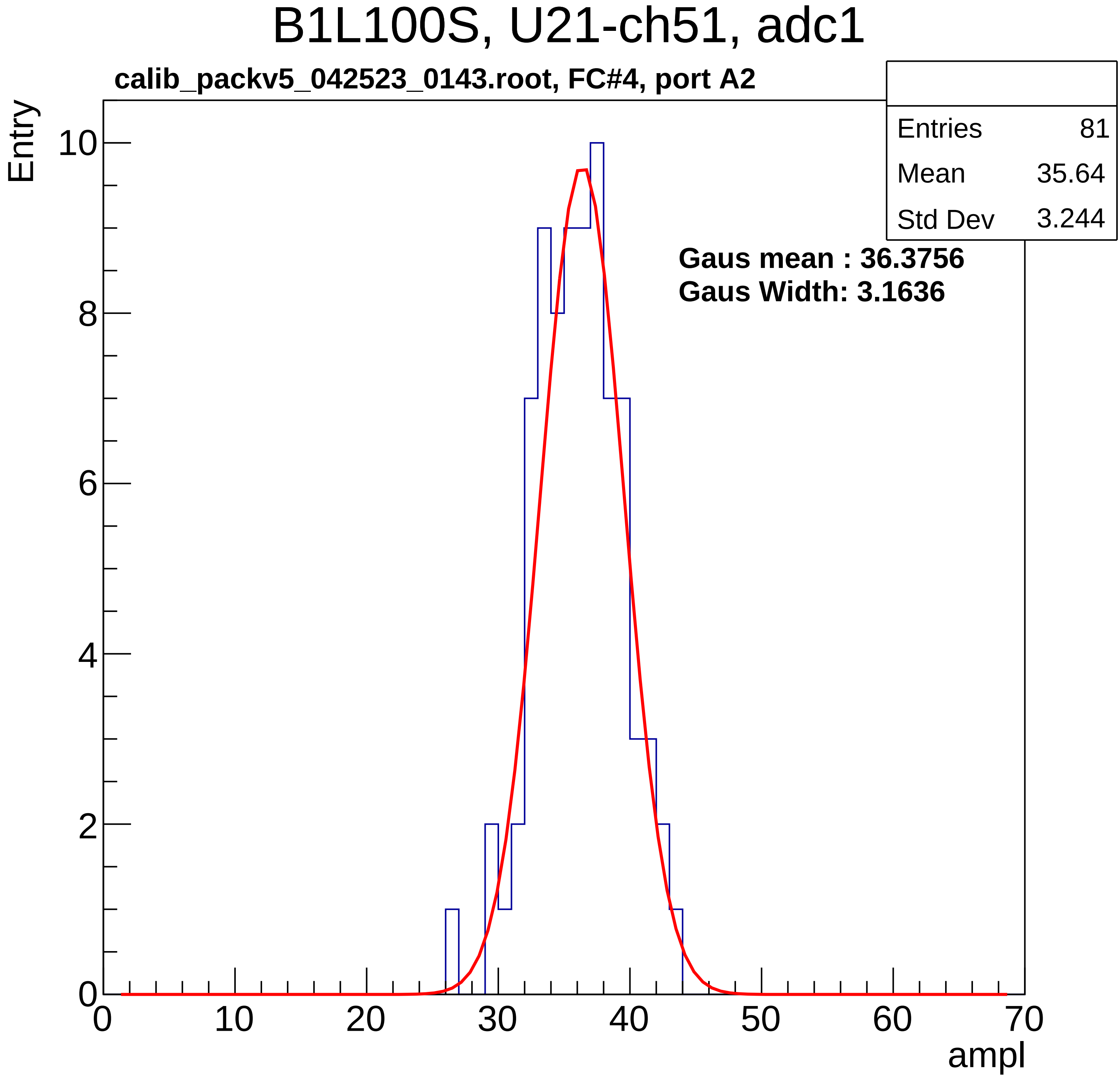

B1L100S, U21-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 69
Mean 57.07
Std Dev 3.241
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

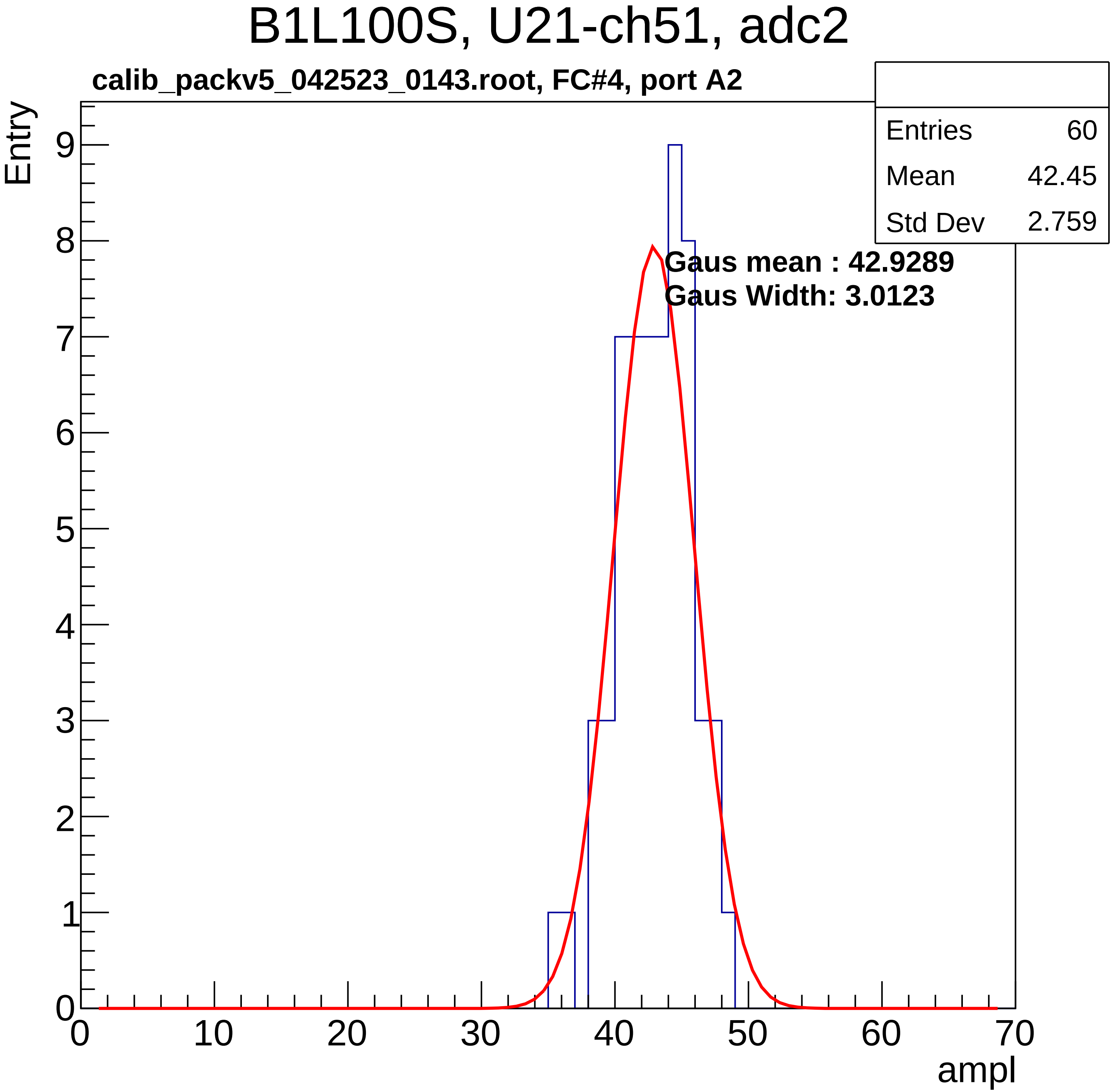

B1L100S, U21-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.08
Std Dev 11.82
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

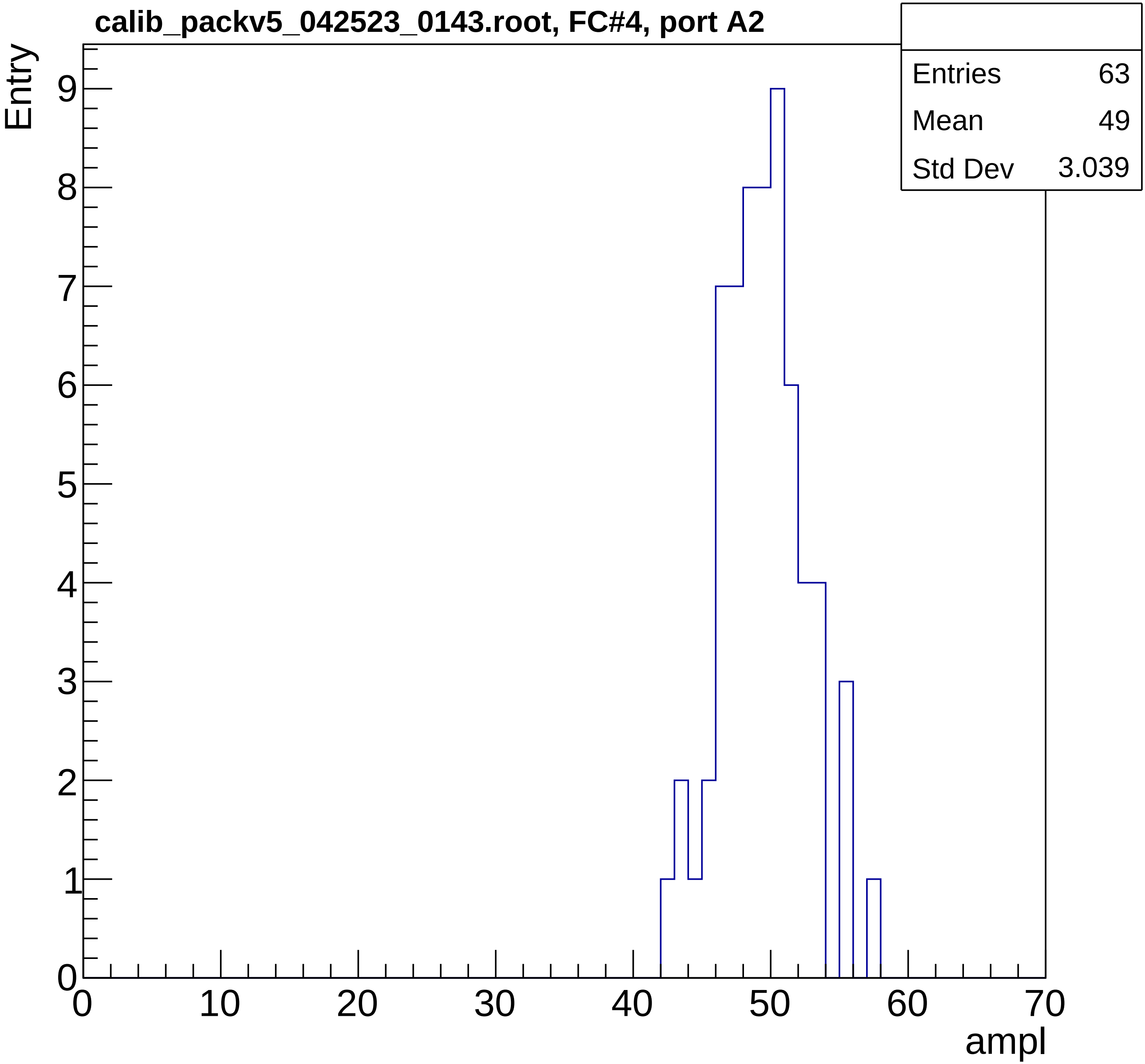

B1L100S, U21-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U21-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 27.98
Std Dev 2.853
Gaus mean : 27.9187
Gaus Width: 3.4487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.64
Std Dev 3.244
Gaus mean : 36.3756
Gaus Width: 3.1636
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

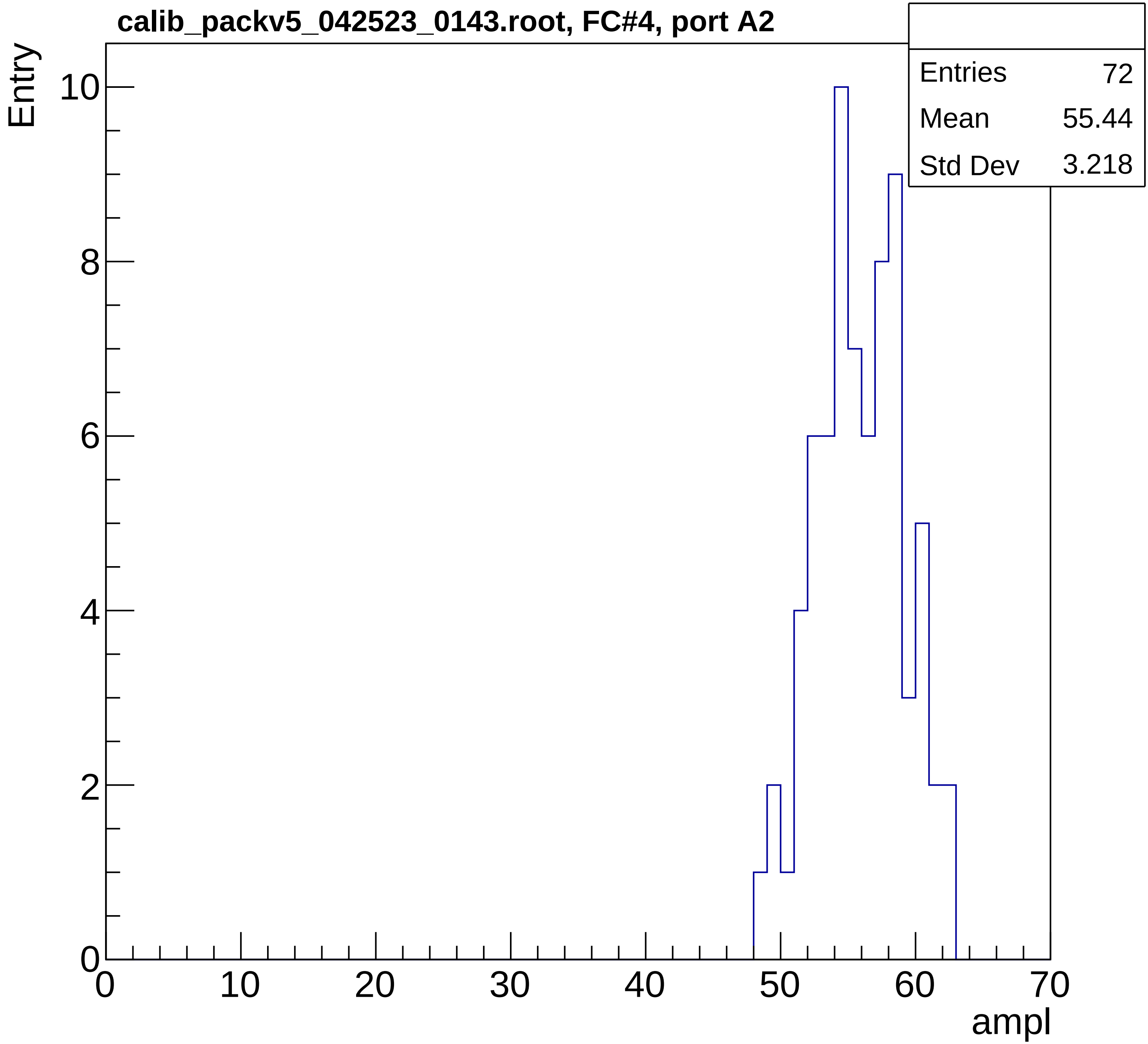

B1L100S, U21-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.45
Std Dev 2.759
Gaus mean : 42.9289
Gaus Width: 3.0123
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

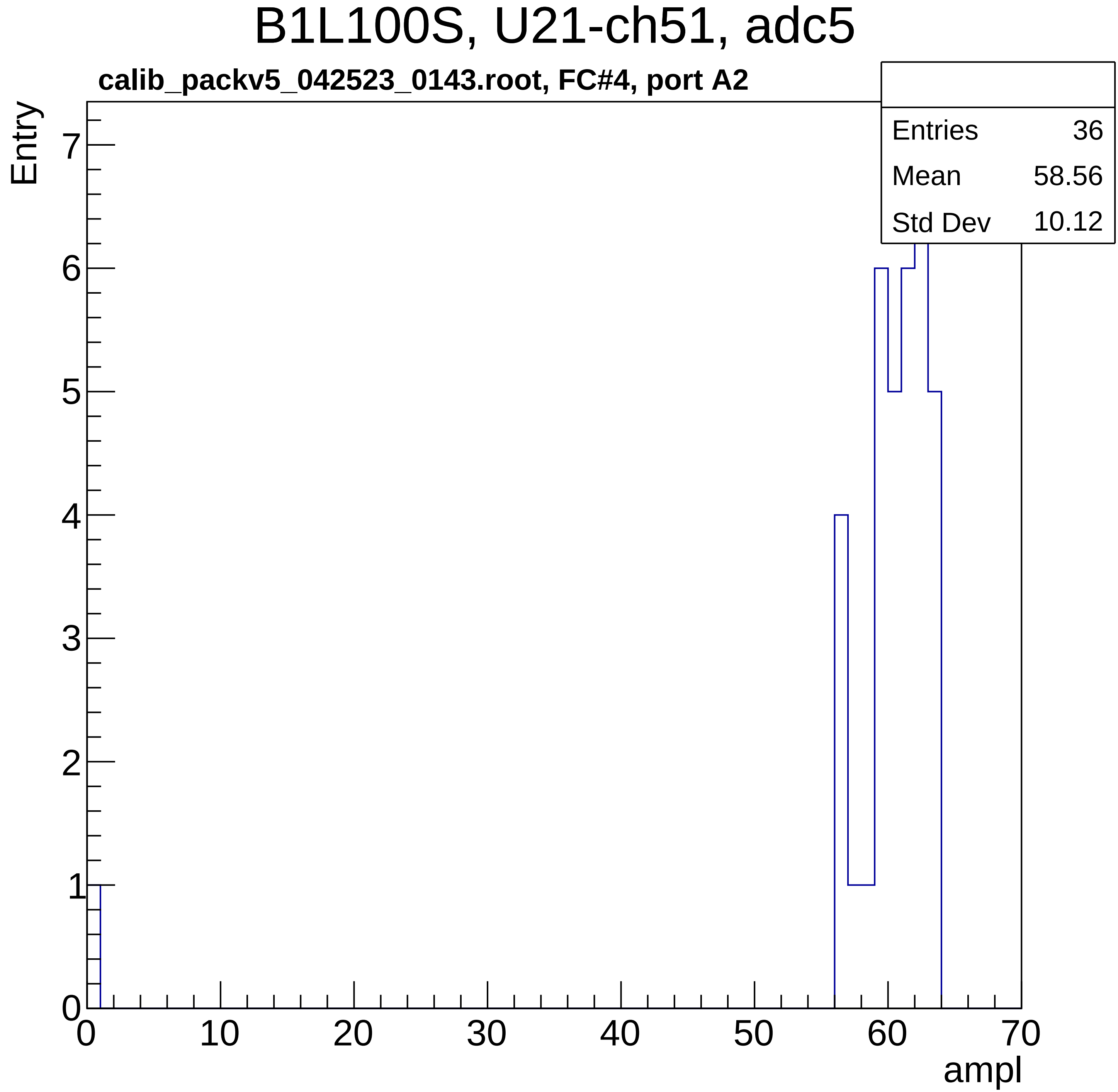
B1L100S, U21-ch51, adc3

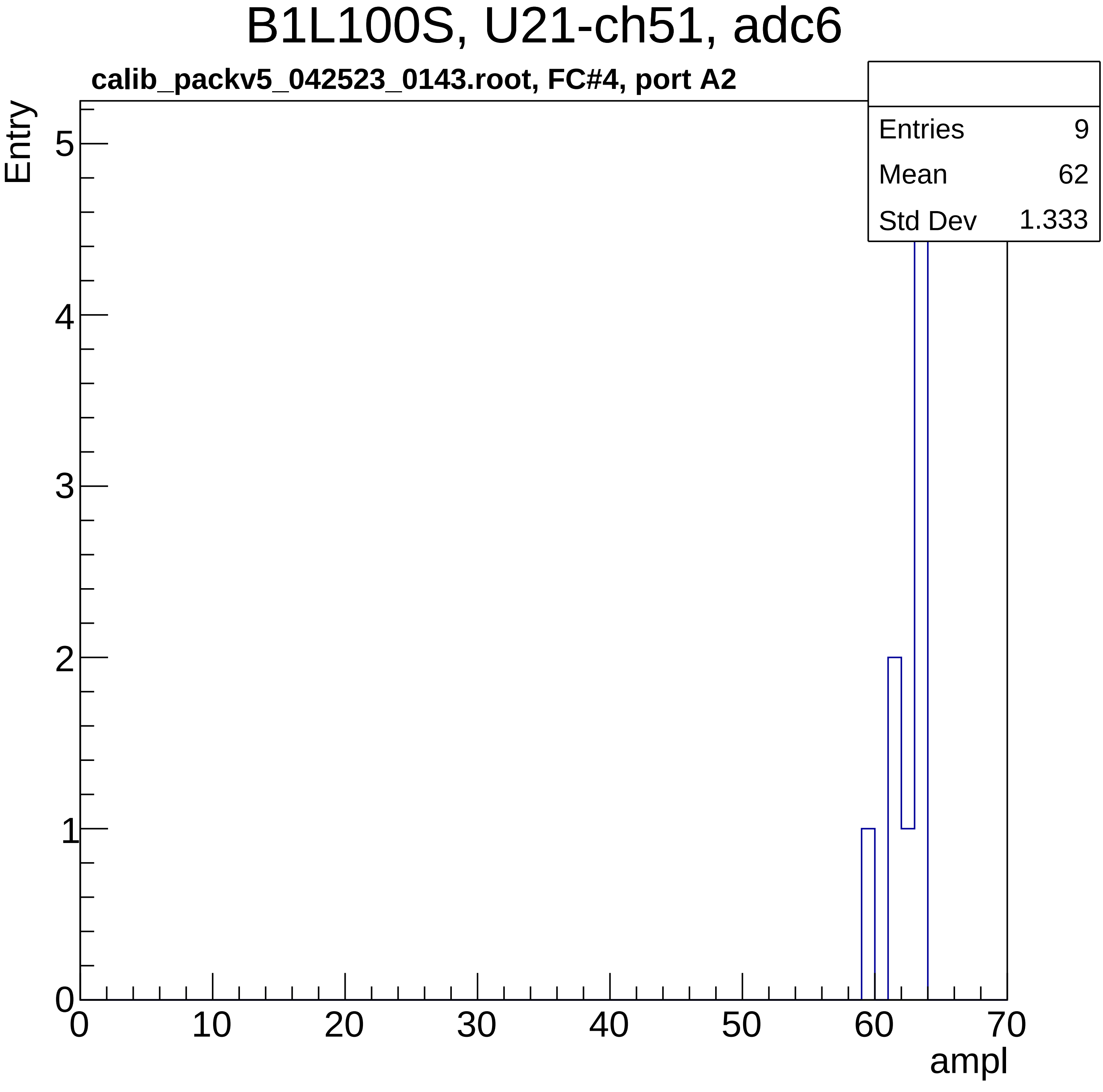


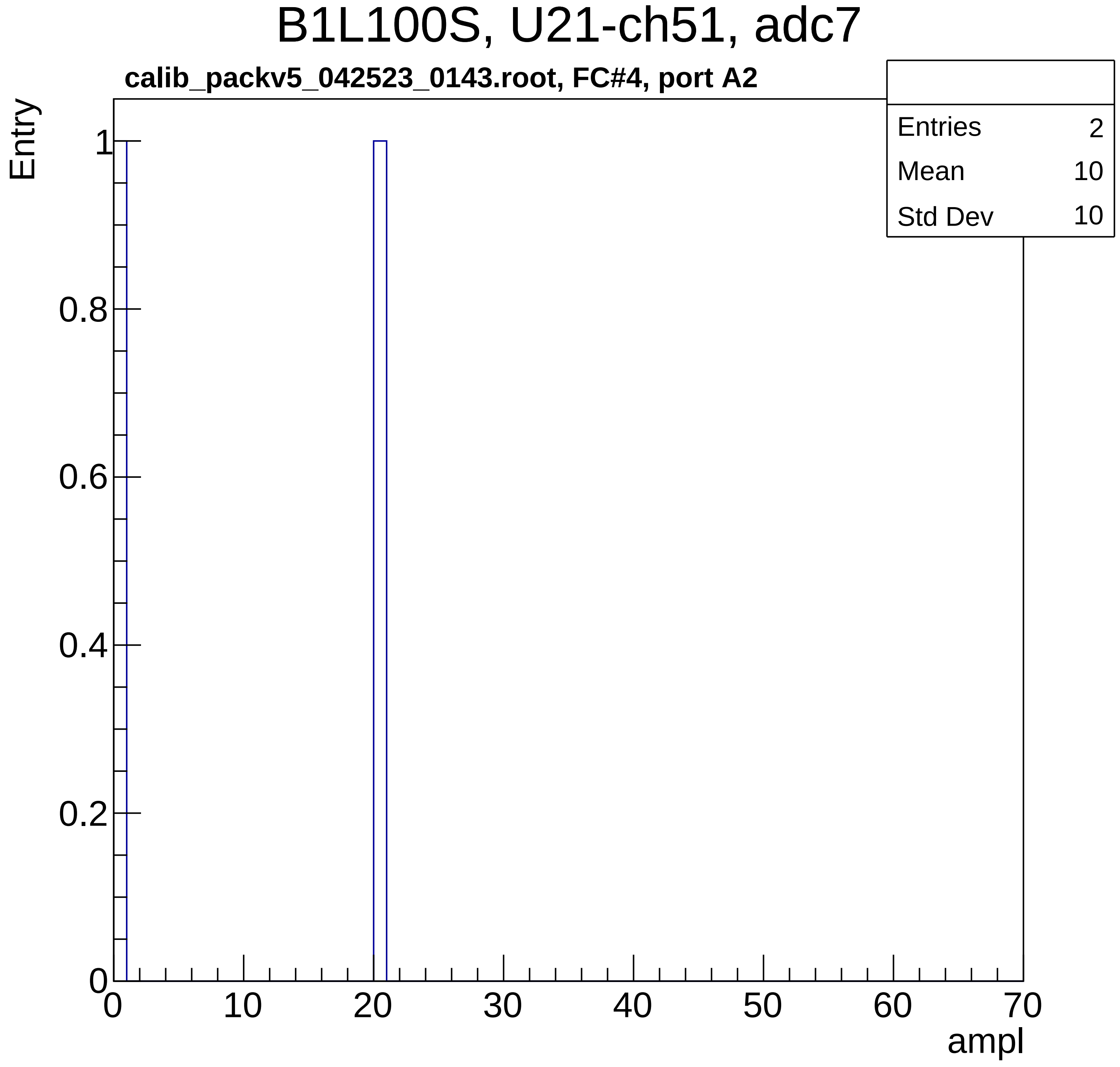
B1L100S, U21-ch51, adc4

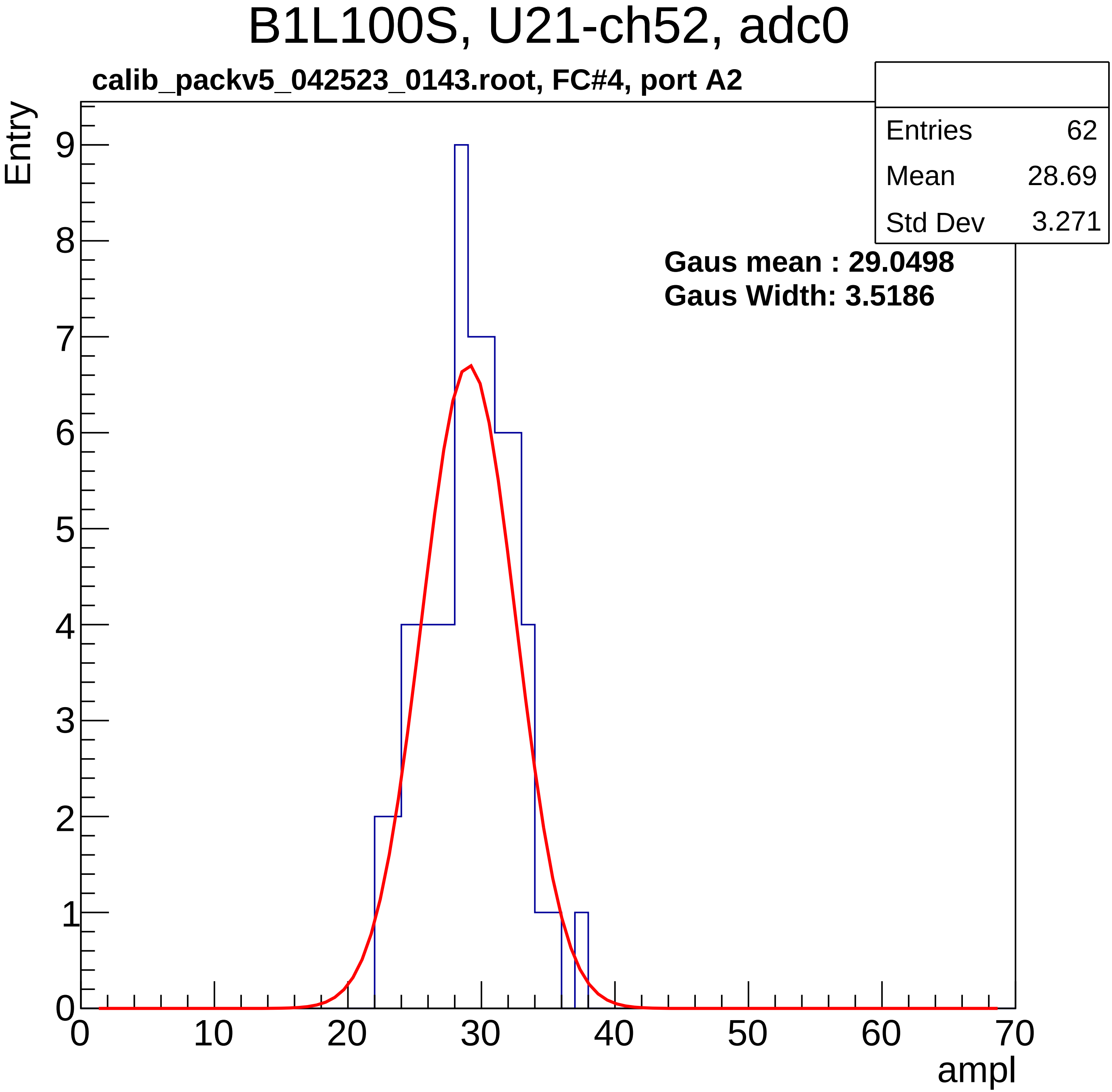
calib_packv5_042523_0143.root, FC#4, port A2

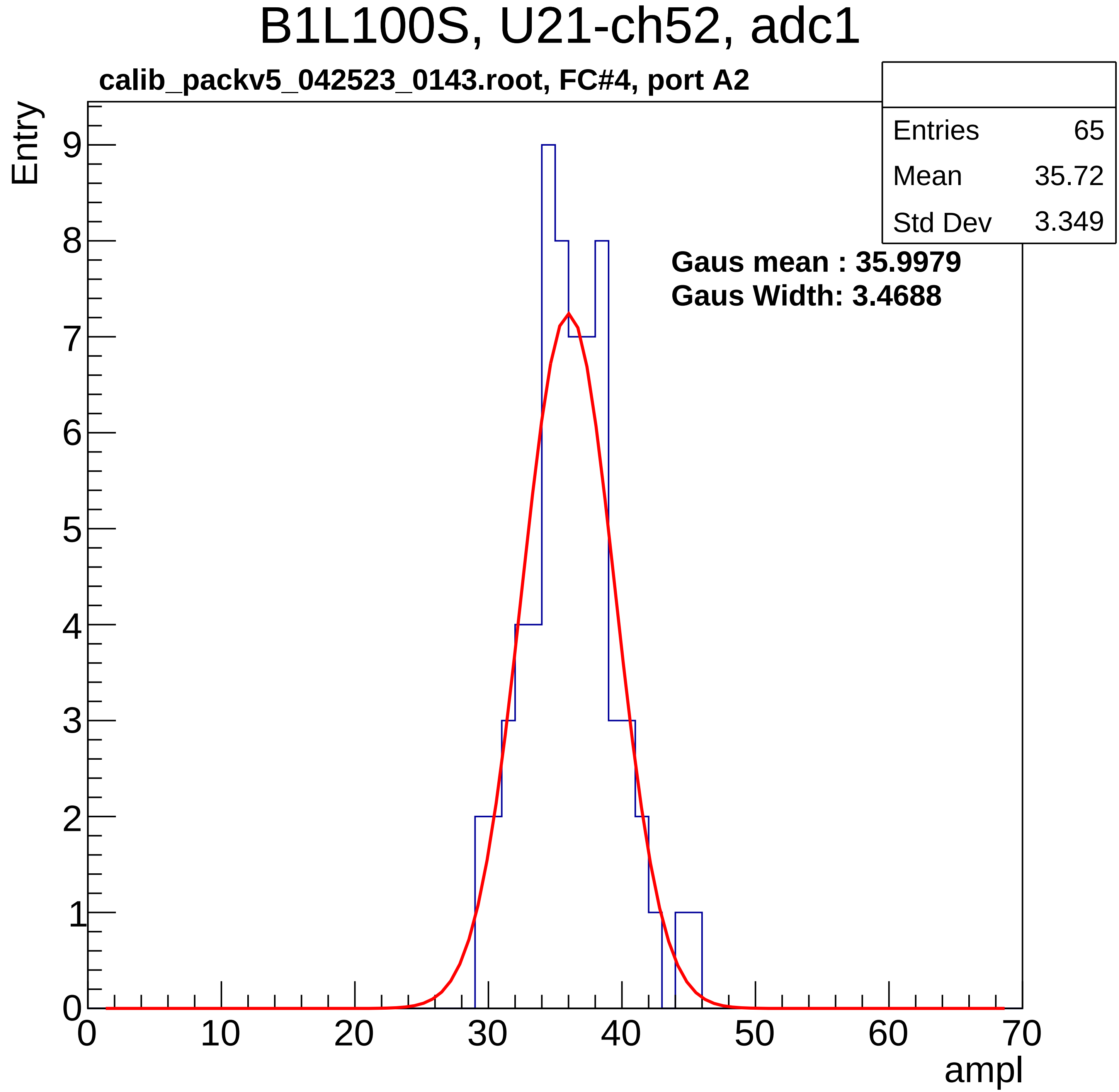


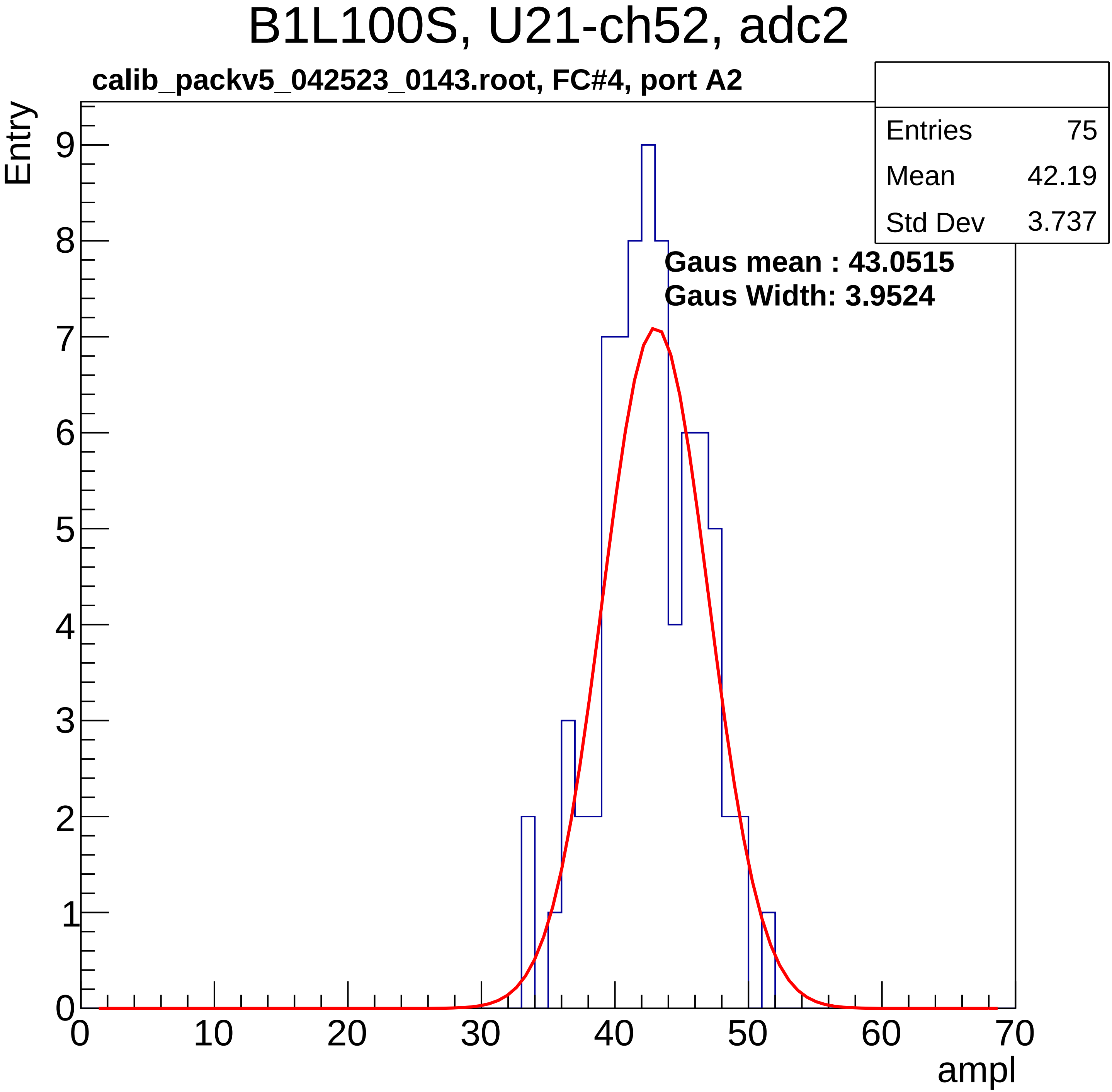

B1L100S, U21-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.56
Std Dev 10.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62
Std Dev 1.333
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

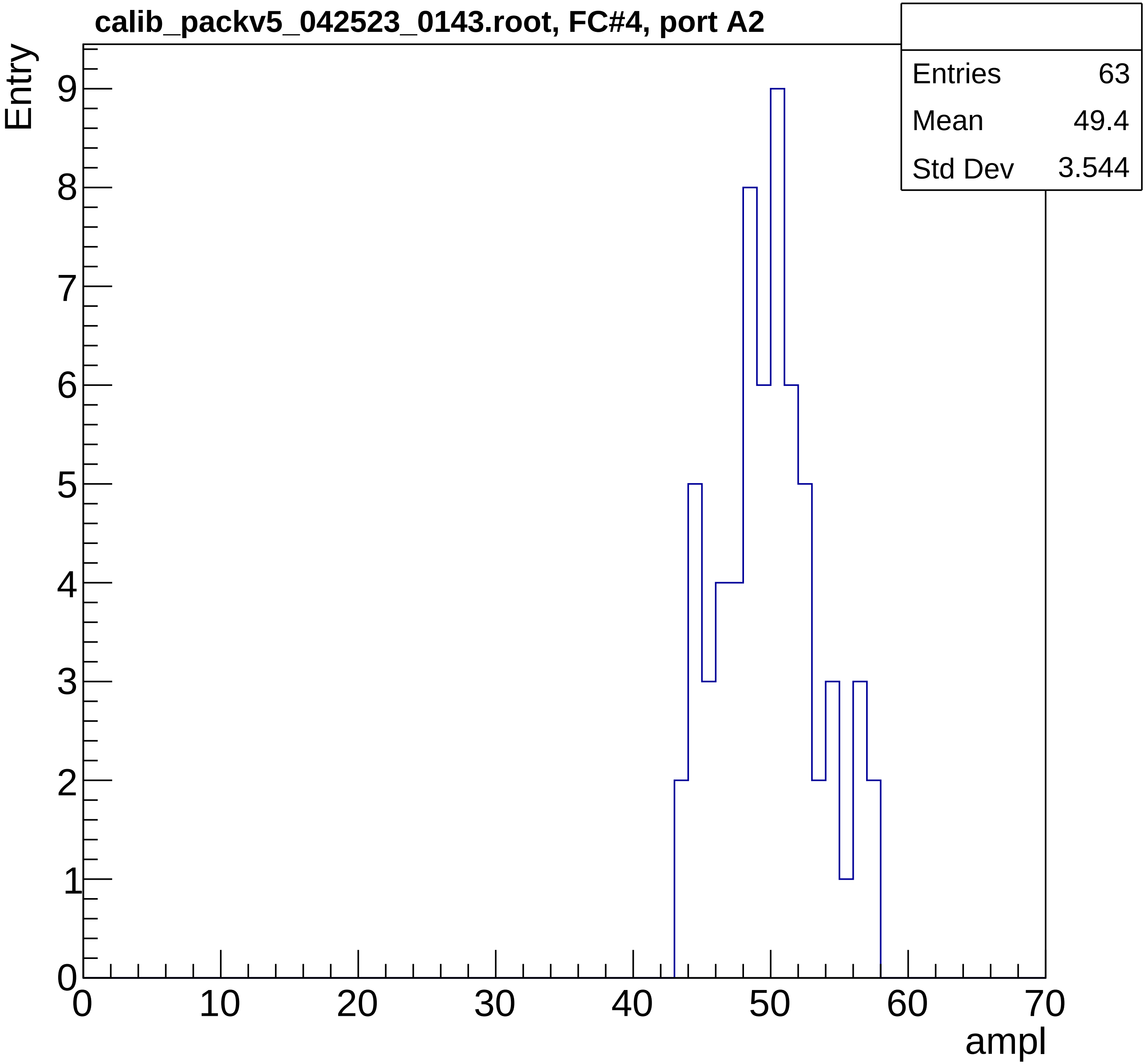

B1L100S, U21-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

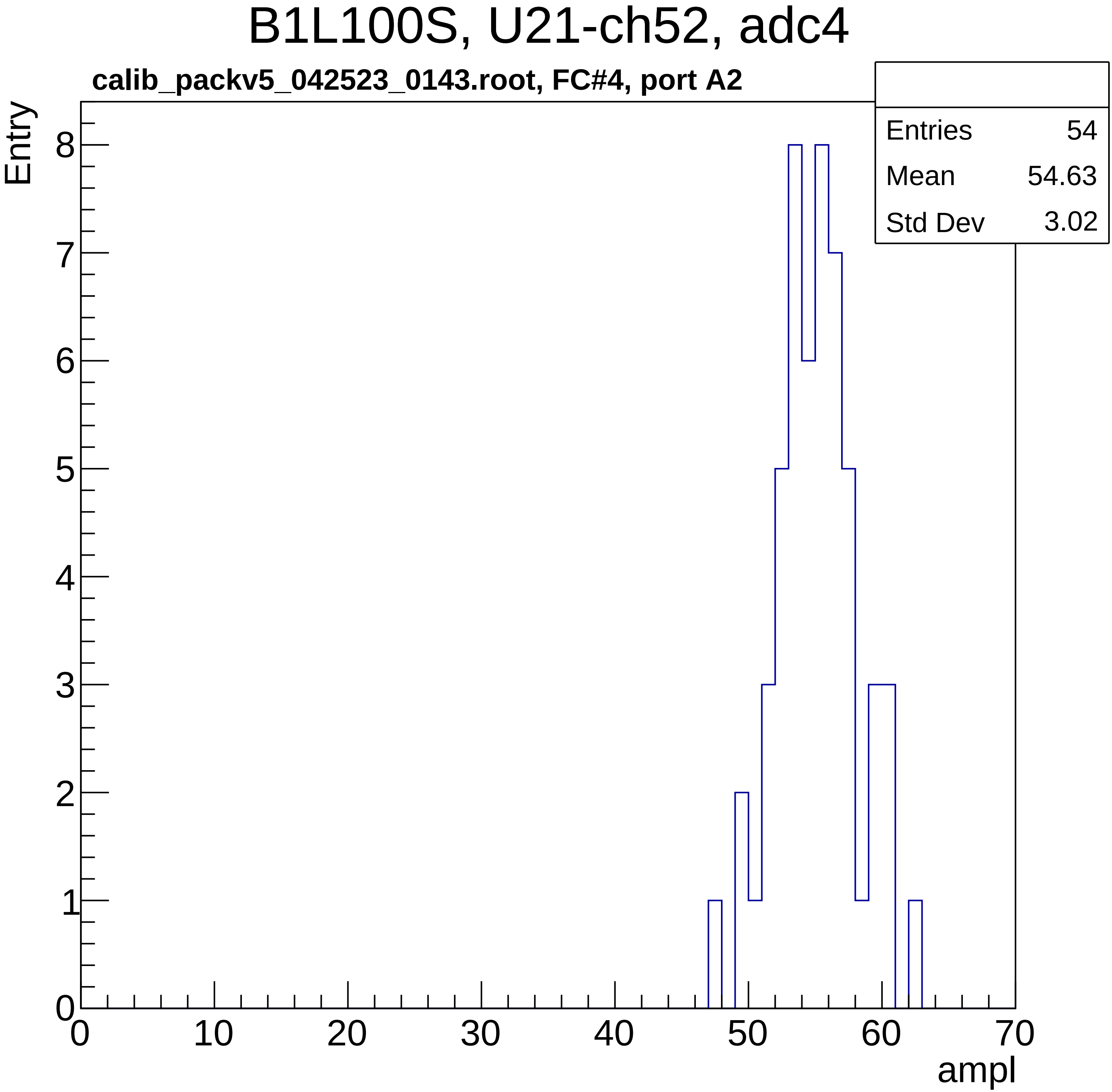

B1L100S, U21-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.69
Std Dev 3.271
Gaus mean : 29.0498
Gaus Width: 3.5186
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

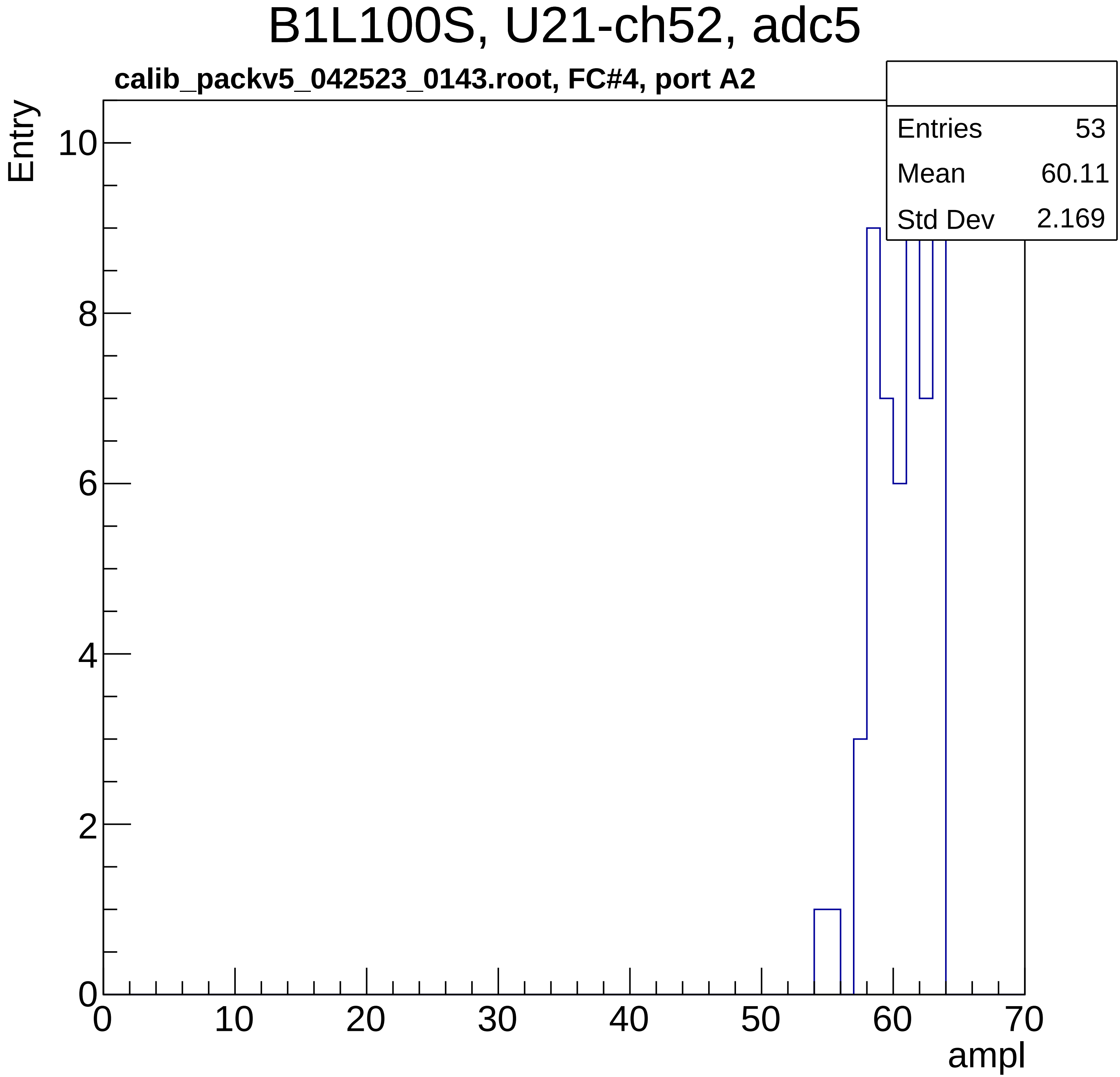

B1L100S, U21-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 35.72
Std Dev 3.349
Gaus mean : 35.9979
Gaus Width: 3.4688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 75
Mean 42.19
Std Dev 3.737
Gaus mean : 43.0515
Gaus Width: 3.9524
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch52, adc3

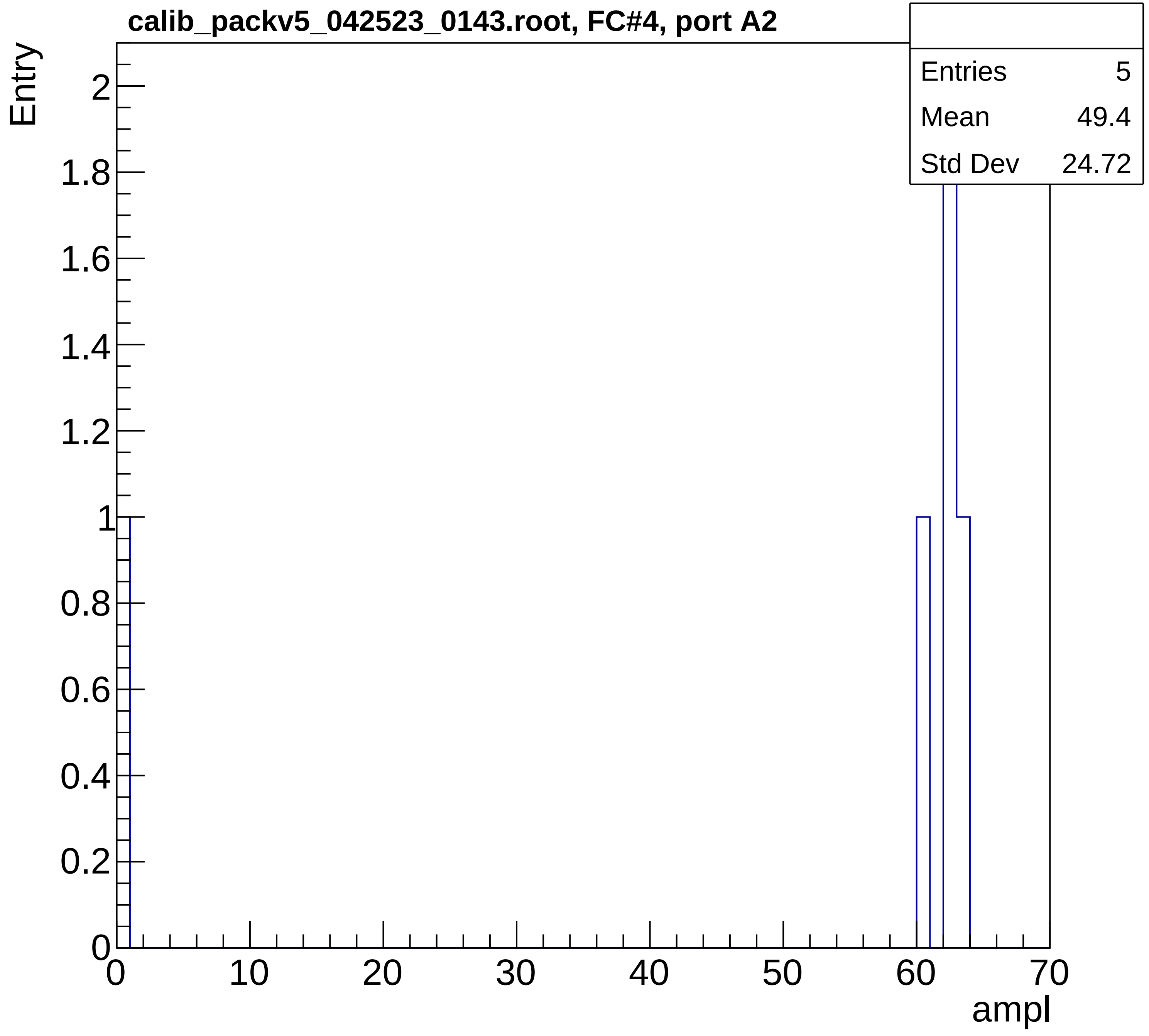


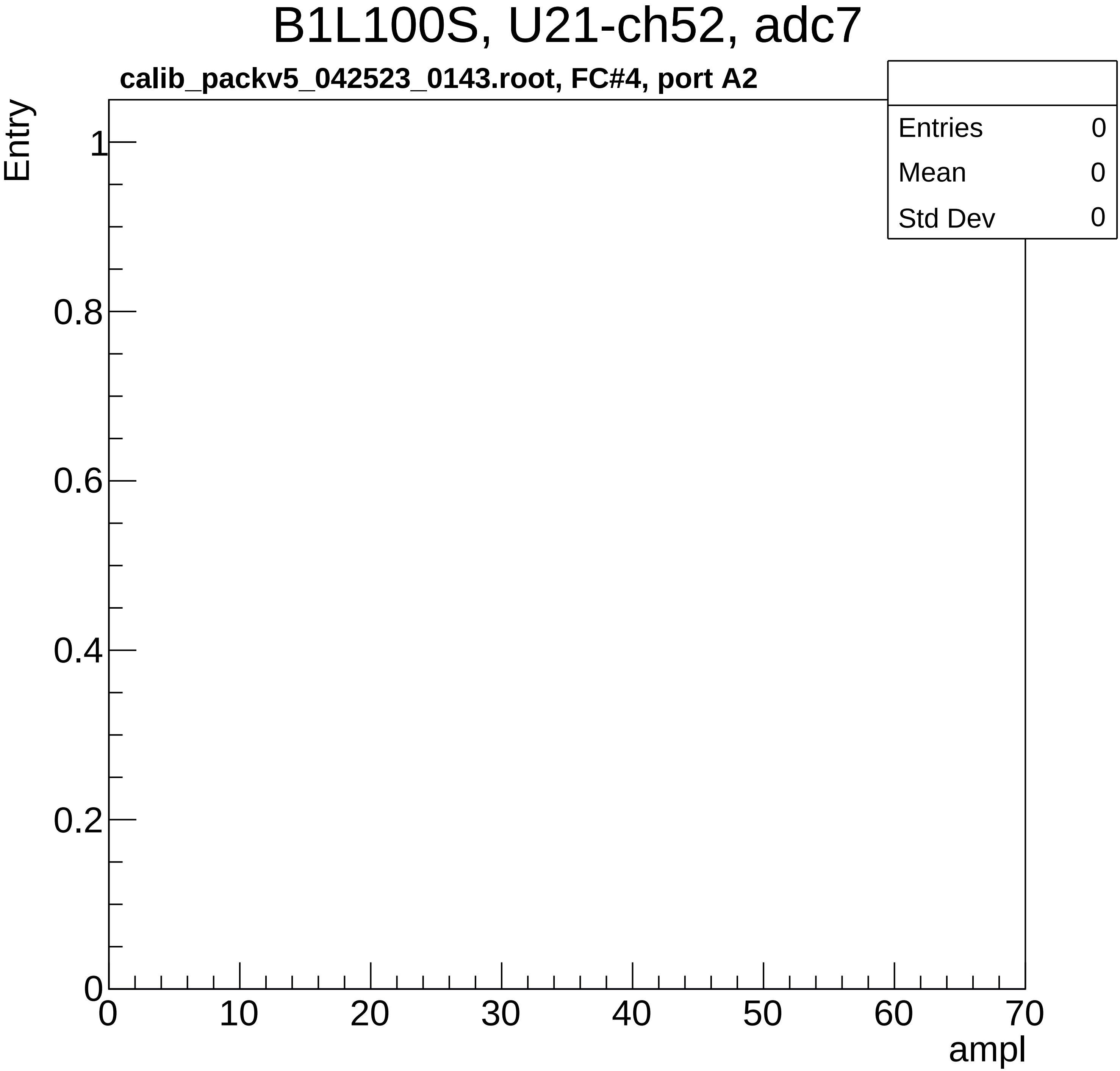

B1L100S, U21-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 54.63
Std Dev 3.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

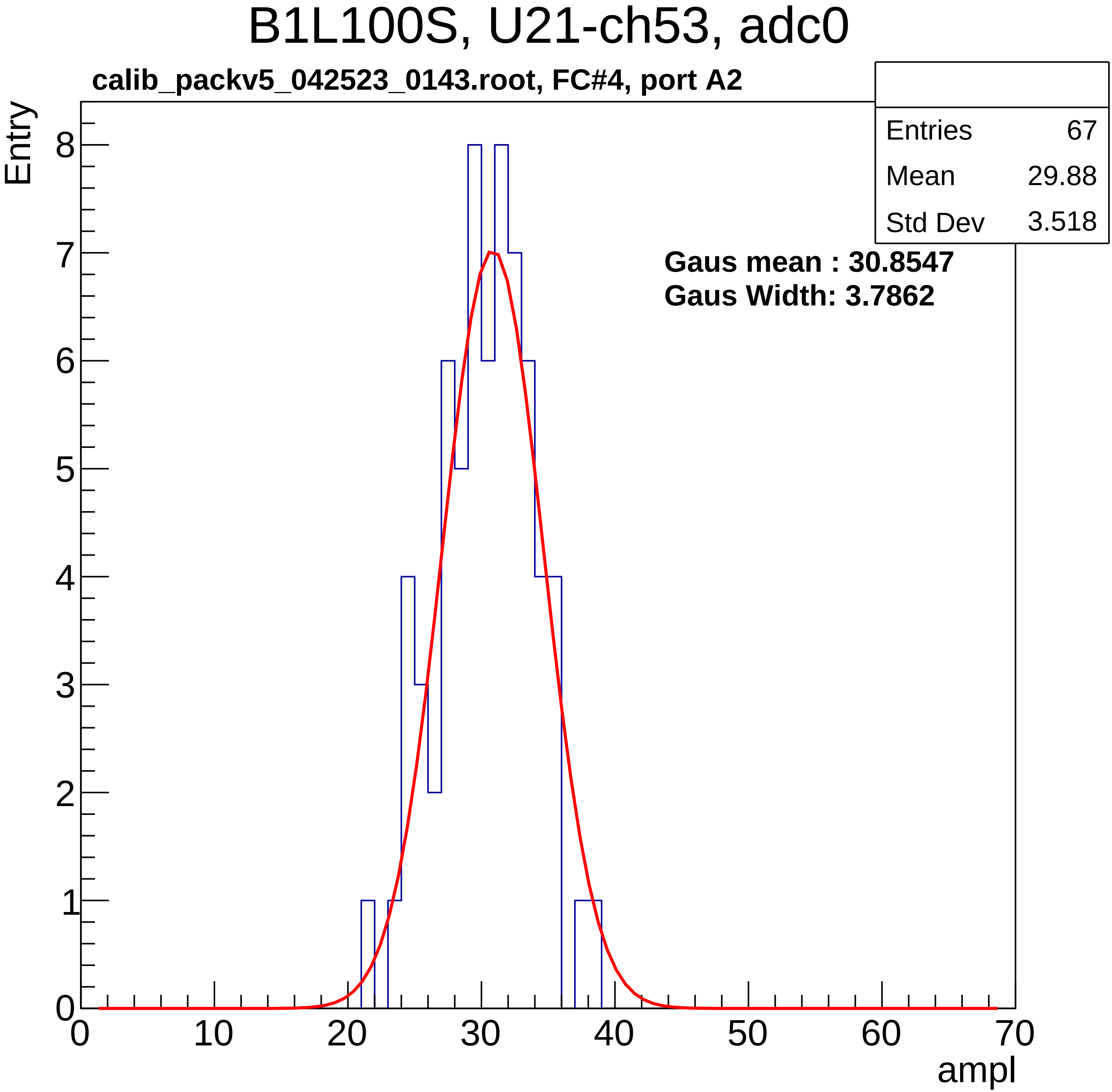

B1L100S, U21-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 53
Mean 60.11
Std Dev 2.169
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

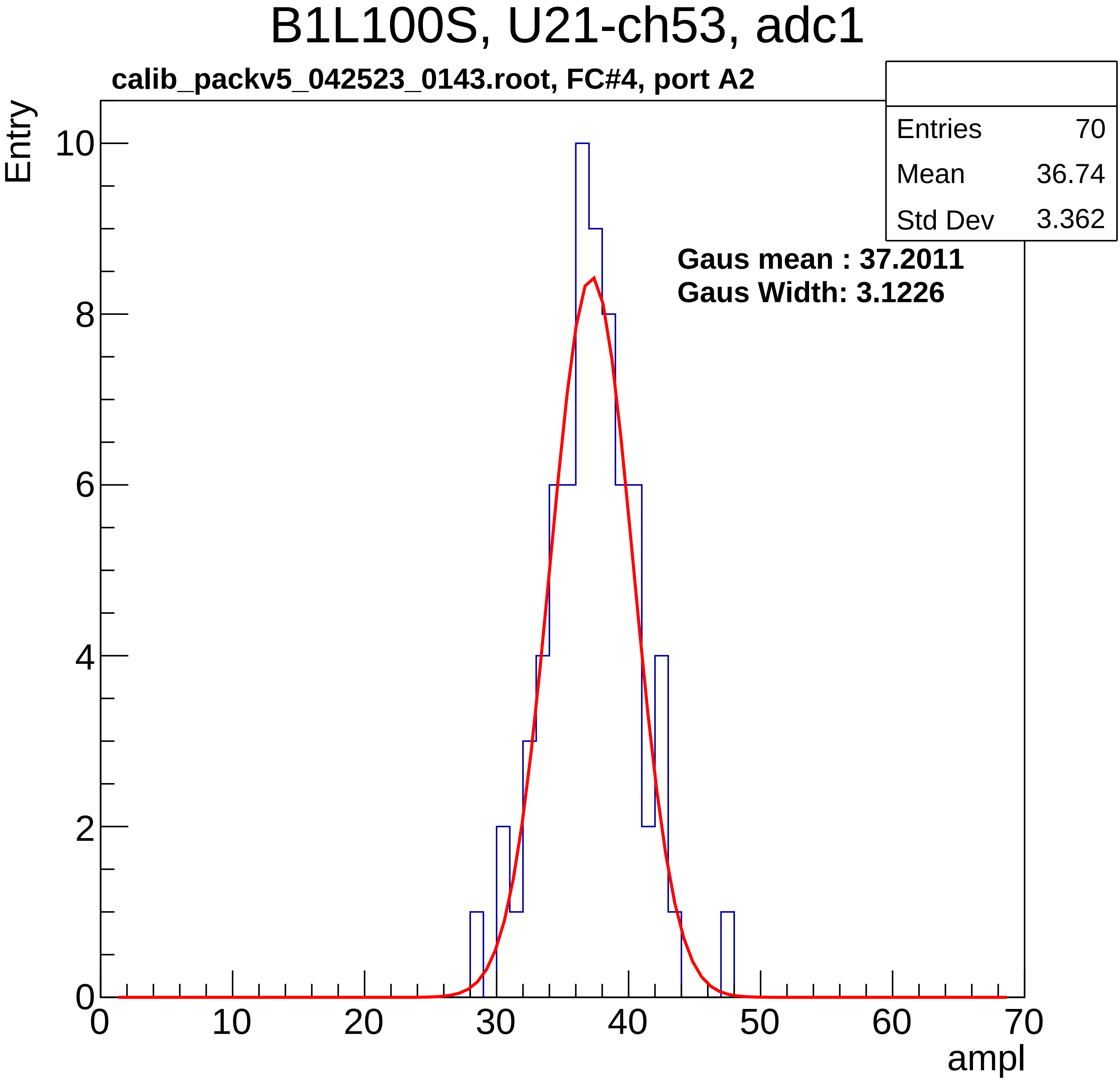
B1L100S, U21-ch52, adc6

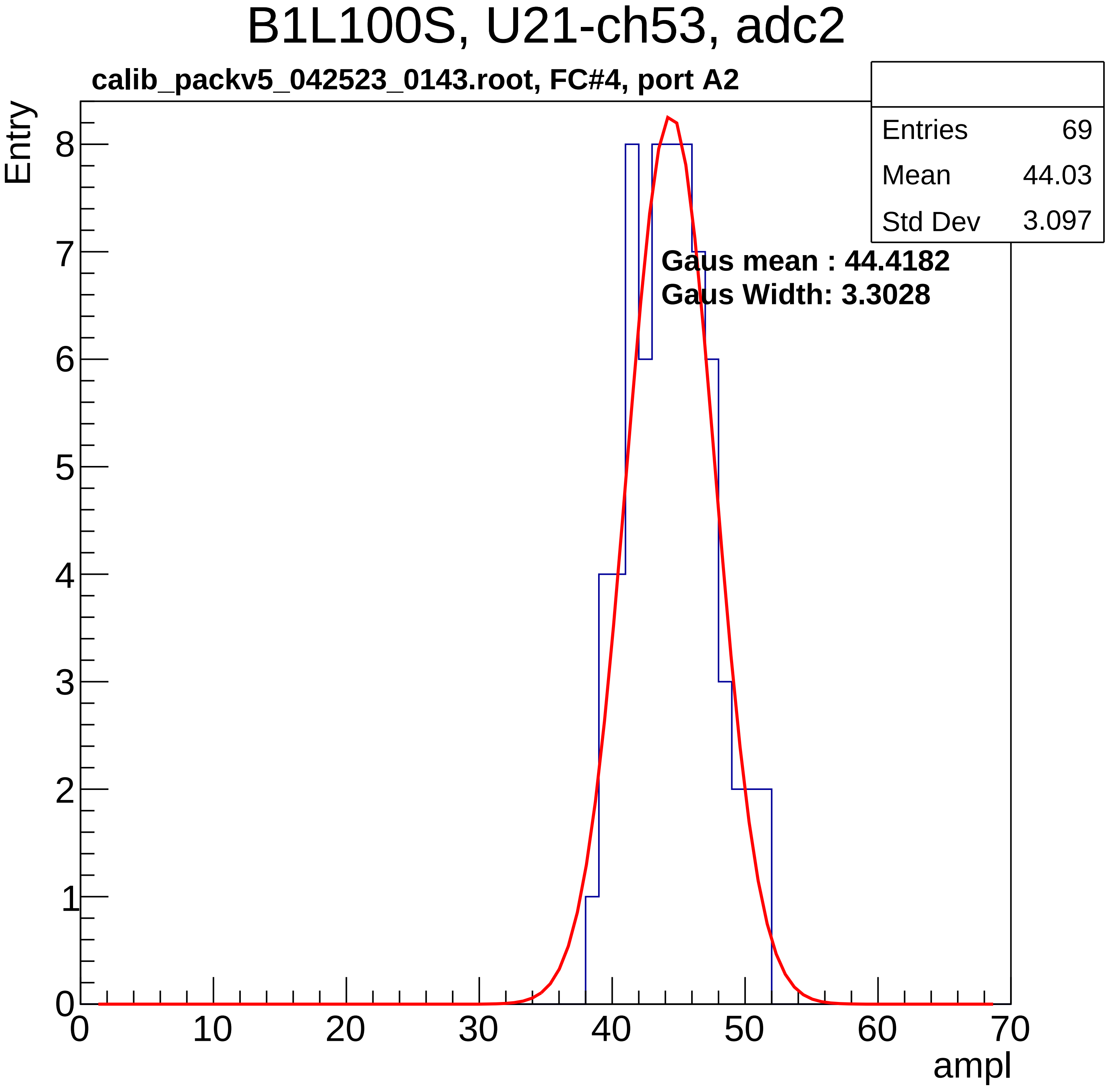
calib_packv5_042523_0143.root, FC#4, port A2

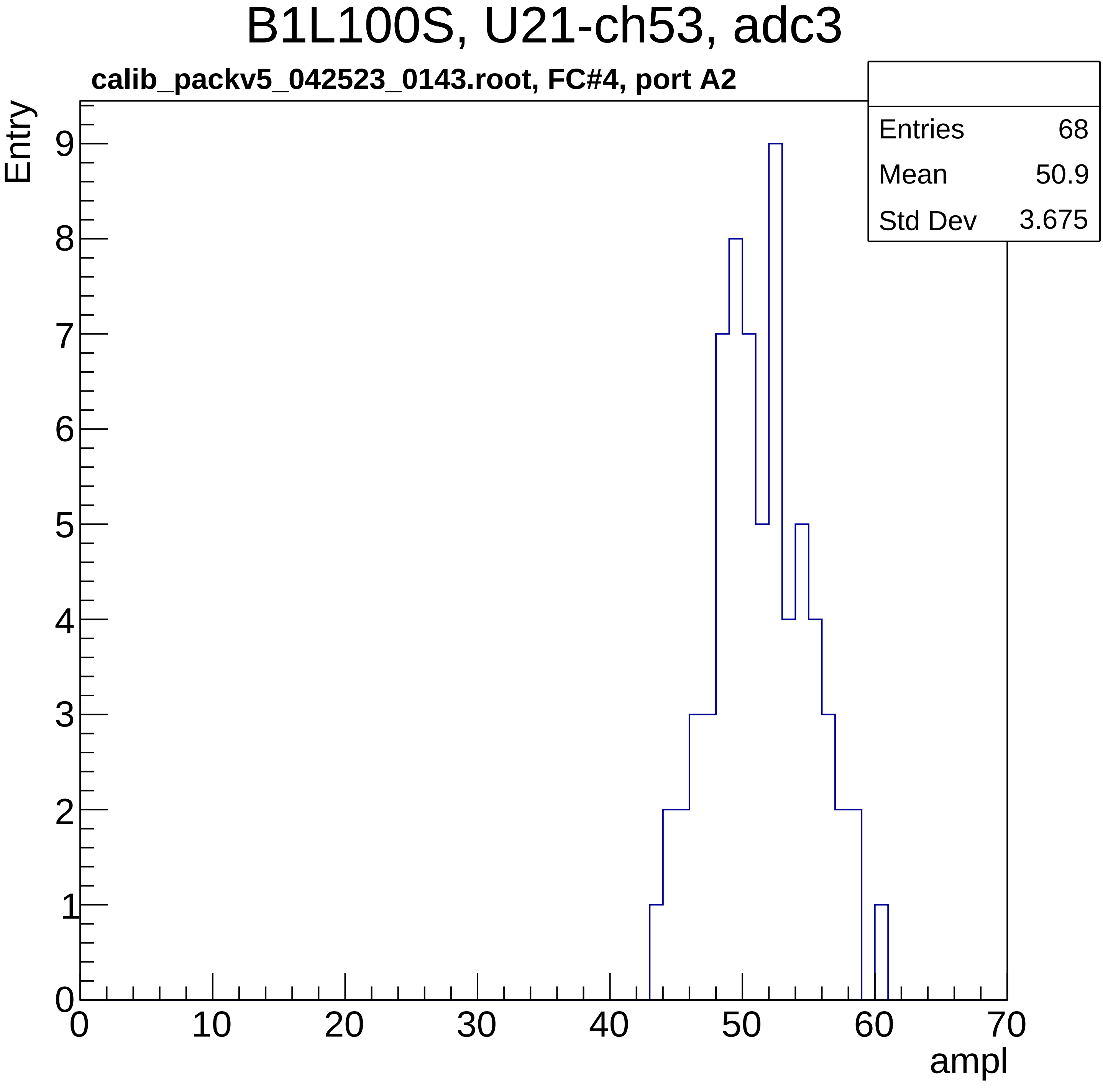


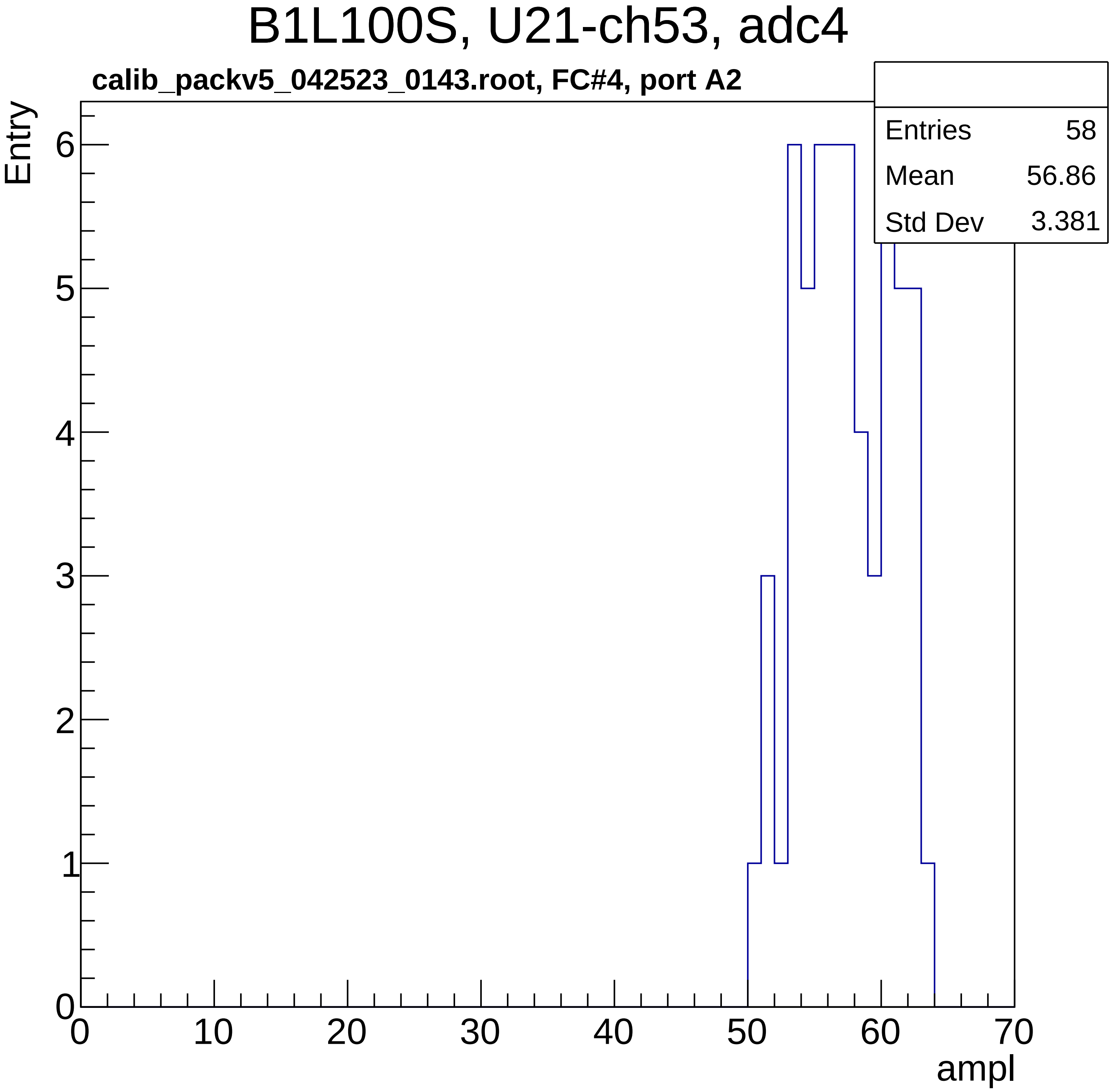

B1L100S, U21-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

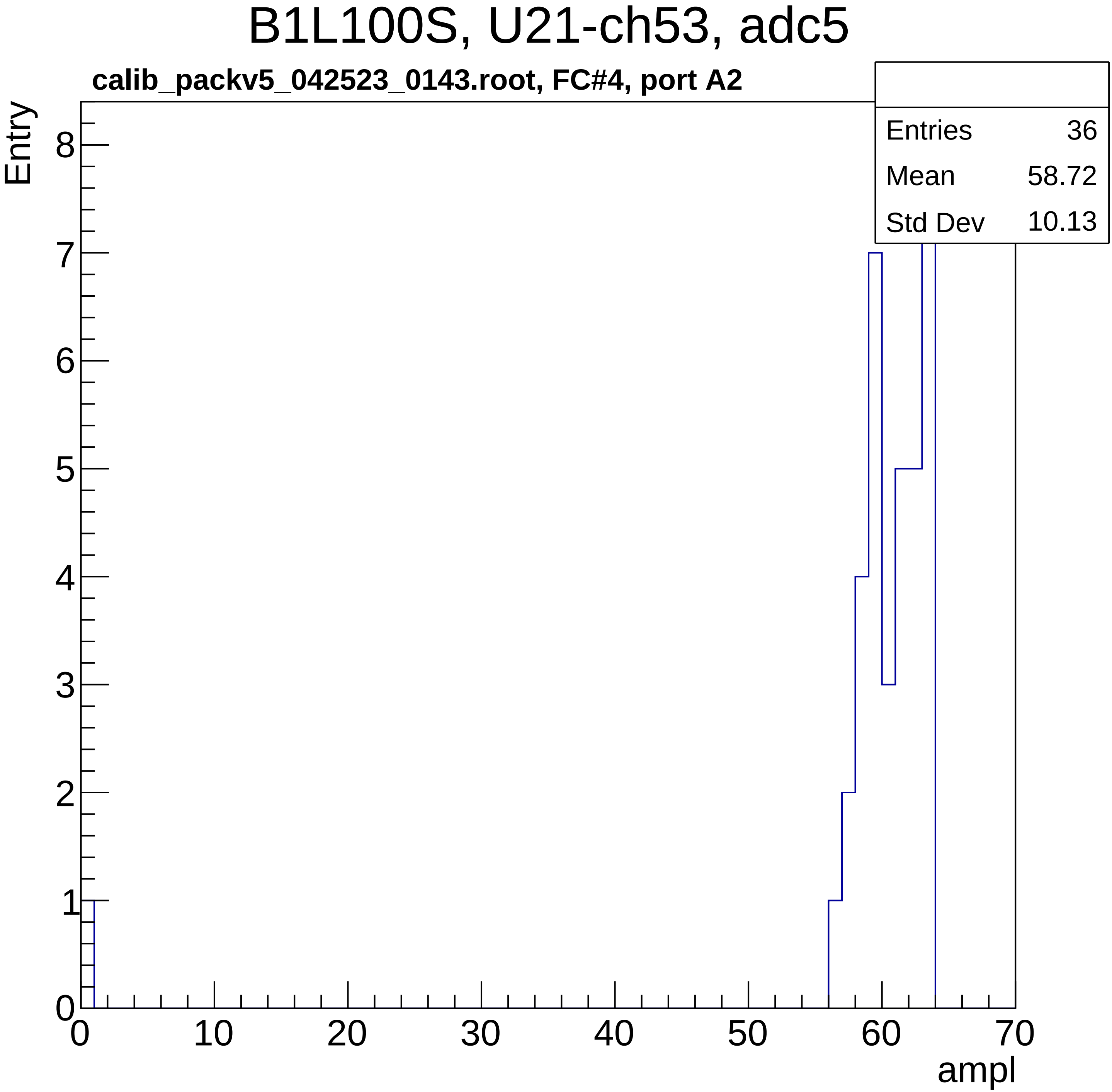

B1L100S, U21-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
Entries 67
Mean 29.88
Std Dev 3.518
Gaus mean : 30.8547
Gaus Width: 3.7862


B1L100S, U21-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 70
Mean 36.74
Std Dev 3.362
Gaus mean : 37.2011
Gaus Width: 3.1226
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 69
Mean 44.03
Std Dev 3.097
Gaus mean : 44.4182
Gaus Width: 3.3028
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

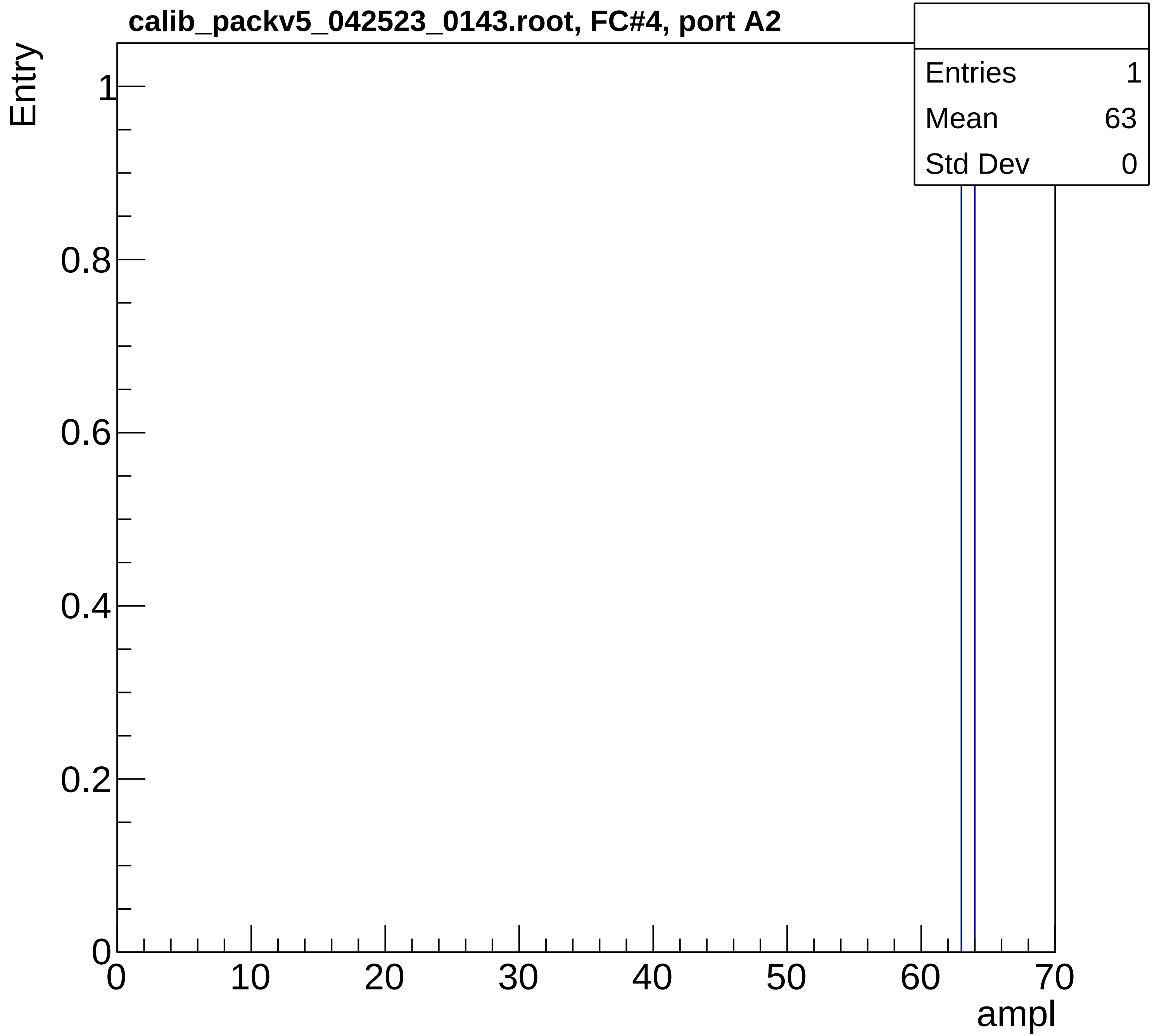

B1L100S, U21-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.9
Std Dev 3.675
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

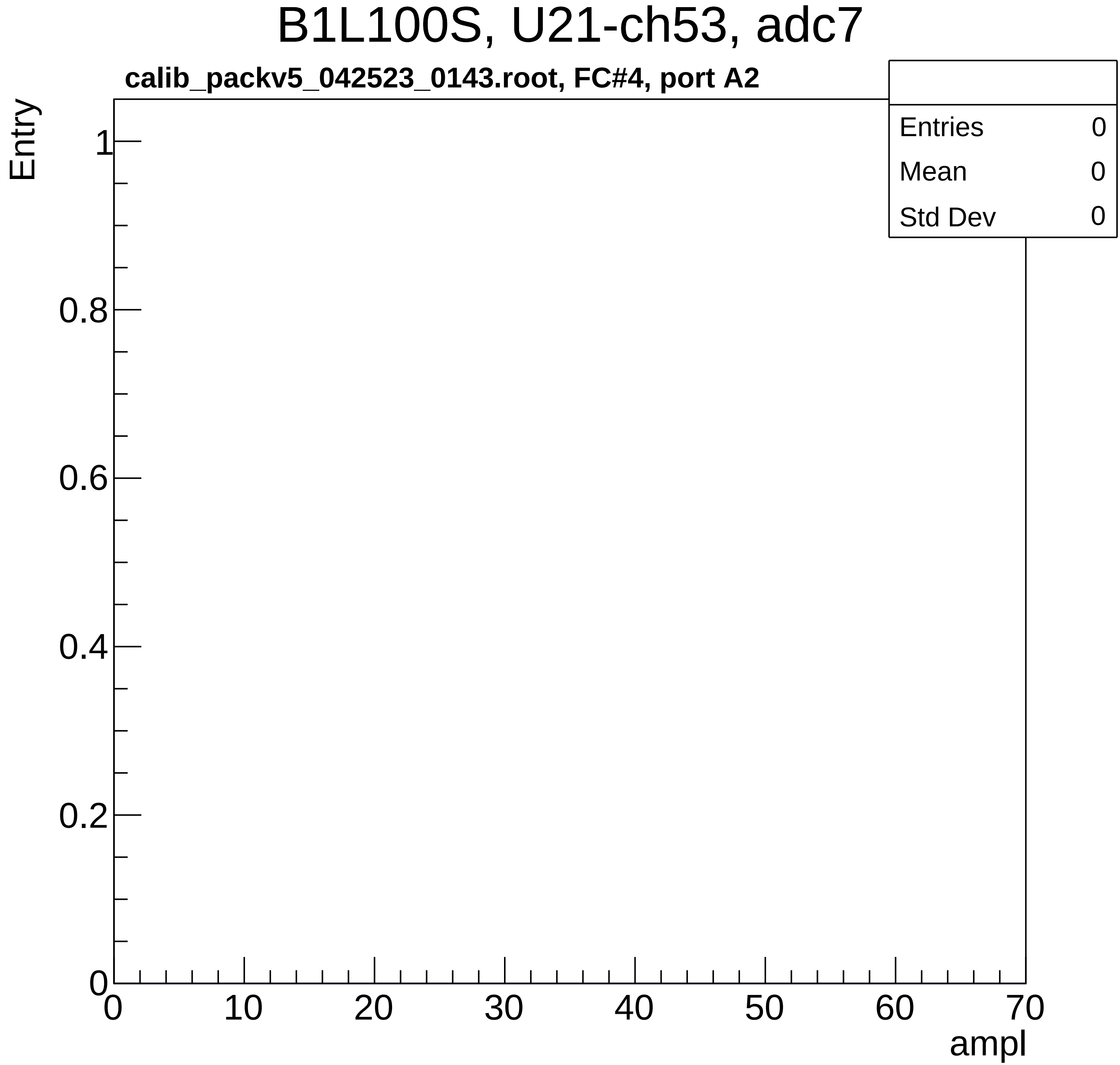

B1L100S, U21-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.86
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

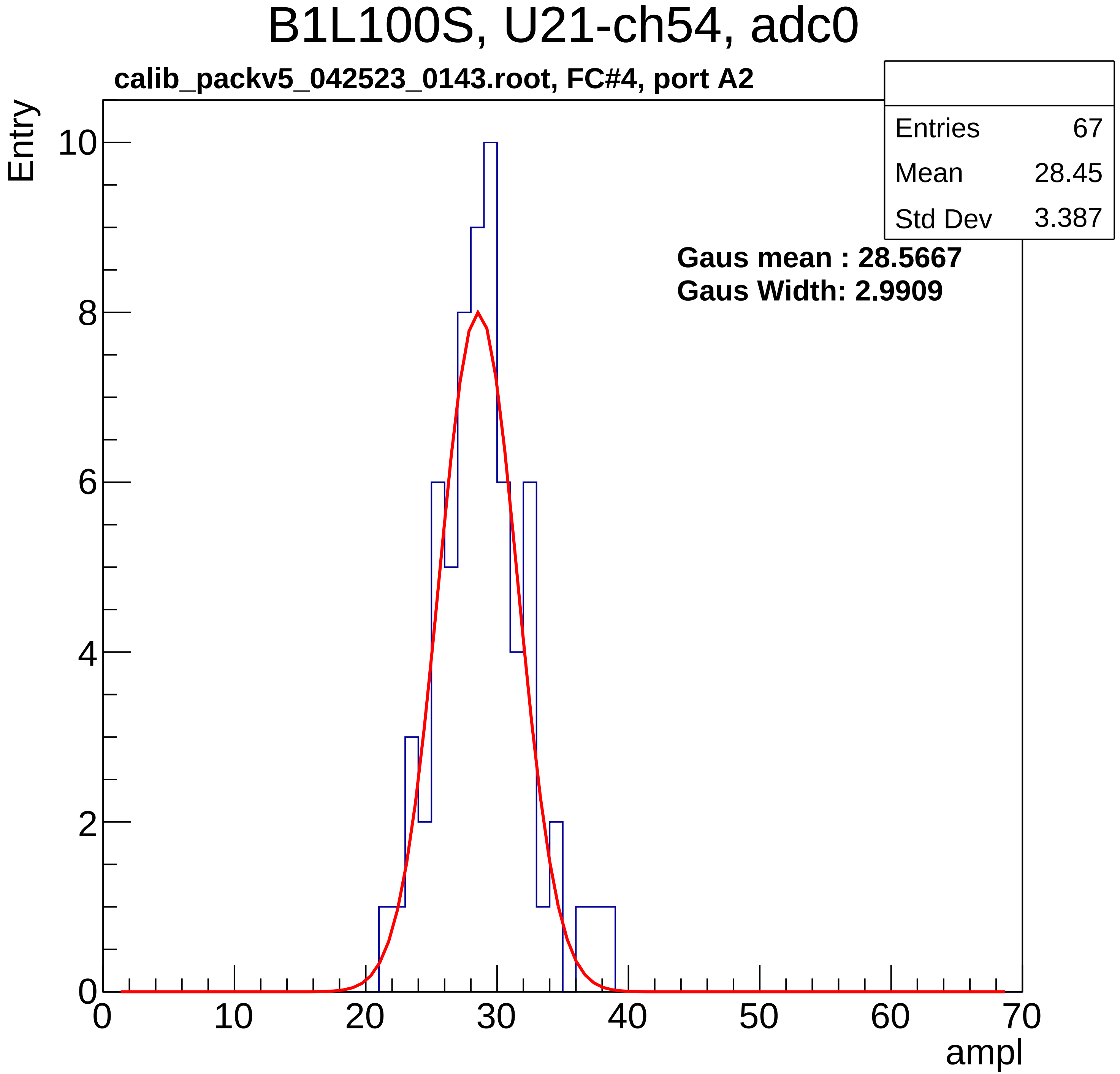

B1L100S, U21-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 36
Mean 58.72
Std Dev 10.13
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

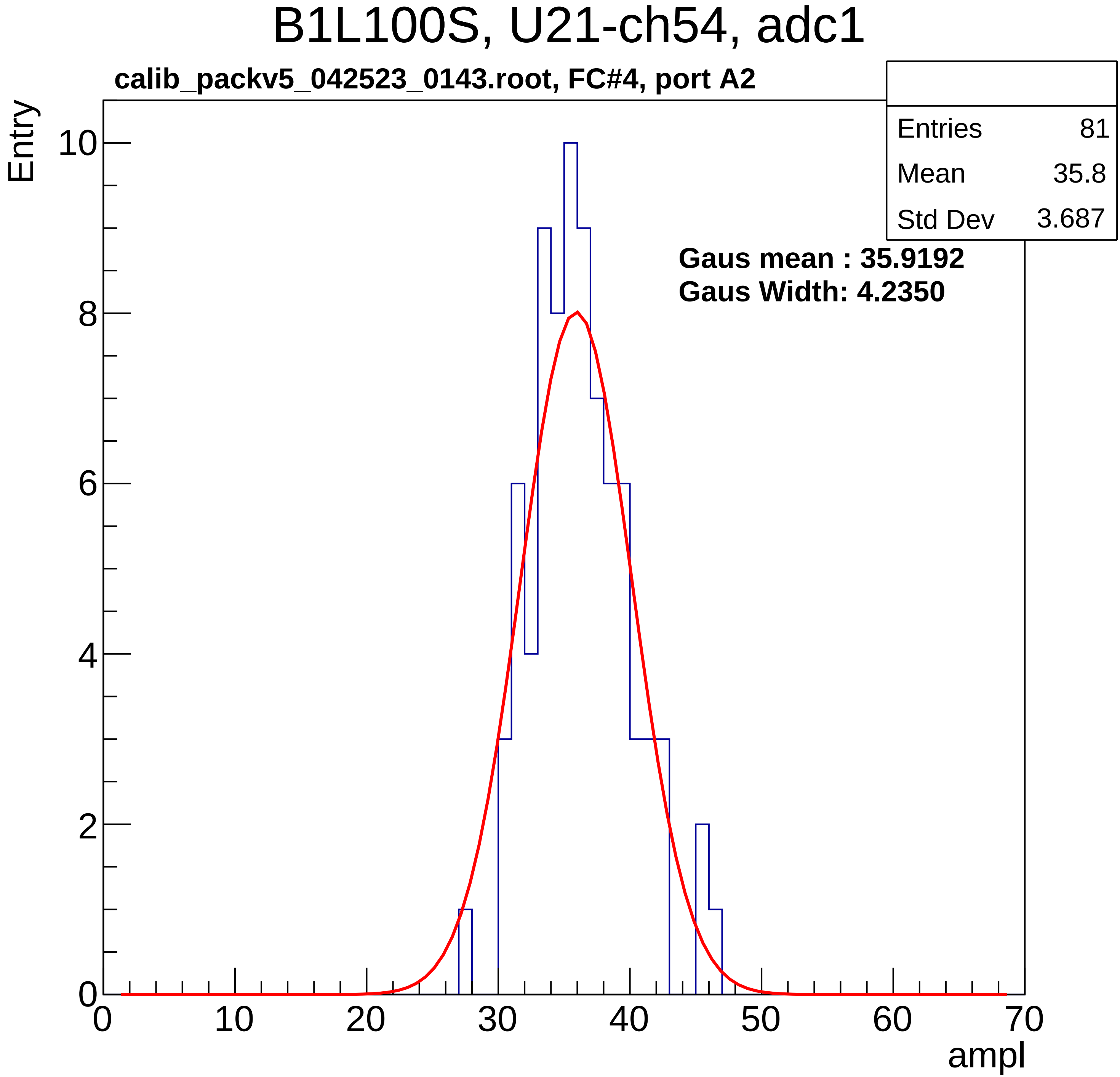
B1L100S, U21-ch53, adc6

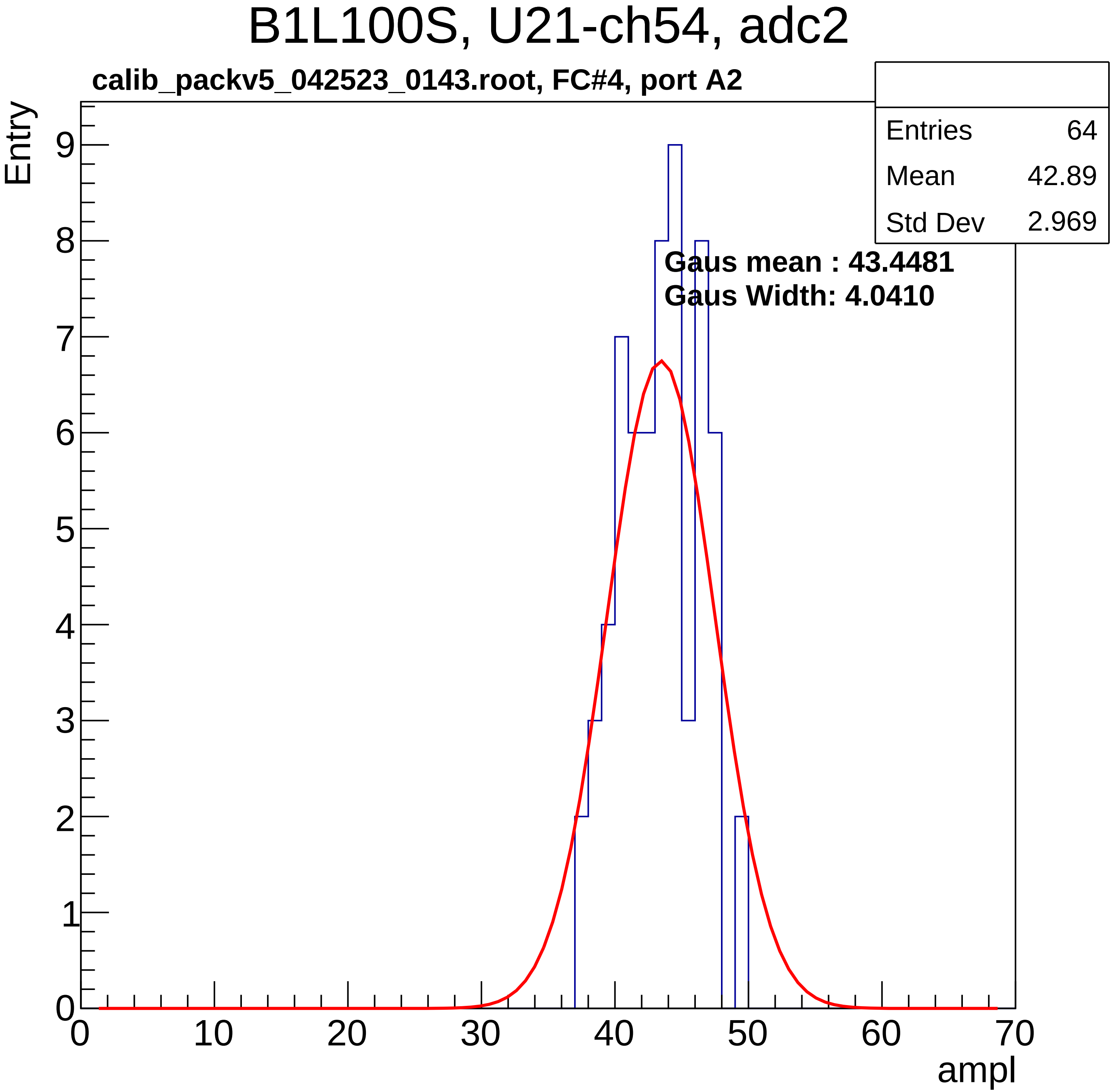
calib_packv5_042523_0143.root, FC#4, port A2

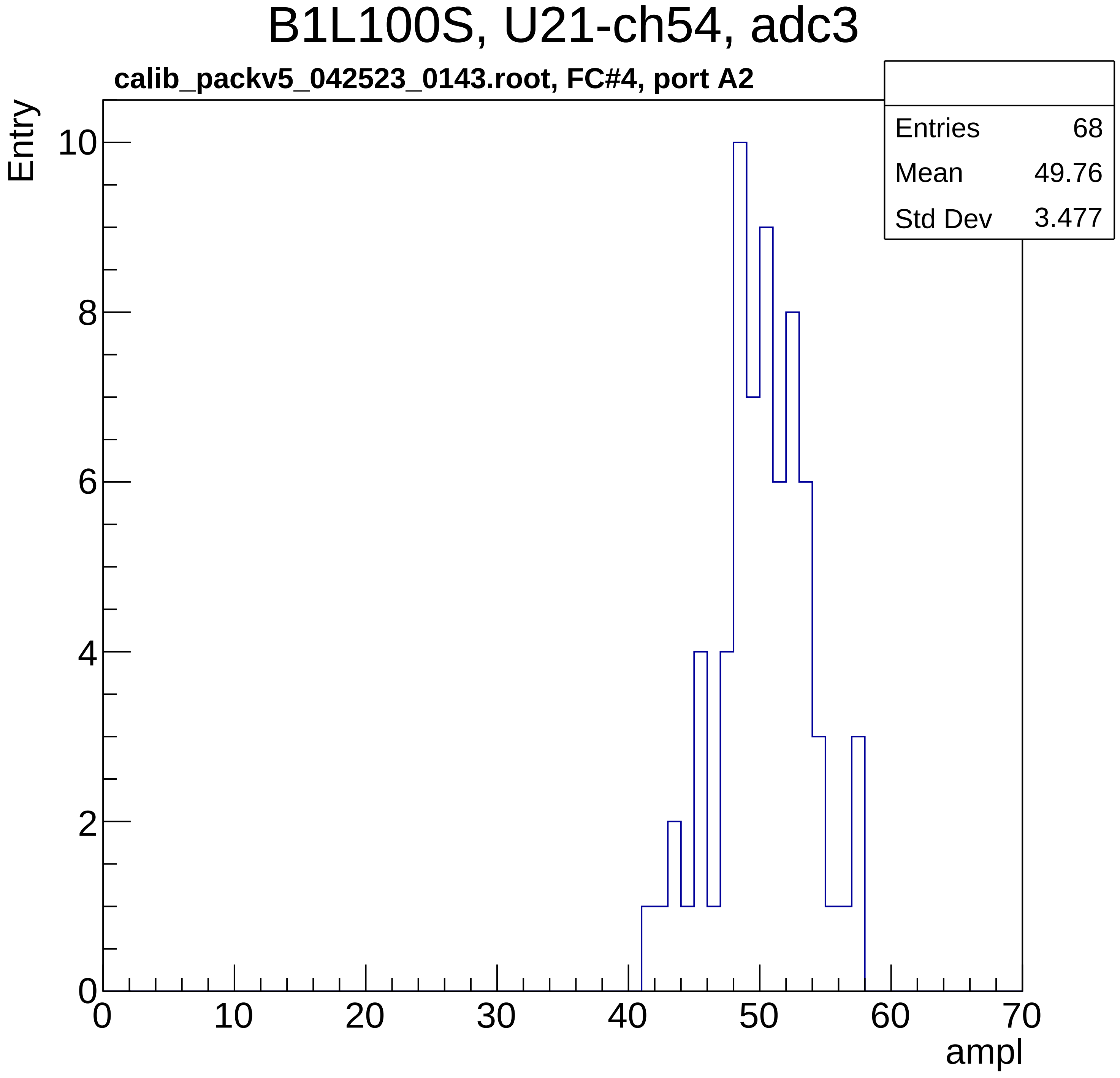



B1L100S, U21-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

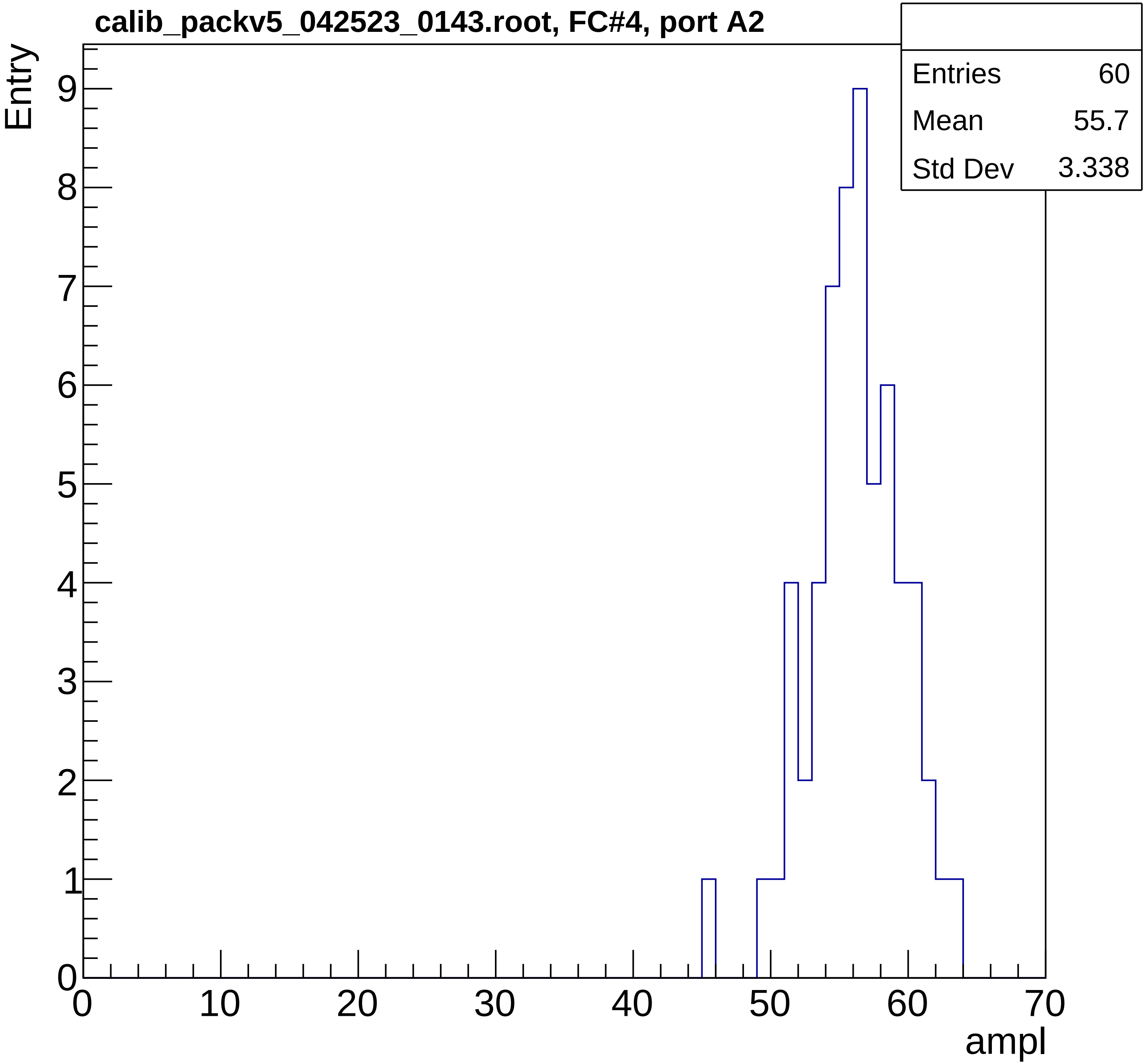

B1L100S, U21-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.45
Std Dev 3.387
Gaus mean : 28.5667
Gaus Width: 2.9909
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

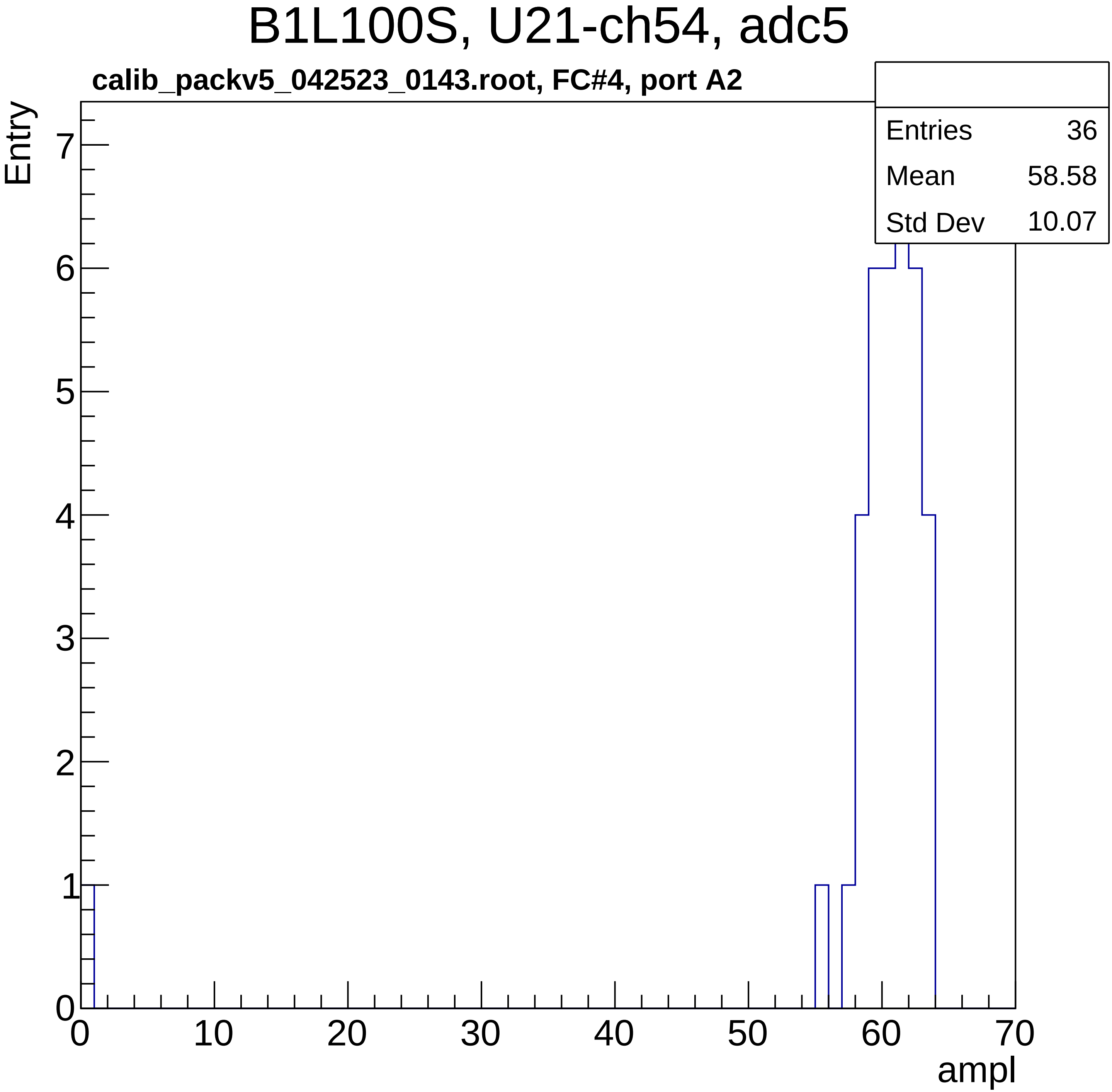

B1L100S, U21-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.8
Std Dev 3.687
Gaus mean : 35.9192
Gaus Width: 4.2350
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

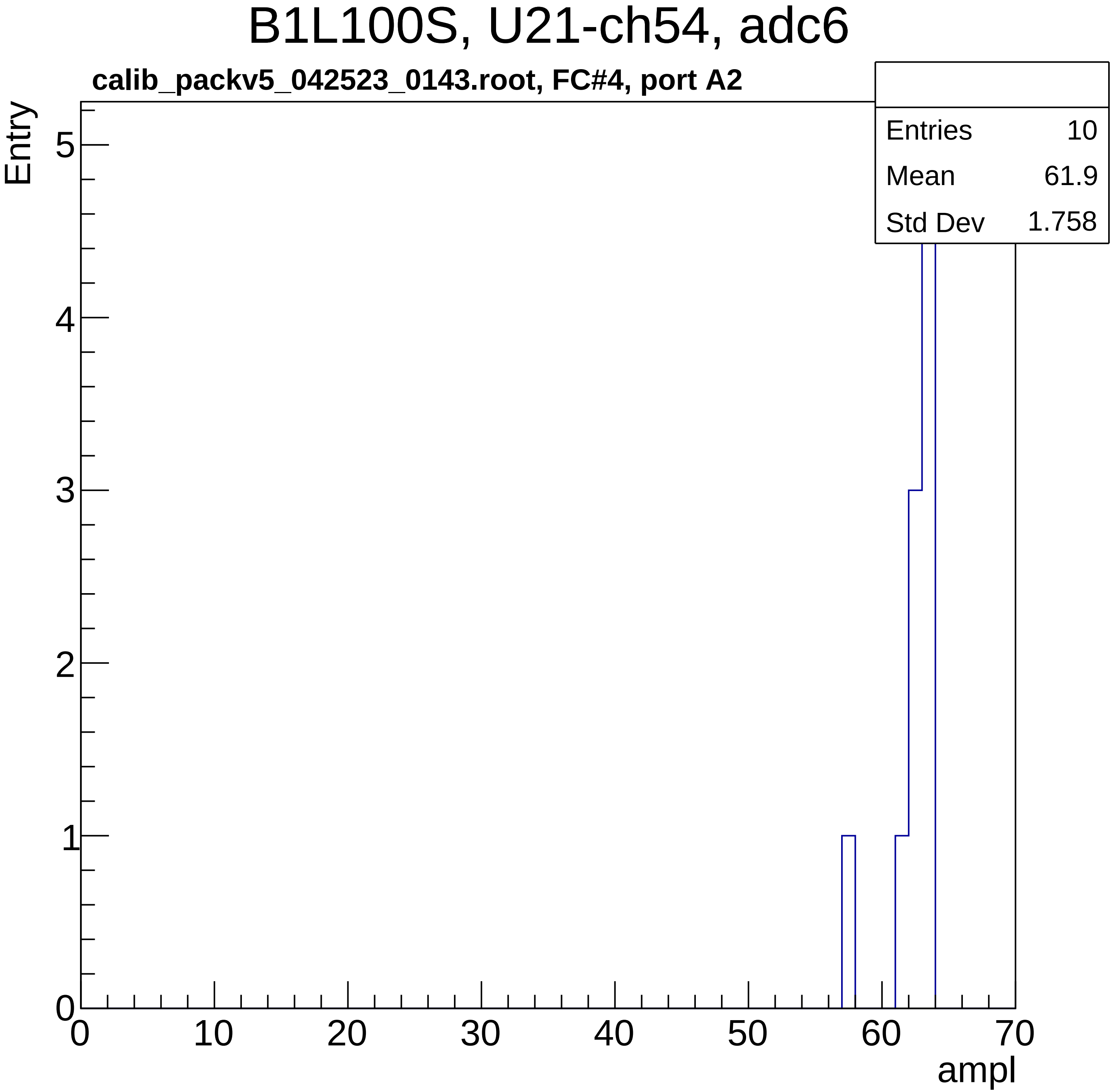

B1L100S, U21-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 64
Mean 42.89
Std Dev 2.969
Gaus mean : 43.4481
Gaus Width: 4.0410
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.76
Std Dev 3.477
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

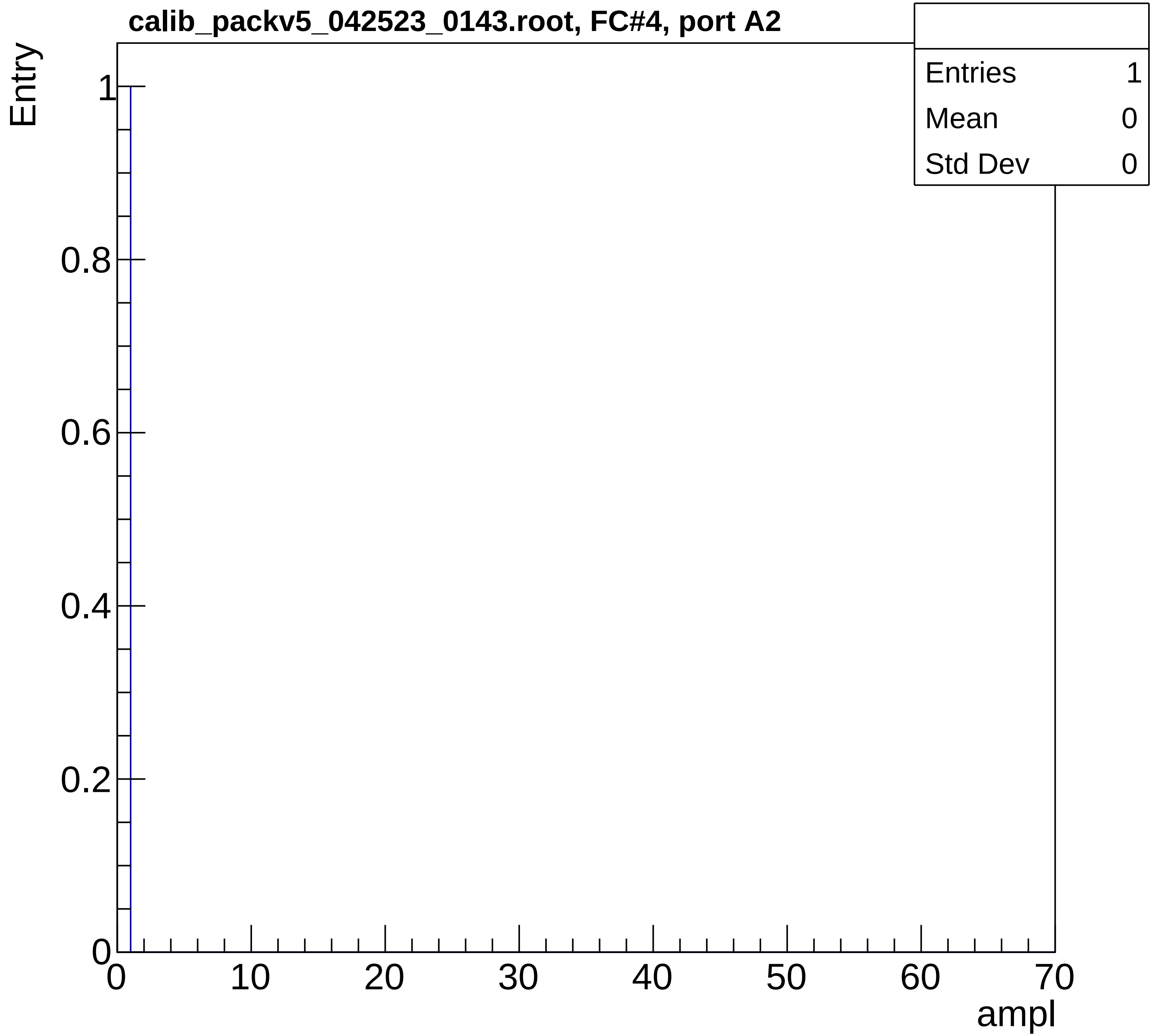
B1L100S, U21-ch54, adc4

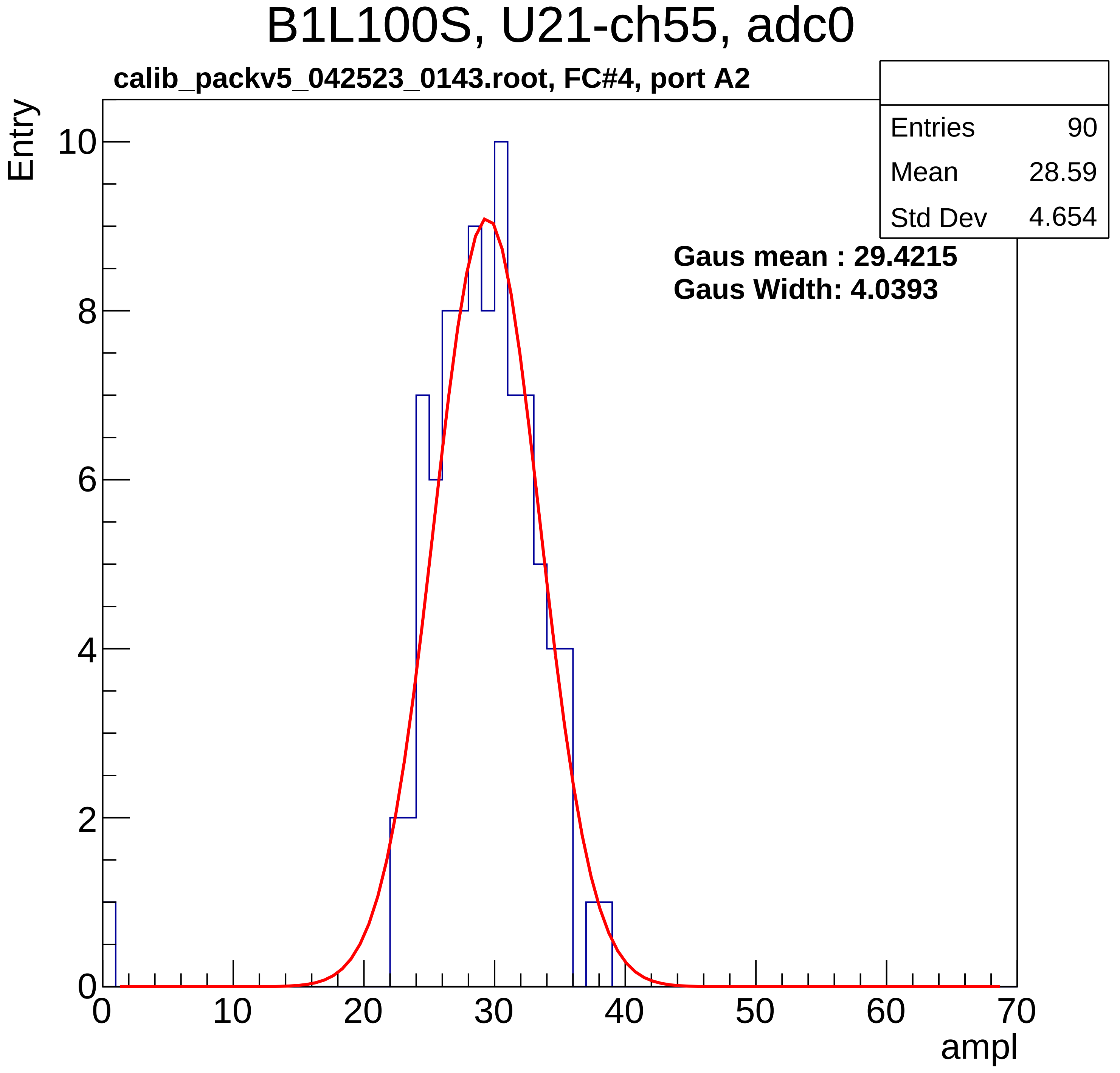


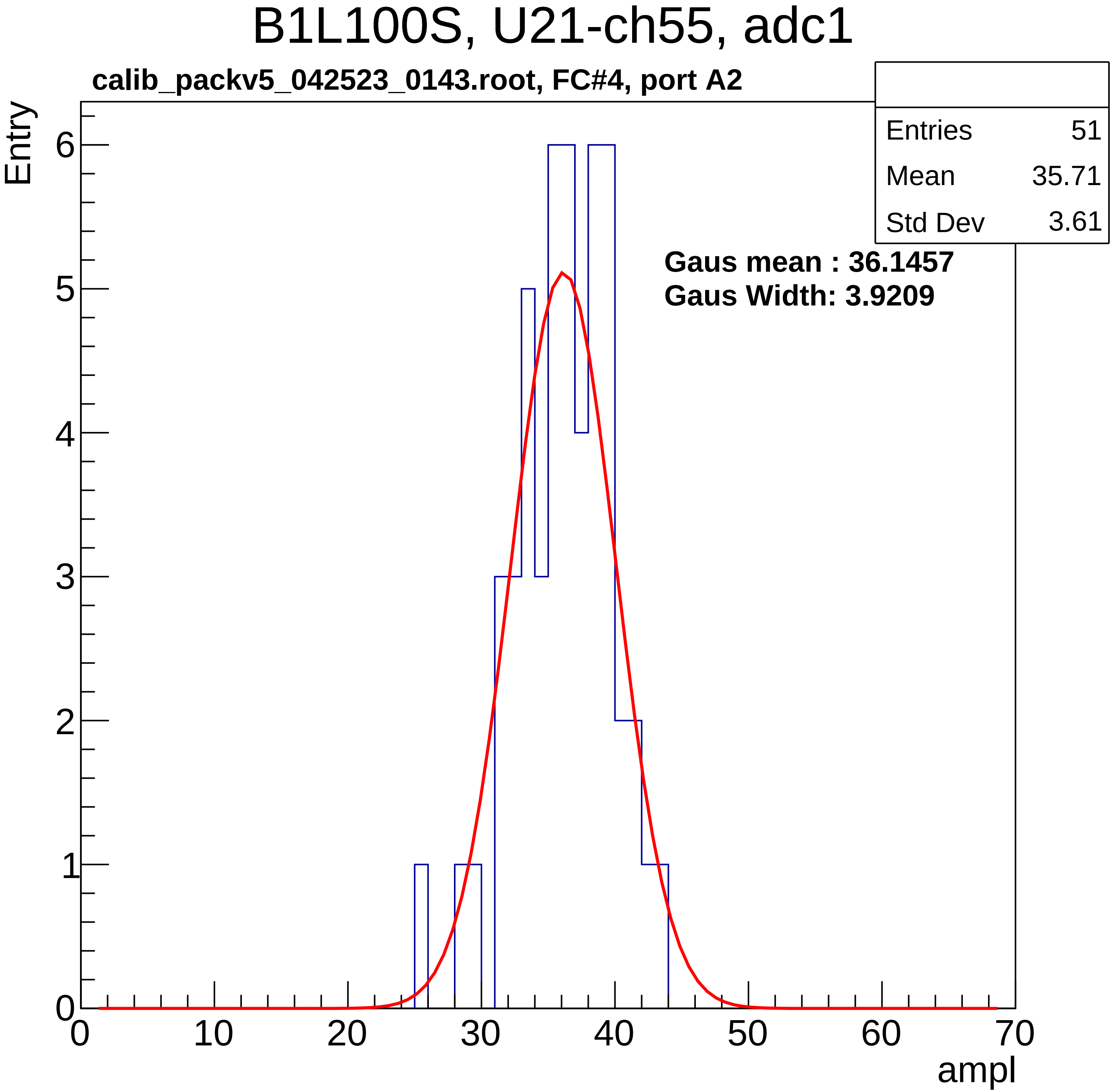

B1L100S, U21-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.58
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.9
Std Dev 1.758
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

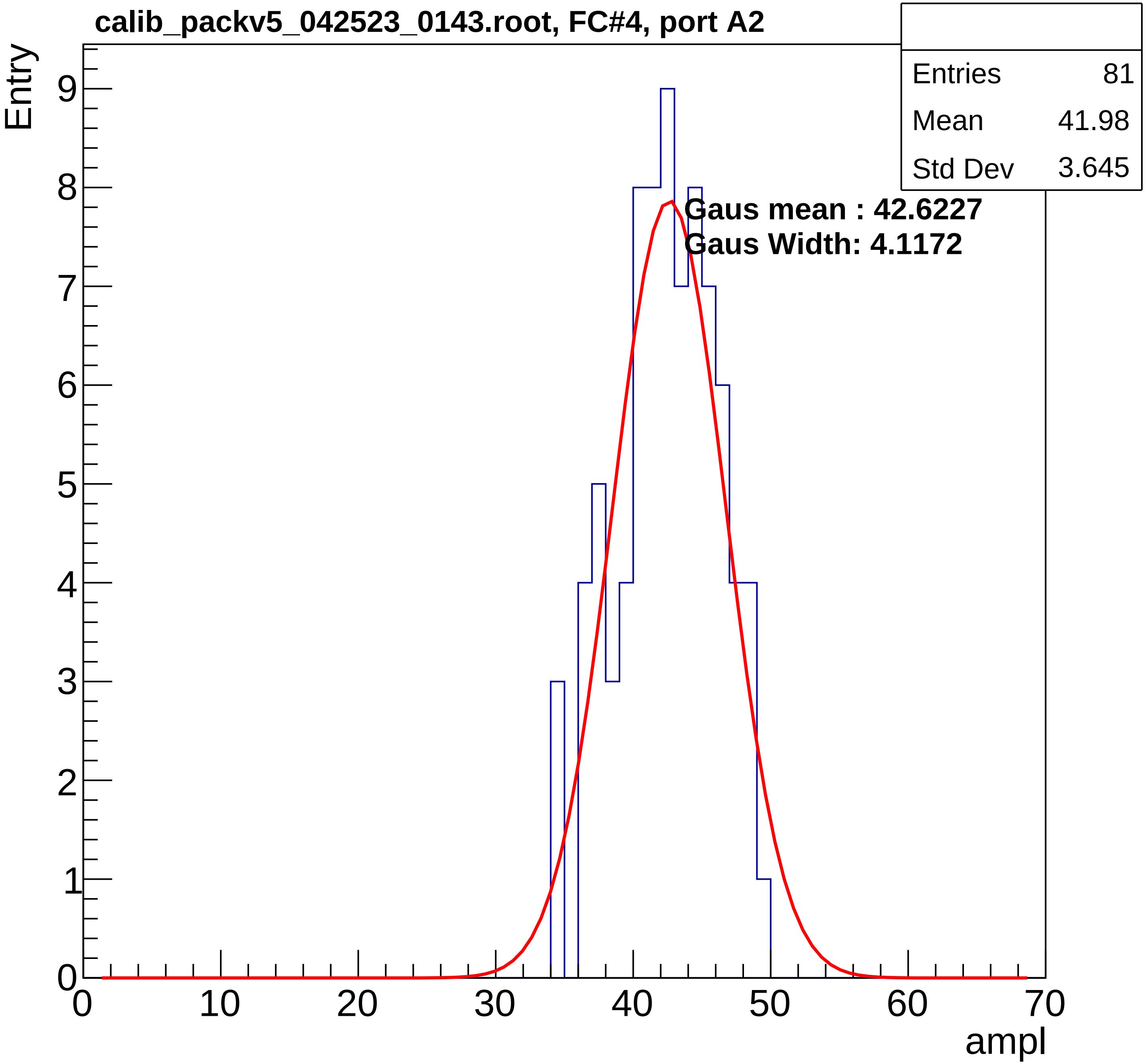
B1L100S, U21-ch54, adc7




B1L100S, U21-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 28.59
Std Dev 4.654
Gaus mean : 29.4215
Gaus Width: 4.0393
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

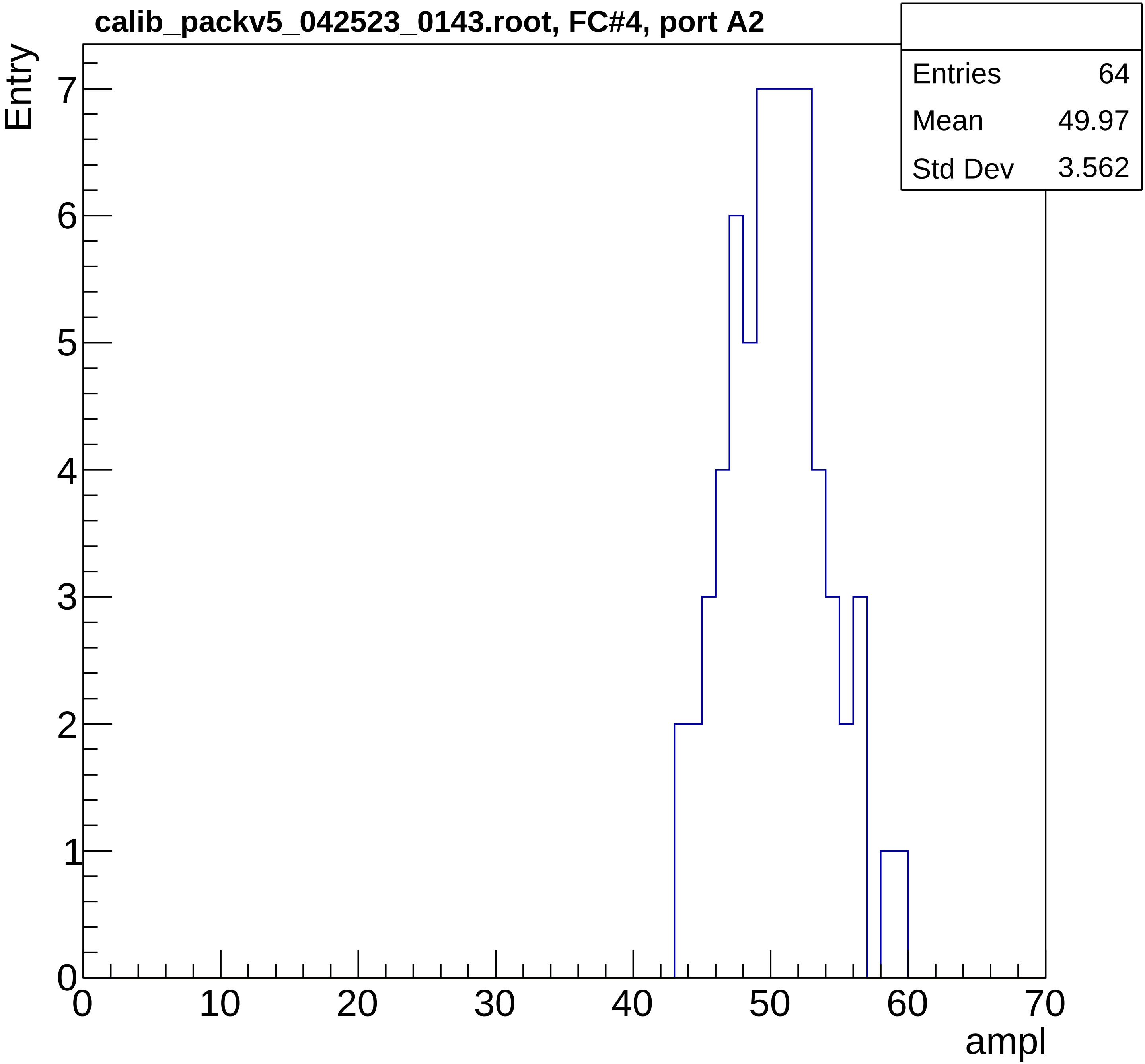

B1L100S, U21-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 35.71
Std Dev 3.61
Gaus mean : 36.1457
Gaus Width: 3.9209
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

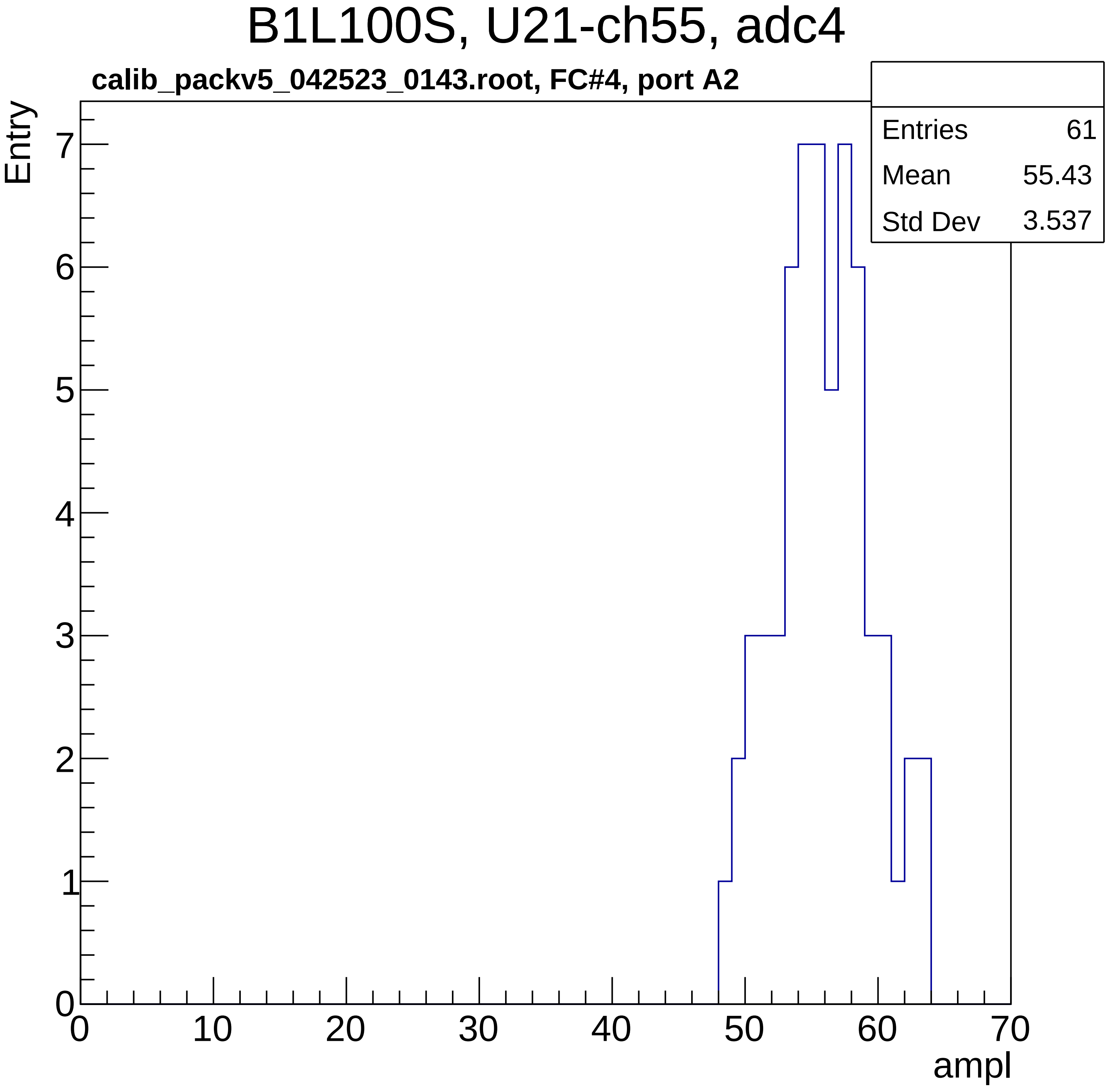
B1L100S, U21-ch55, adc2

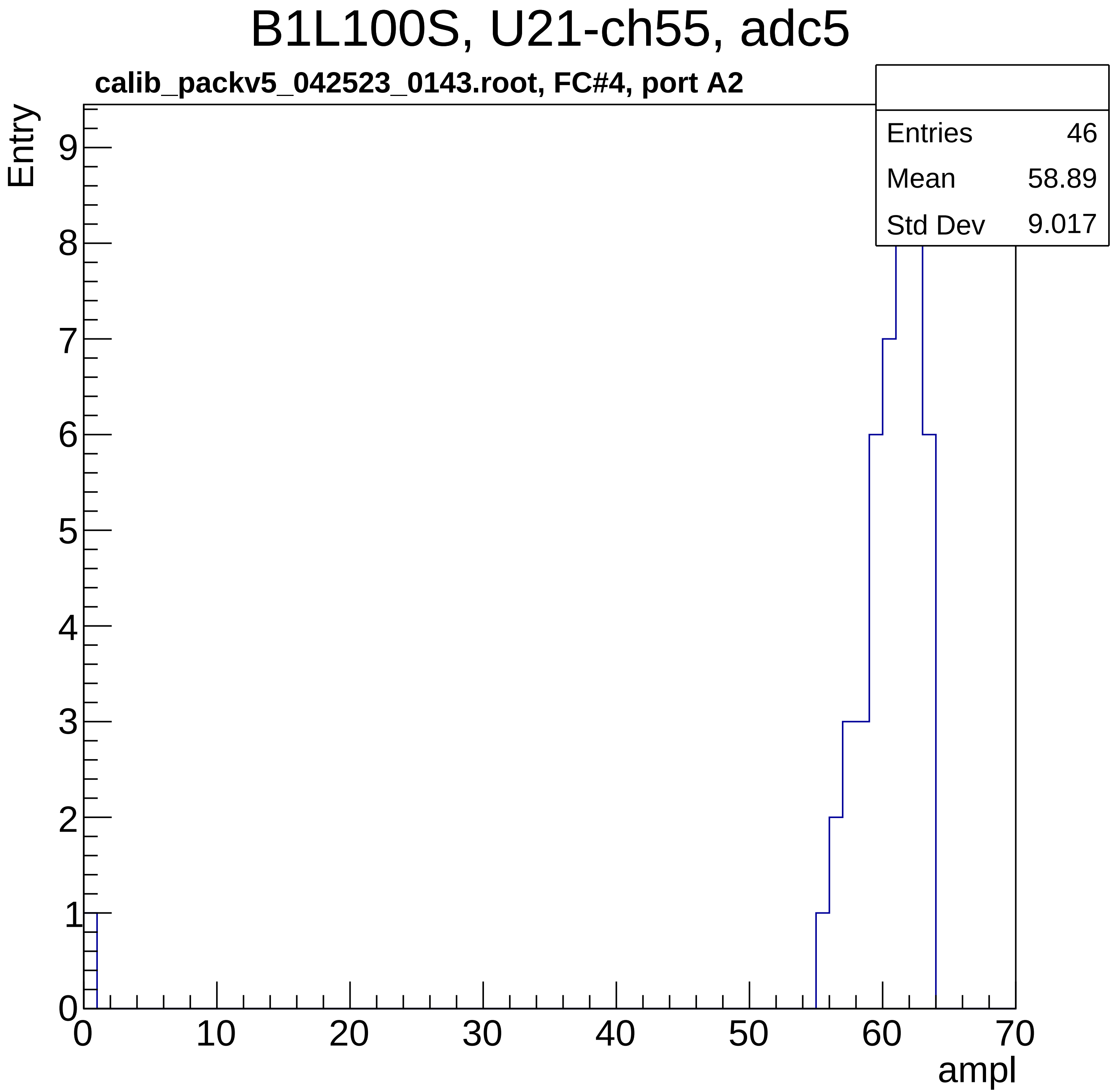


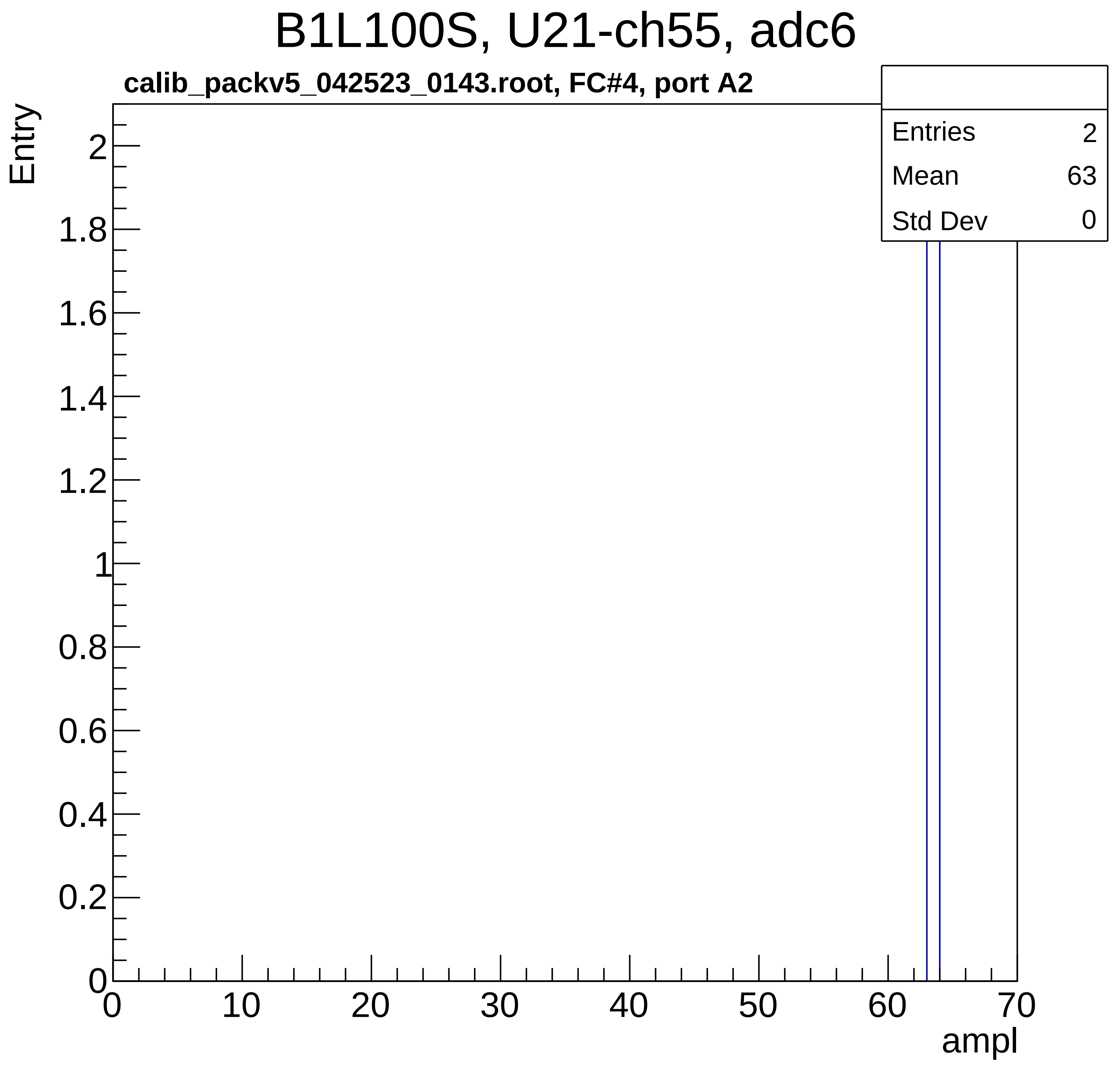
B1L100S, U21-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

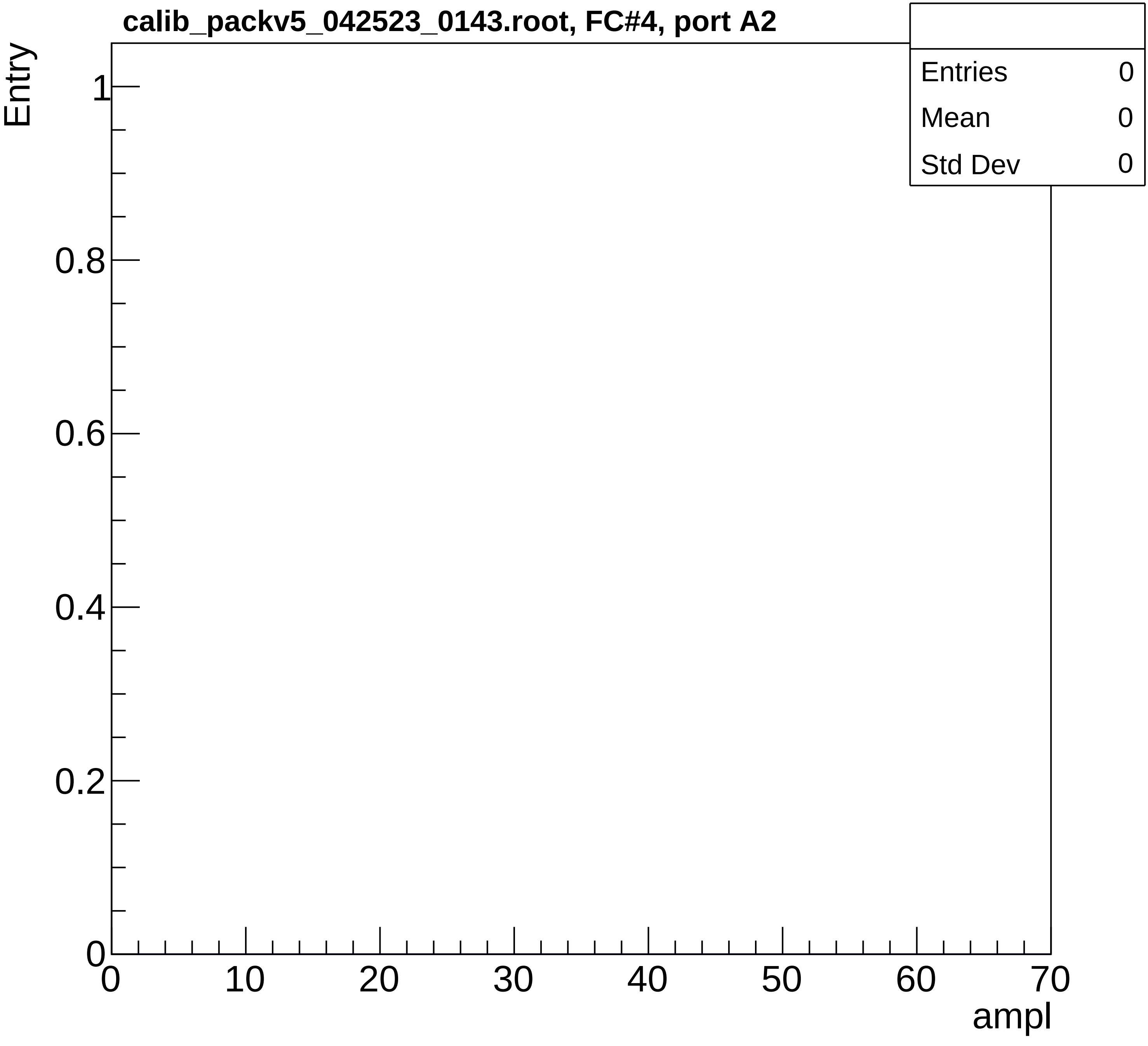


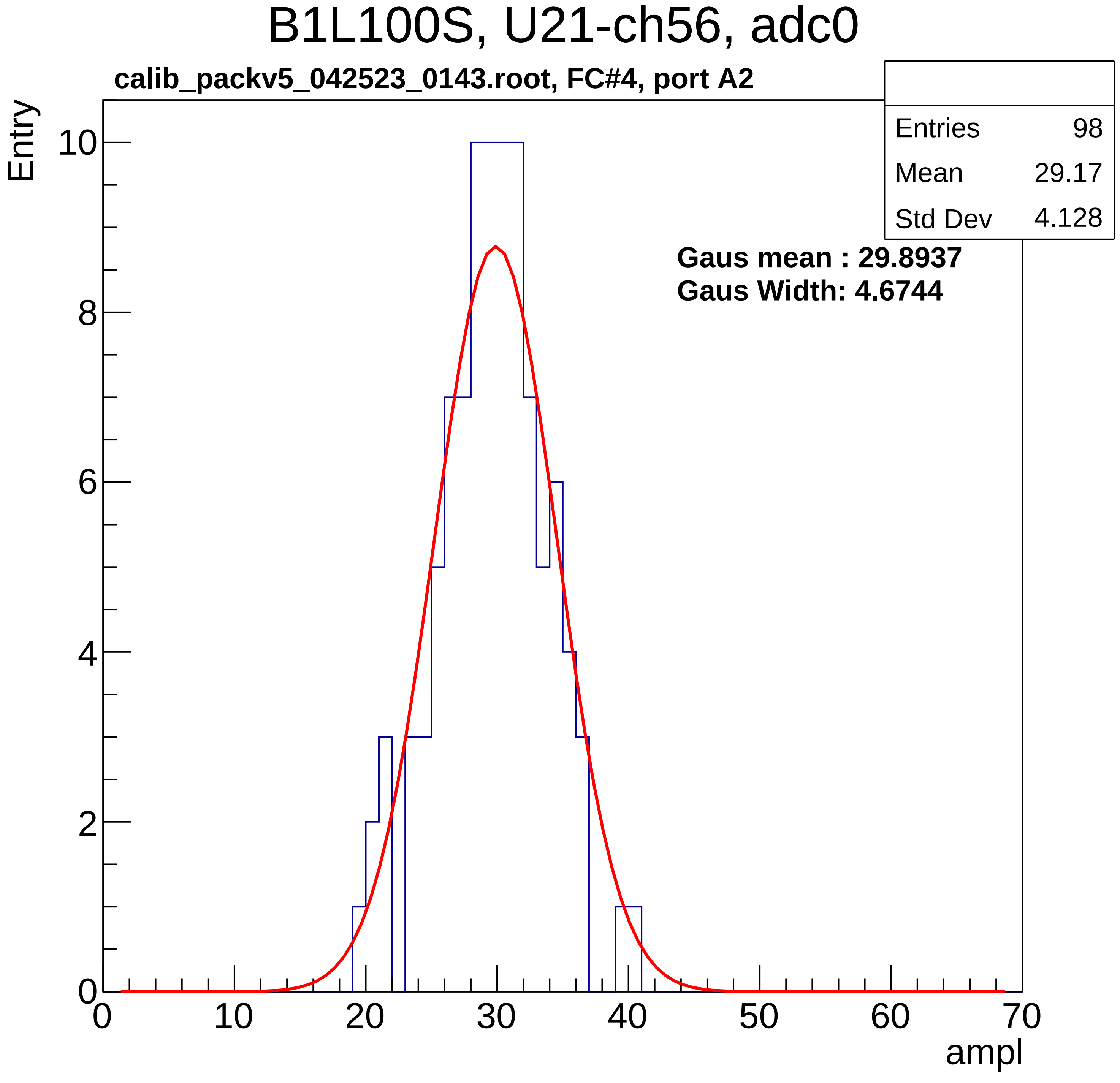

B1L100S, U21-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.43
Std Dev 3.537
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

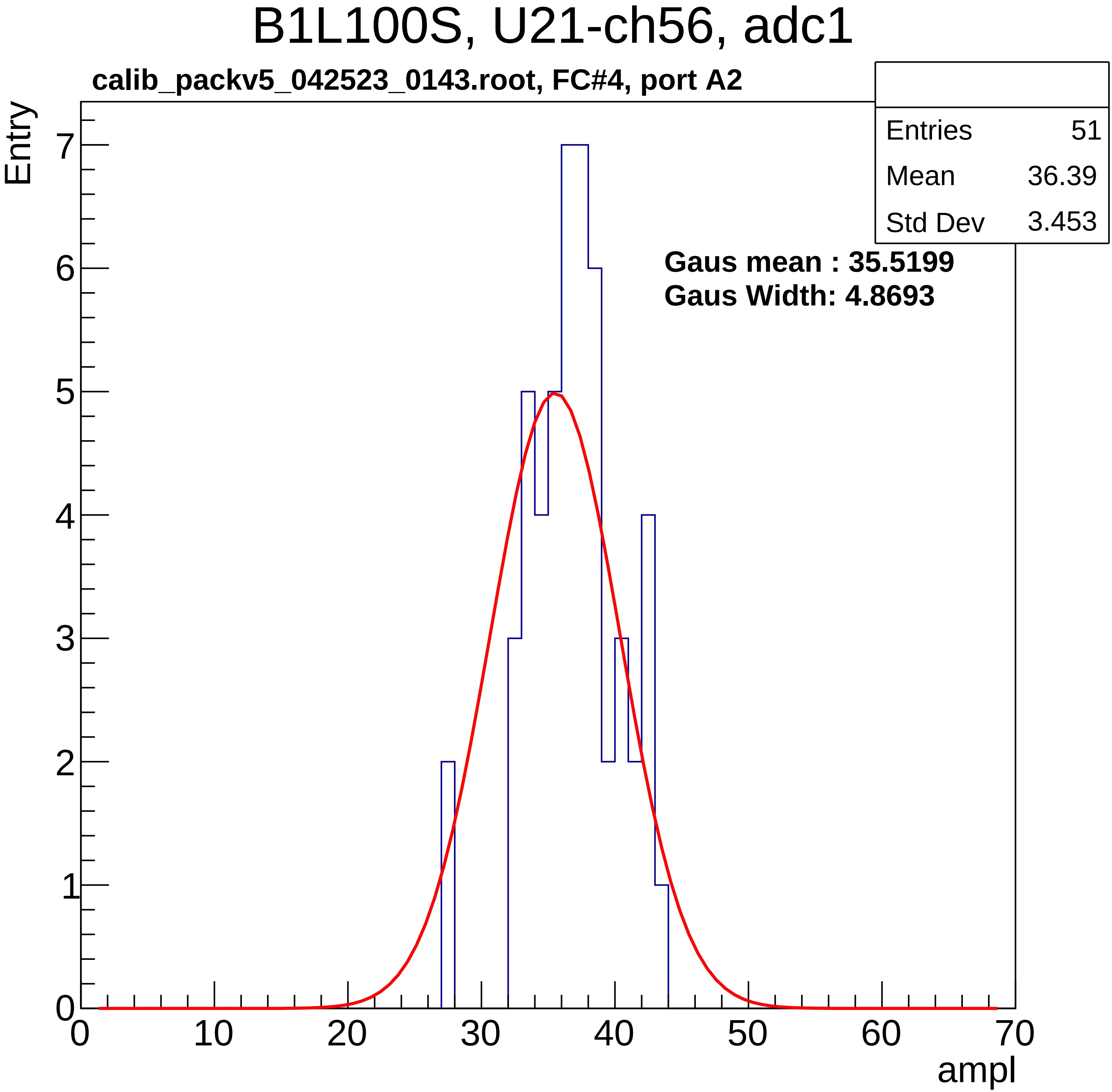

B1L100S, U21-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.89
Std Dev 9.017
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

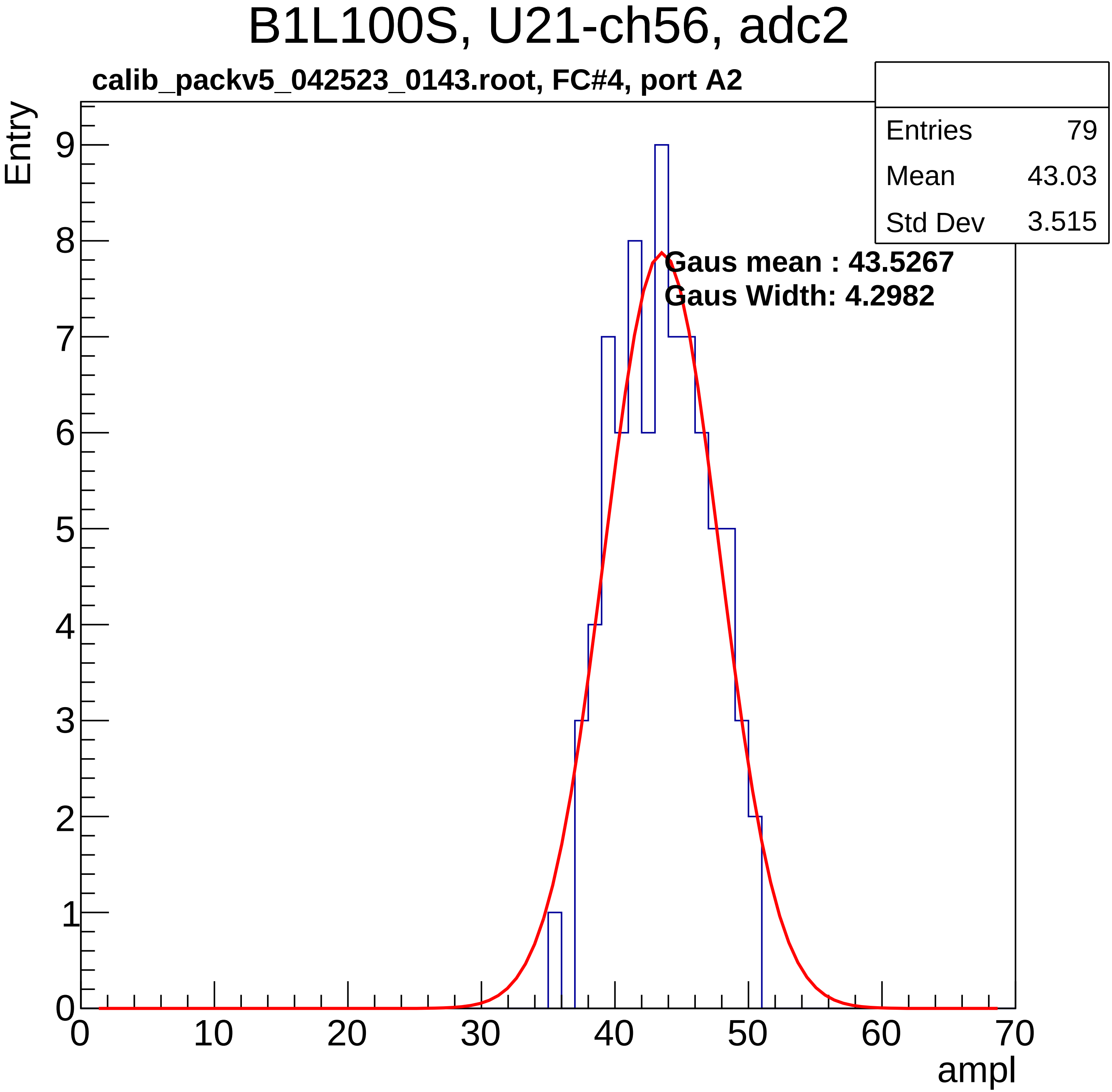

B1L100S, U21-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

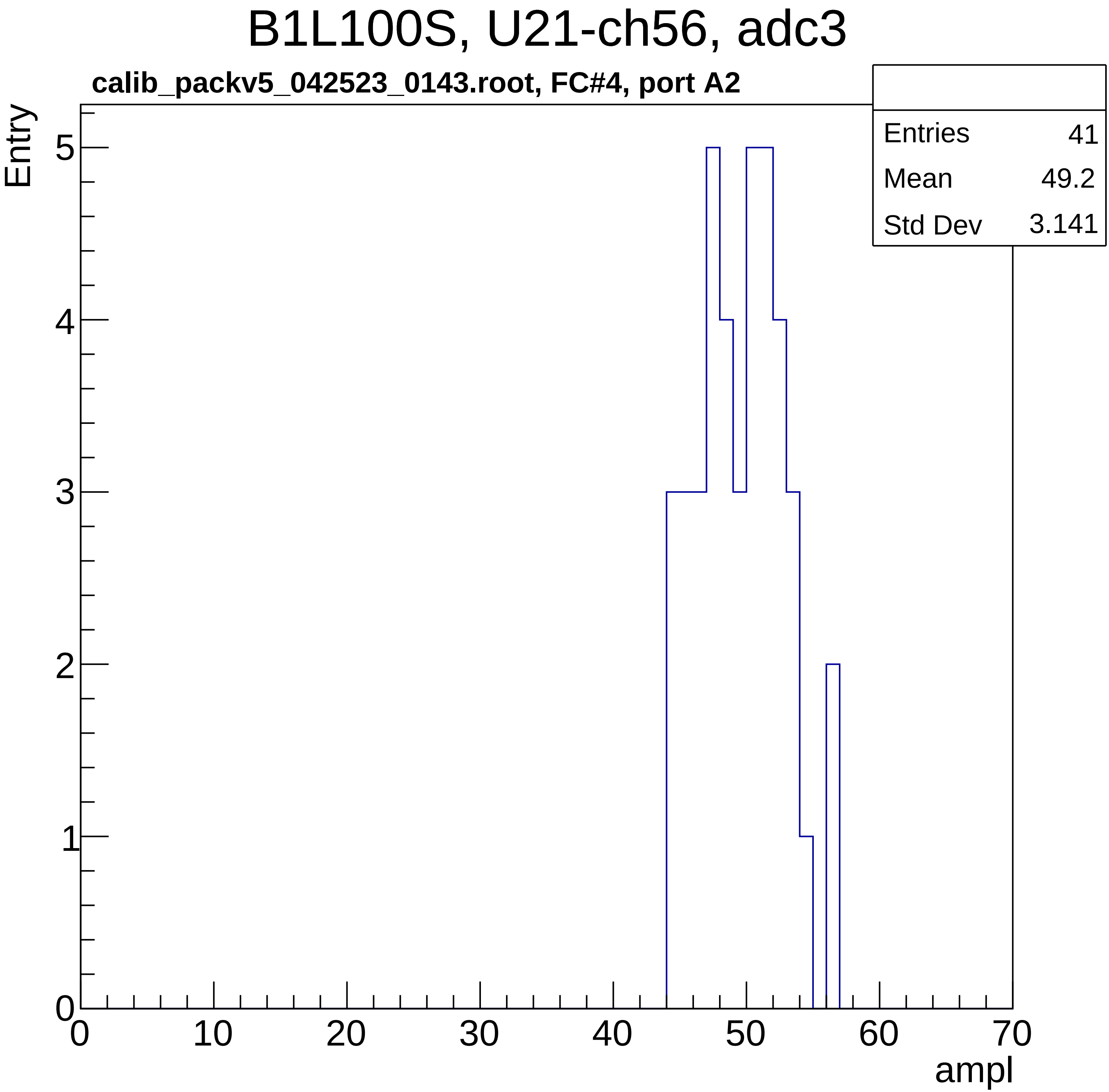
B1L100S, U21-ch55, adc7



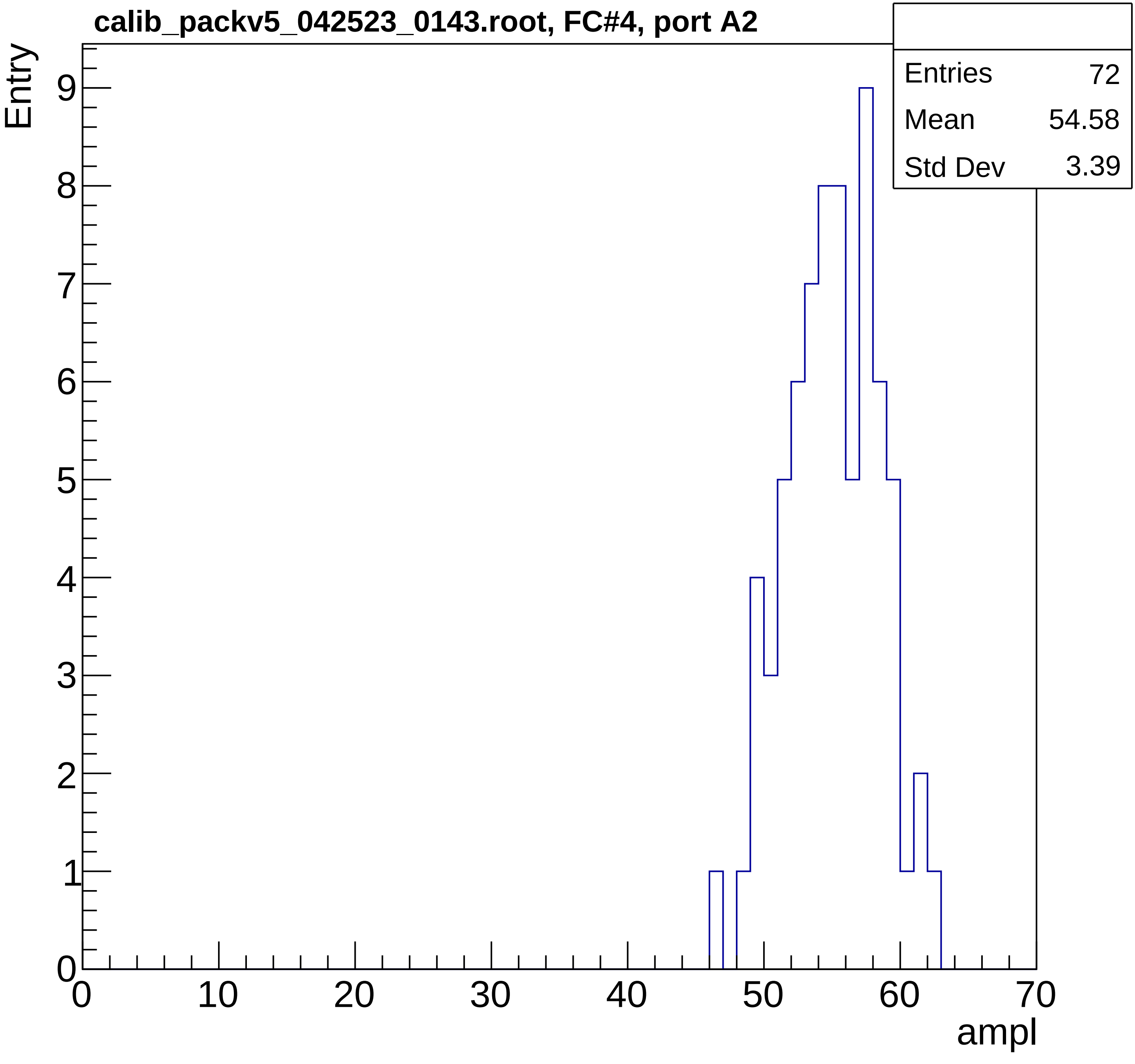

B1L100S, U21-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 98
Mean 29.17
Std Dev 4.128
Gaus mean : 29.8937
Gaus Width: 4.6744
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

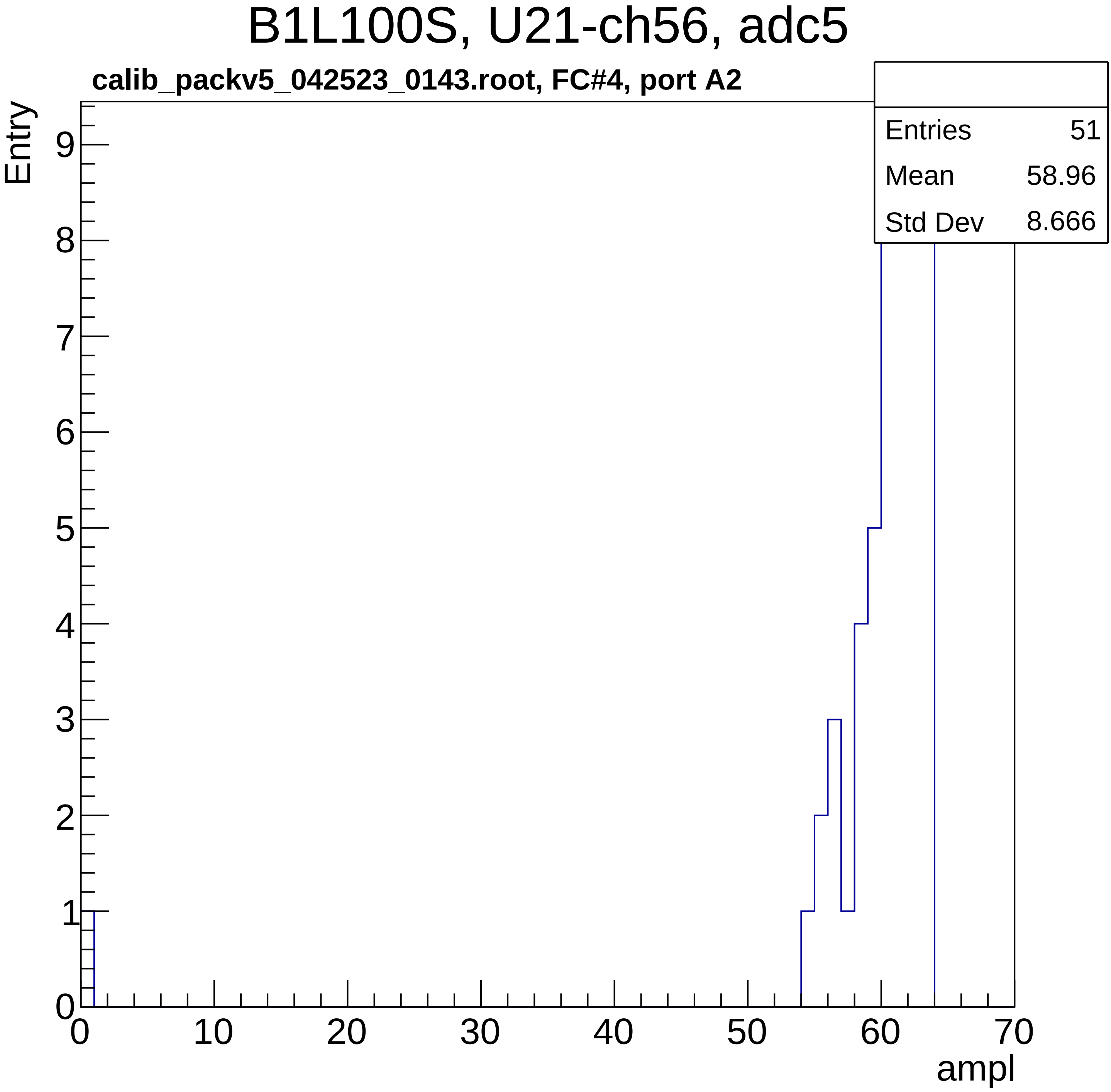

B1L100S, U21-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 36.39
Std Dev 3.453
Gaus mean : 35.5199
Gaus Width: 4.8693
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

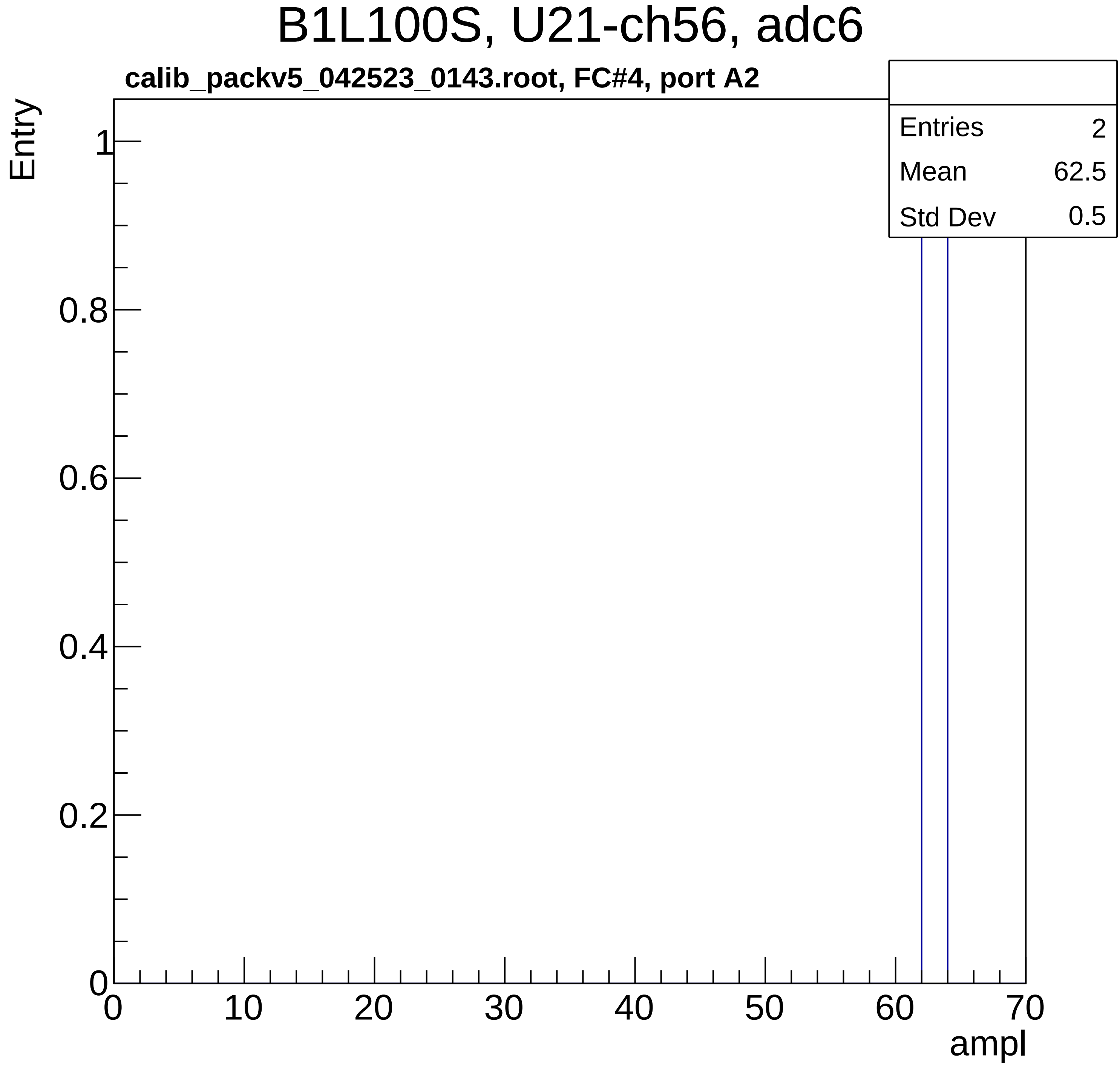

B1L100S, U21-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 43.03
Std Dev 3.515
Gaus mean : 43.5267
Gaus Width: 4.2982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

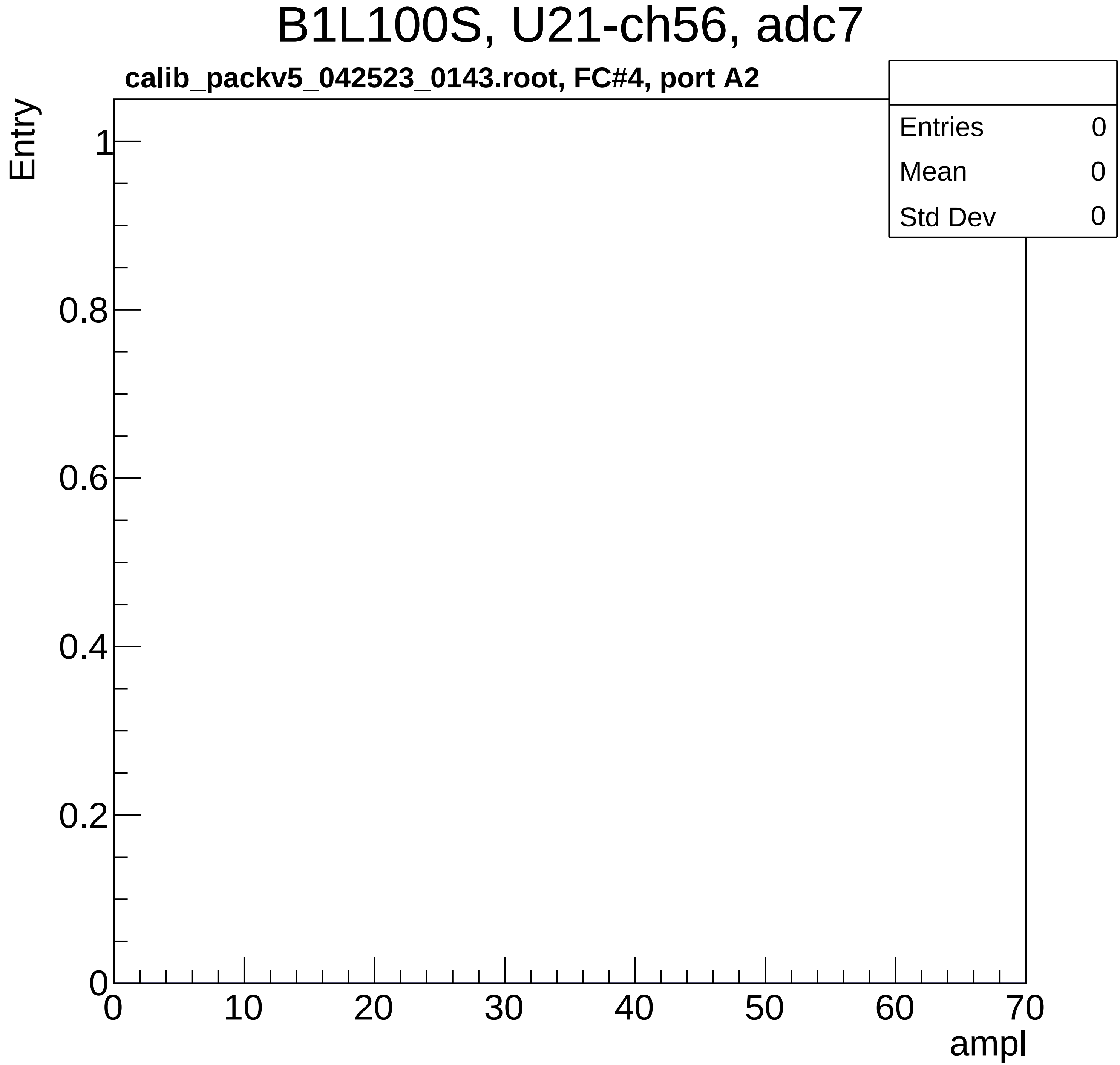

B1L100S, U21-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 49.2
Std Dev 3.141
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

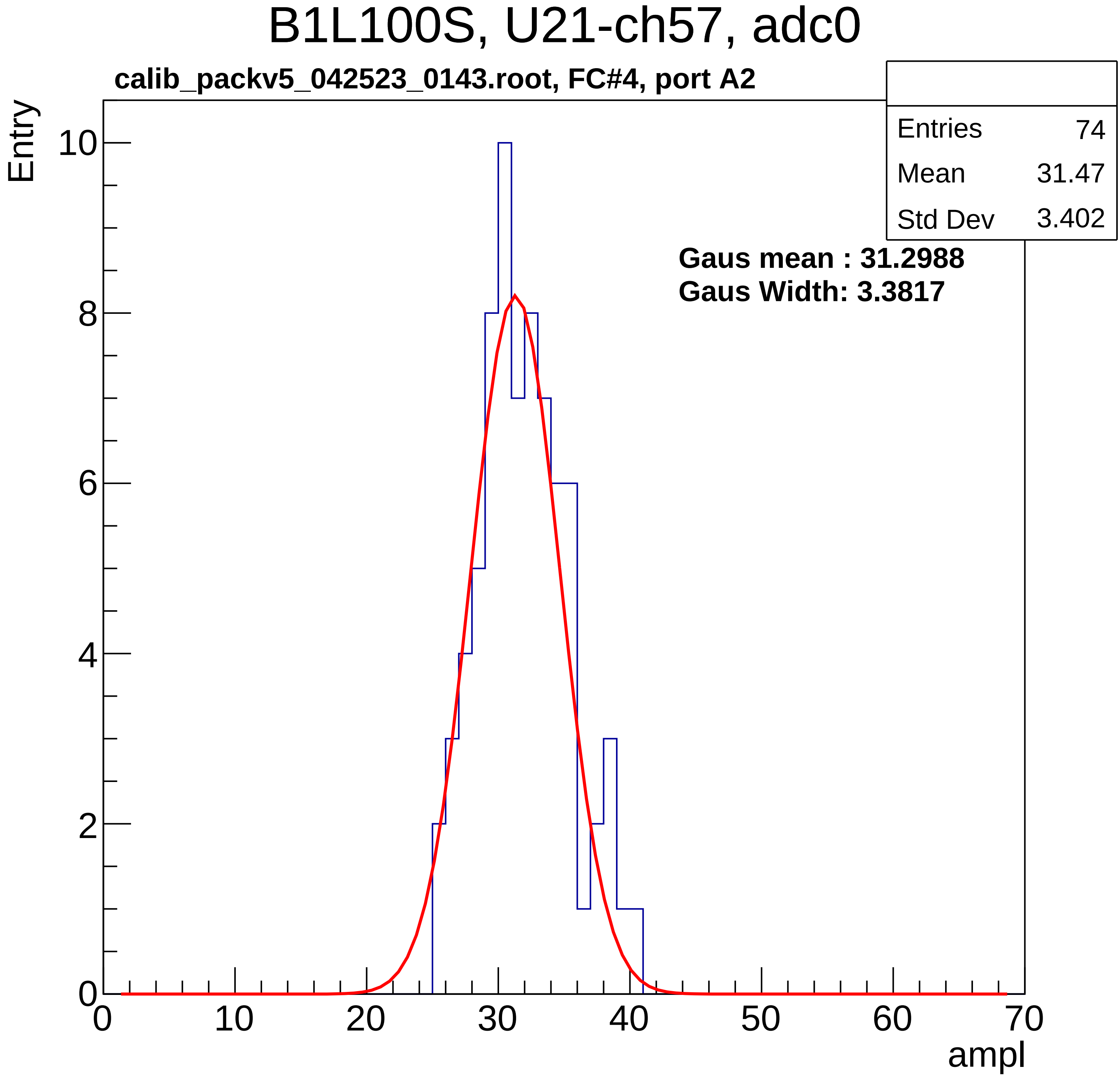
B1L100S, U21-ch56, adc4

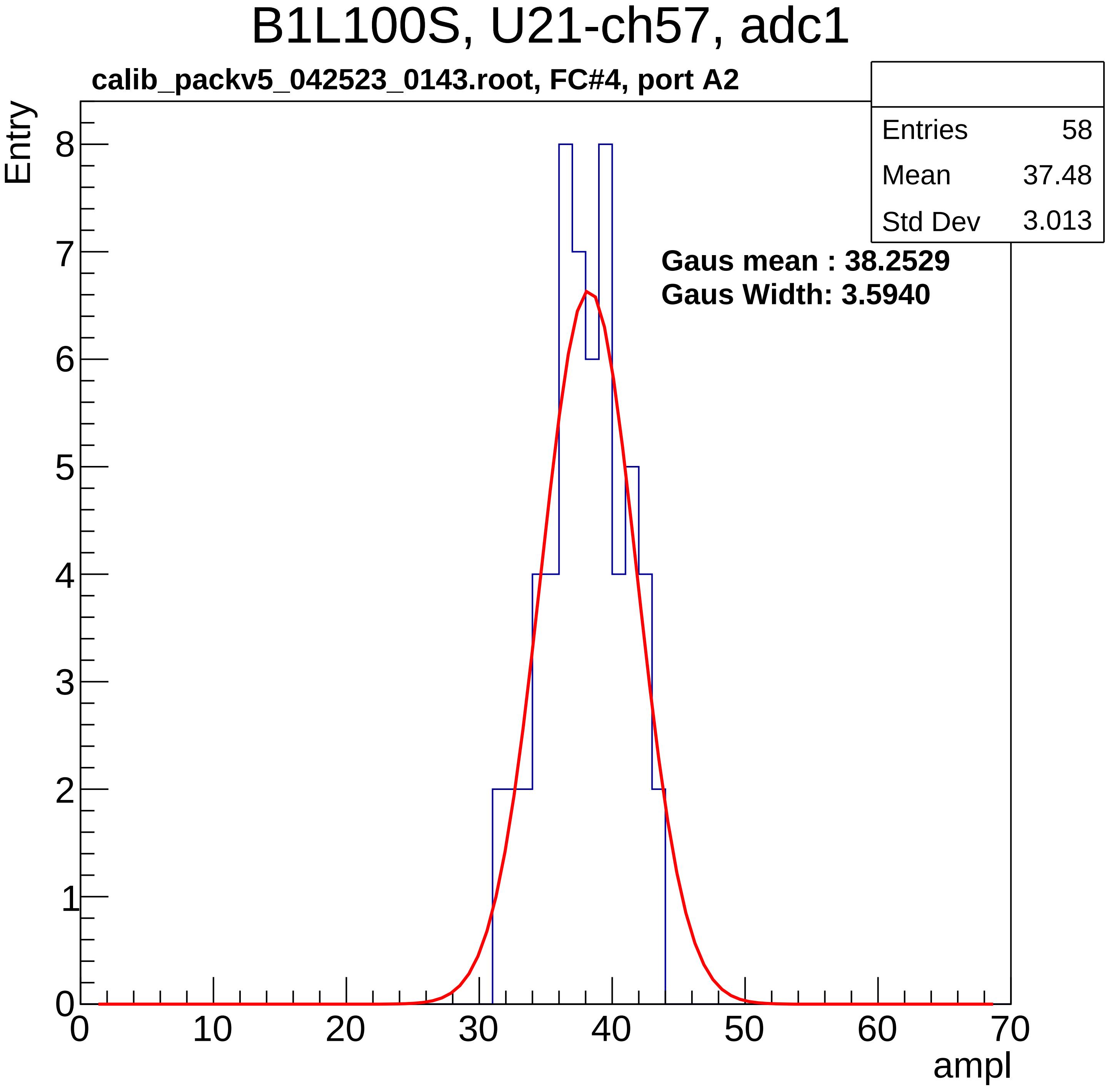


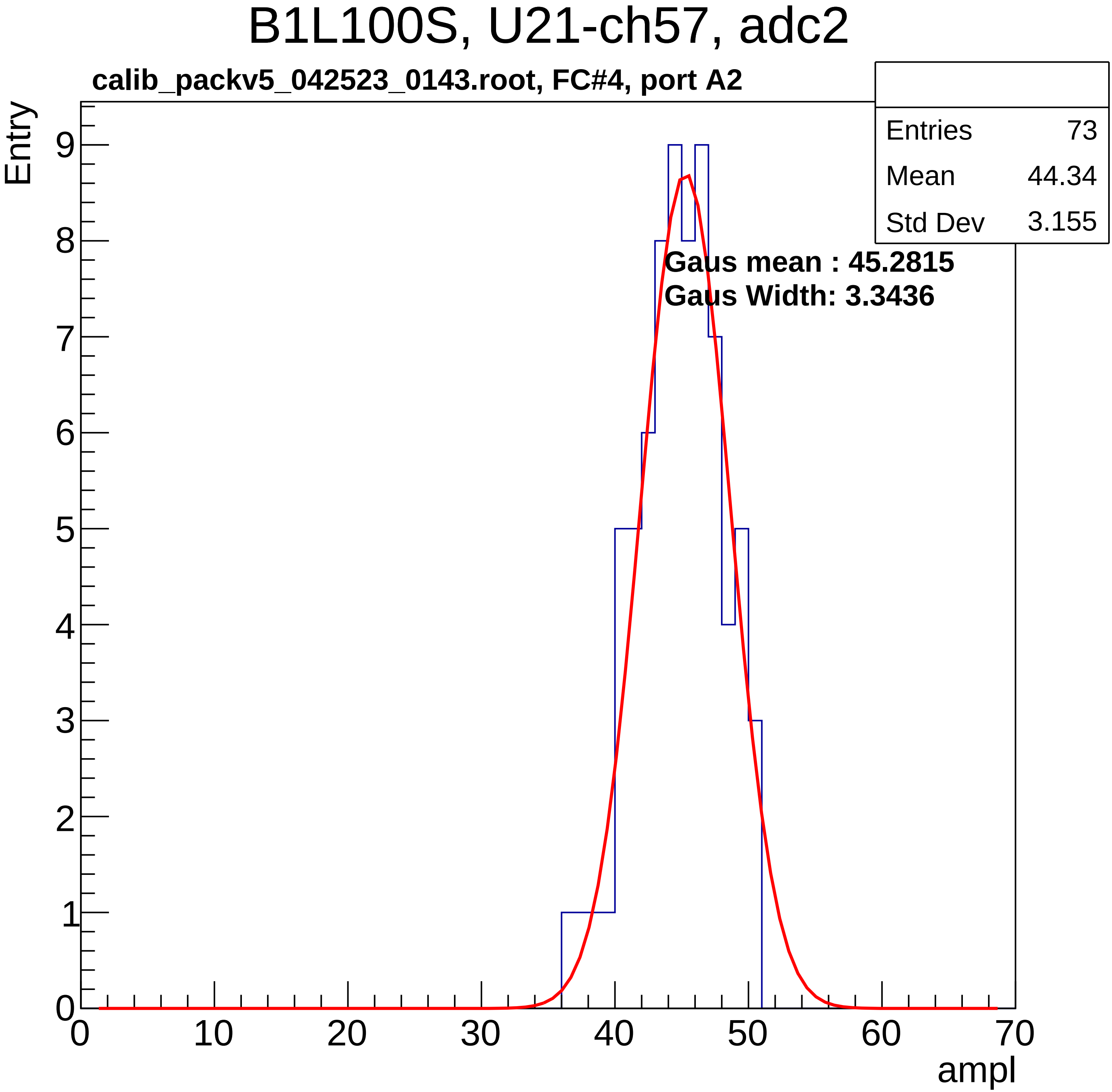

B1L100S, U21-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.96
Std Dev 8.666
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 2
Mean 62.5
Std Dev 0.5
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U21-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

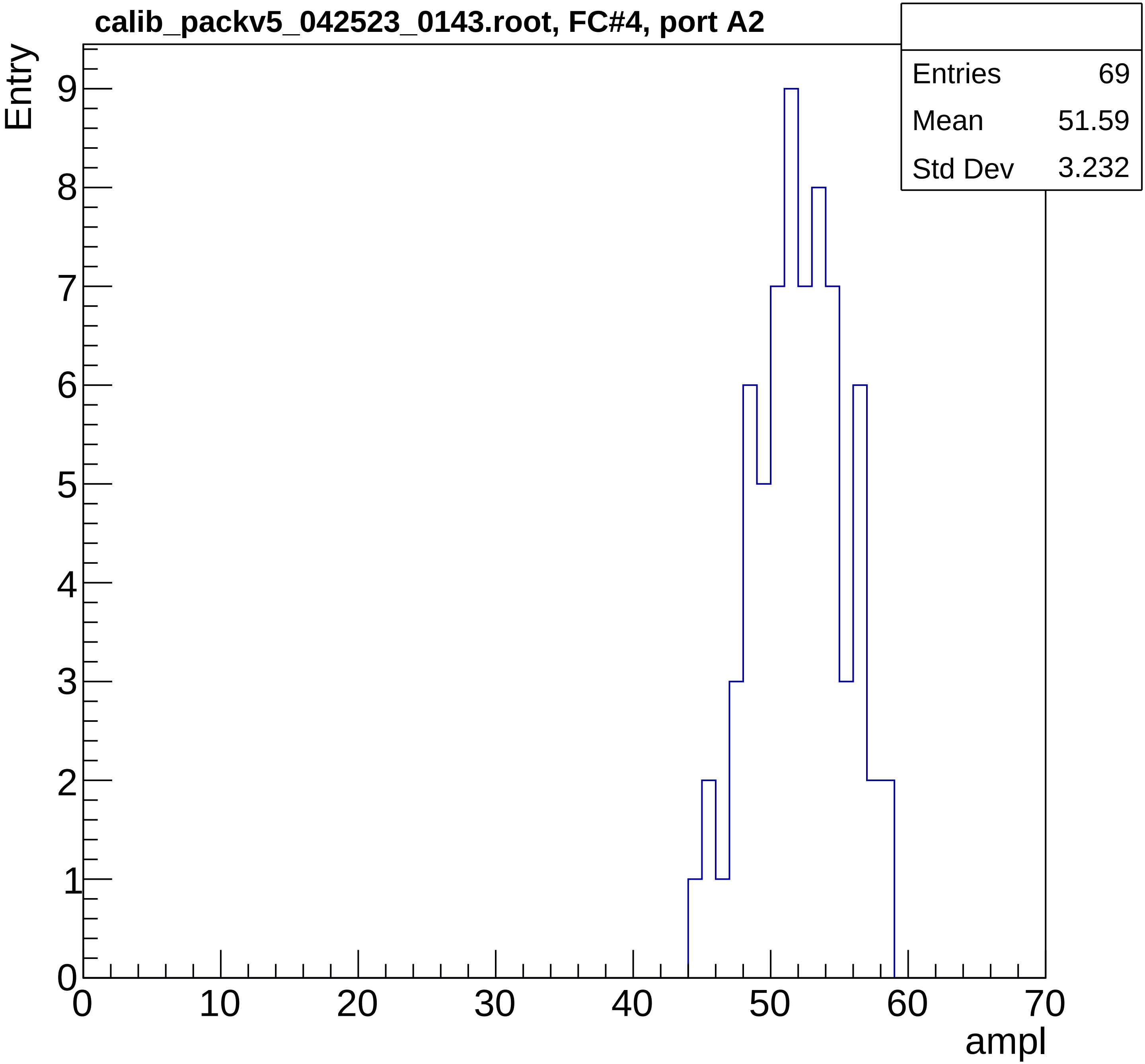

B1L100S, U21-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 31.47
Std Dev 3.402
Gaus mean : 31.2988
Gaus Width: 3.3817
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 37.48
Std Dev 3.013
Gaus mean : 38.2529
Gaus Width: 3.5940
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

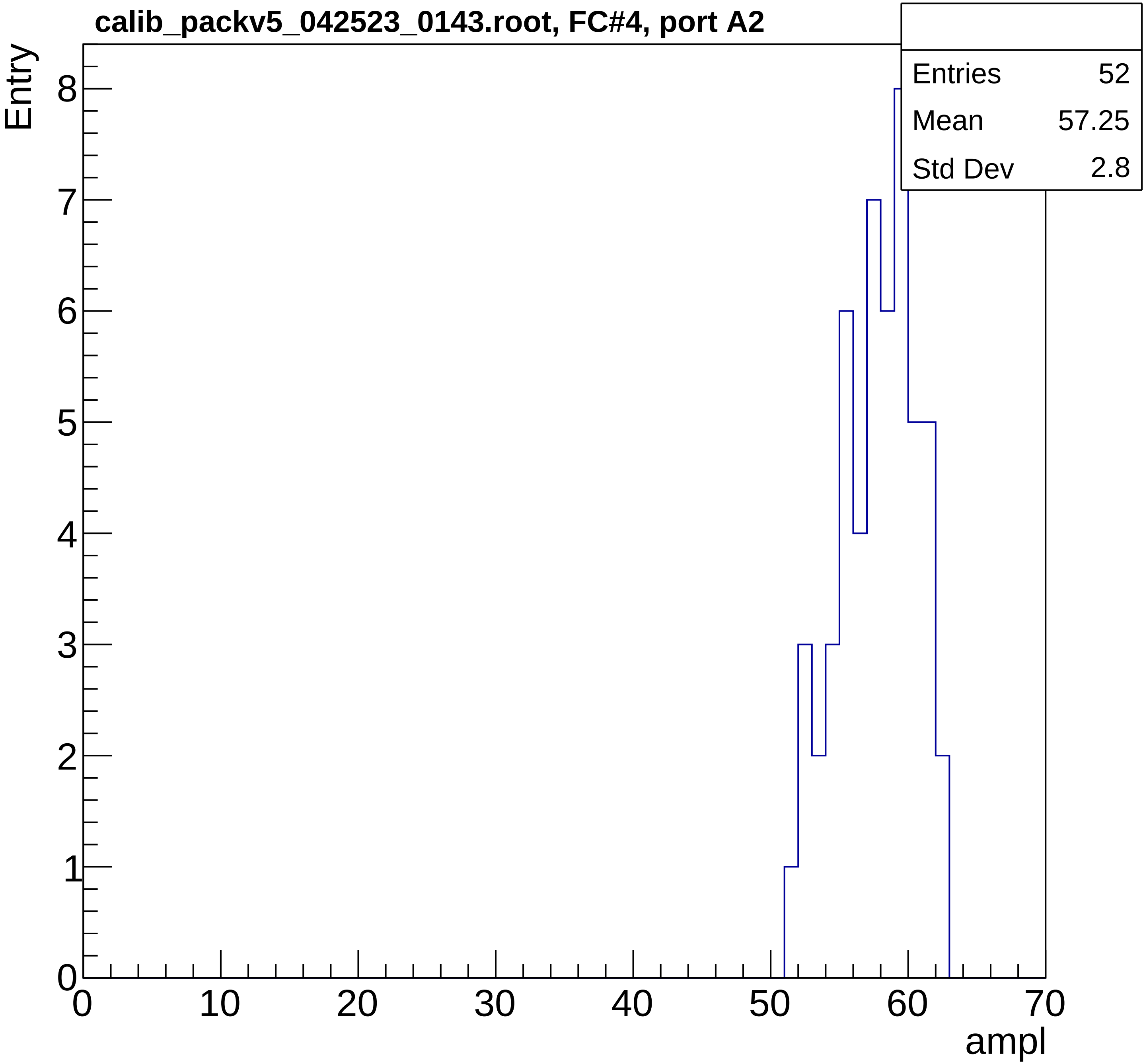

B1L100S, U21-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 73
Mean 44.34
Std Dev 3.155
Gaus mean : 45.2815
Gaus Width: 3.3436
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

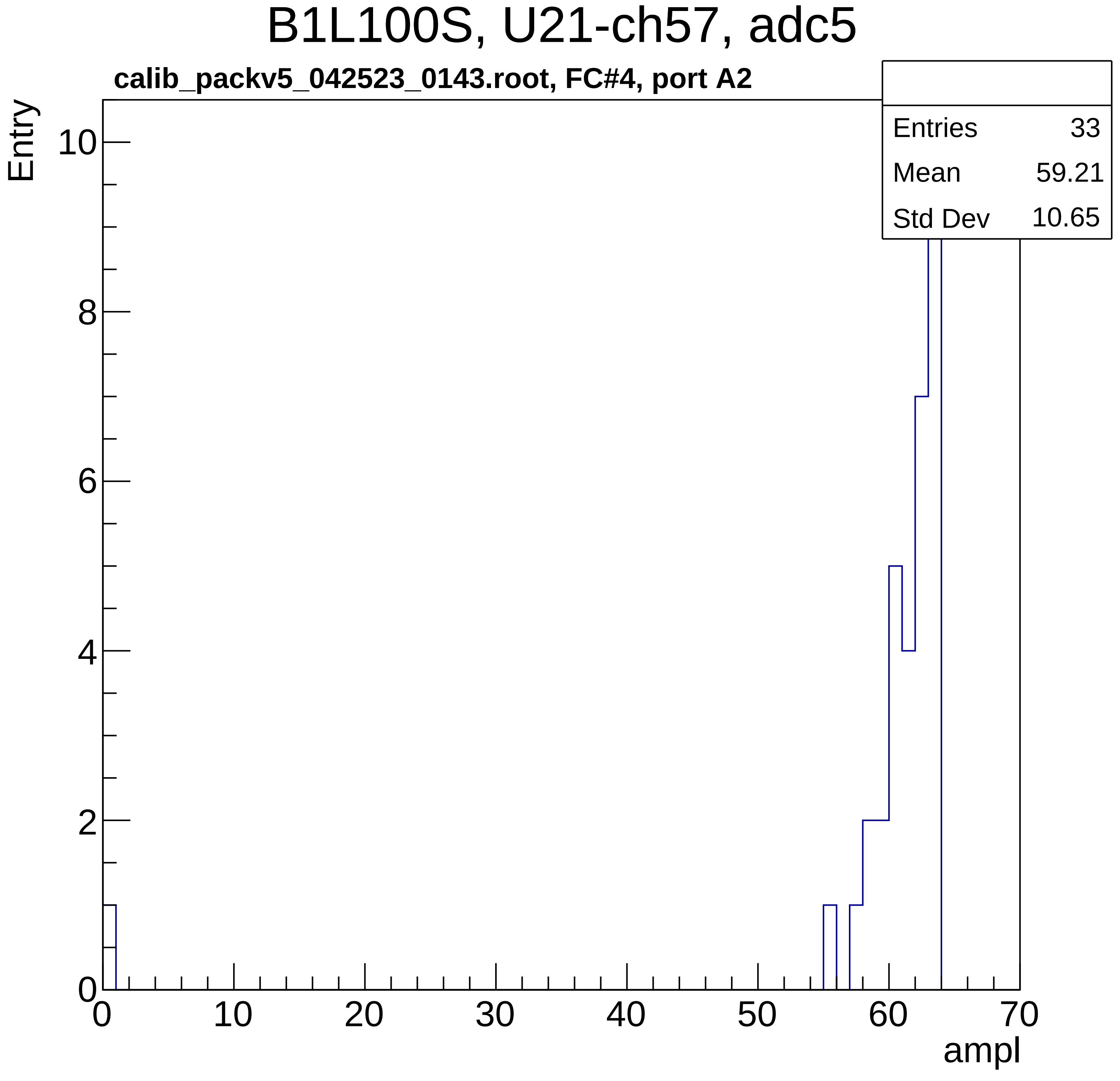
B1L100S, U21-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2

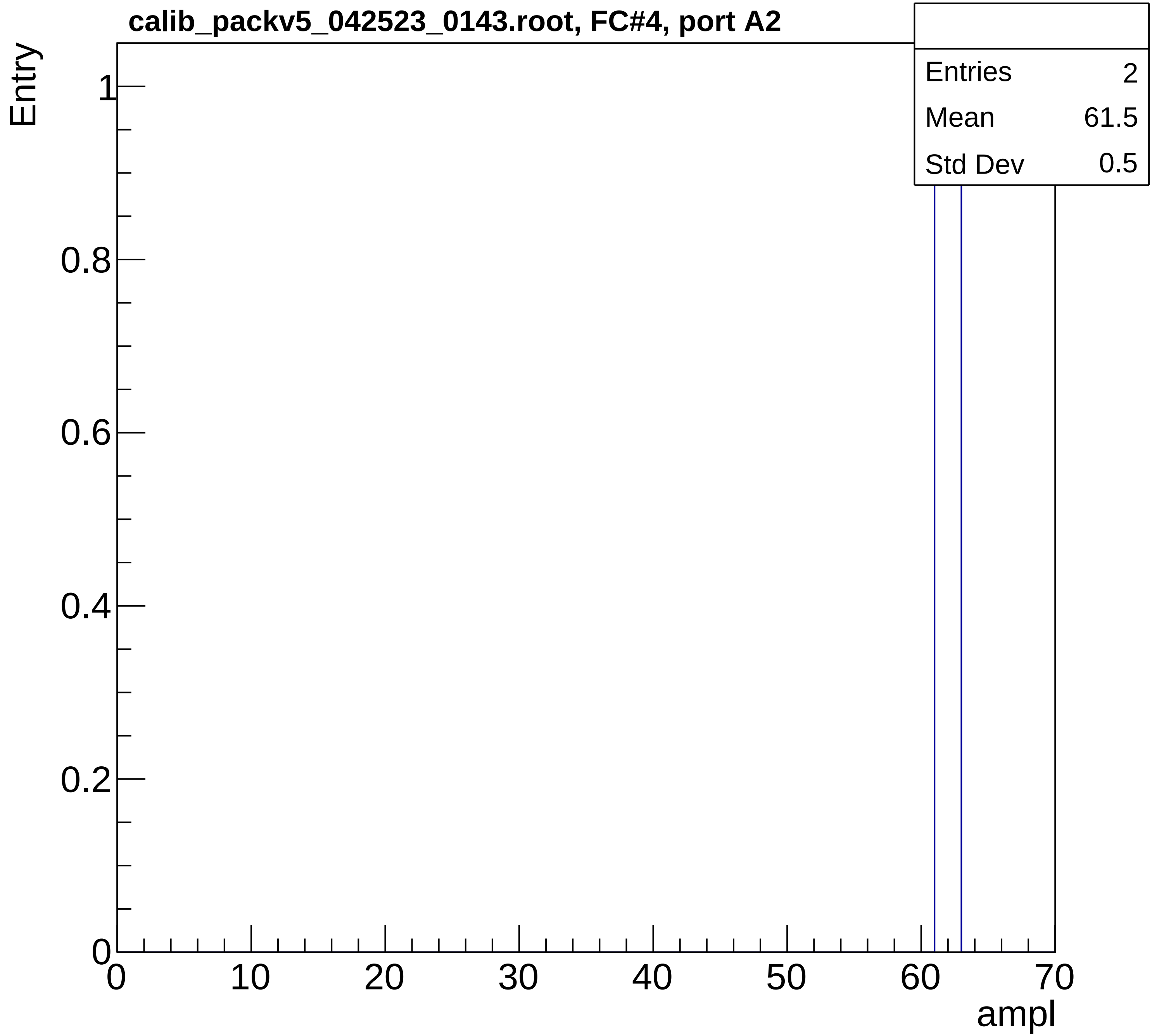


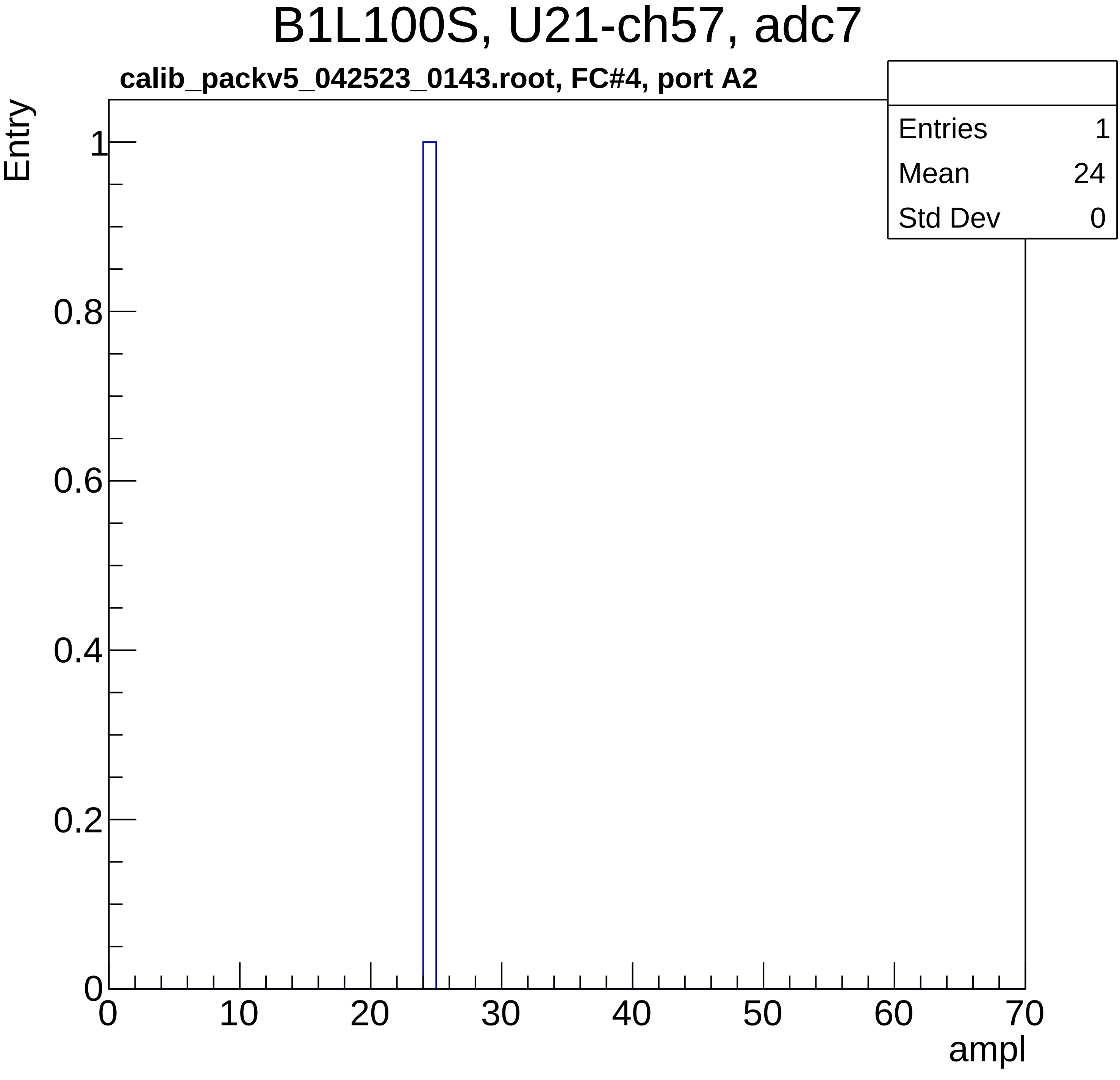
B1L100S, U21-ch57, adc4

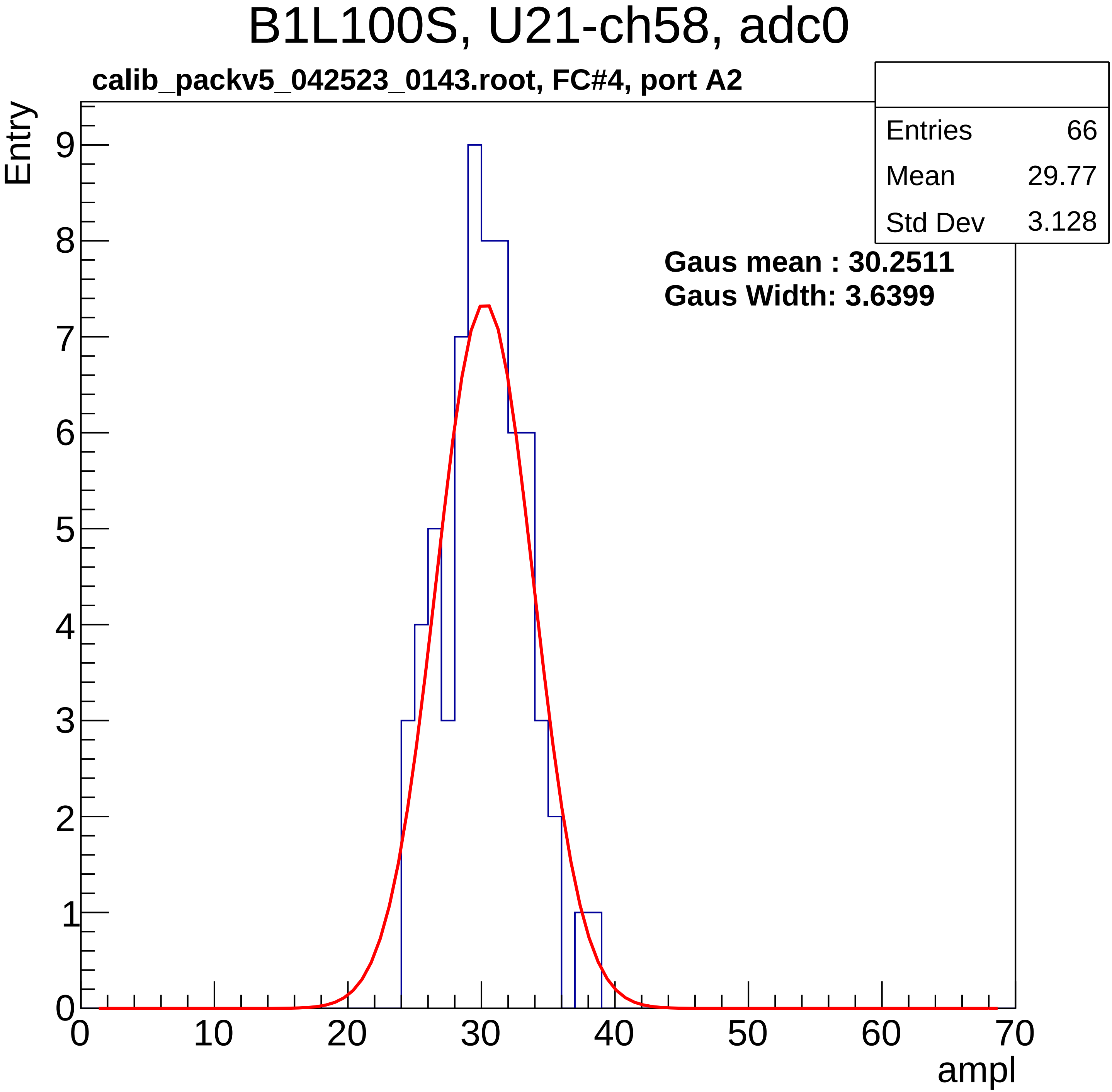


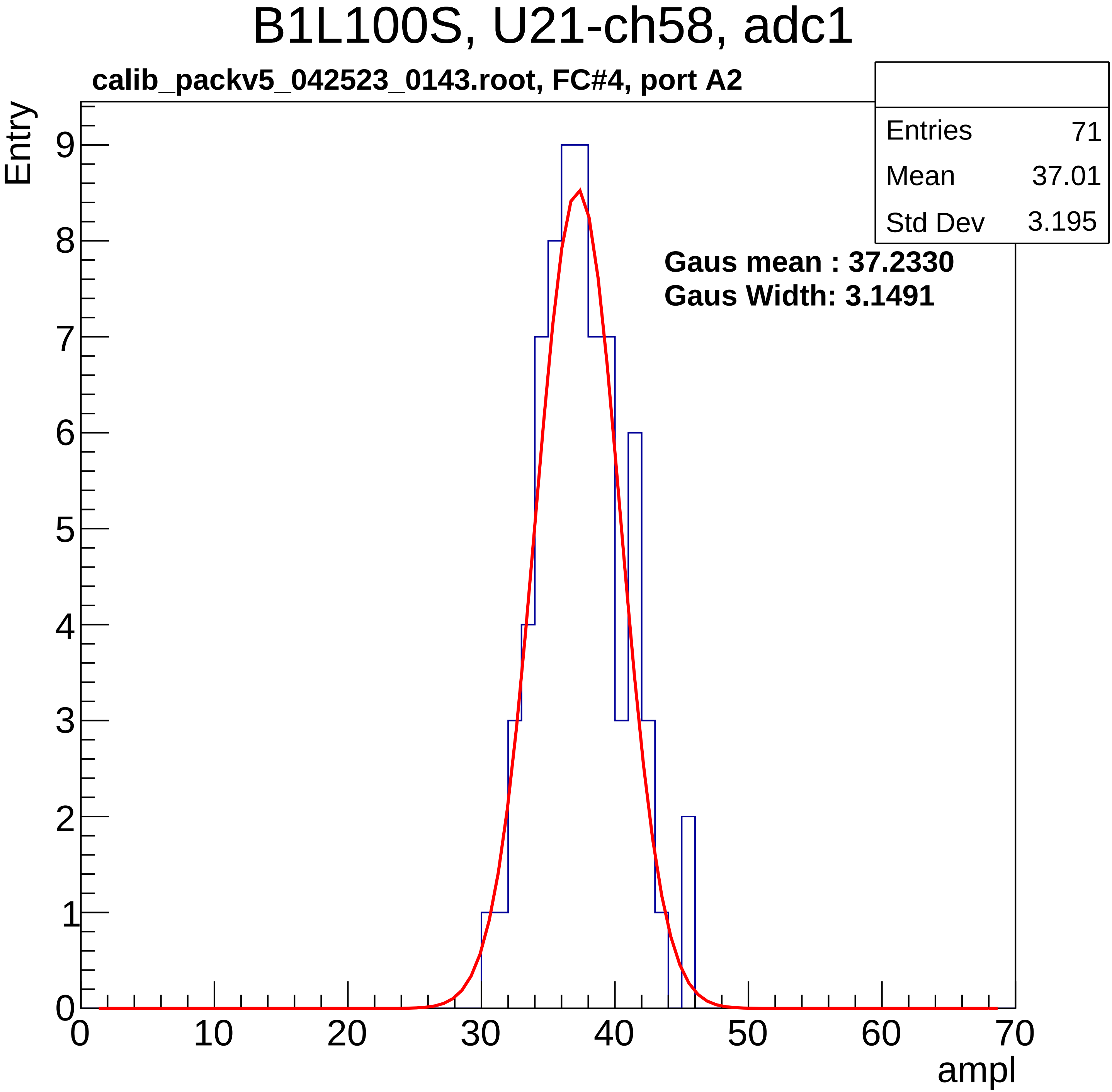

B1L100S, U21-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.21
Std Dev 10.65
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

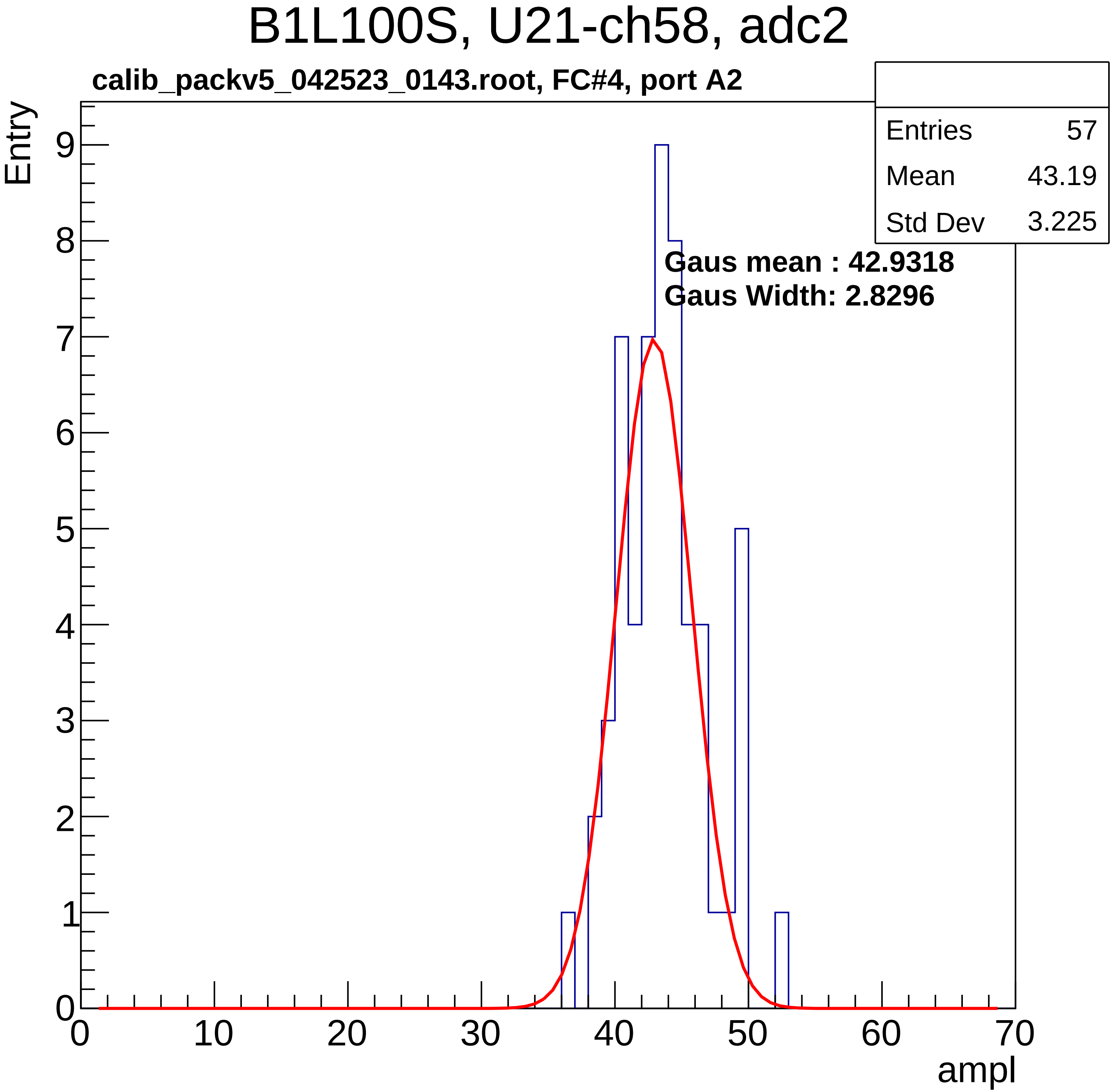
B1L100S, U21-ch57, adc6




B1L100S, U21-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

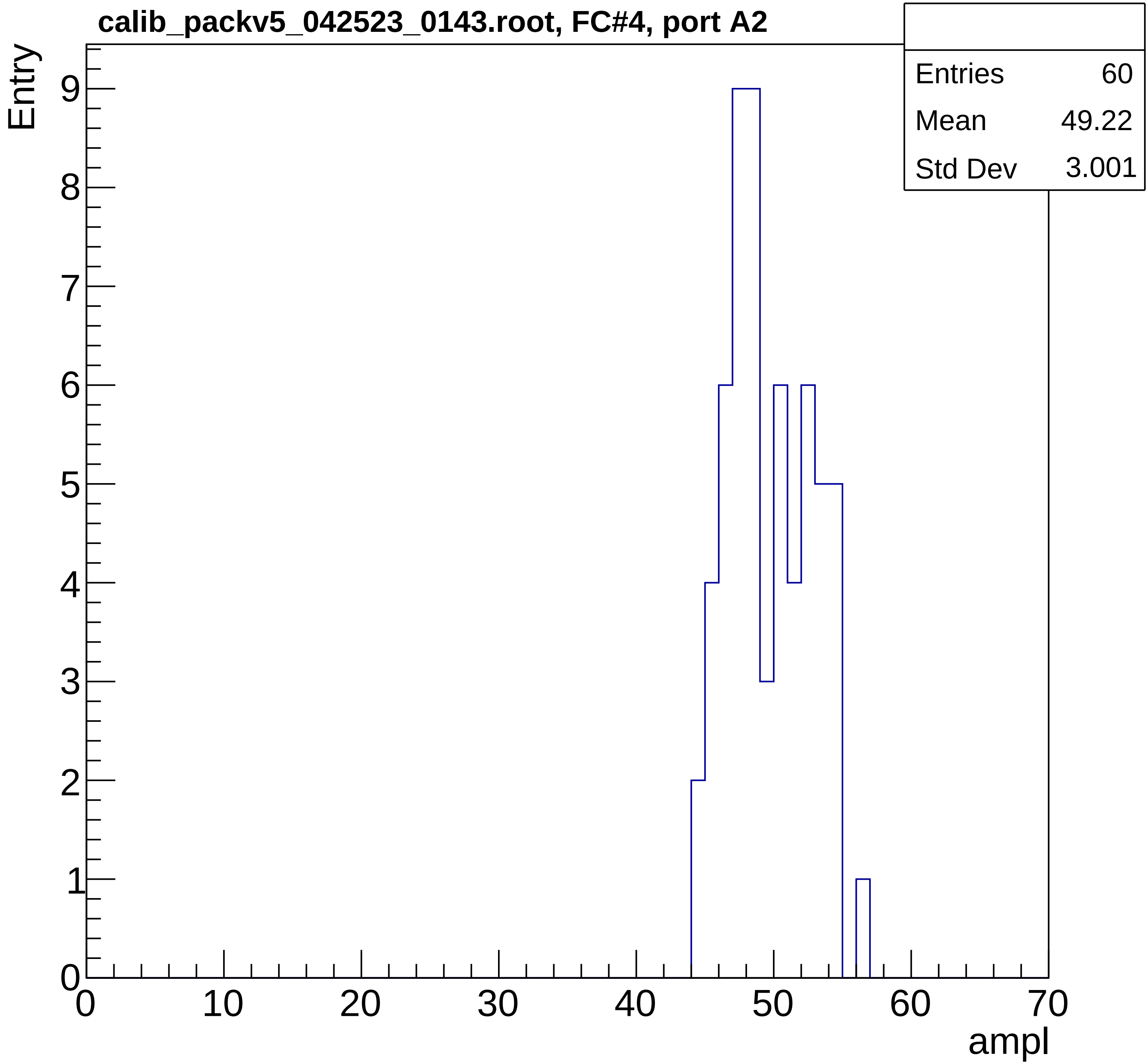

B1L100S, U21-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.77
Std Dev 3.128
Gaus mean : 30.2511
Gaus Width: 3.6399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

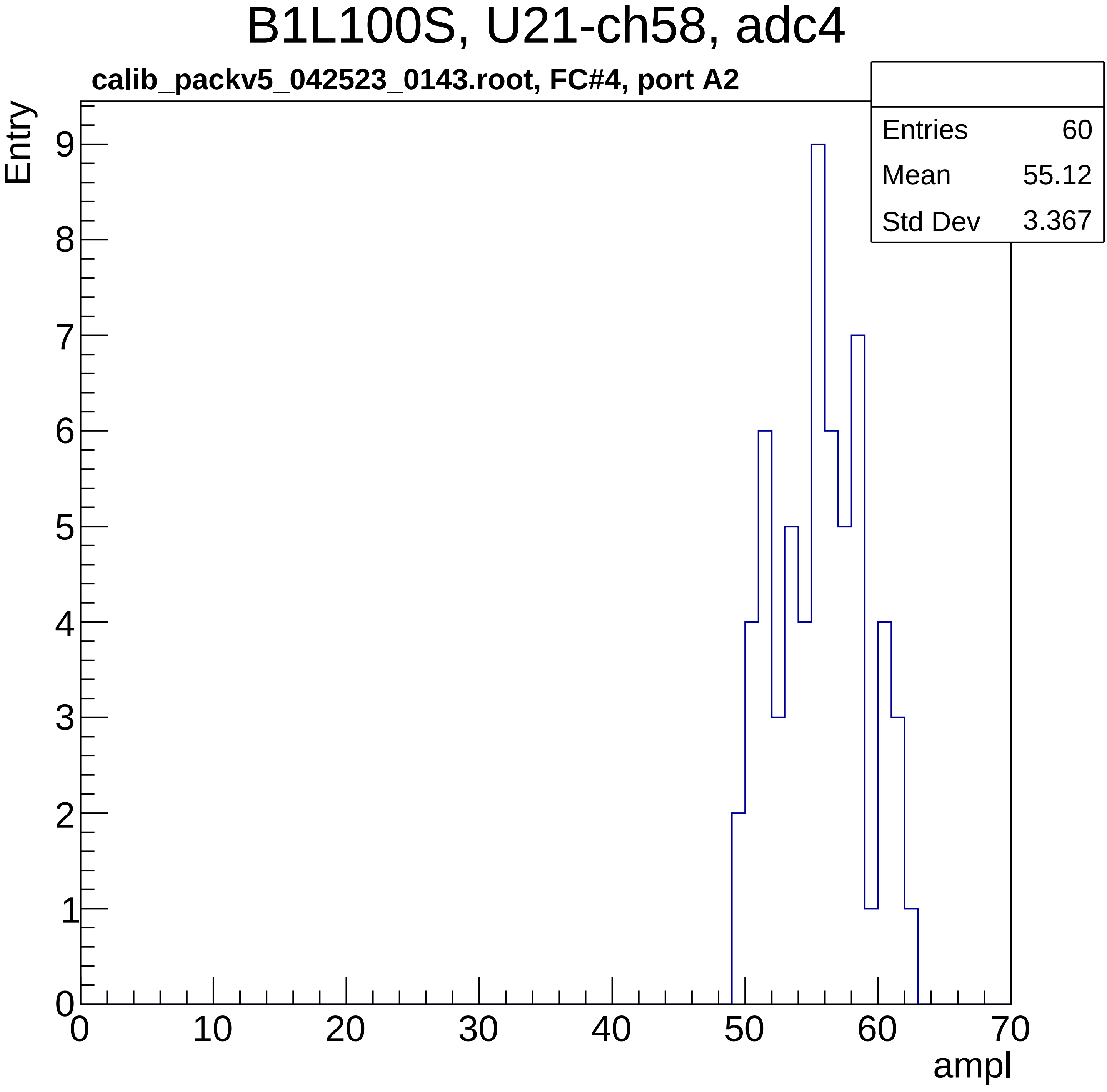

B1L100S, U21-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.01
Std Dev 3.195
Gaus mean : 37.2330
Gaus Width: 3.1491
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 57
Mean 43.19
Std Dev 3.225
Gaus mean : 42.9318
Gaus Width: 2.8296
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

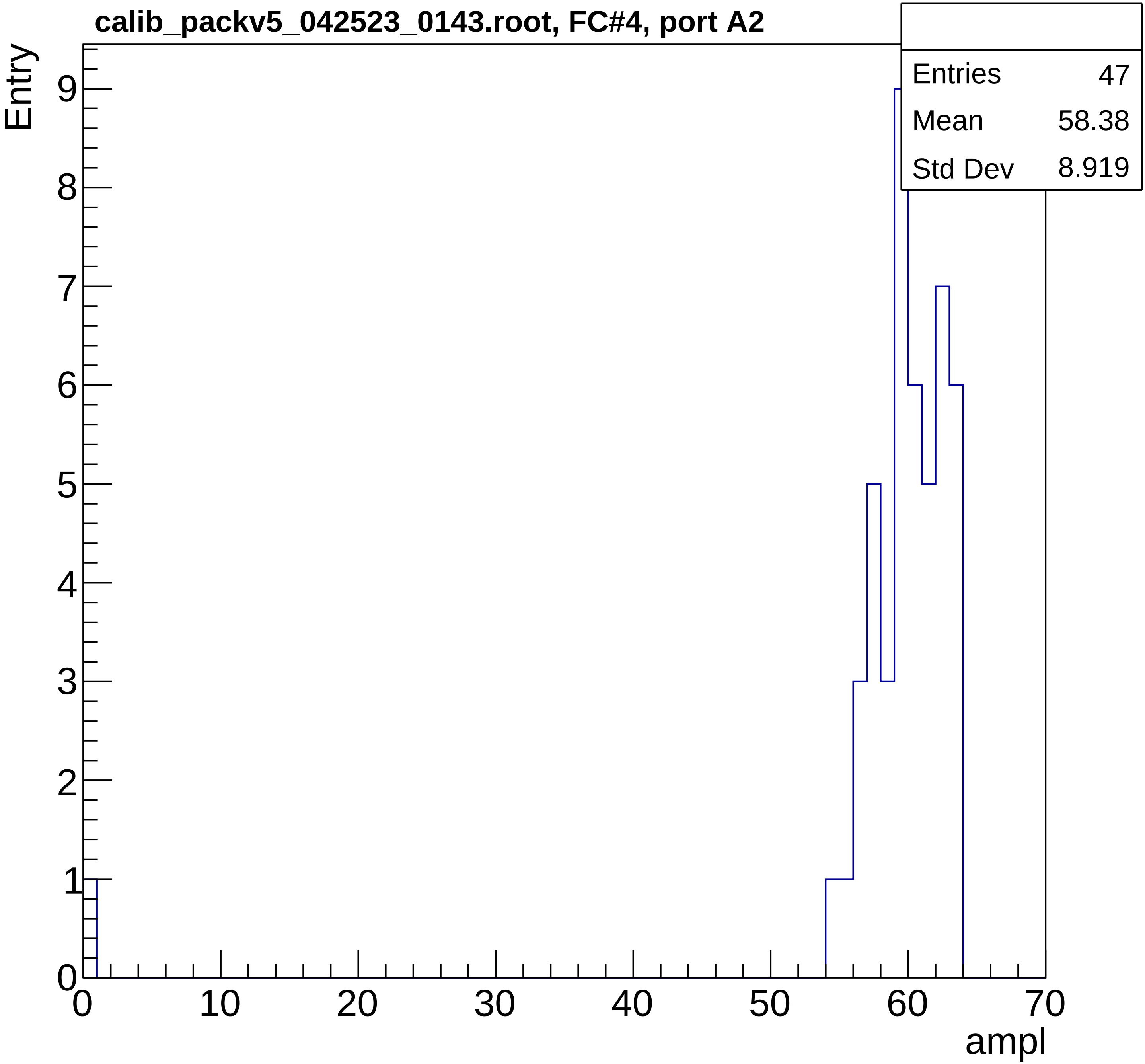
B1L100S, U21-ch58, adc3

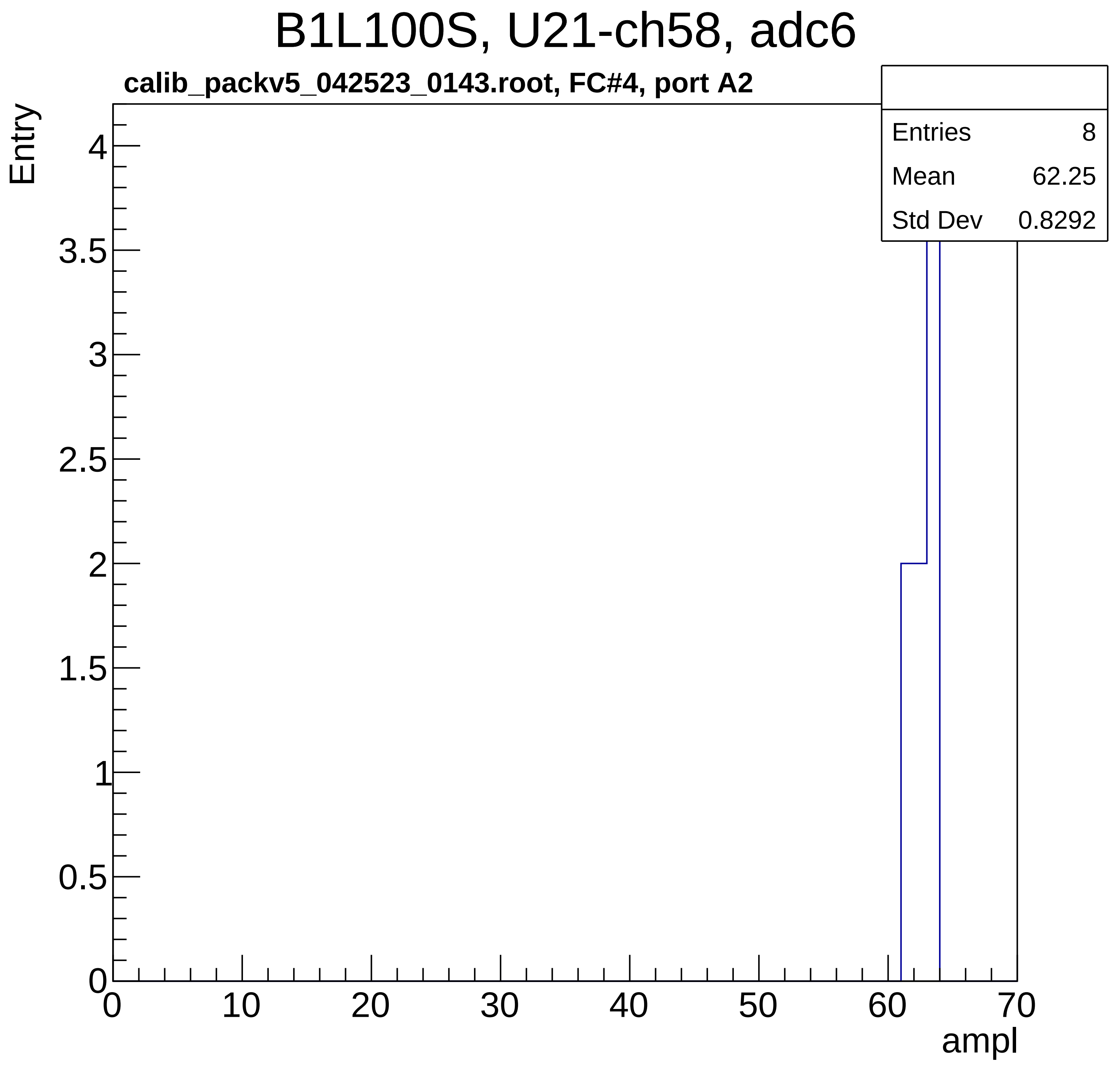
calib_packv5_042523_0143.root, FC#4, port A2



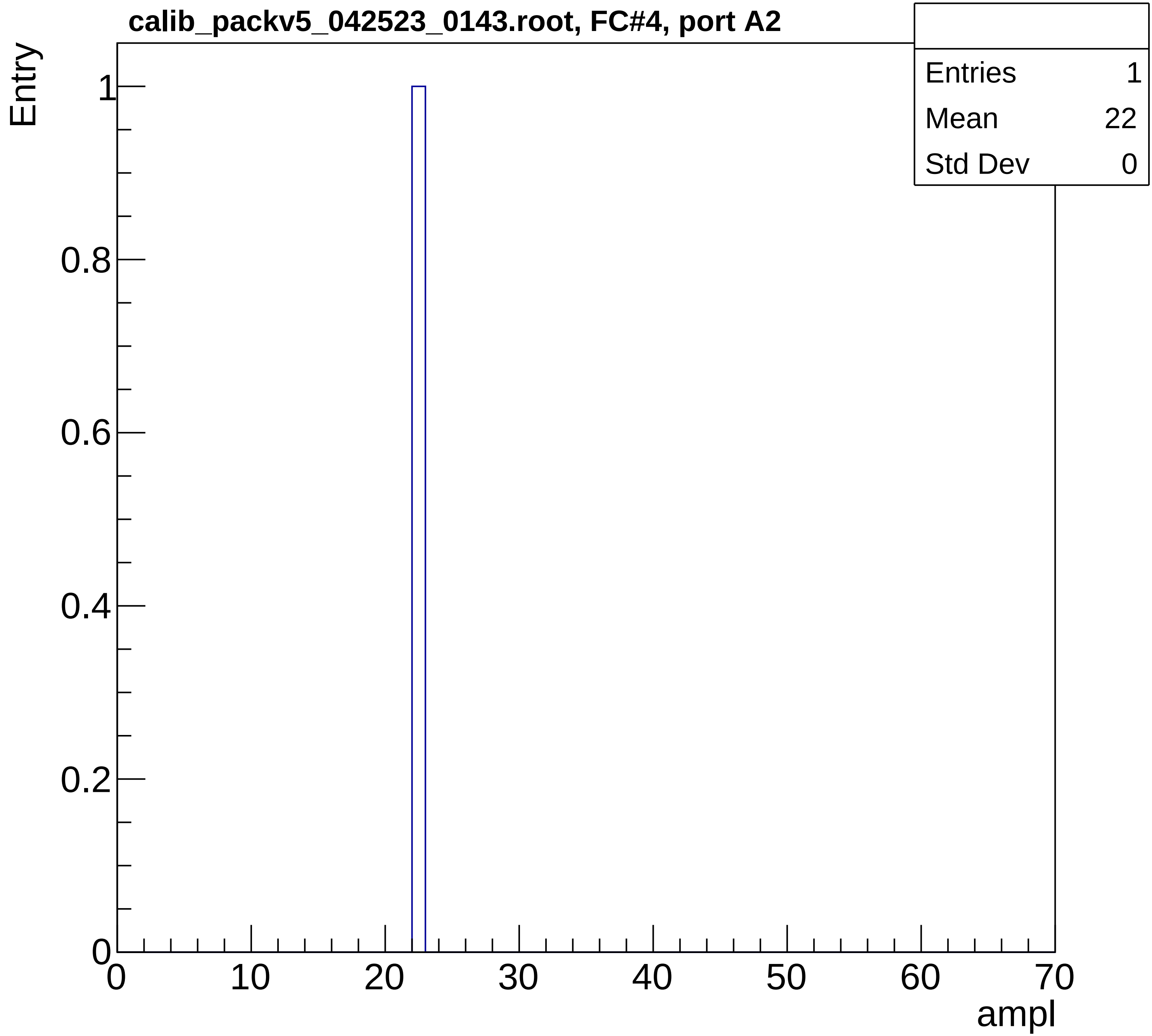

B1L100S, U21-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.12
Std Dev 3.367
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

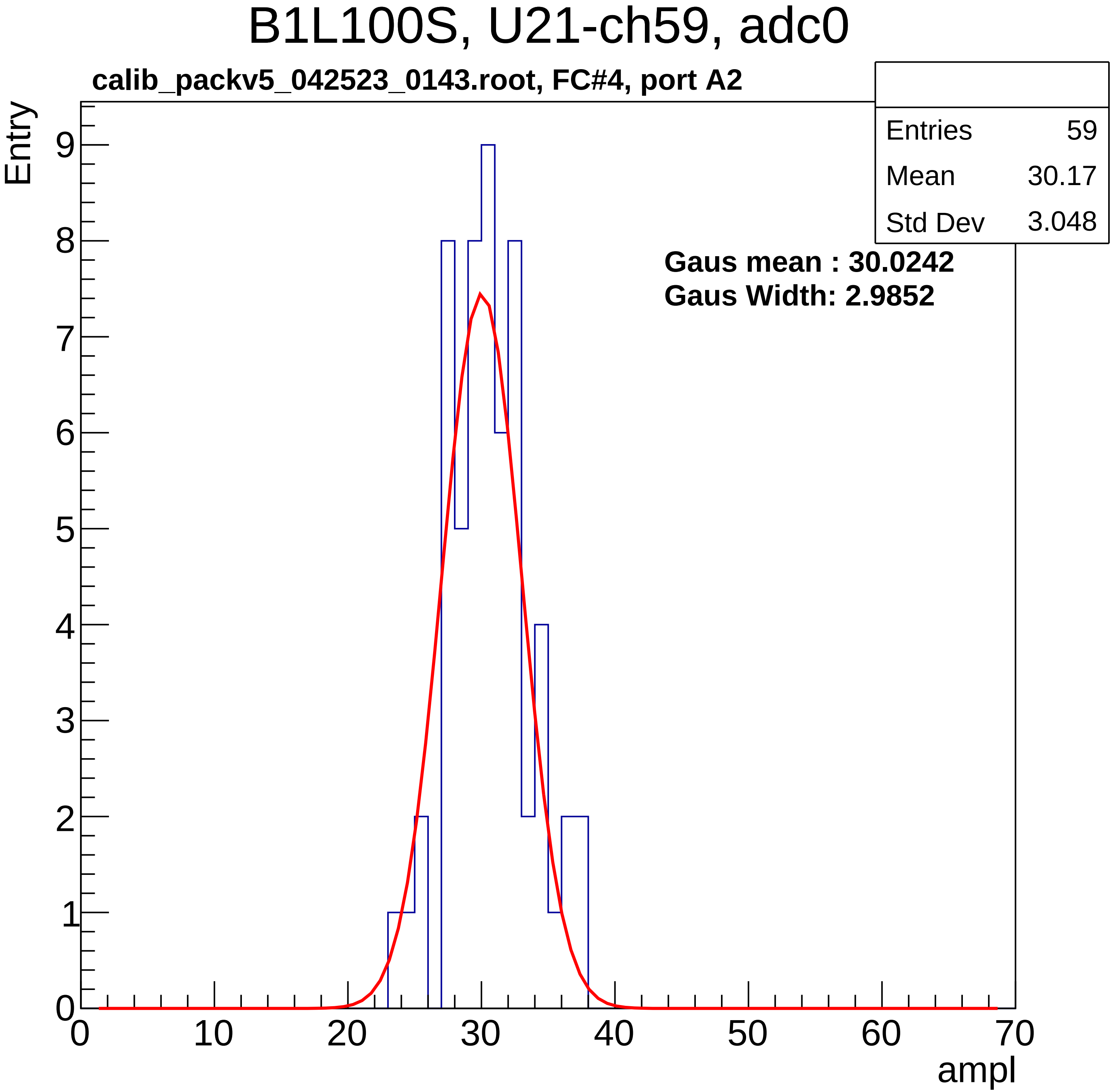
B1L100S, U21-ch58, adc5

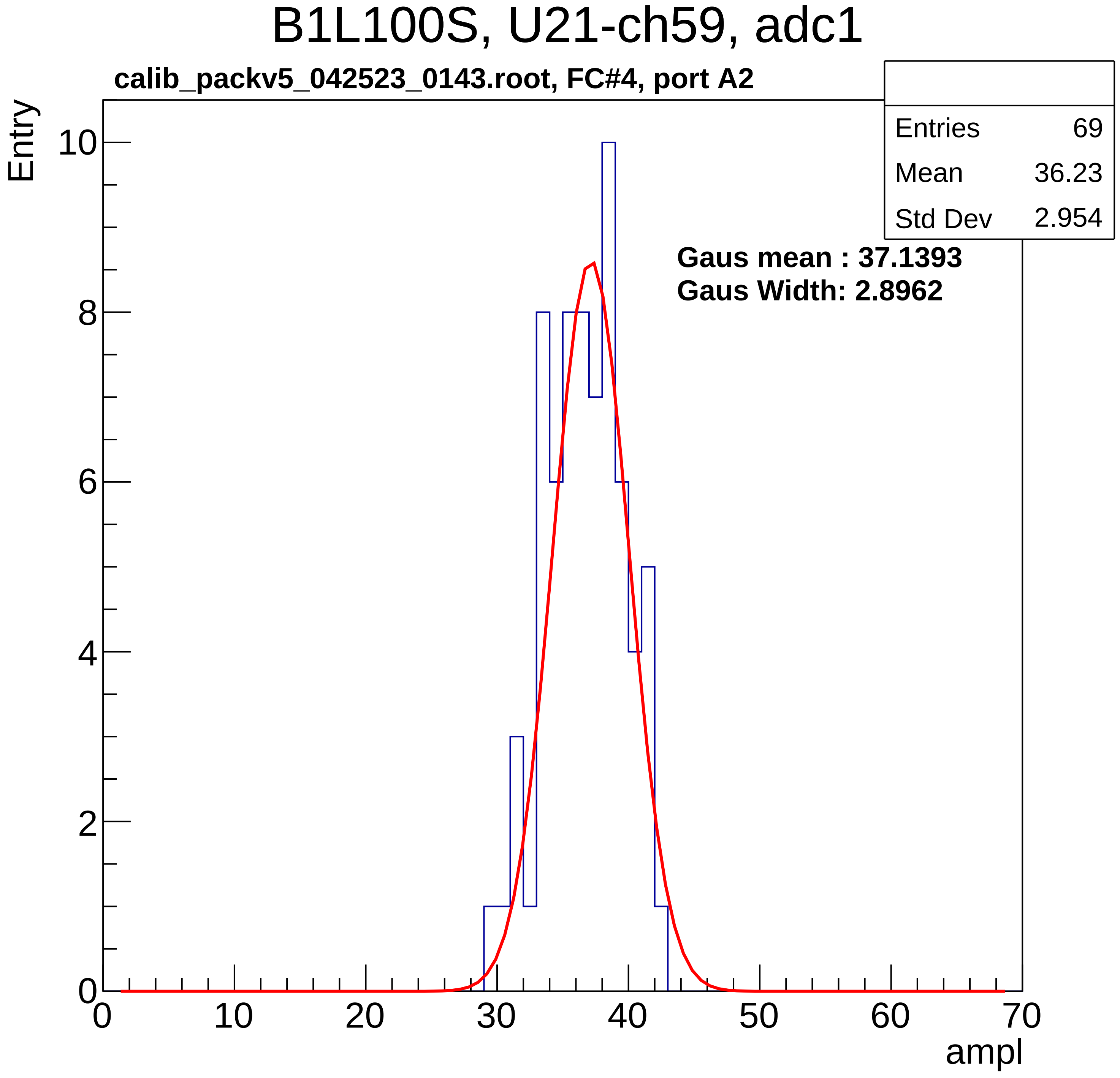


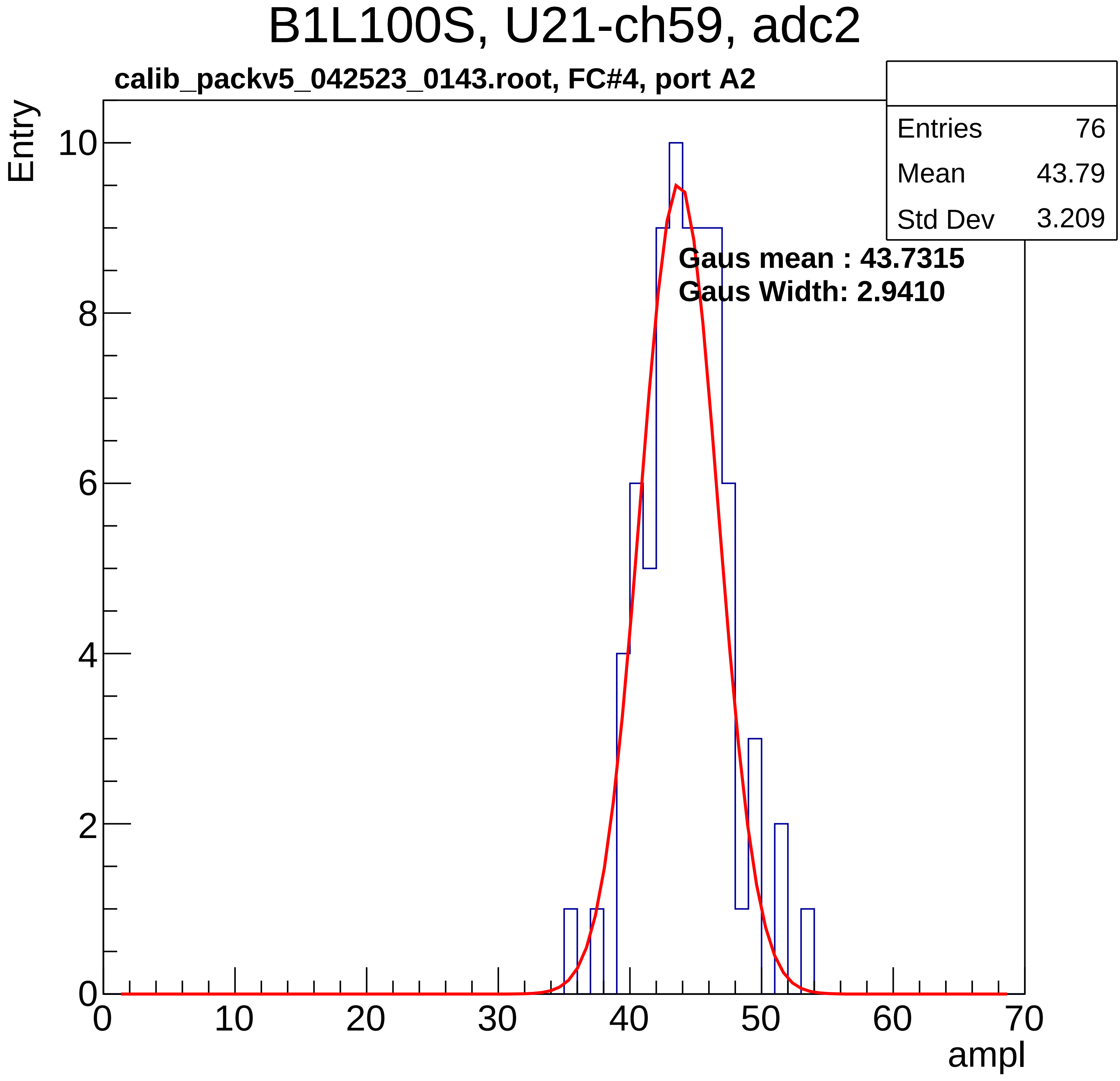

B1L100S, U21-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 8
Mean 62.25
Std Dev 0.8292
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

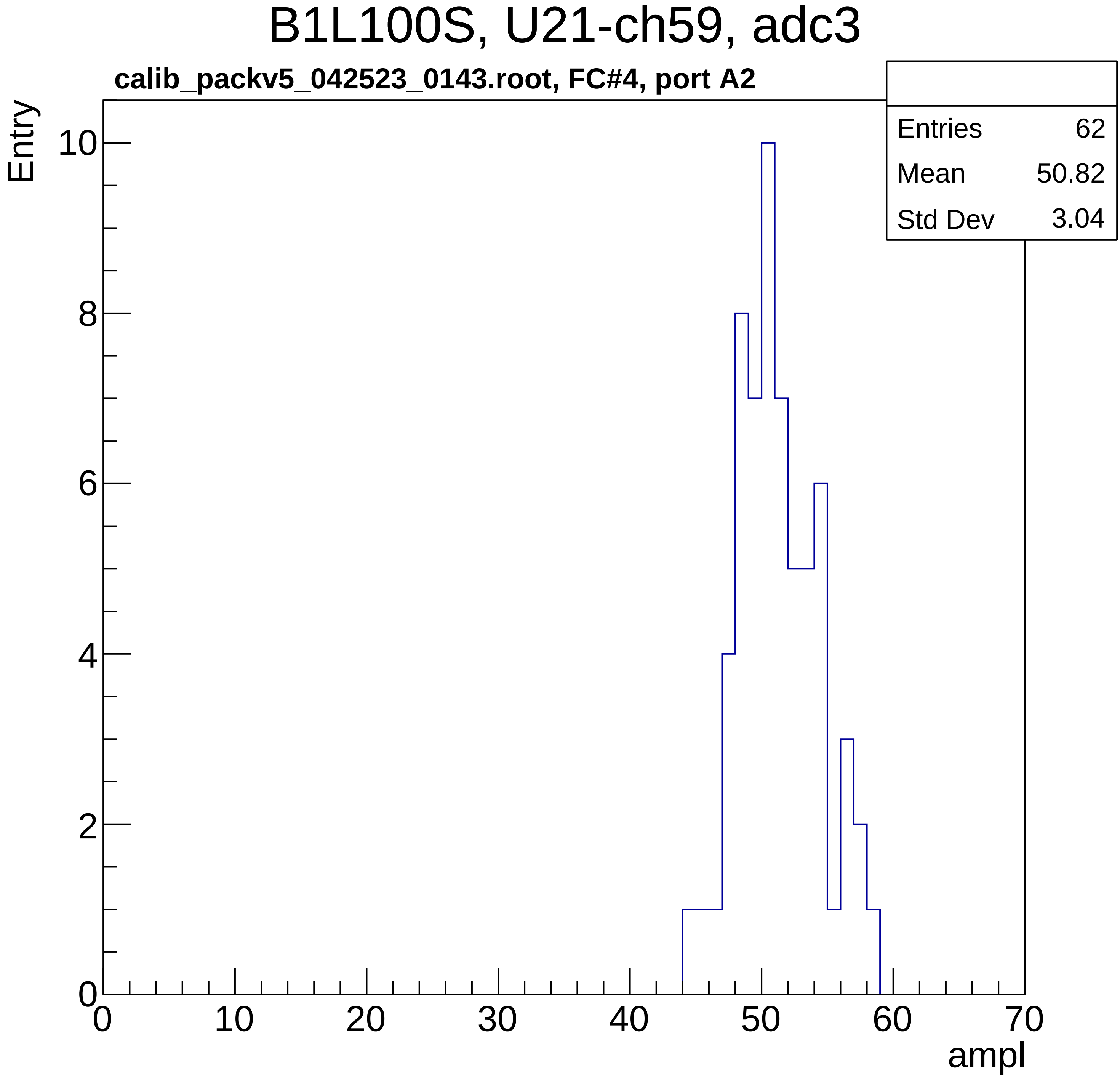
B1L100S, U21-ch58, adc7

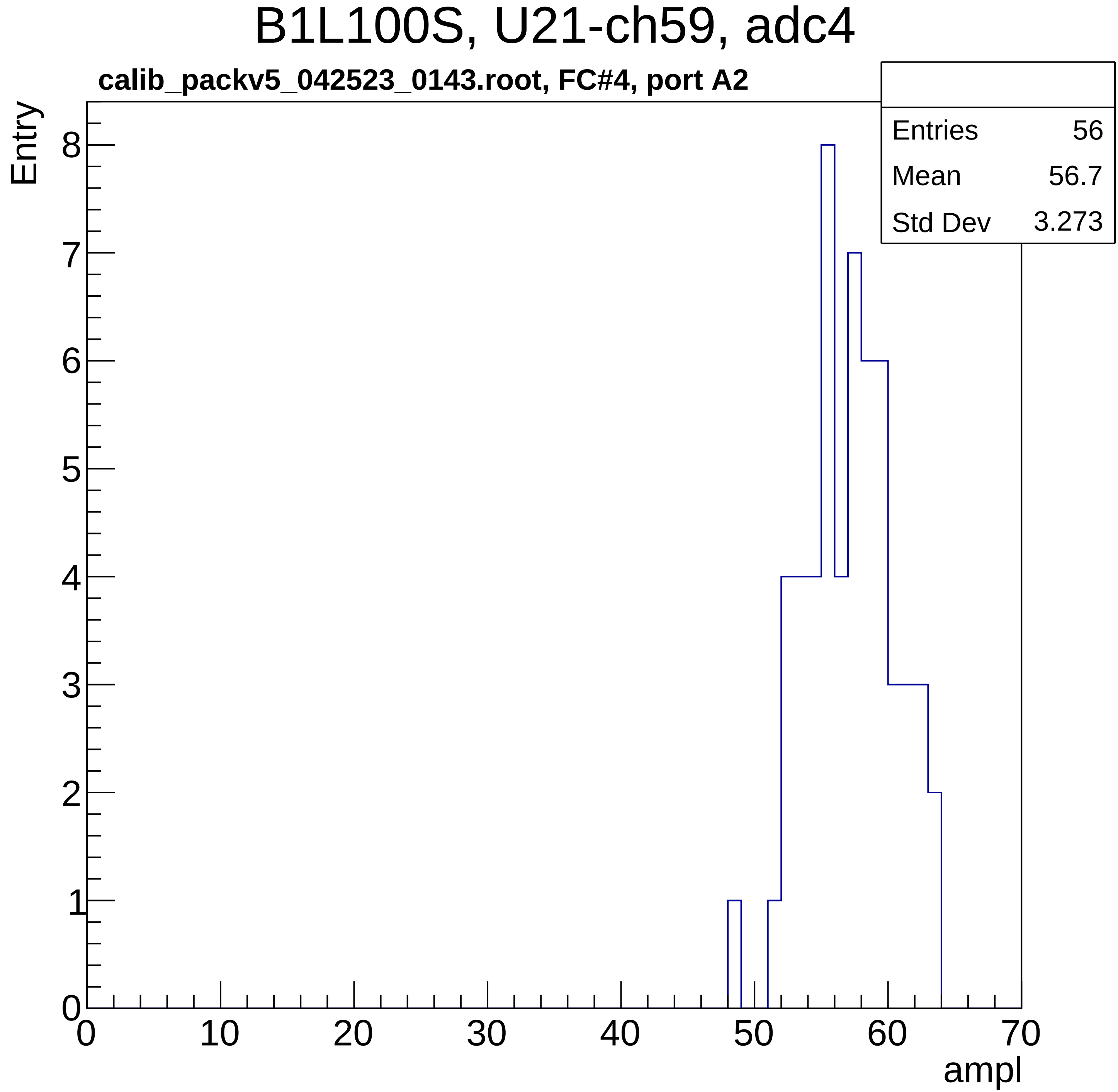



B1L100S, U21-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 30.17
Std Dev 3.048
Gaus mean : 30.0242
Gaus Width: 2.9852
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.23
Std Dev 2.954
Gaus mean : 37.1393
Gaus Width: 2.8962
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 43.79
Std Dev 3.209
Gaus mean : 43.7315
Gaus Width: 2.9410
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

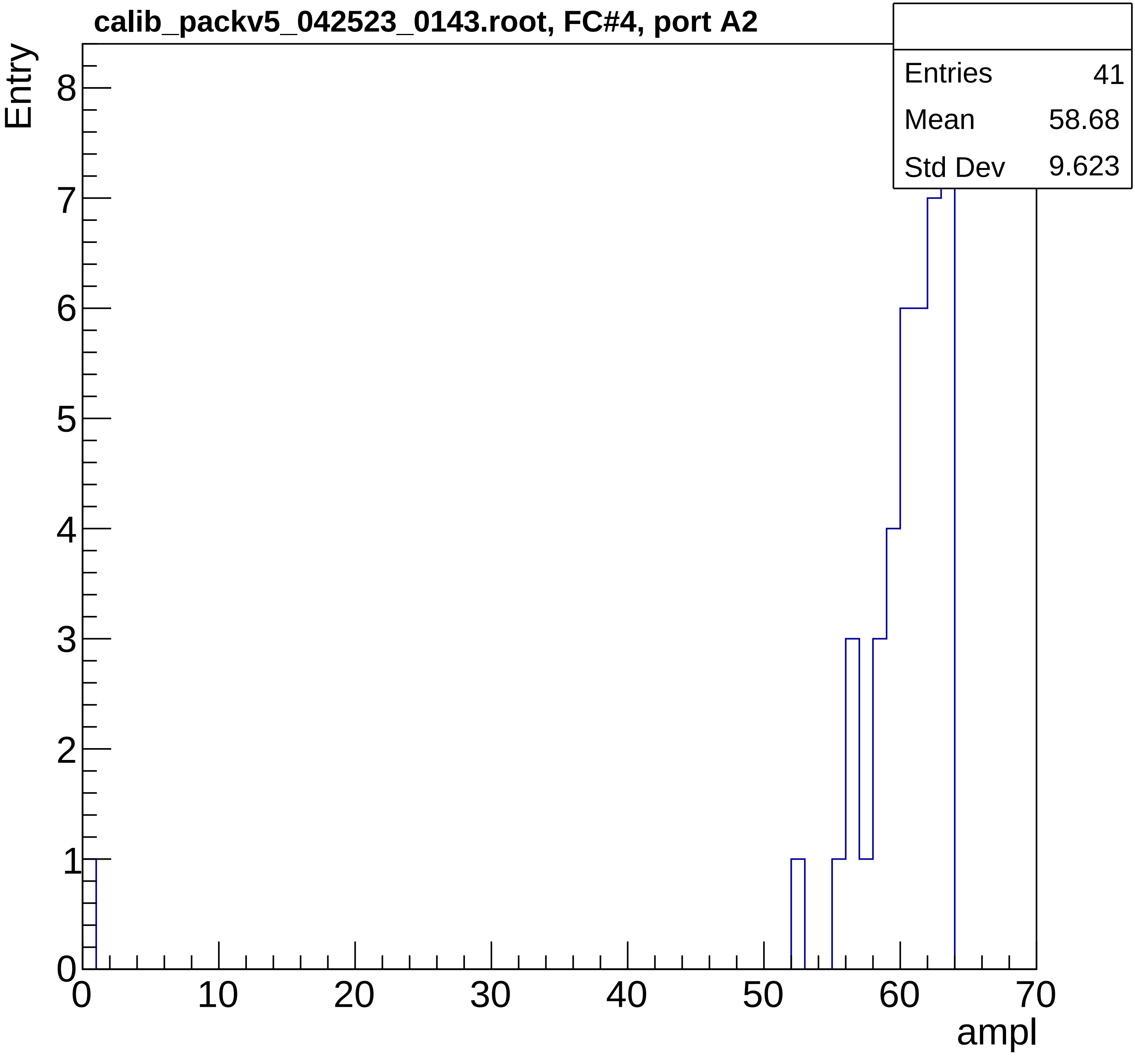

B1L100S, U21-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.82
Std Dev 3.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

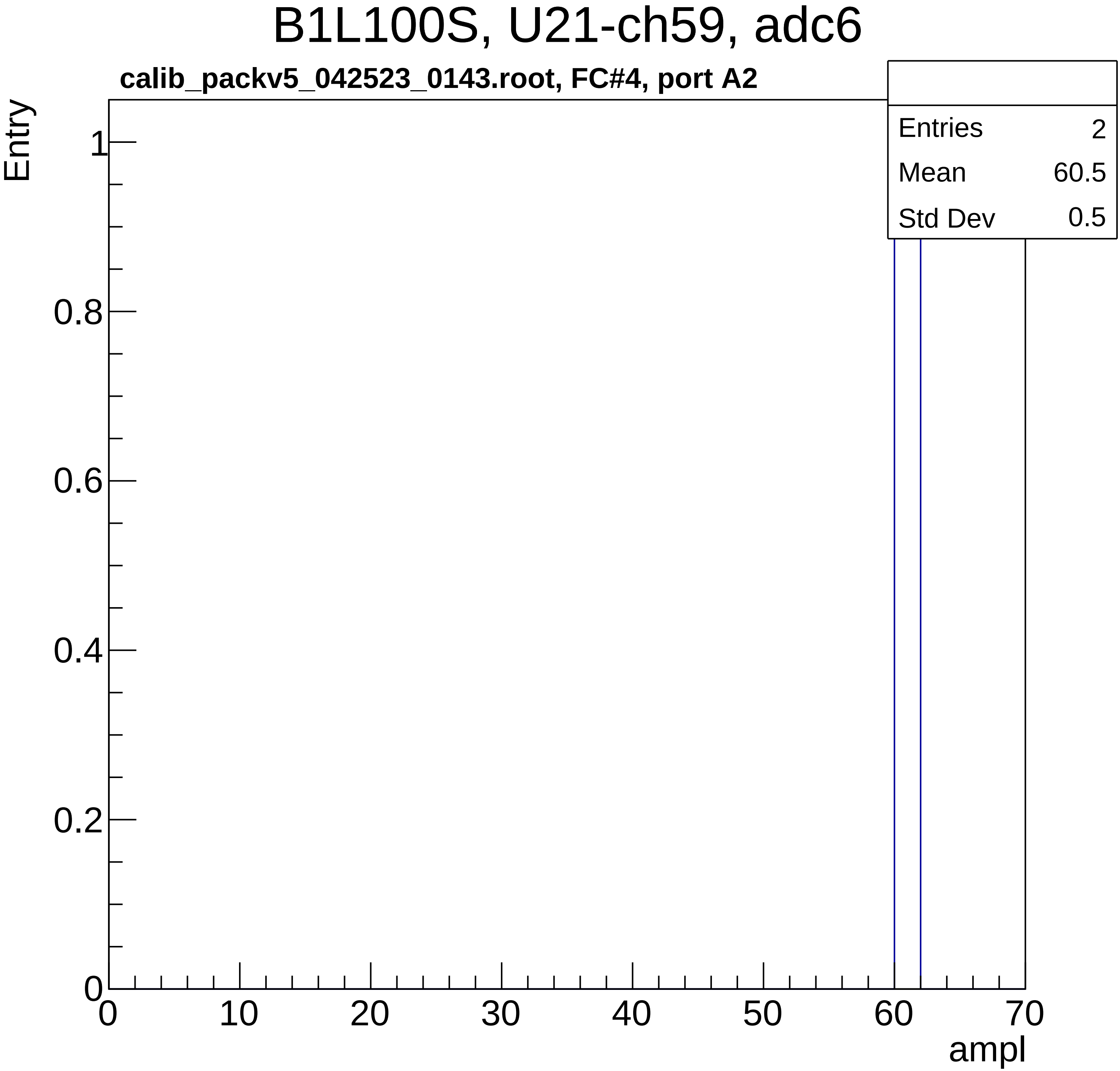


B1L100S, U21-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.7
Std Dev 3.273
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

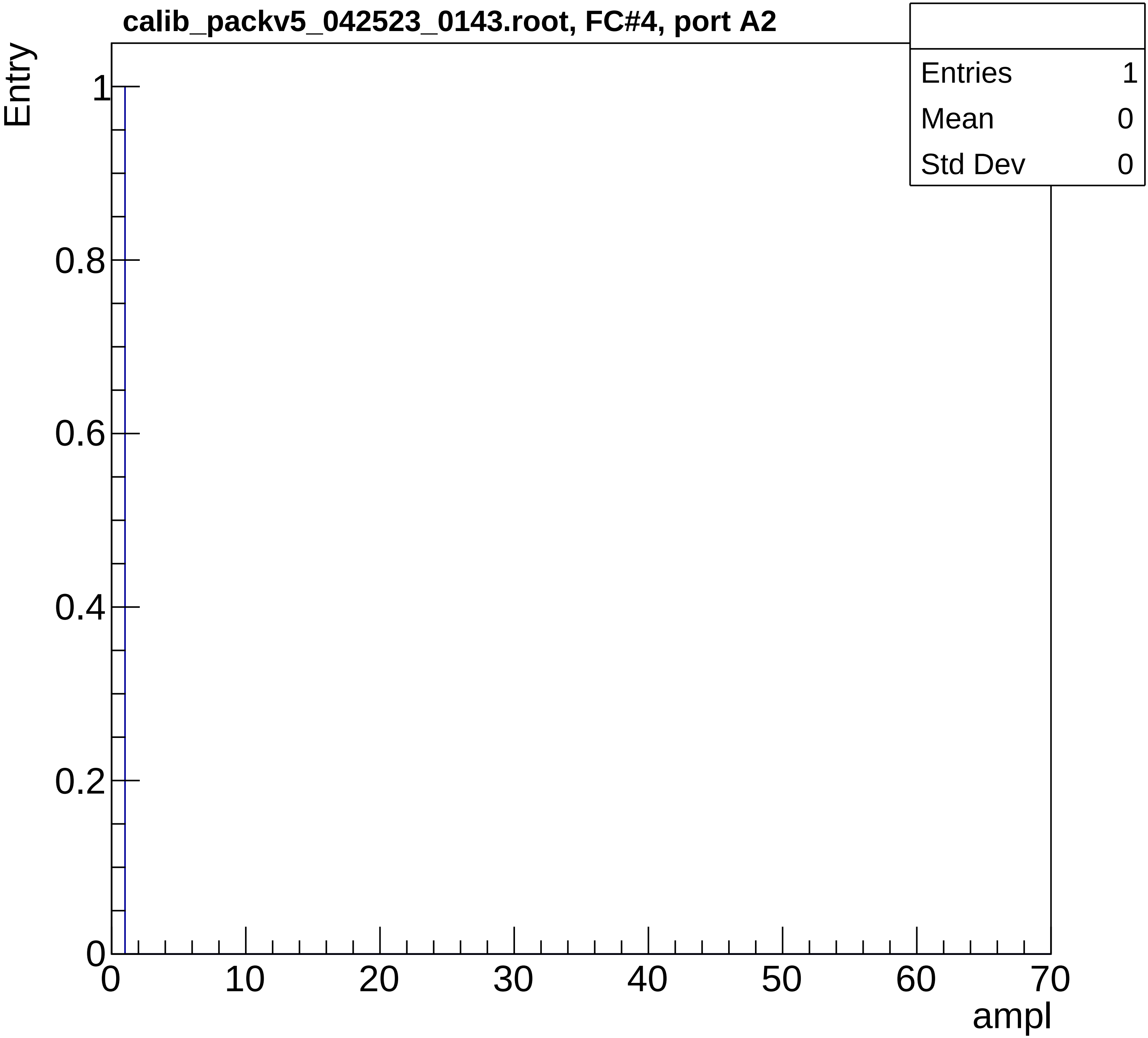
B1L100S, U21-ch59, adc5

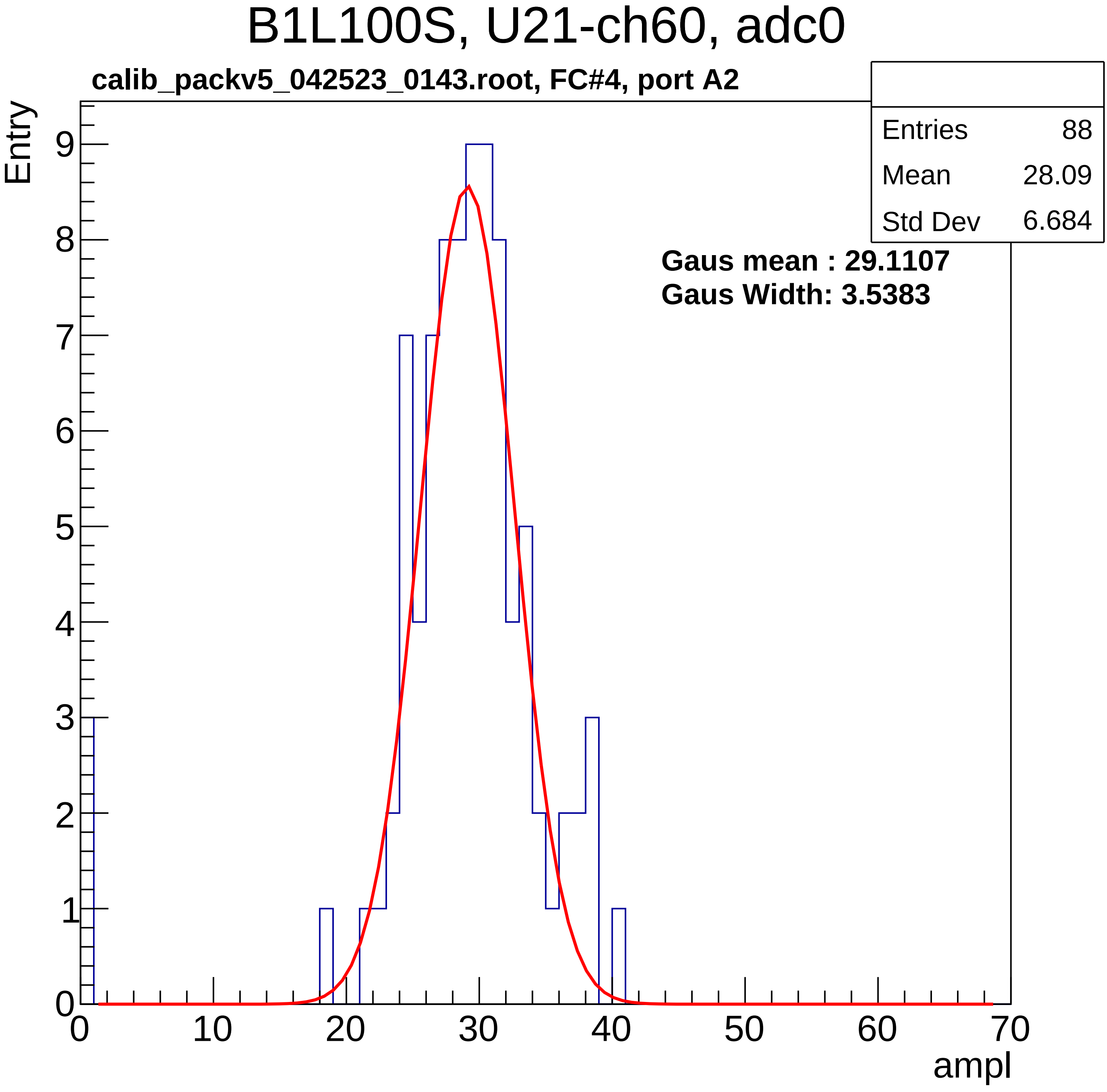
calib_packv5_042523_0143.root, FC#4, port A2

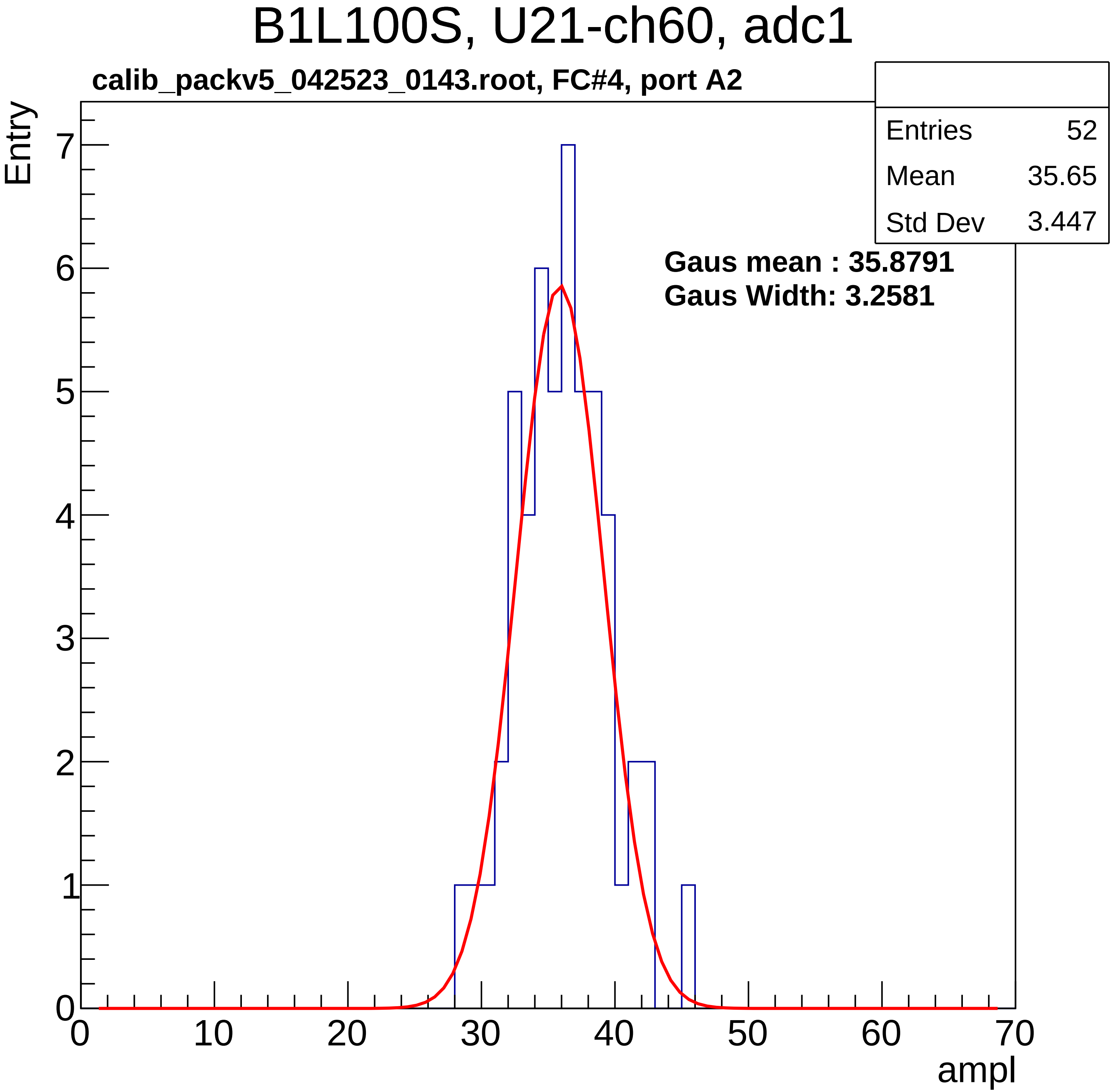


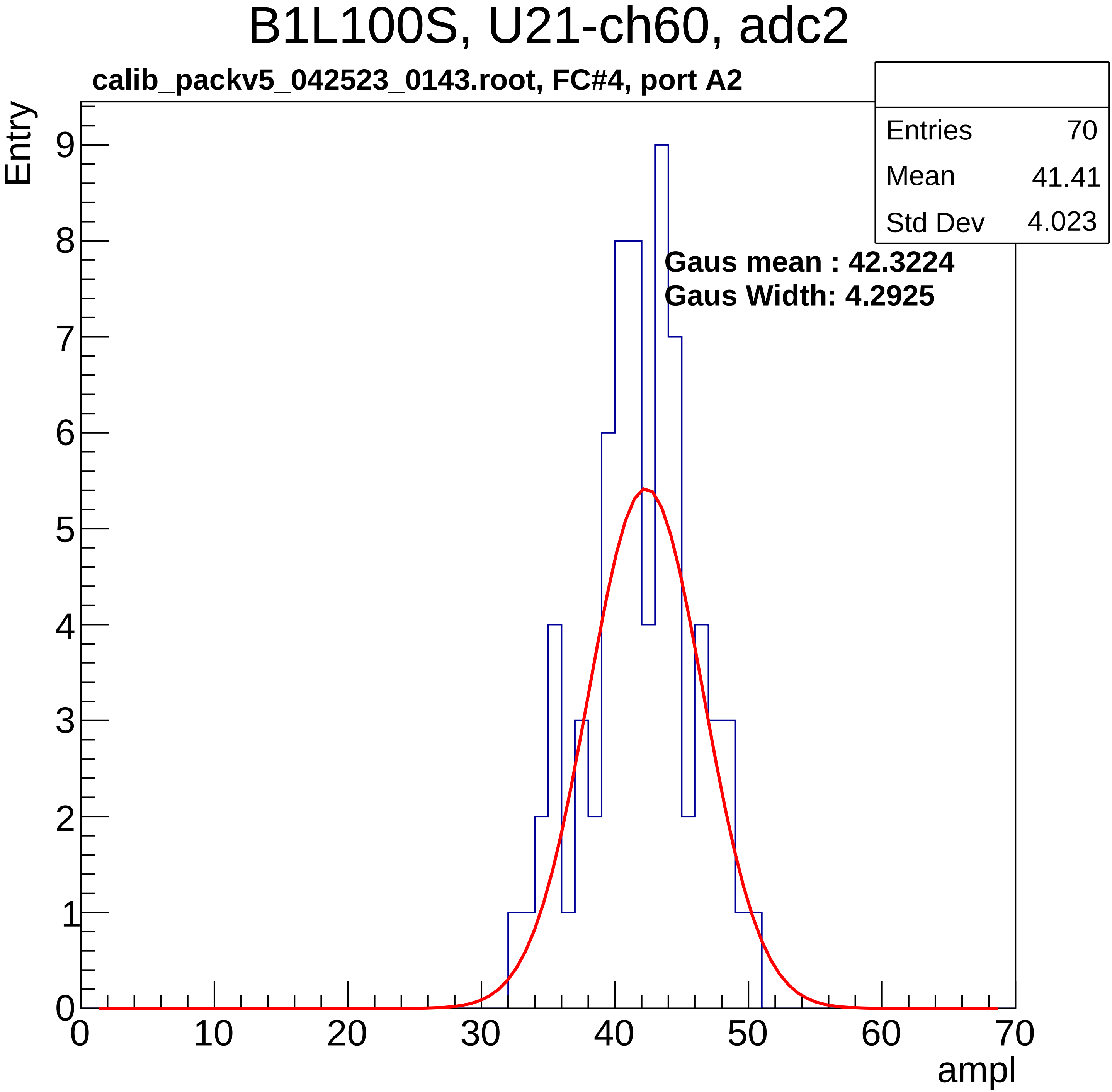

B1L100S, U21-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 60.5
Std Dev 0.5
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

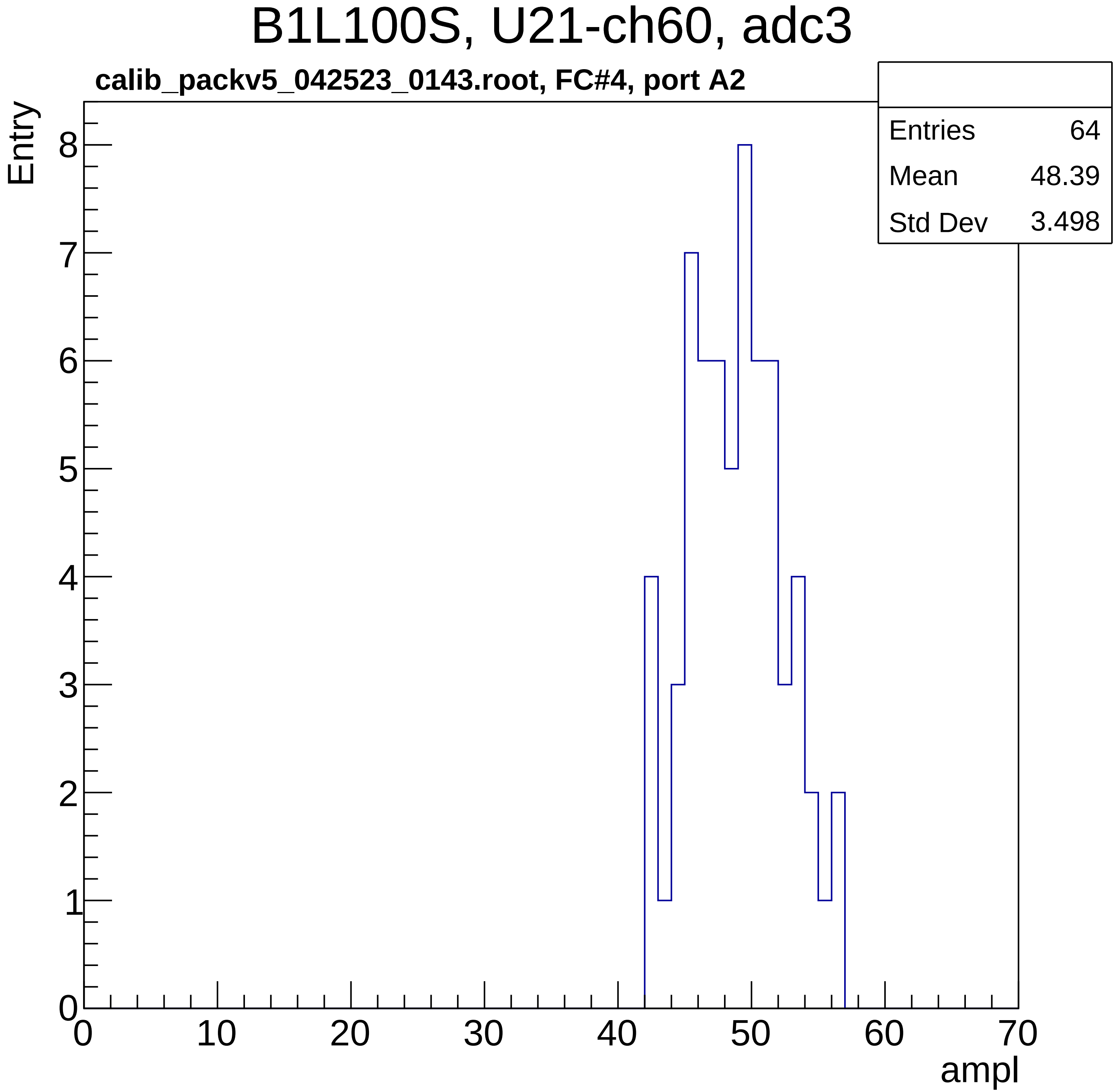
B1L100S, U21-ch59, adc7

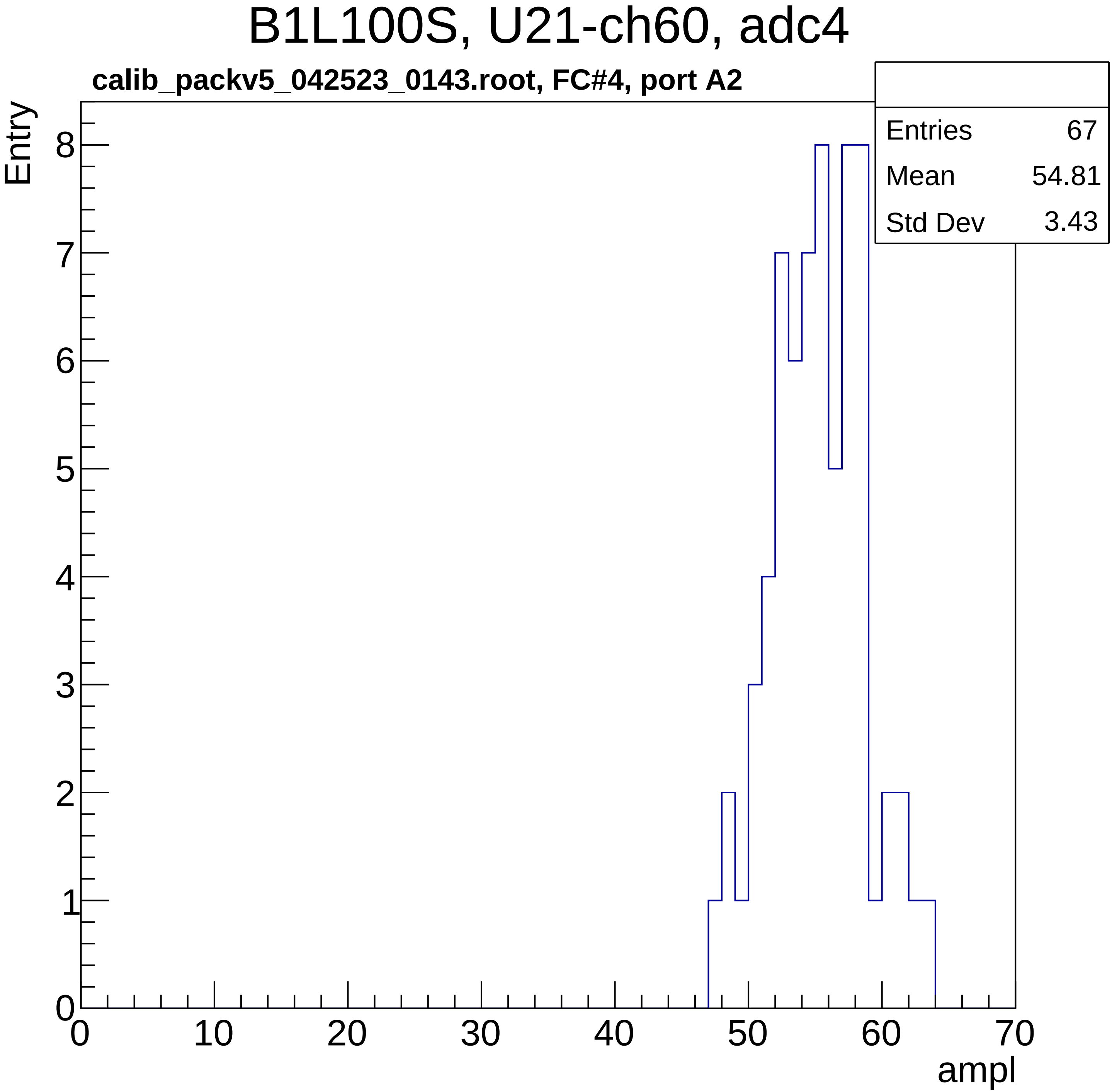


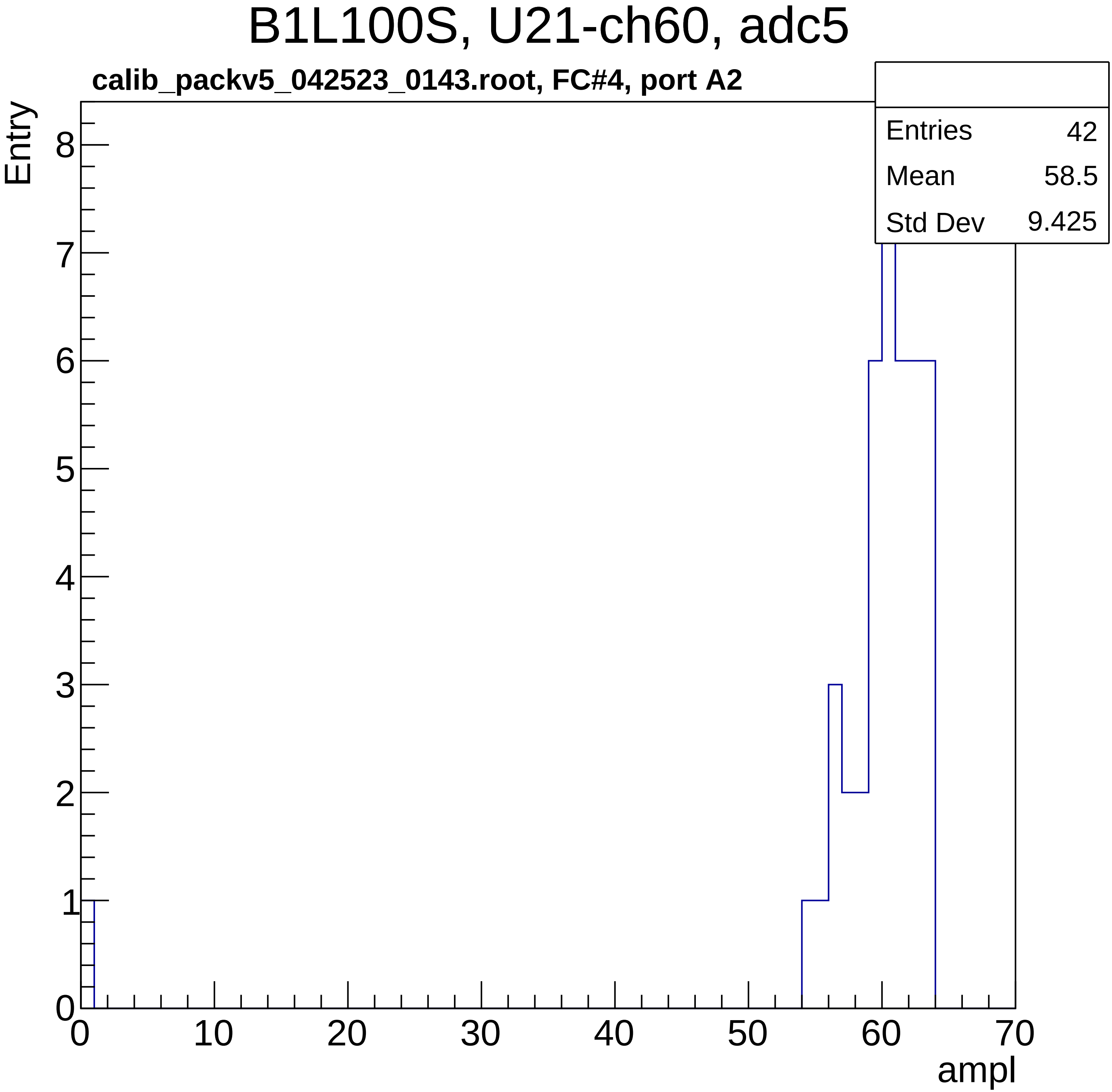

B1L100S, U21-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 28.09
Std Dev 6.684
Gaus mean : 29.1107
Gaus Width: 3.5383
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

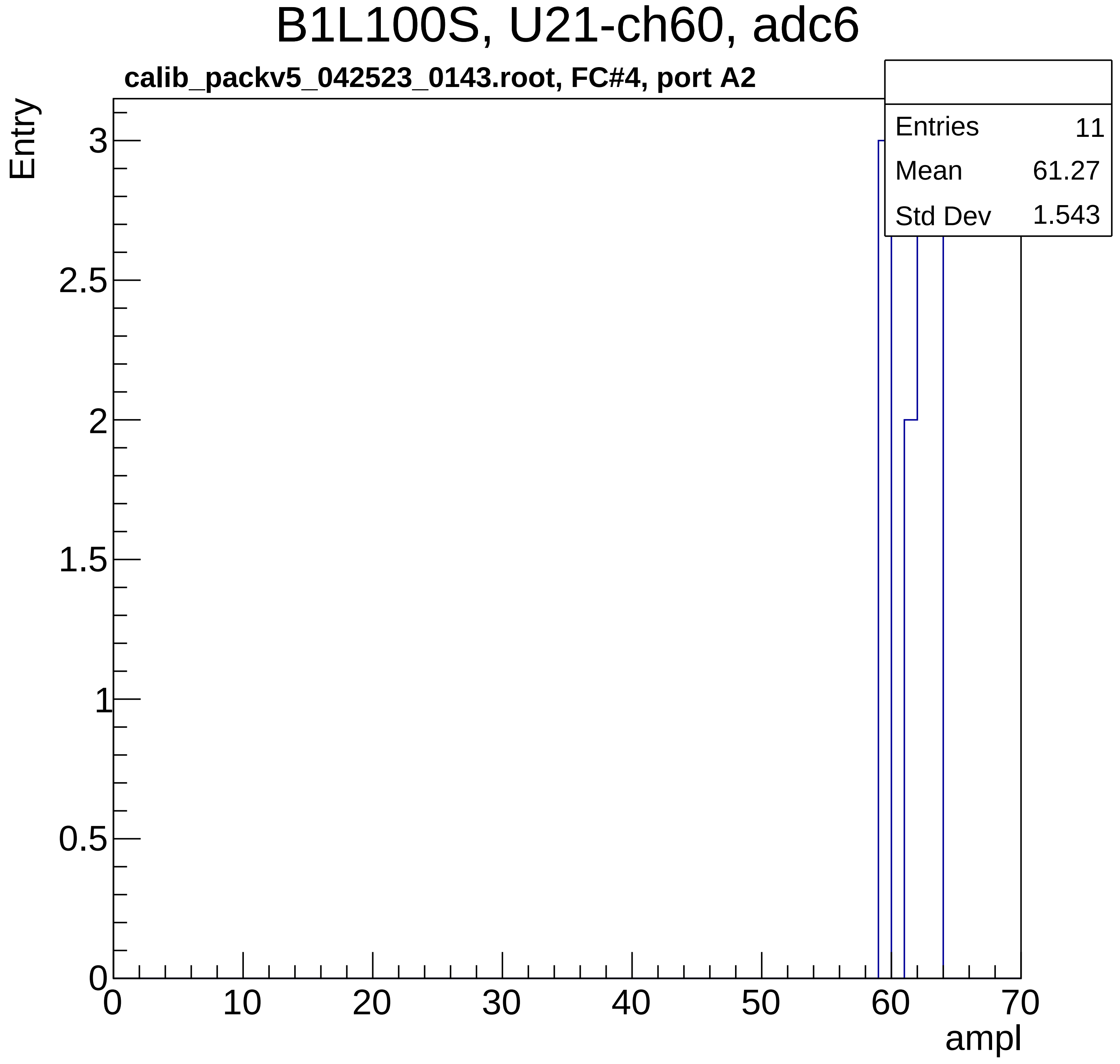

B1L100S, U21-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 35.65
Std Dev 3.447
Gaus mean : 35.8791
Gaus Width: 3.2581
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

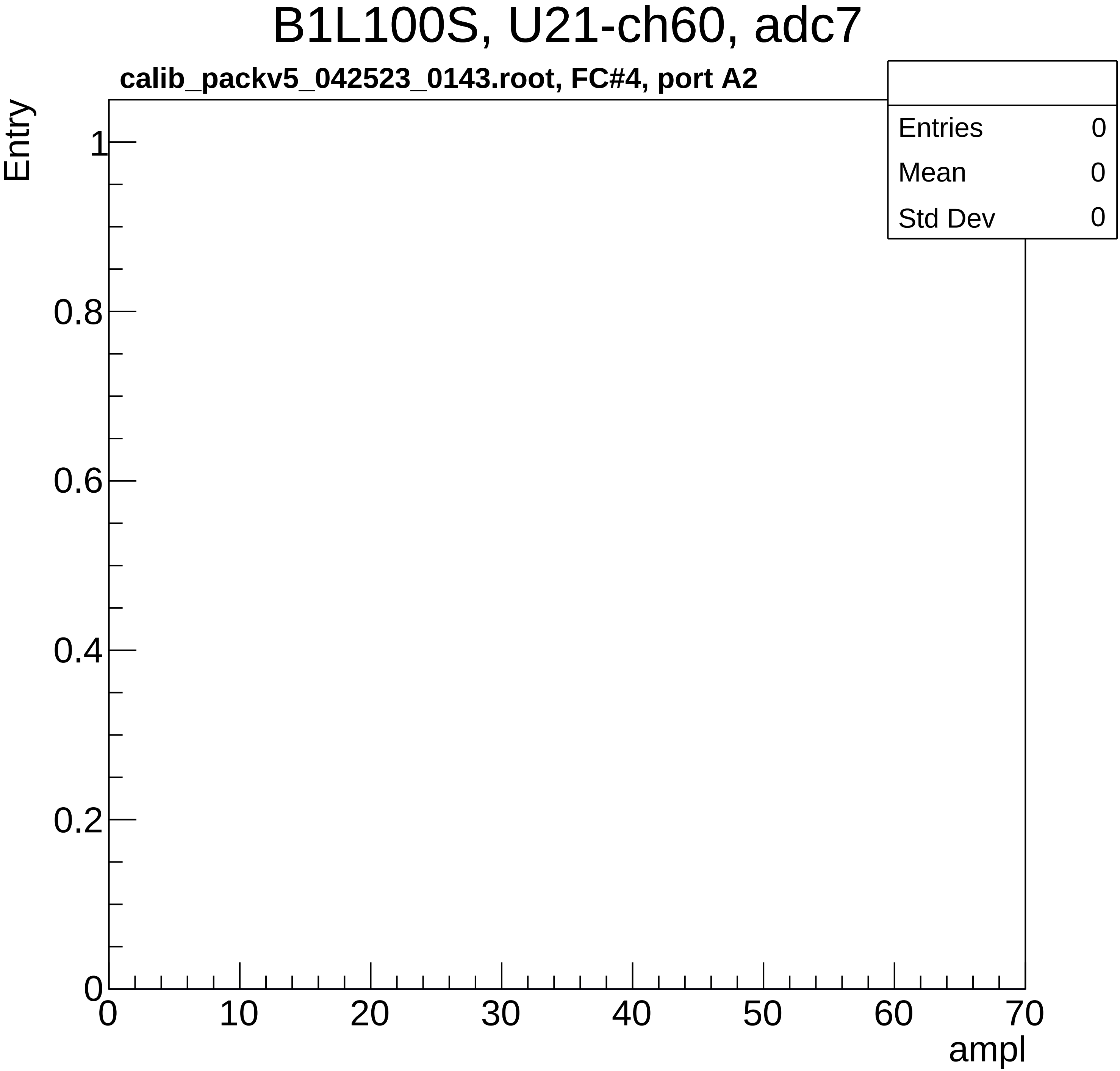

B1L100S, U21-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 41.41
Std Dev 4.023
Gaus mean : 42.3224
Gaus Width: 4.2925
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

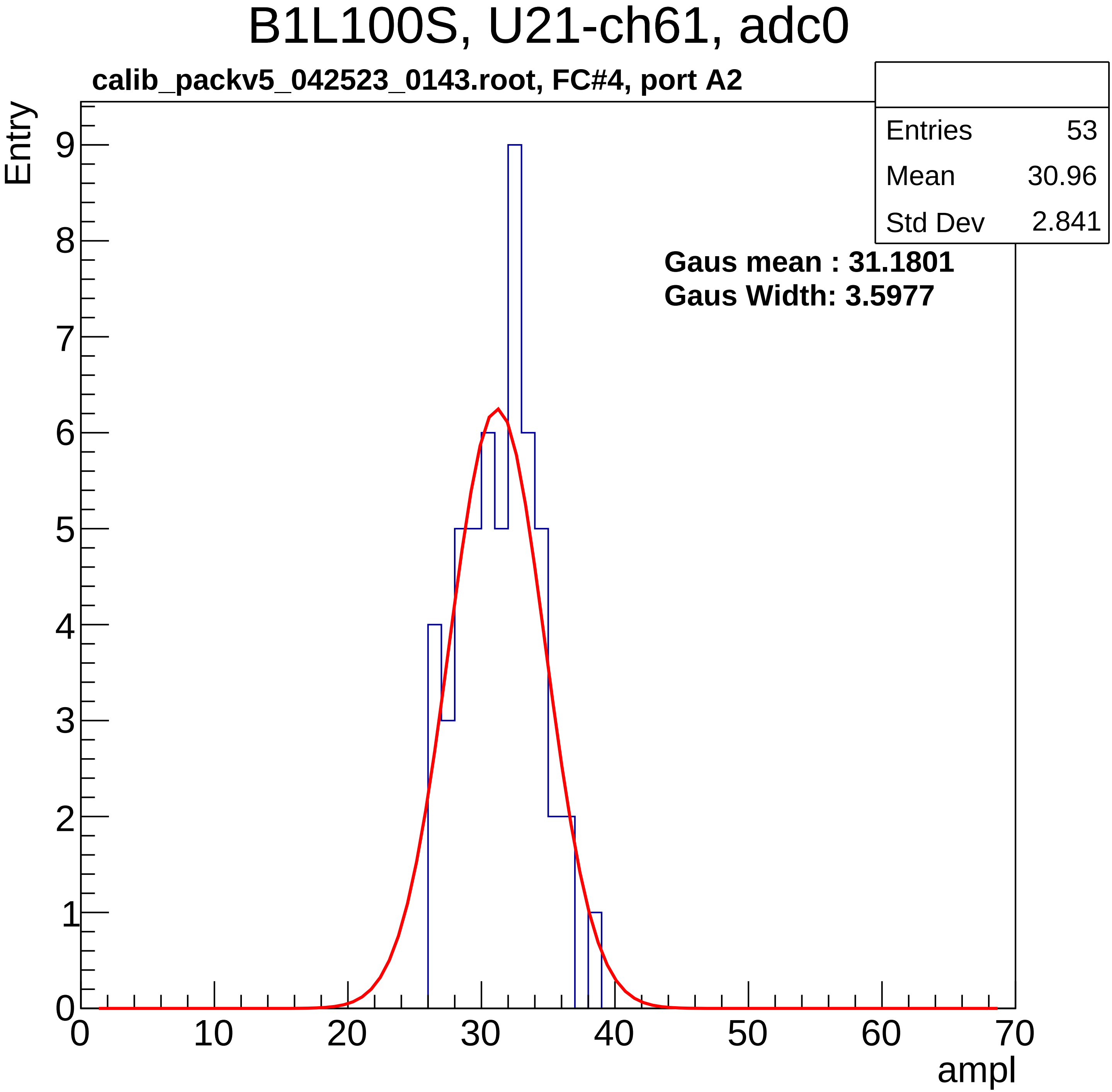

B1L100S, U21-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 48.39
Std Dev 3.498
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

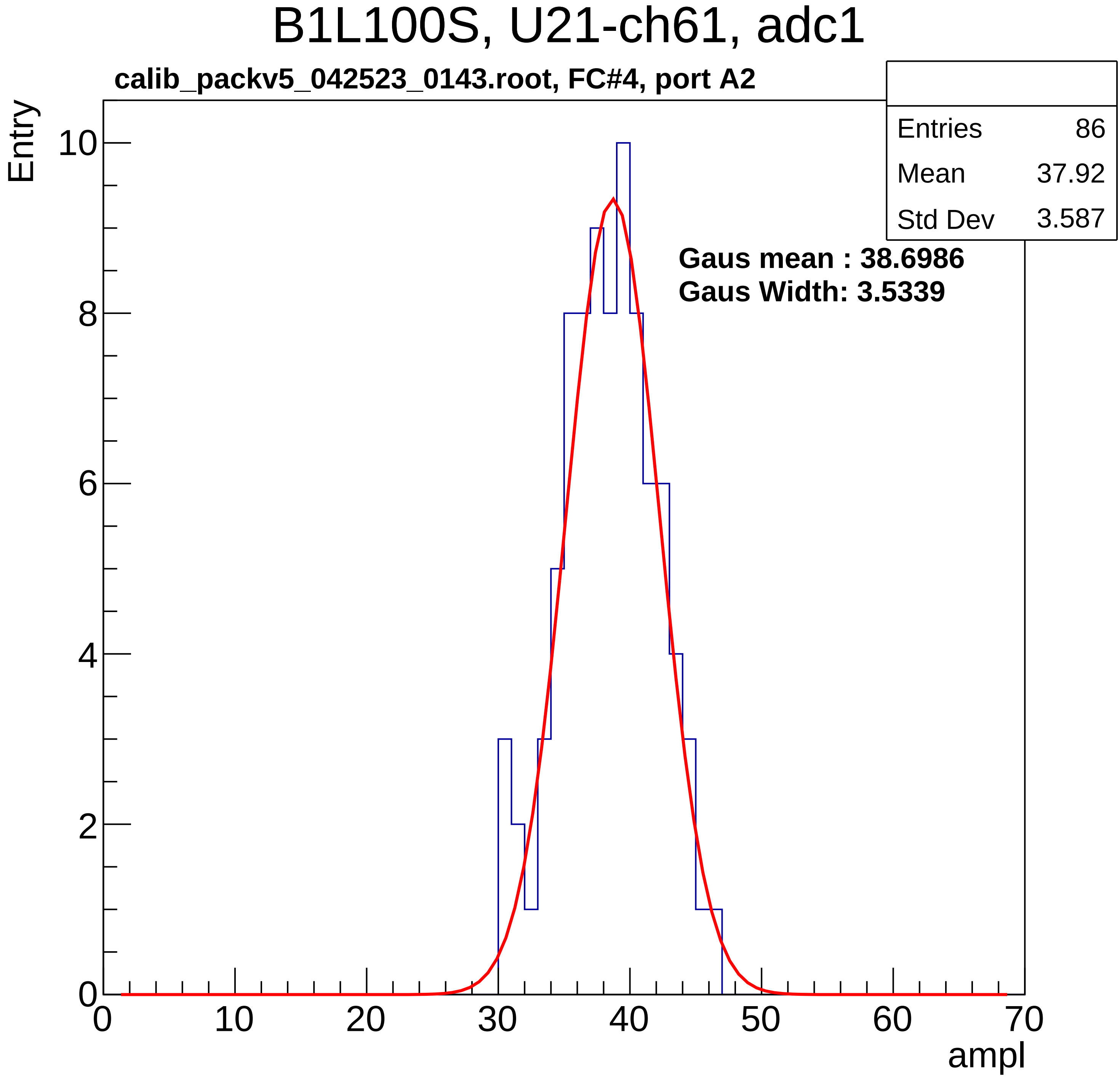

B1L100S, U21-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 54.81
Std Dev 3.43
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.5
Std Dev 9.425
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

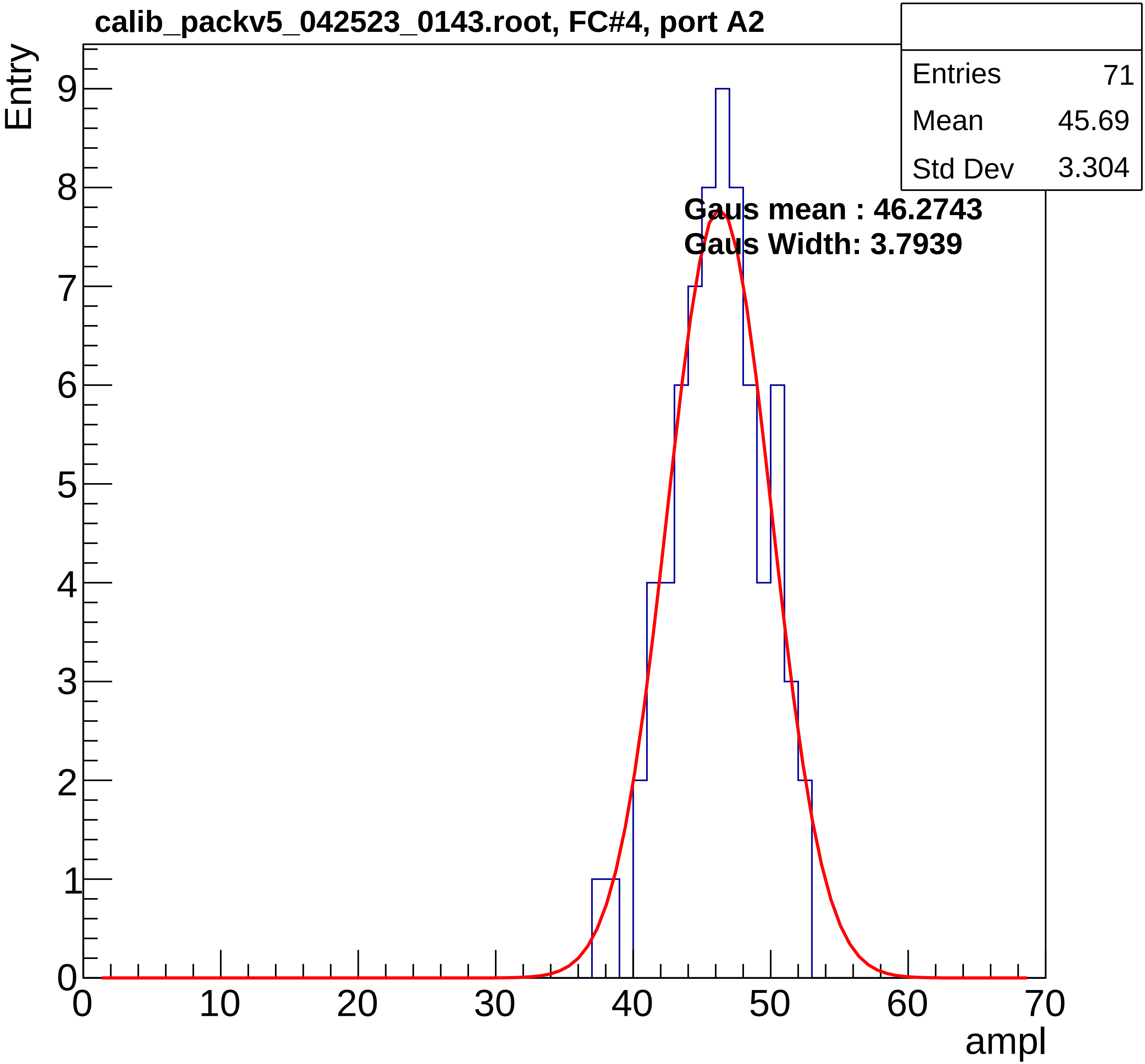

B1L100S, U21-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 11
Mean 61.27
Std Dev 1.543
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 30.96
Std Dev 2.841
Gaus mean : 31.1801
Gaus Width: 3.5977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

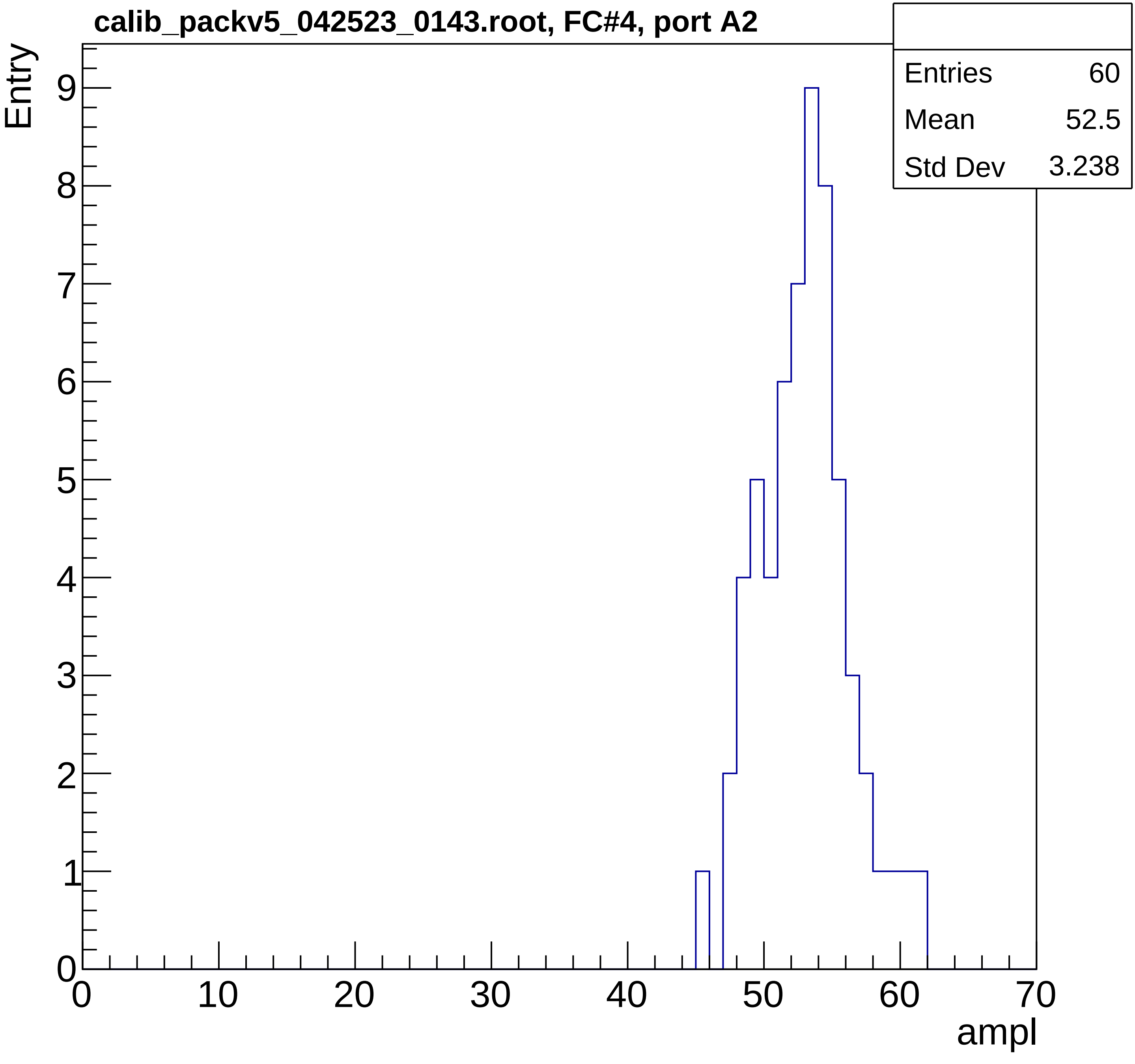

B1L100S, U21-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 37.92
Std Dev 3.587
Gaus mean : 38.6986
Gaus Width: 3.5339
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

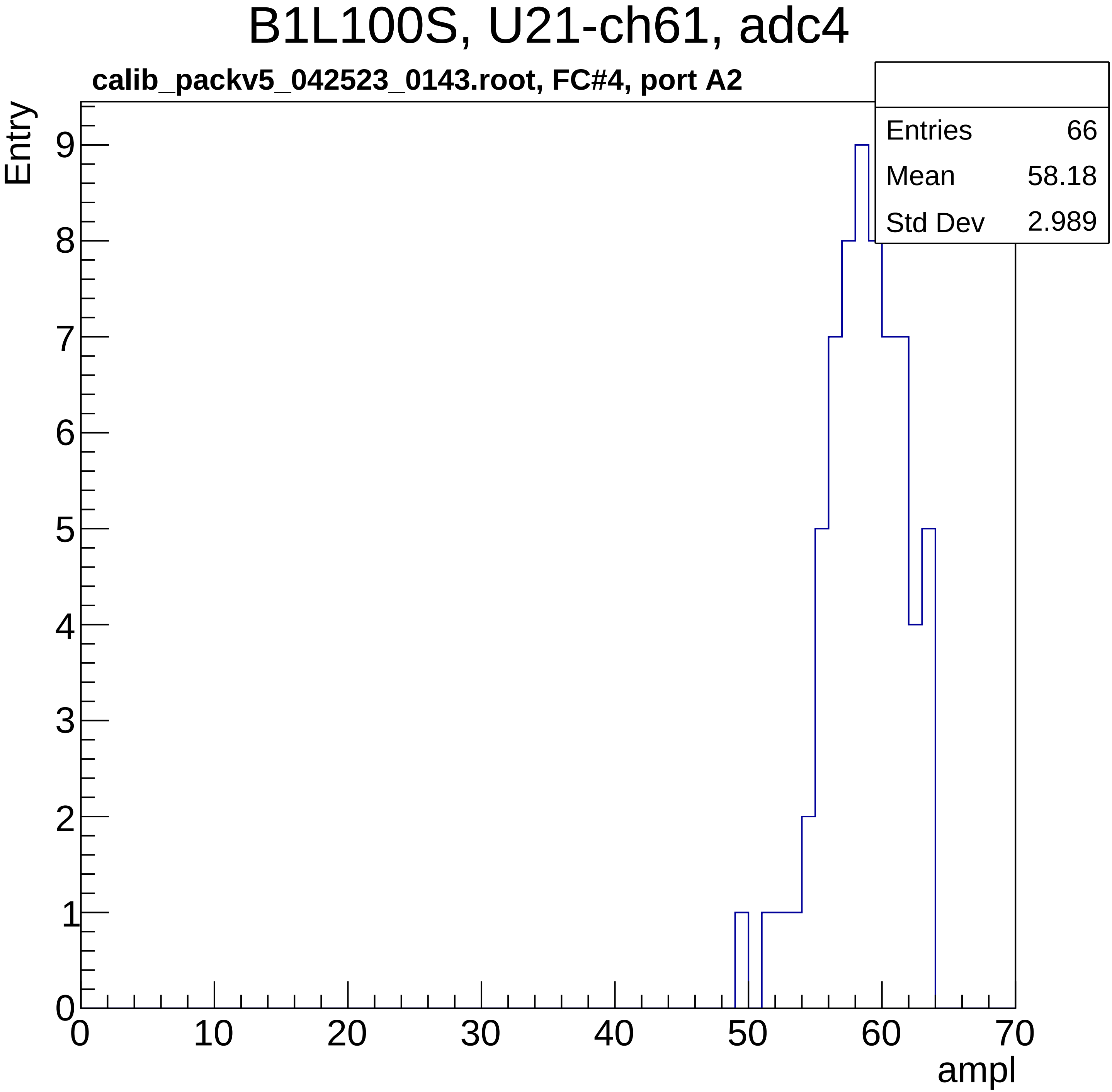
B1L100S, U21-ch61, adc2



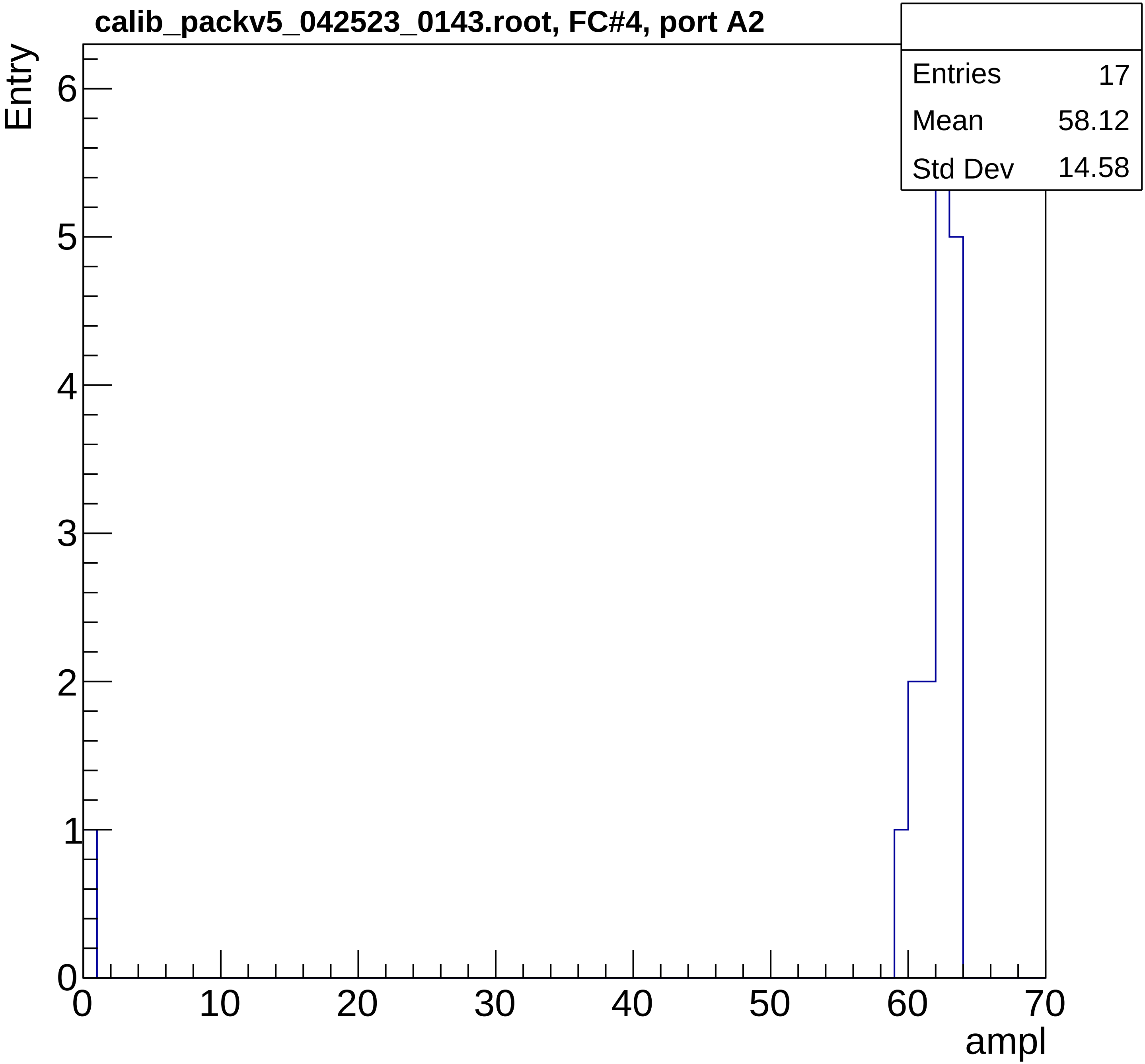
B1L100S, U21-ch61, adc3

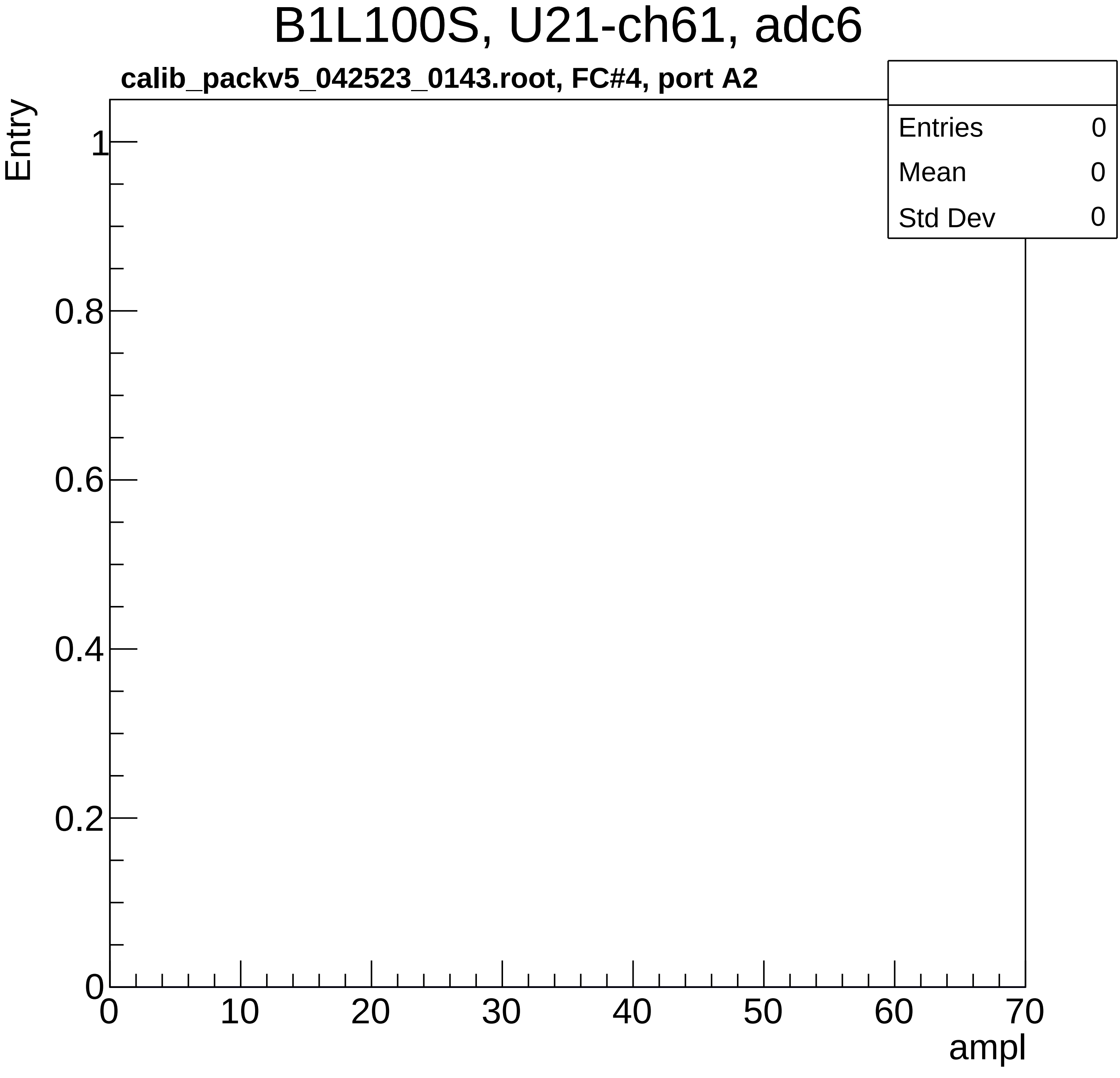
calib_packv5_042523_0143.root, FC#4, port A2

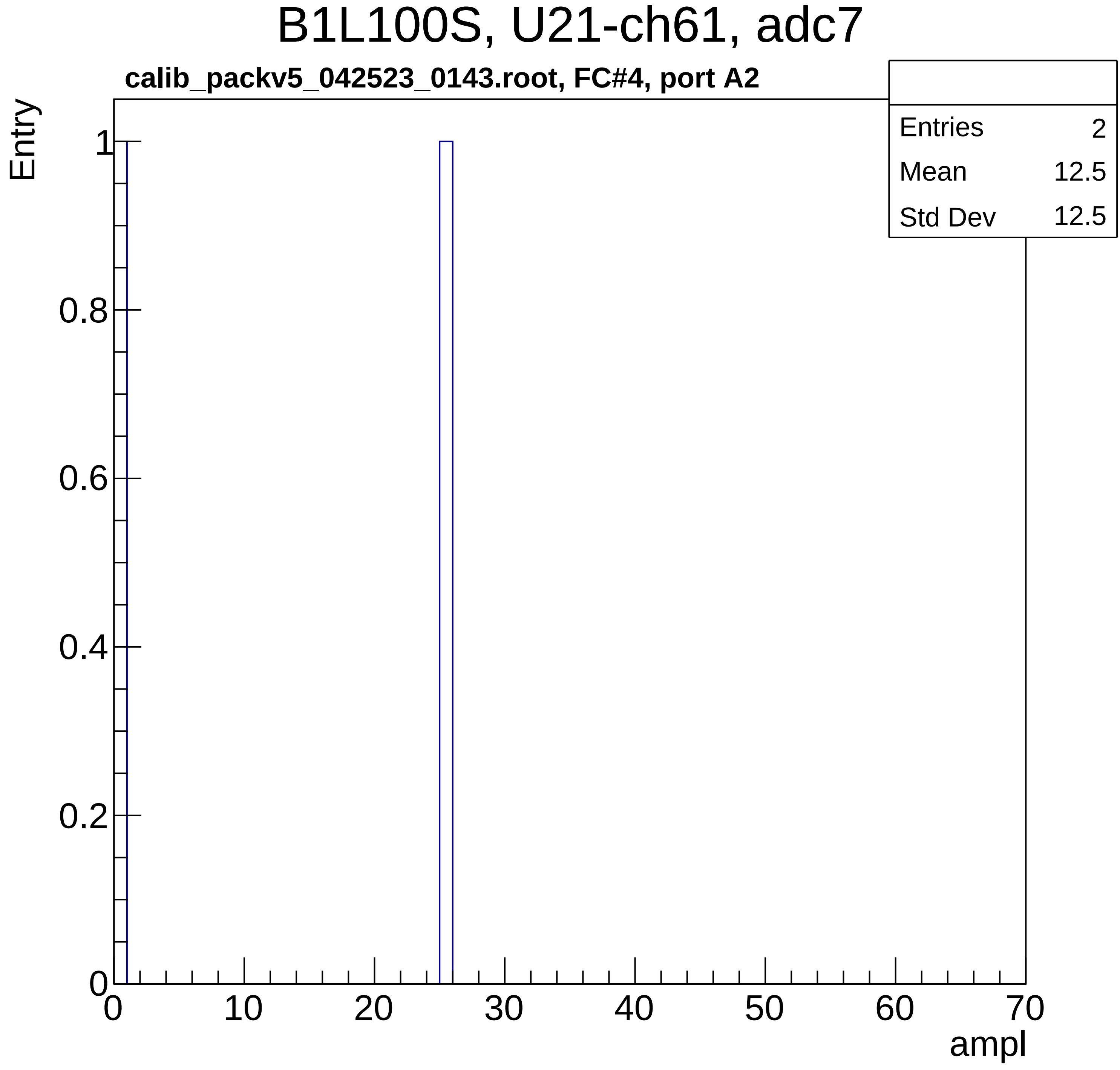


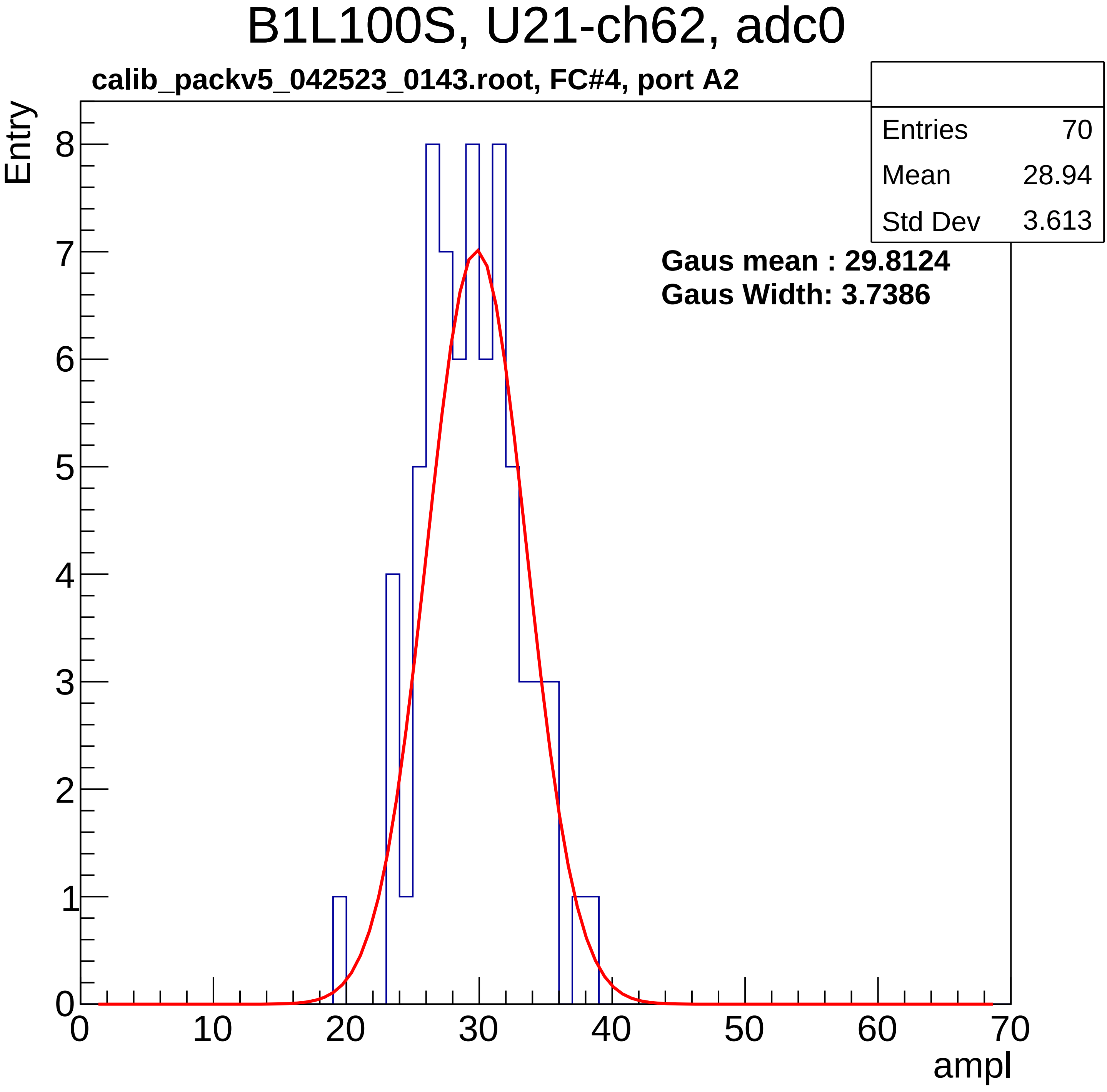

B1L100S, U21-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 58.18
Std Dev 2.989
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

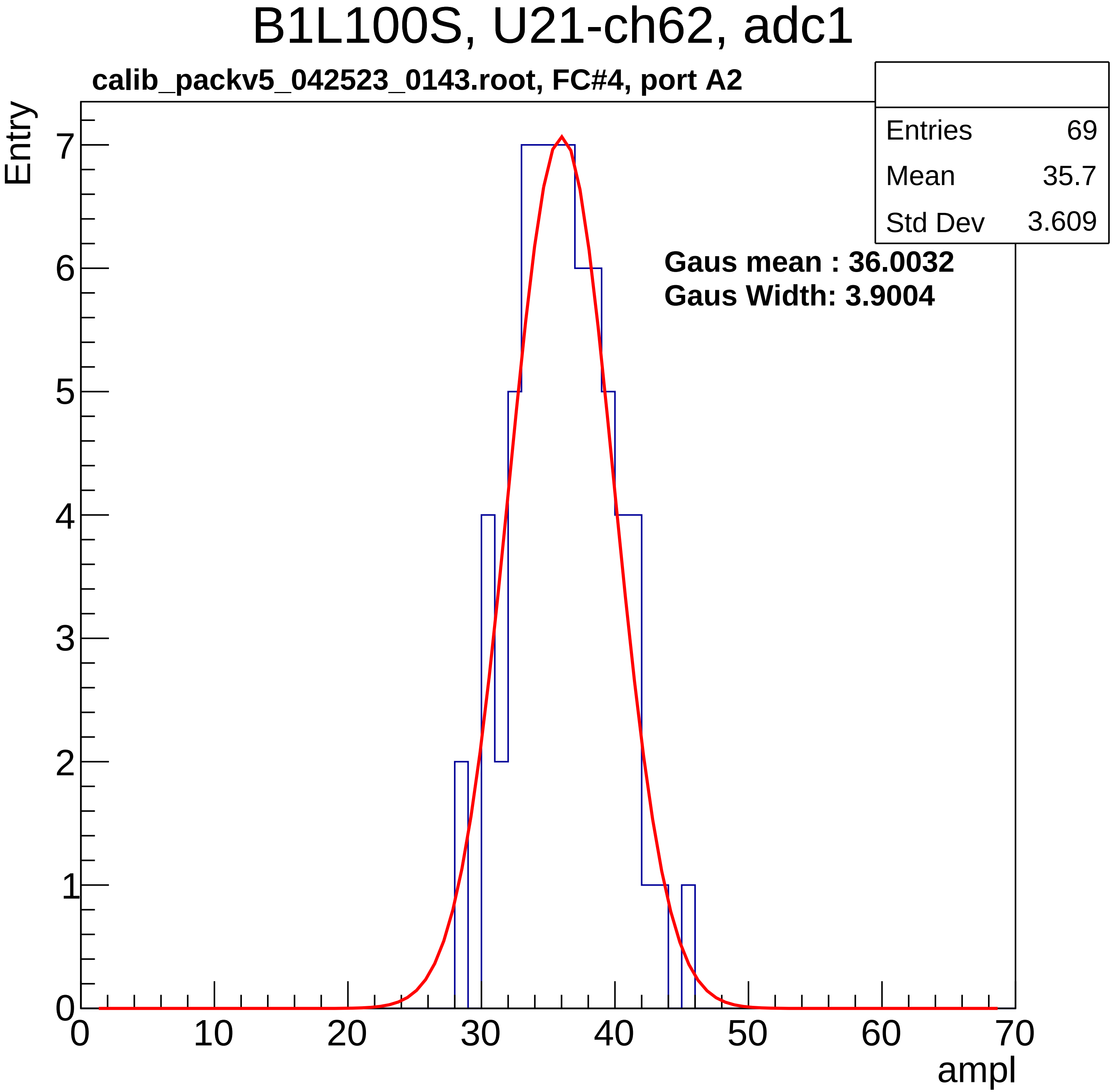
B1L100S, U21-ch61, adc5

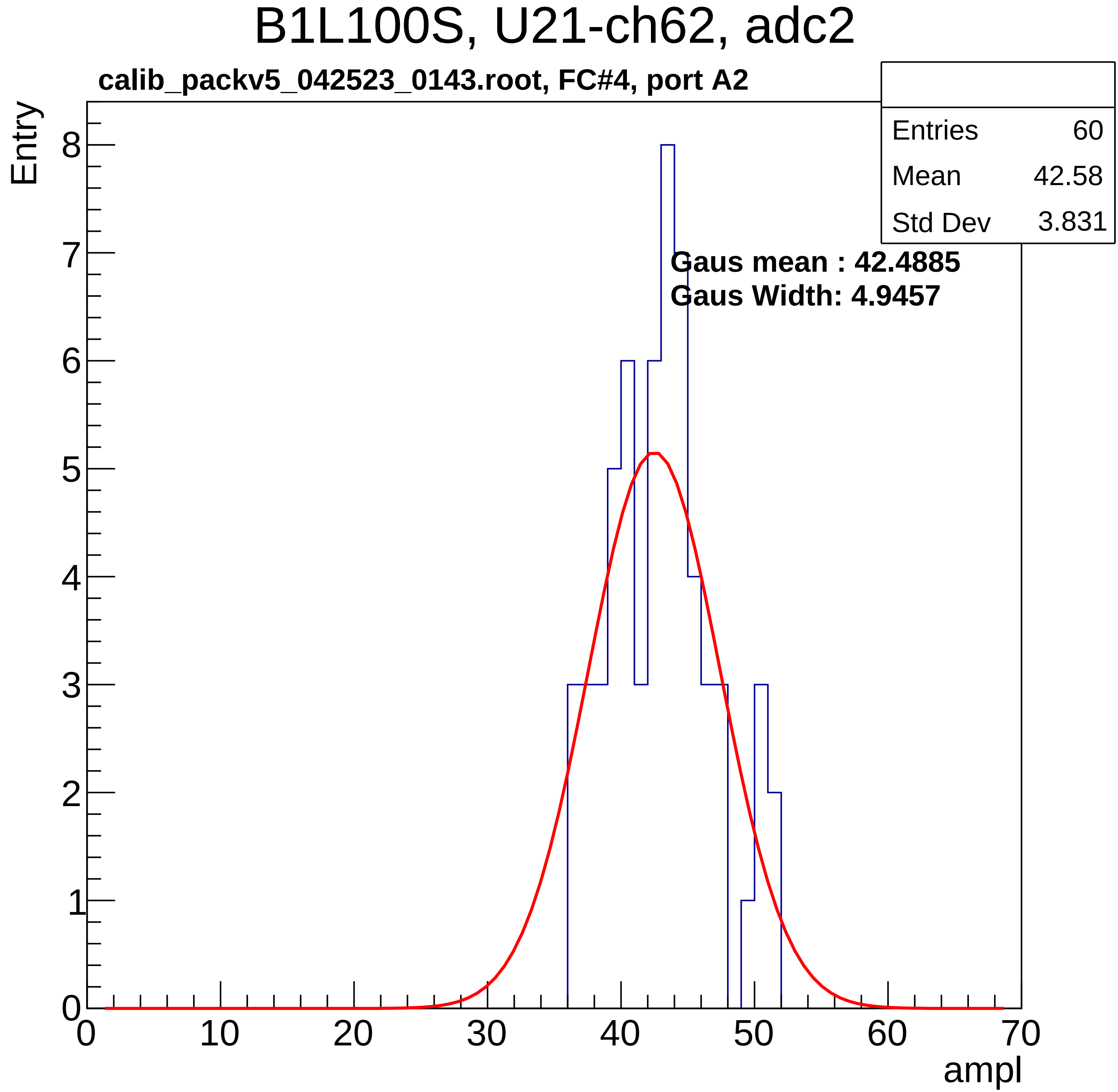



B1L100S, U21-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

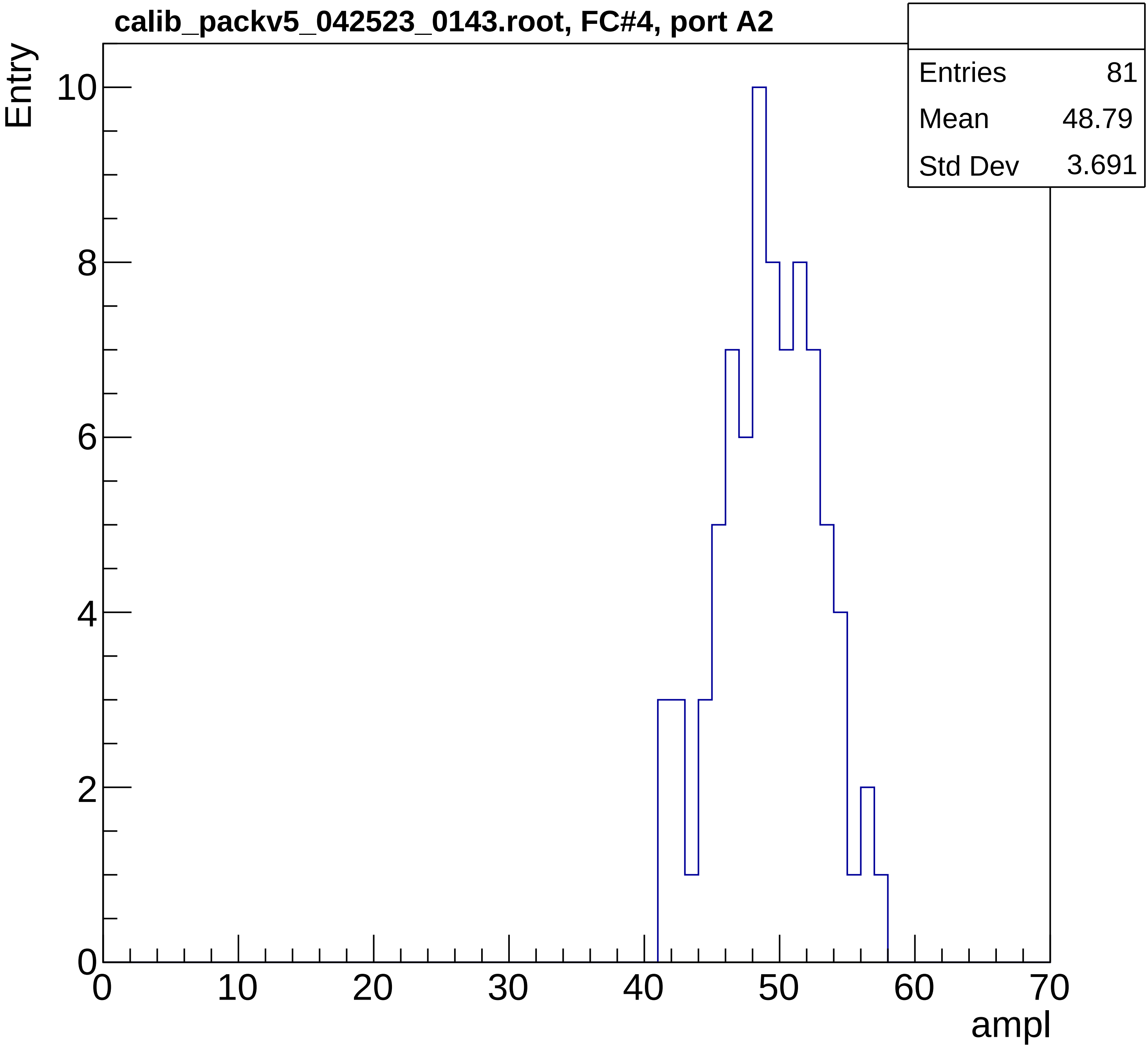

B1L100S, U21-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.94
Std Dev 3.613
Gaus mean : 29.8124
Gaus Width: 3.7386
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 69
Mean 35.7
Std Dev 3.609
Gaus mean : 36.0032
Gaus Width: 3.9004
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.58
Std Dev 3.831
Gaus mean : 42.4885
Gaus Width: 4.9457
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

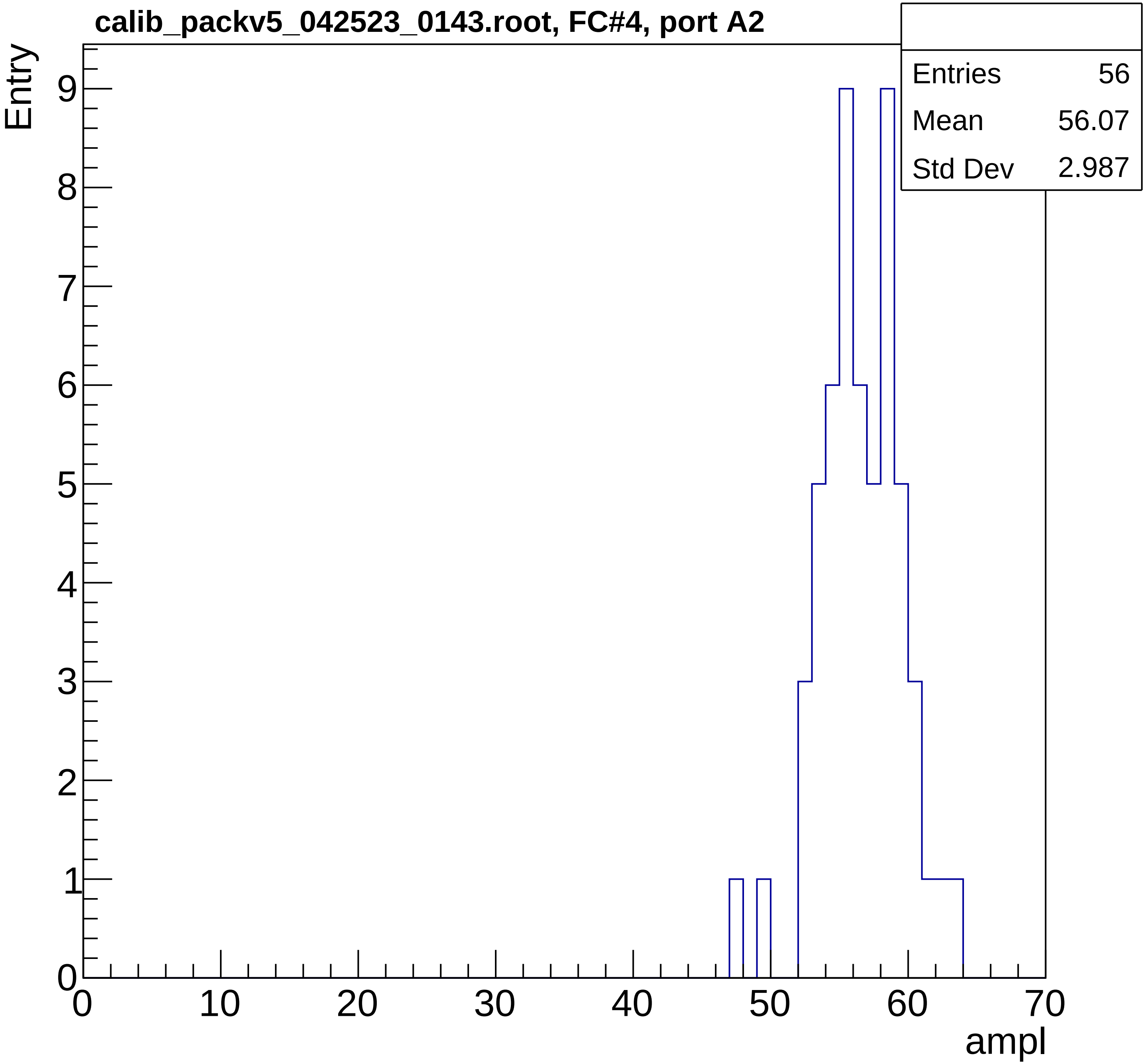
B1L100S, U21-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2



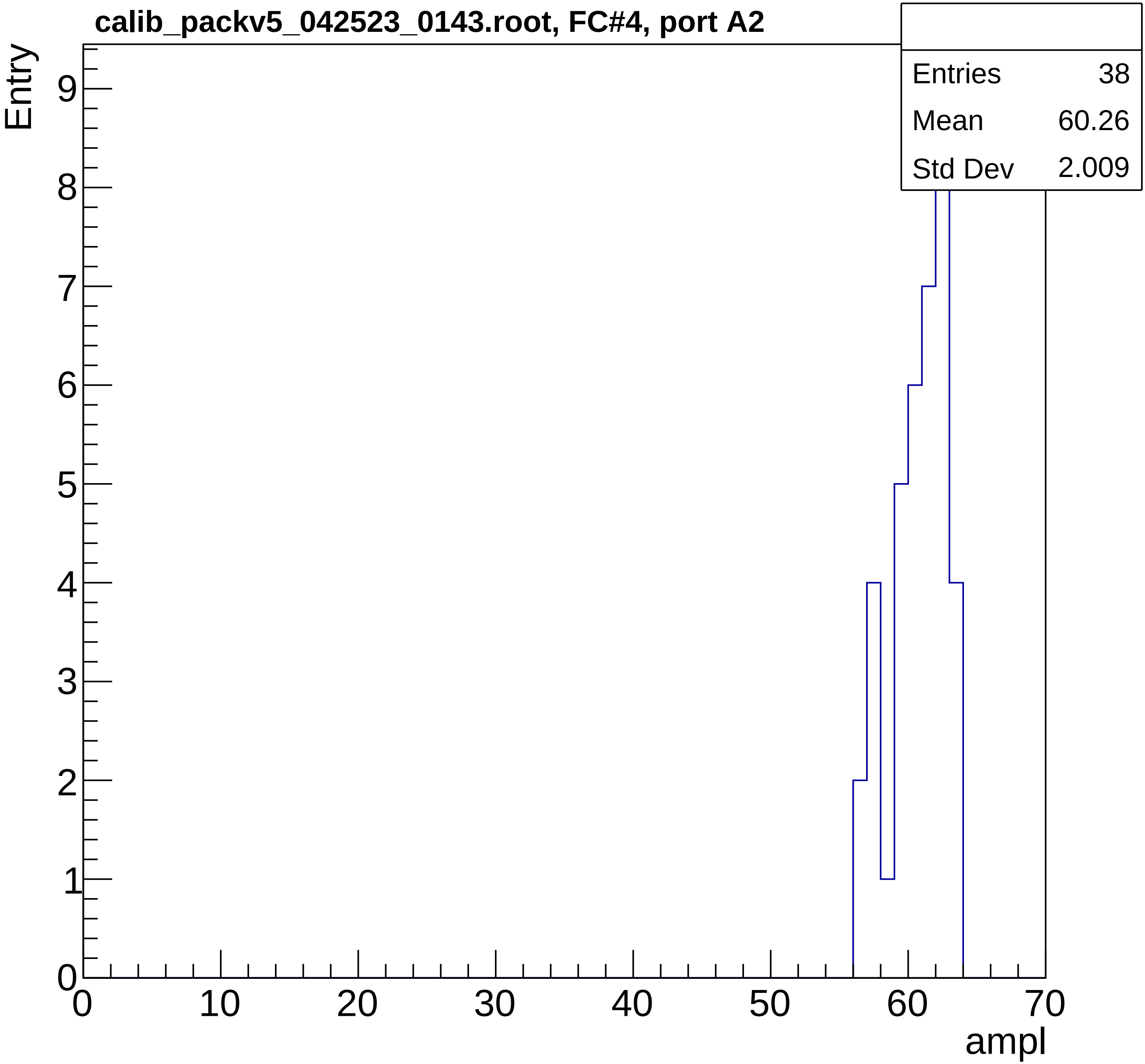
B1L100S, U21-ch62, adc4

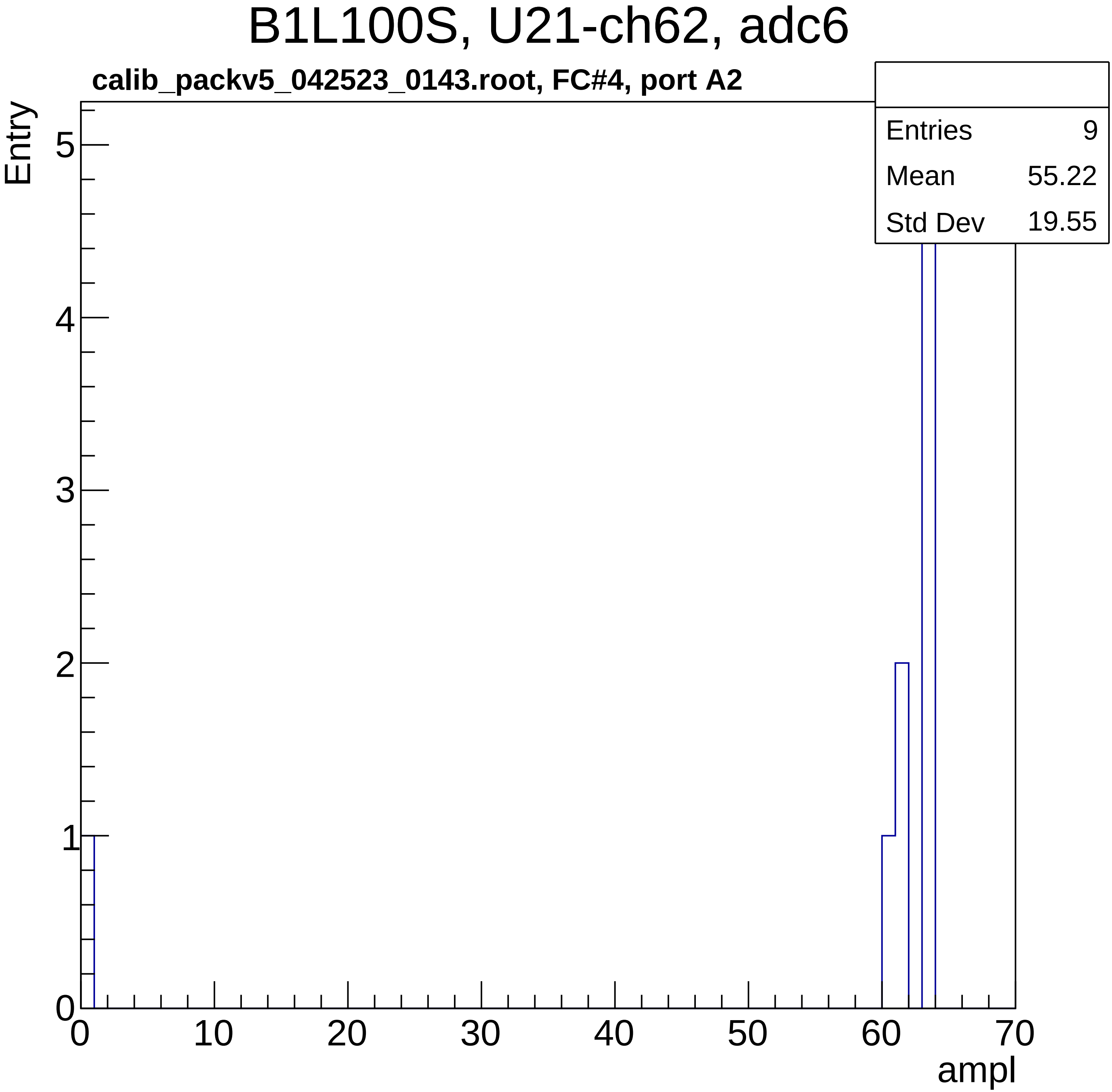
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U21-ch62, adc5

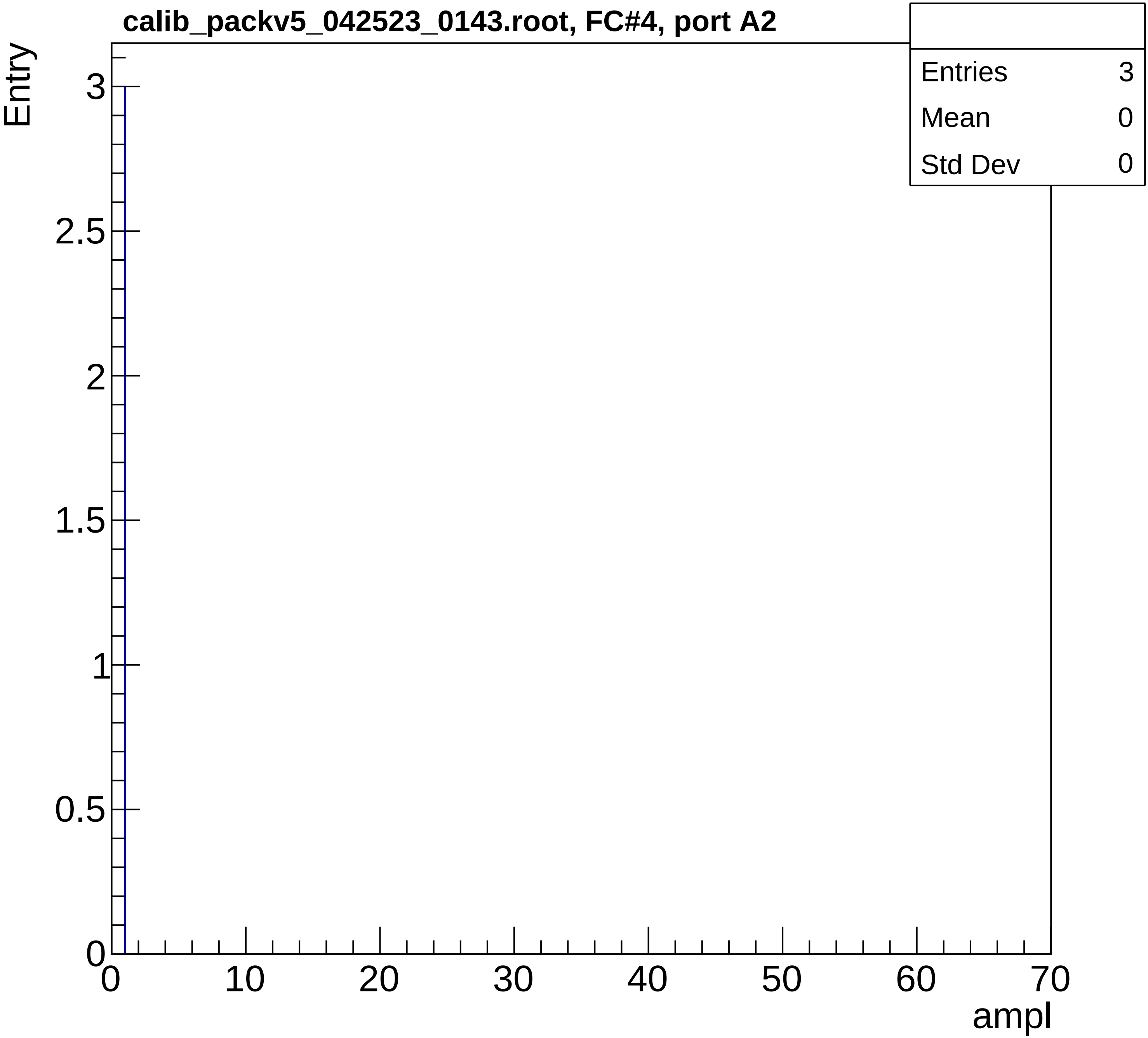
calib_packv5_042523_0143.root, FC#4, port A2




B1L100S, U21-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 9
Mean 55.22
Std Dev 19.55
0
1
2
3
4
5
0
10
20
30
40
50
60
70

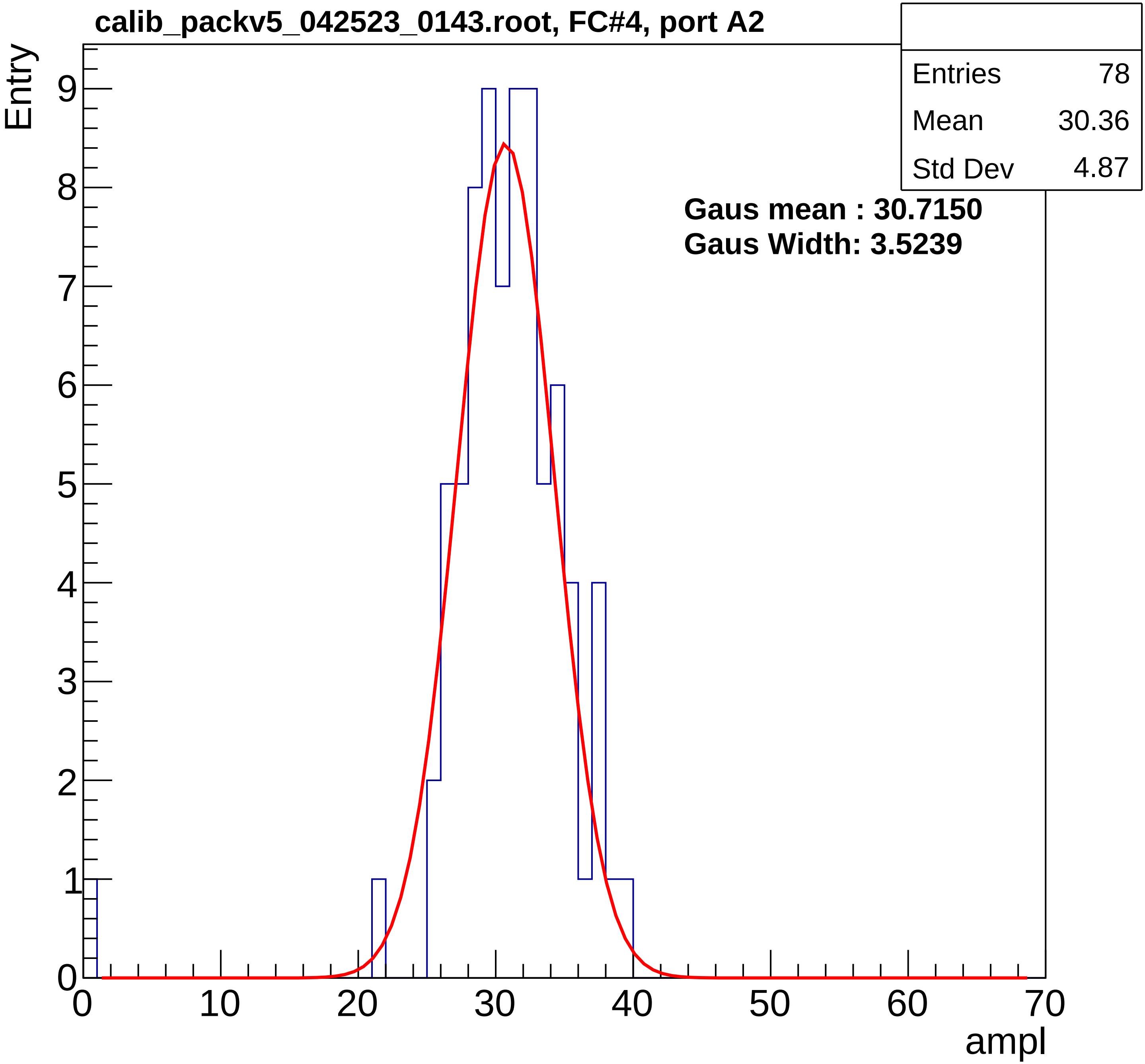
B1L100S, U21-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

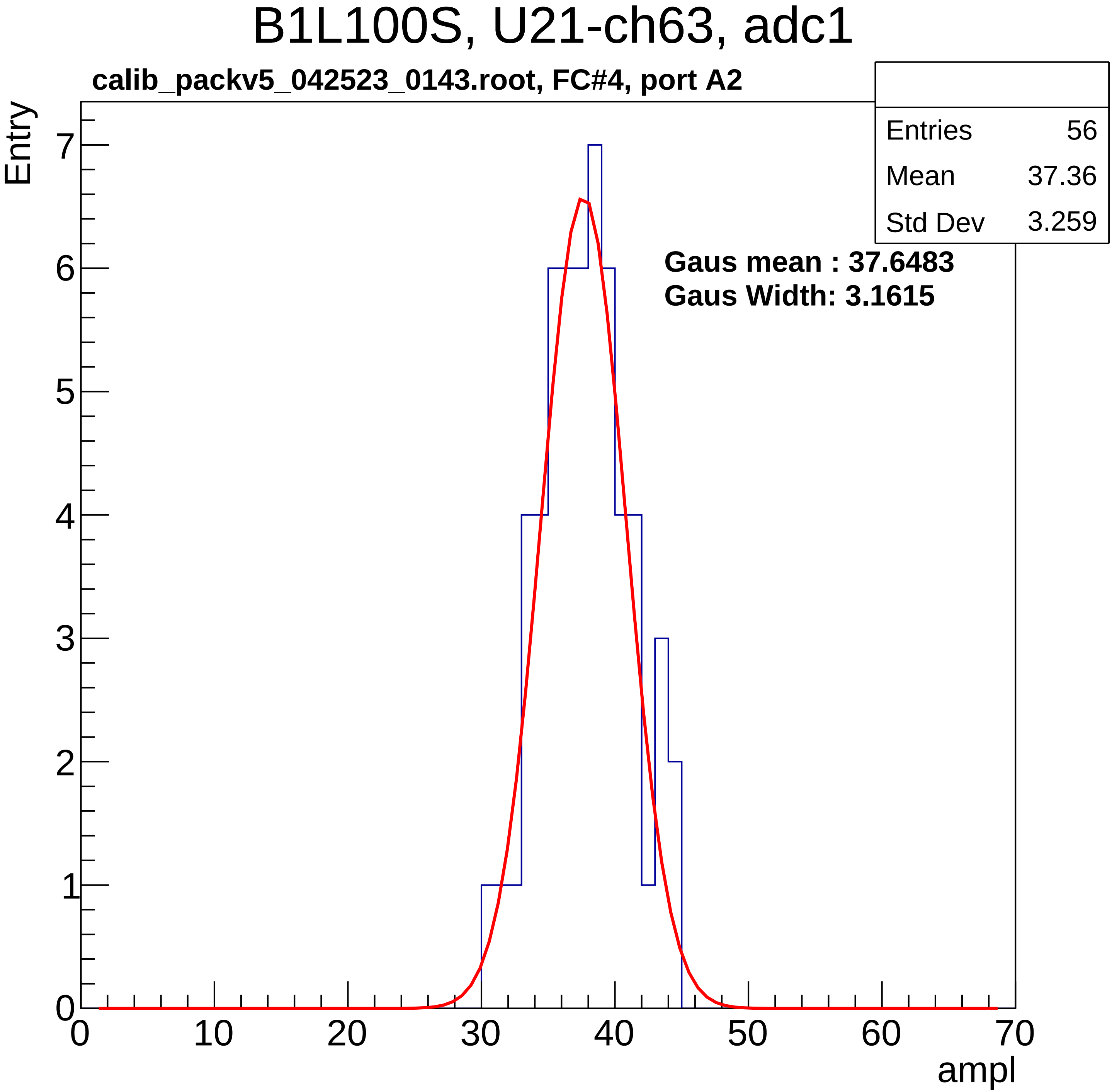


B1L100S, U21-ch63, adc0

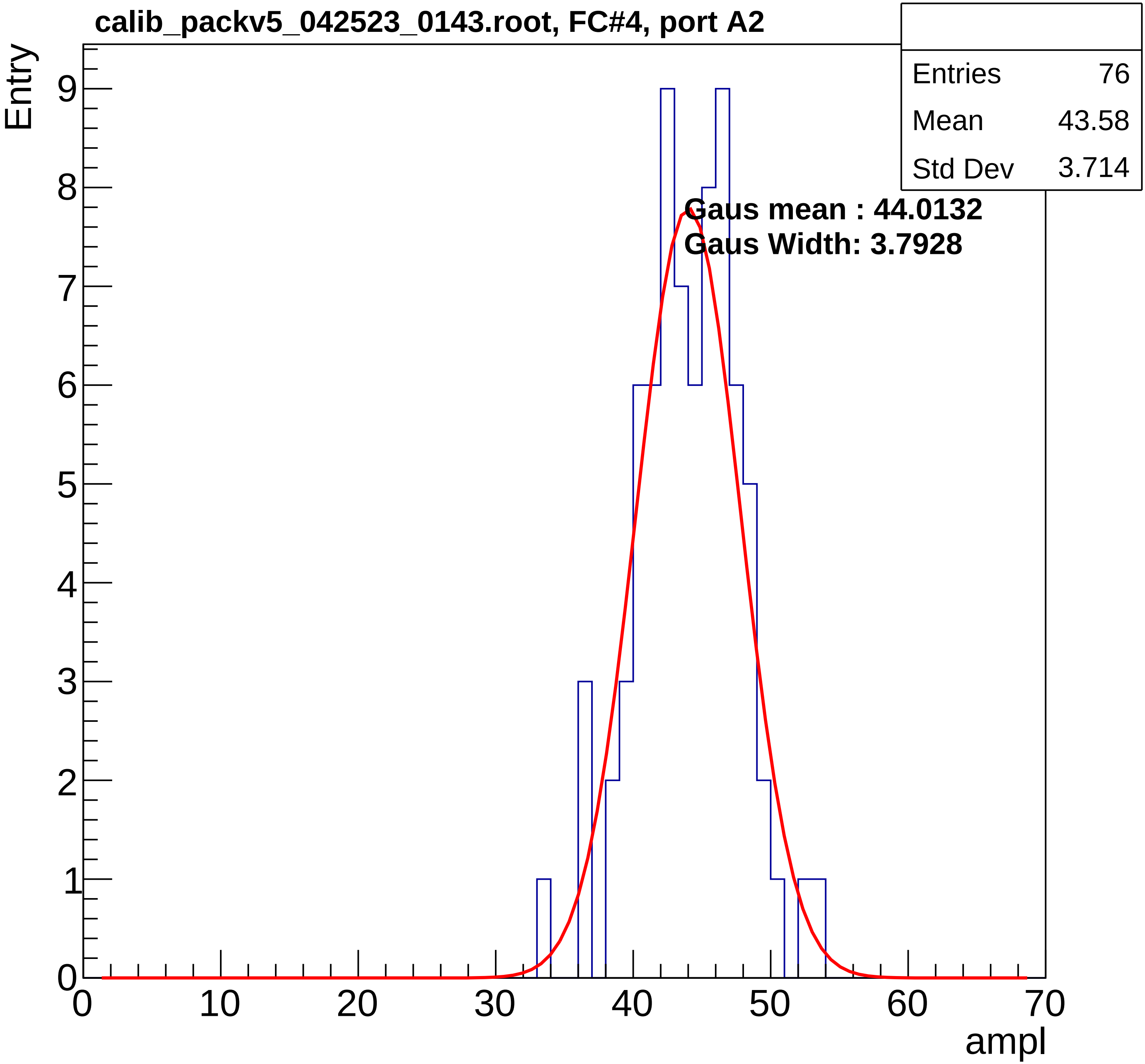
calib_packv5_042523_0143.root, FC#4, port A2



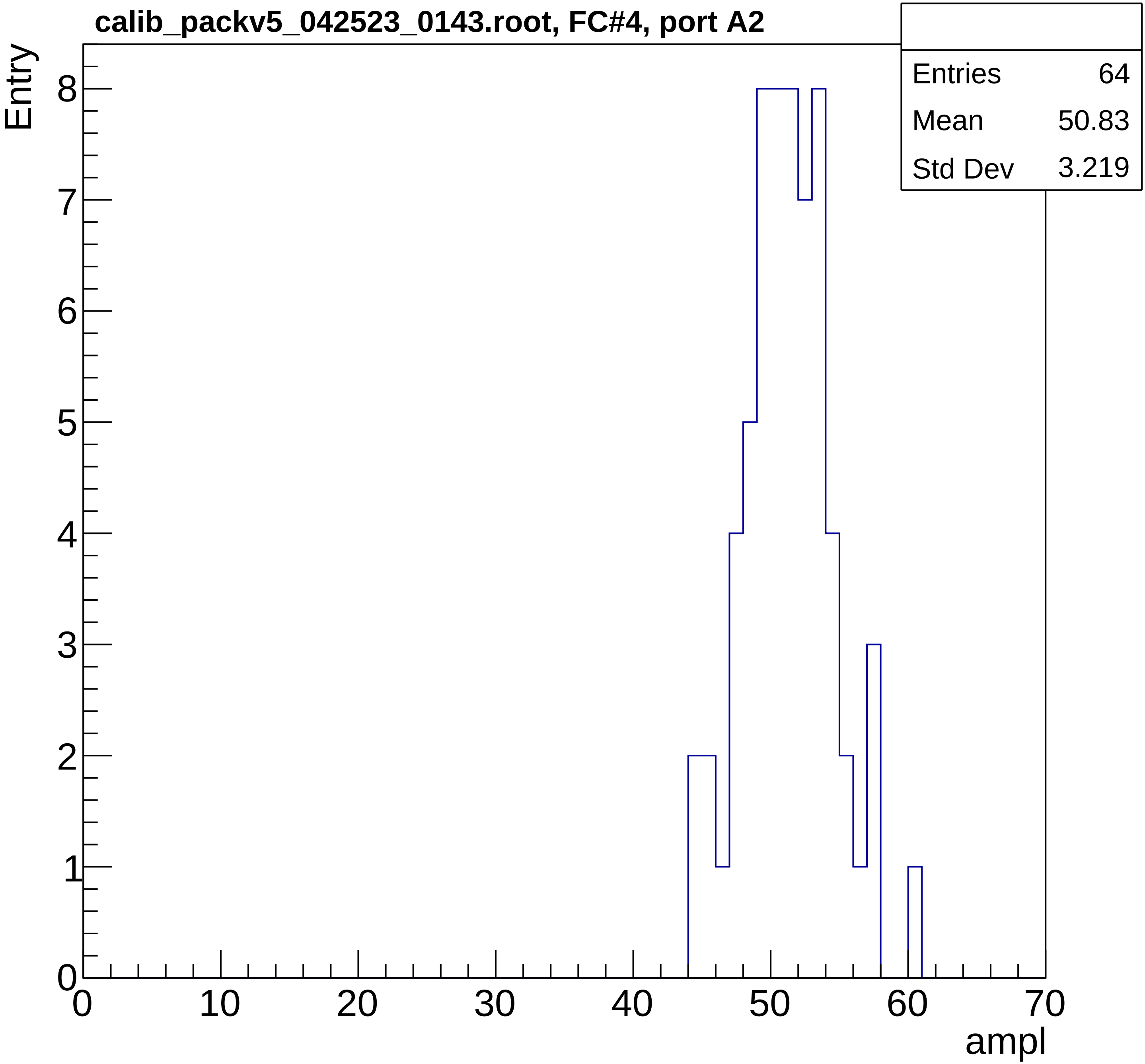
Gaus mean : 30.7150
Gaus Width: 3.5239


B1L100S, U21-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 37.36
Std Dev 3.259
Gaus mean : 37.6483
Gaus Width: 3.1615
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

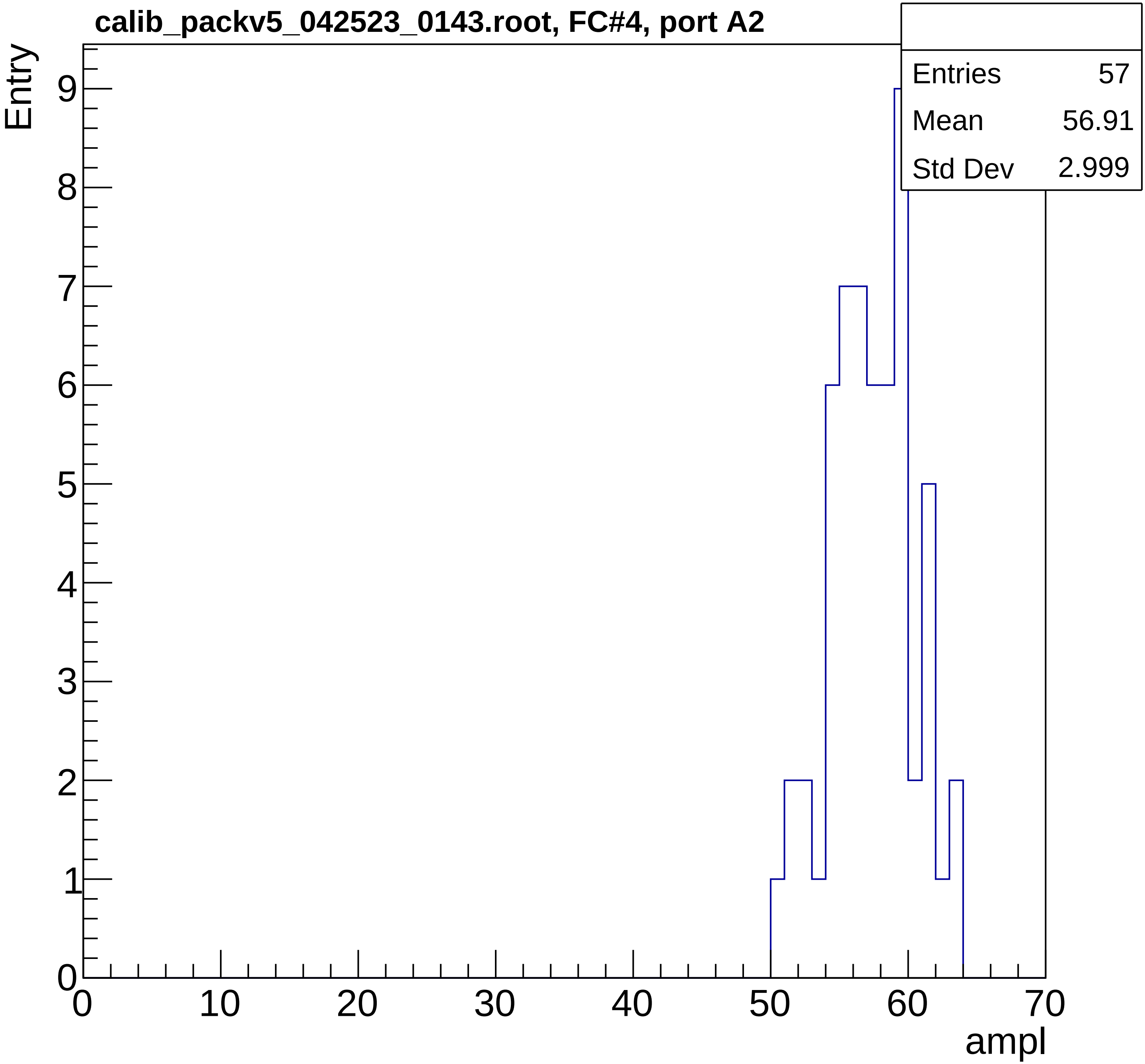
B1L100S, U21-ch63, adc2

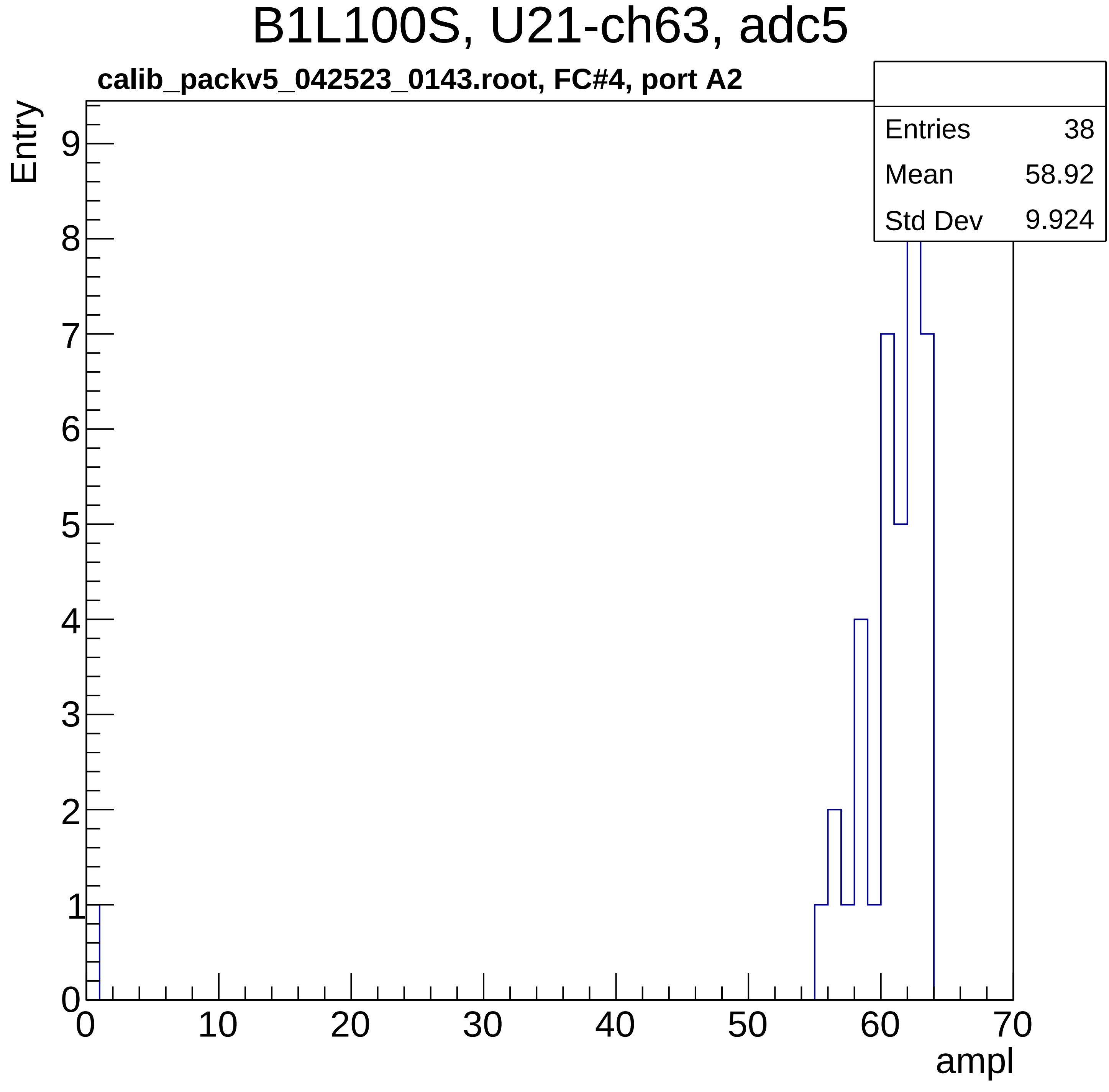


B1L100S, U21-ch63, adc3

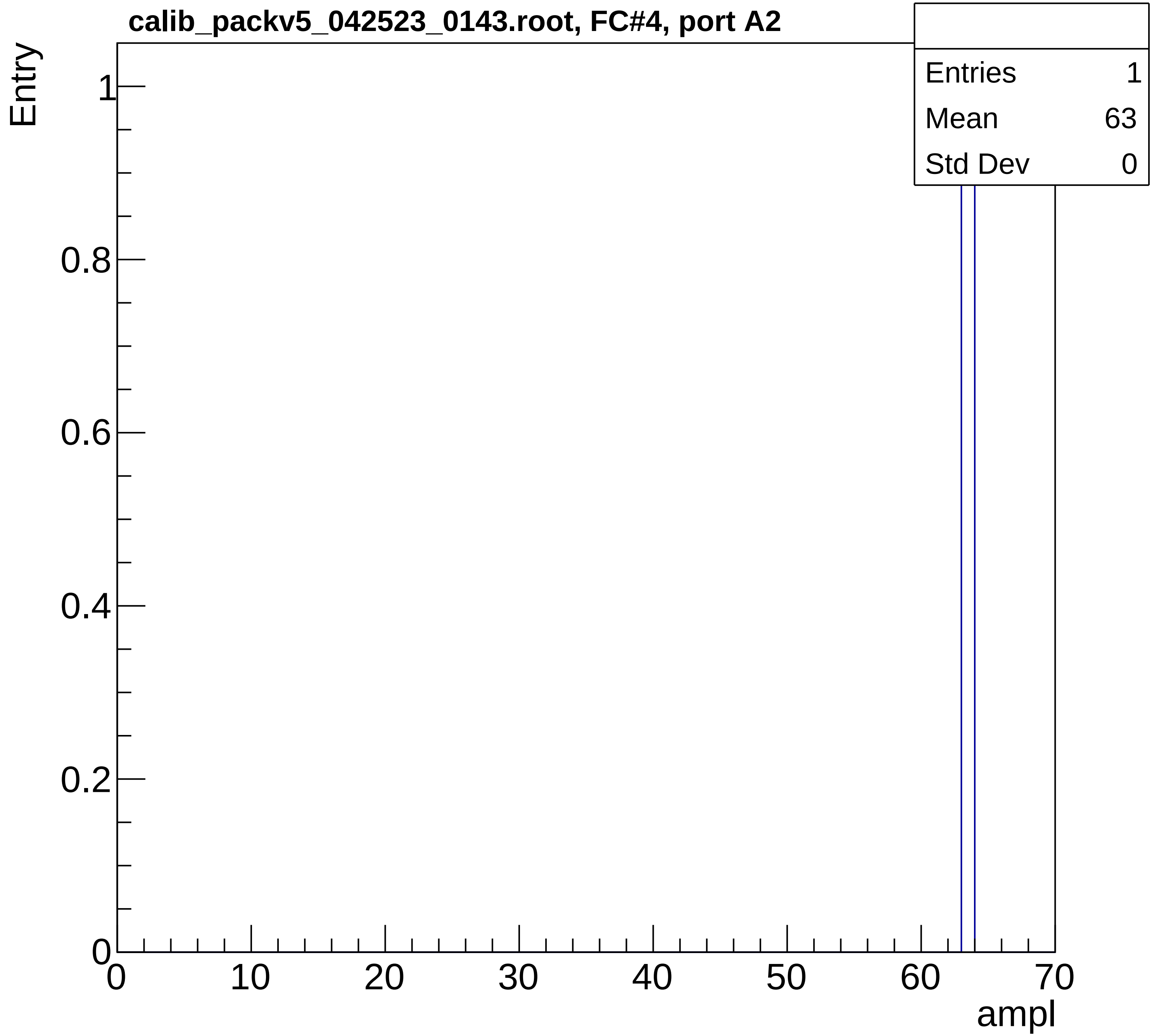


B1L100S, U21-ch63, adc4



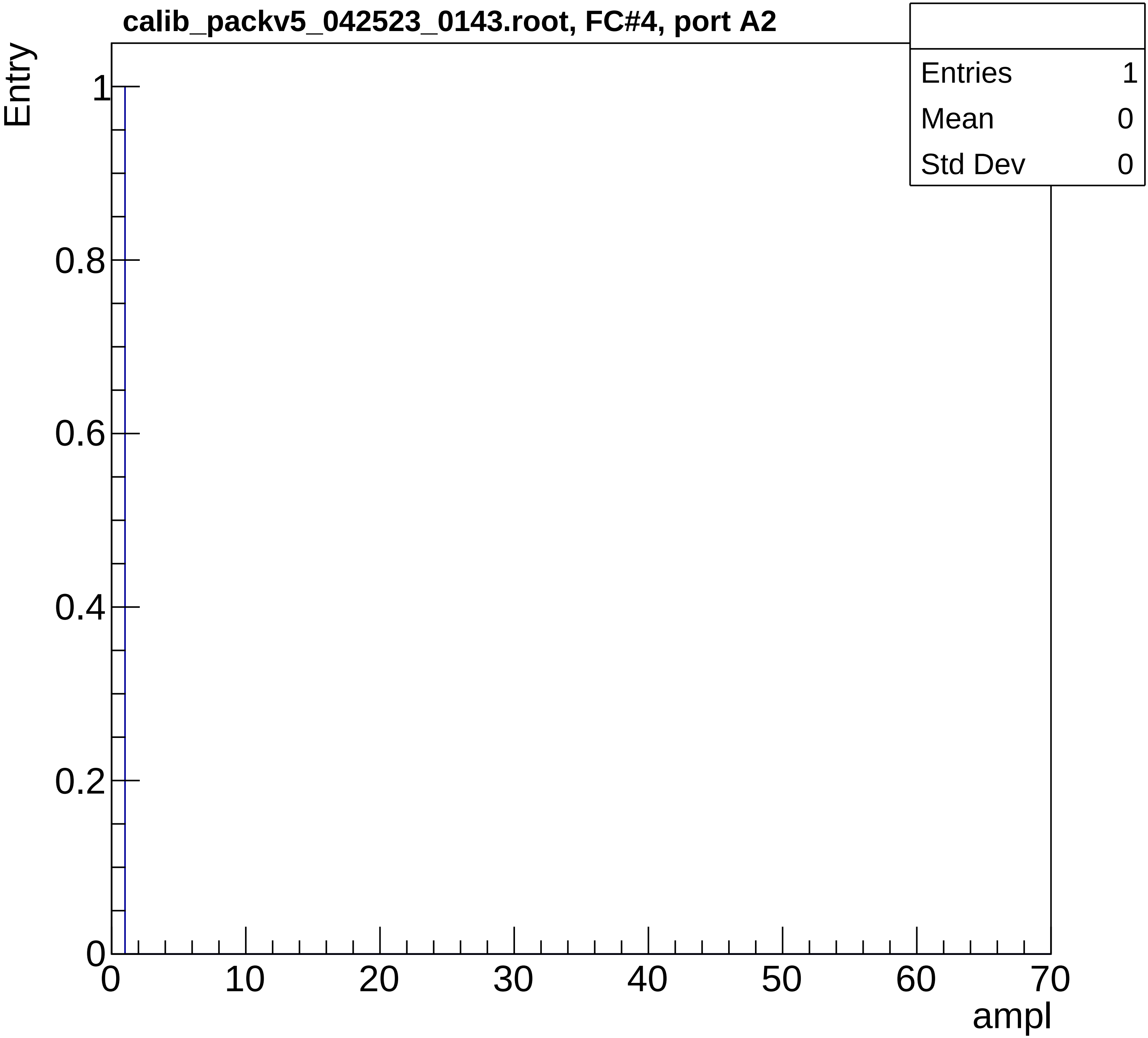

B1L100S, U21-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.92
Std Dev 9.924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

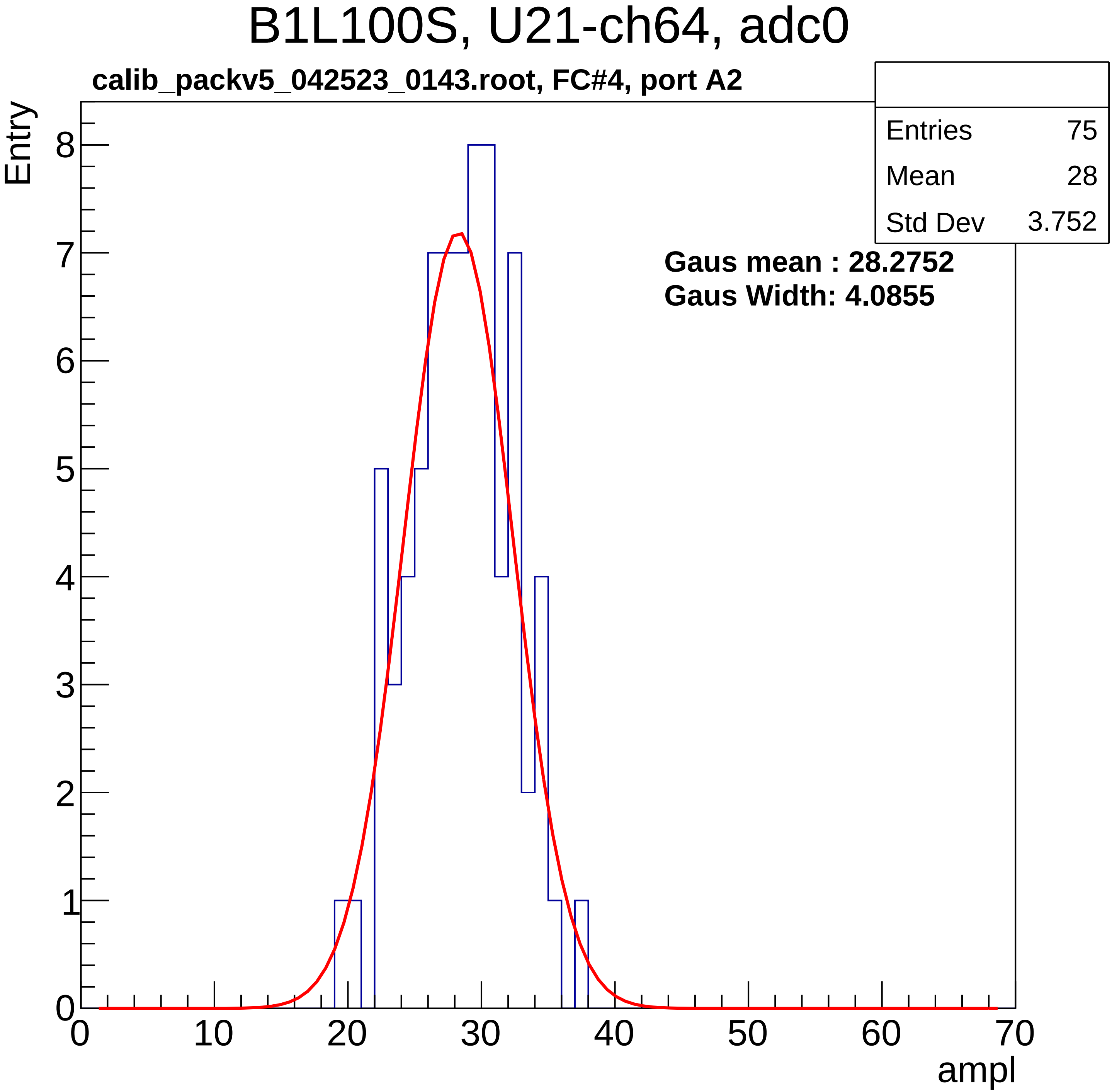
B1L100S, U21-ch63, adc6

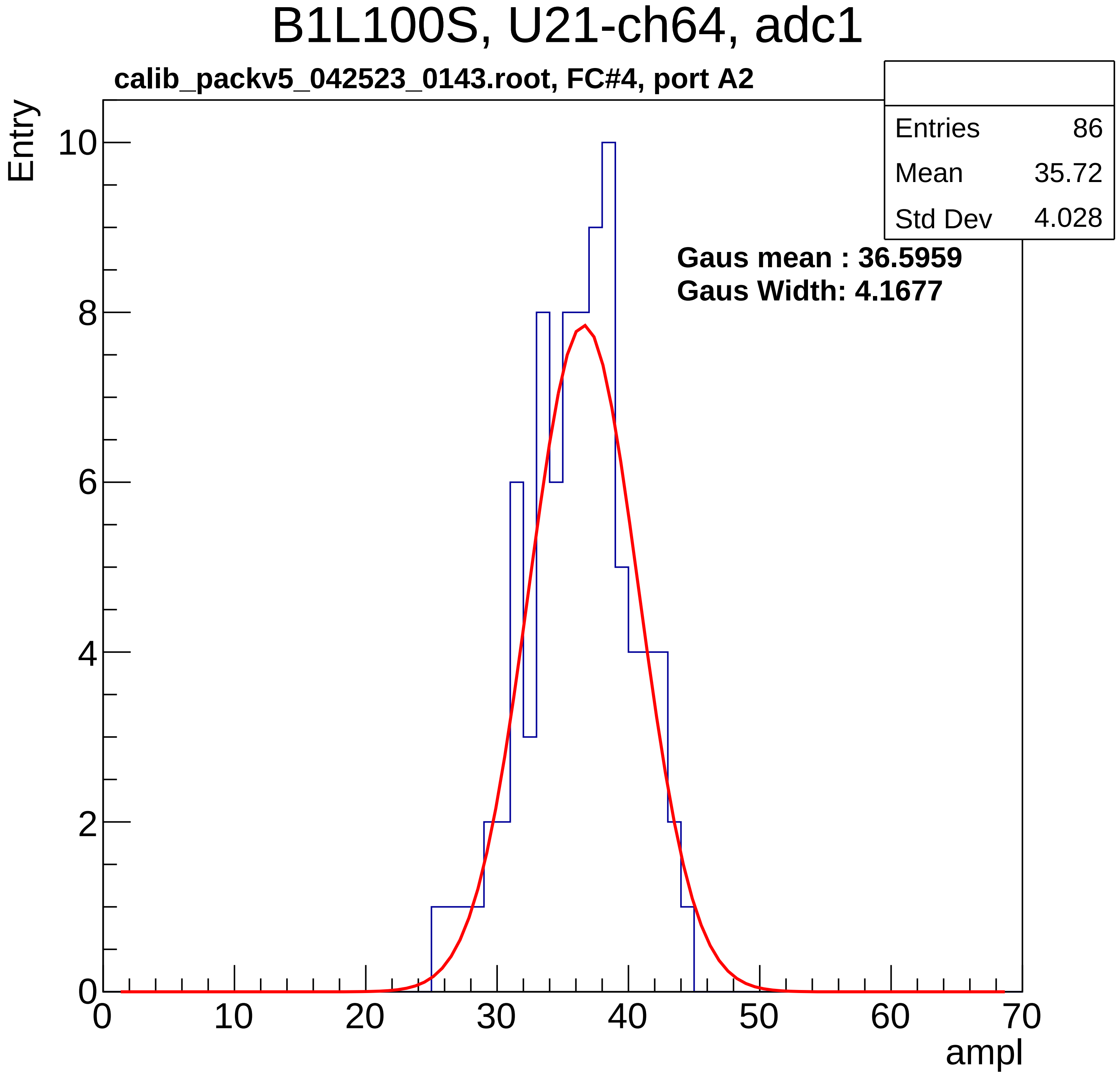


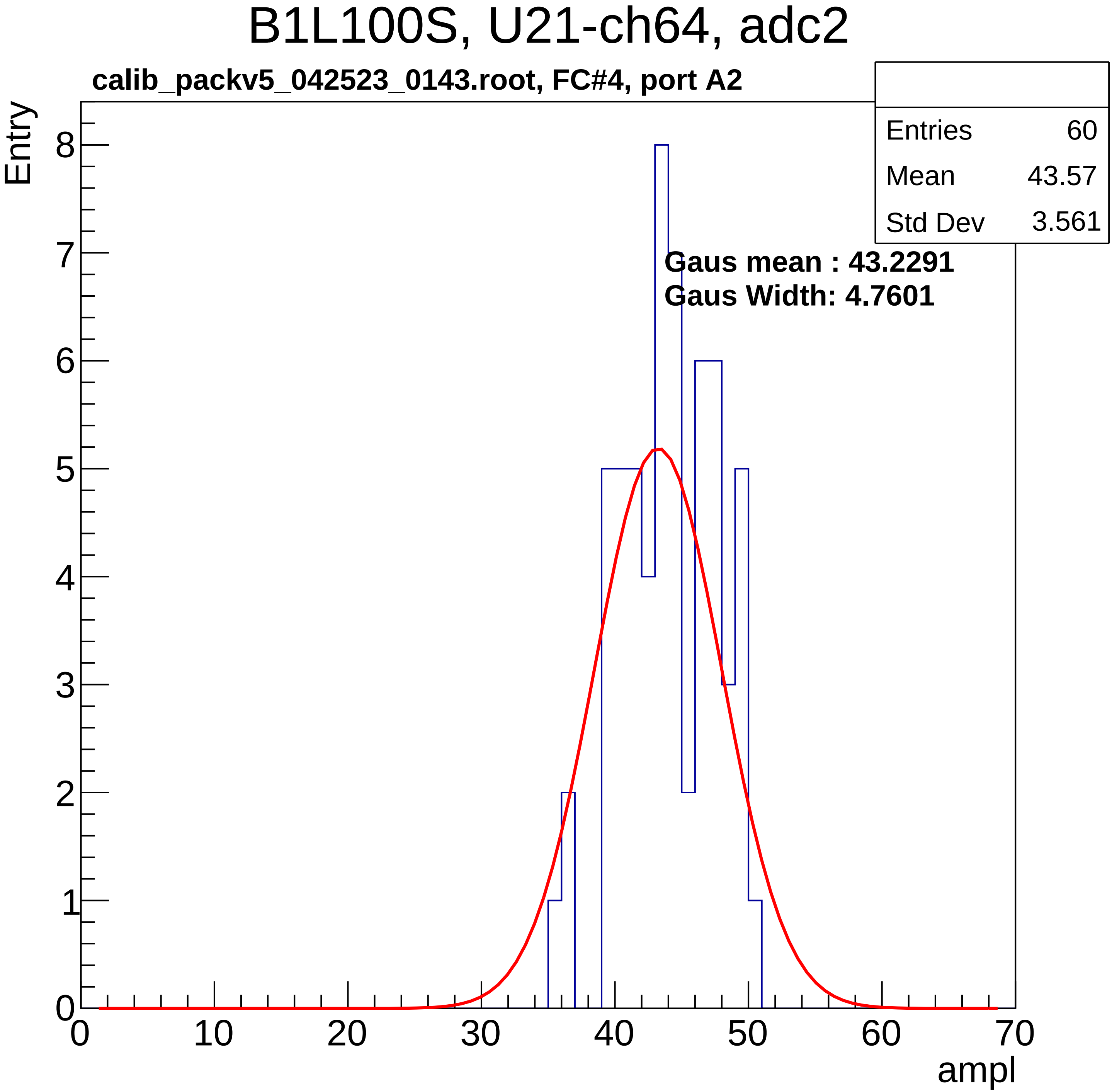
B1L100S, U21-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2



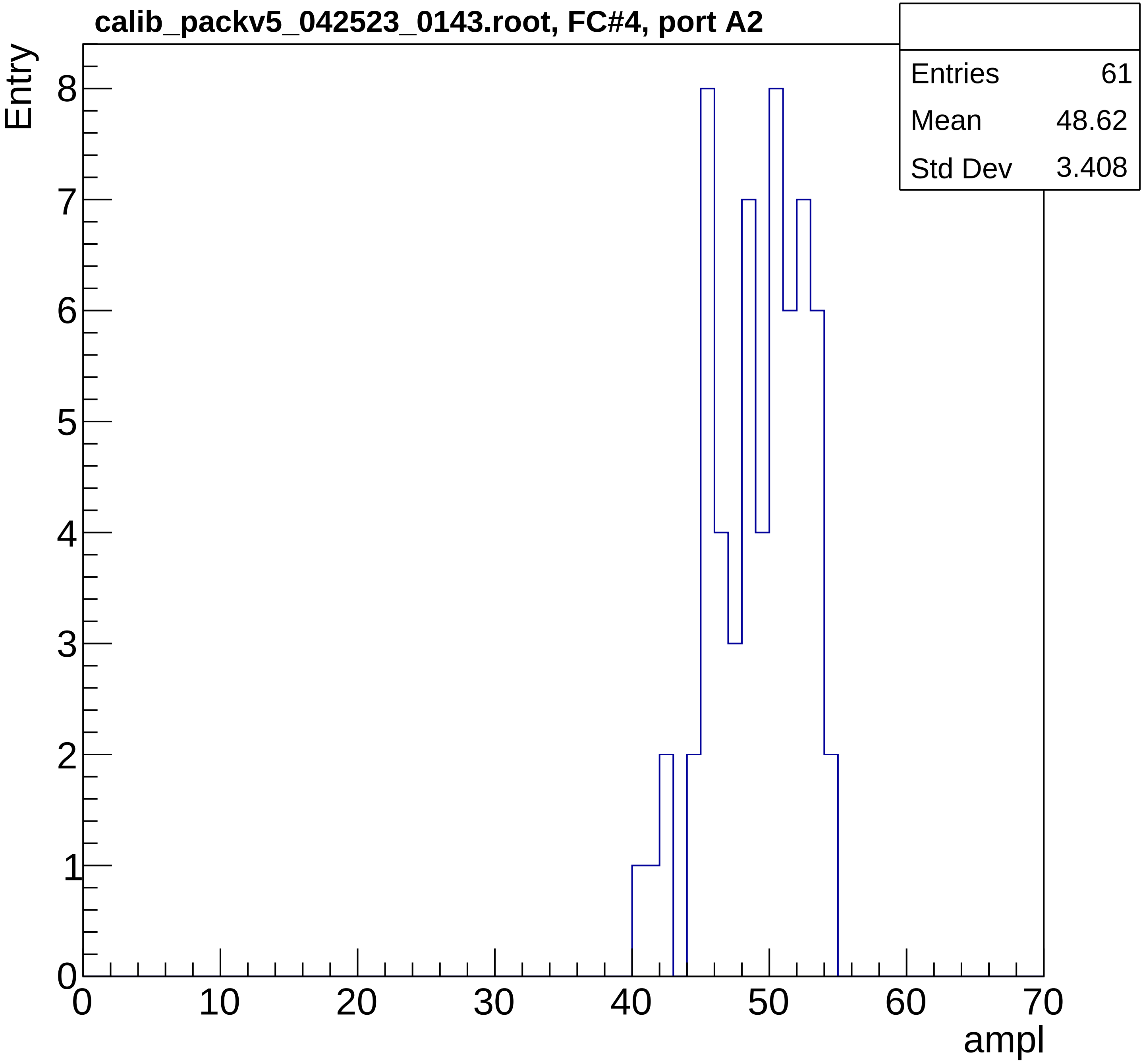

B1L100S, U21-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28
Std Dev 3.752
Gaus mean : 28.2752
Gaus Width: 4.0855
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 86
Mean 35.72
Std Dev 4.028
Gaus mean : 36.5959
Gaus Width: 4.1677
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.57
Std Dev 3.561
Gaus mean : 43.2291
Gaus Width: 4.7601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

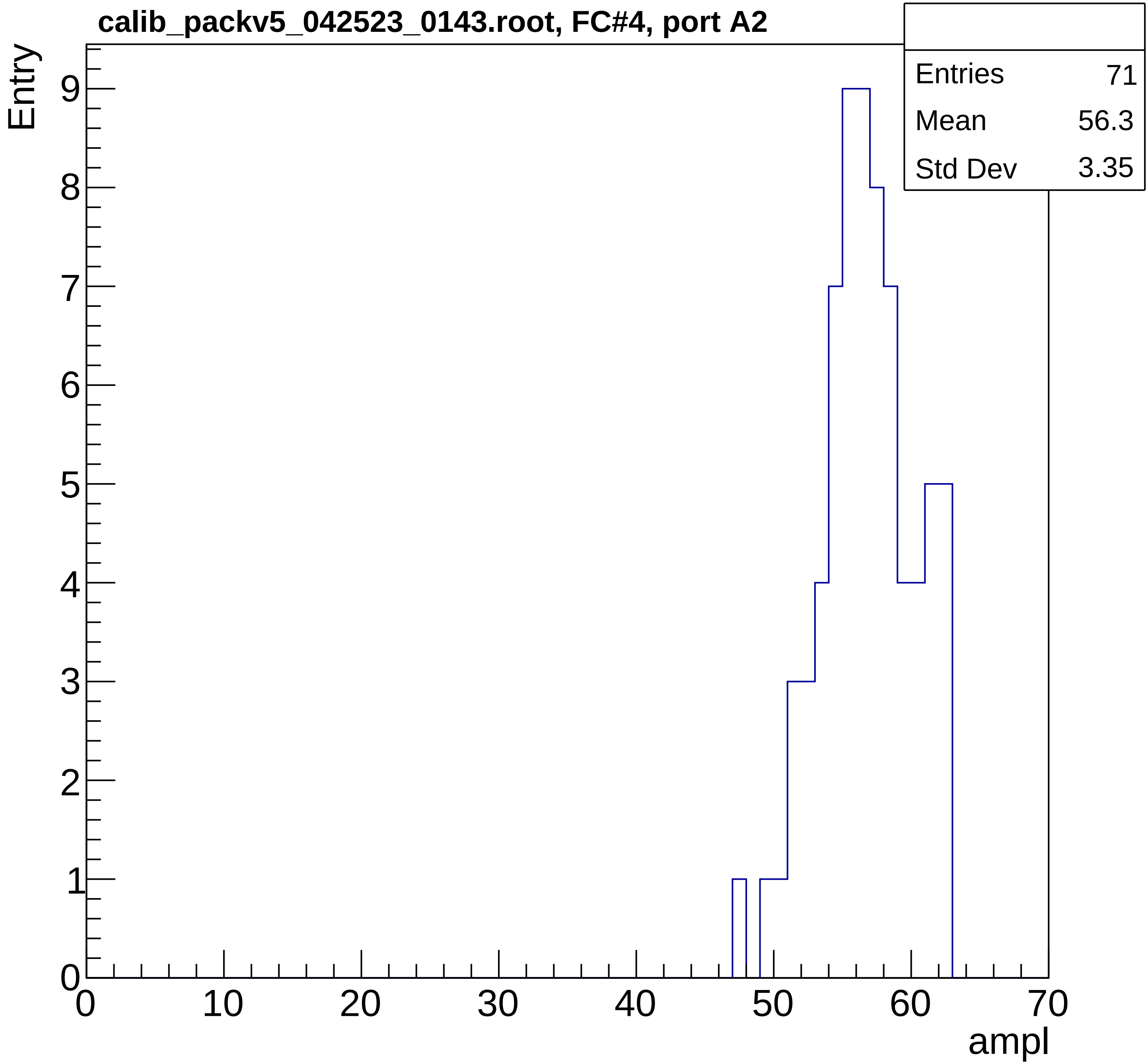
B1L100S, U21-ch64, adc3

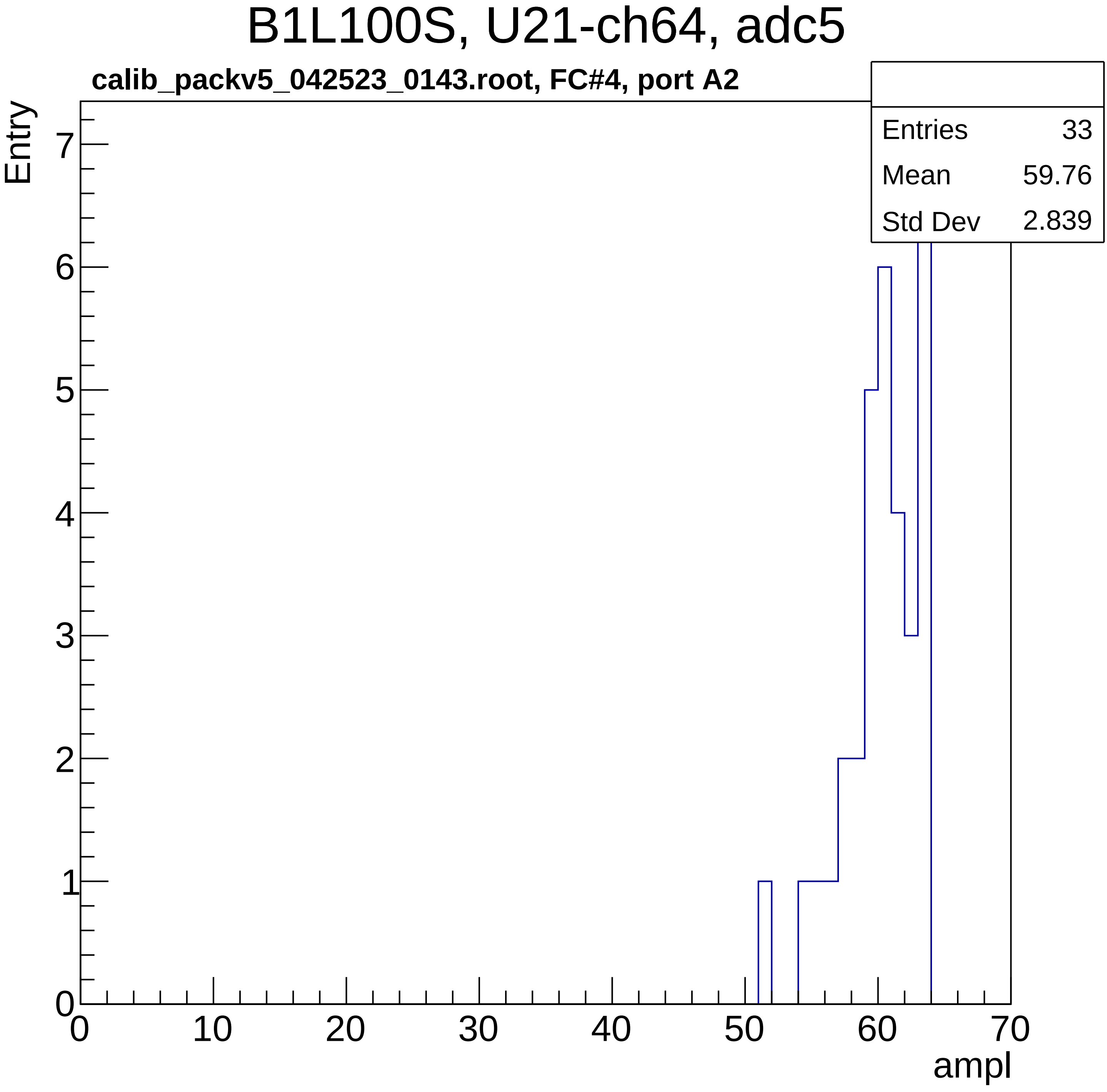
calib_packv5_042523_0143.root, FC#4, port A2

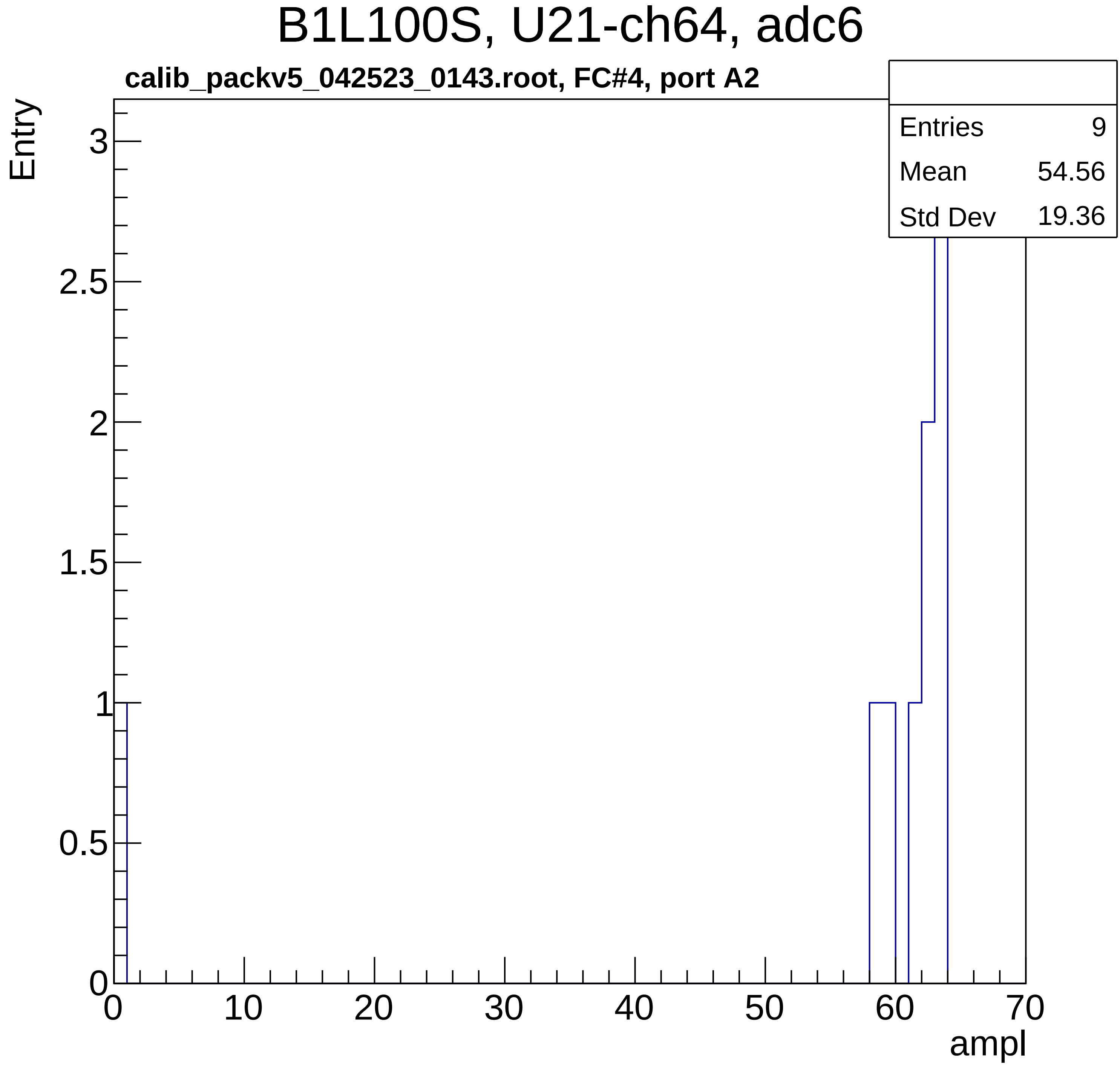


B1L100S, U21-ch64, adc4

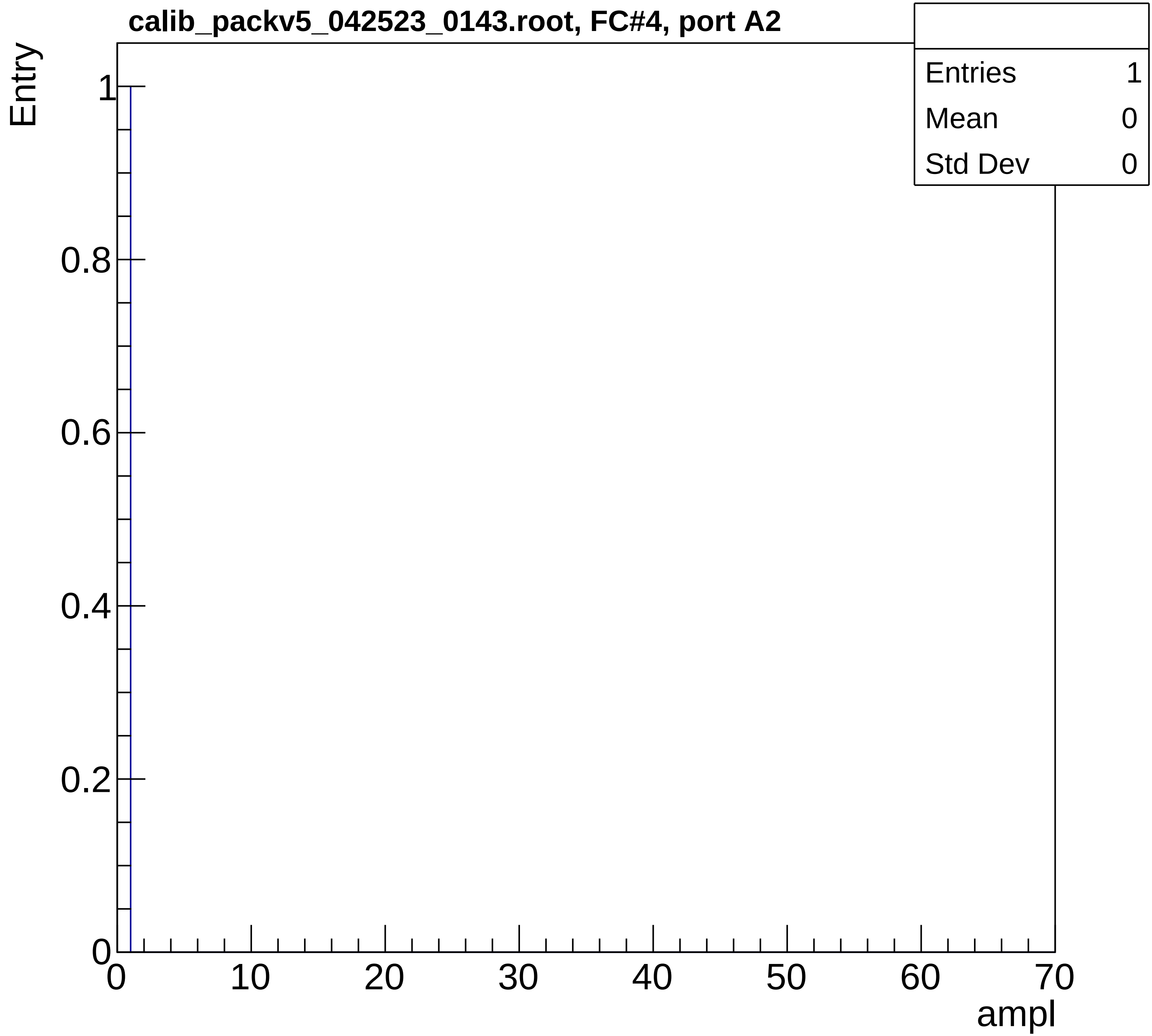
calib_packv5_042523_0143.root, FC#4, port A2

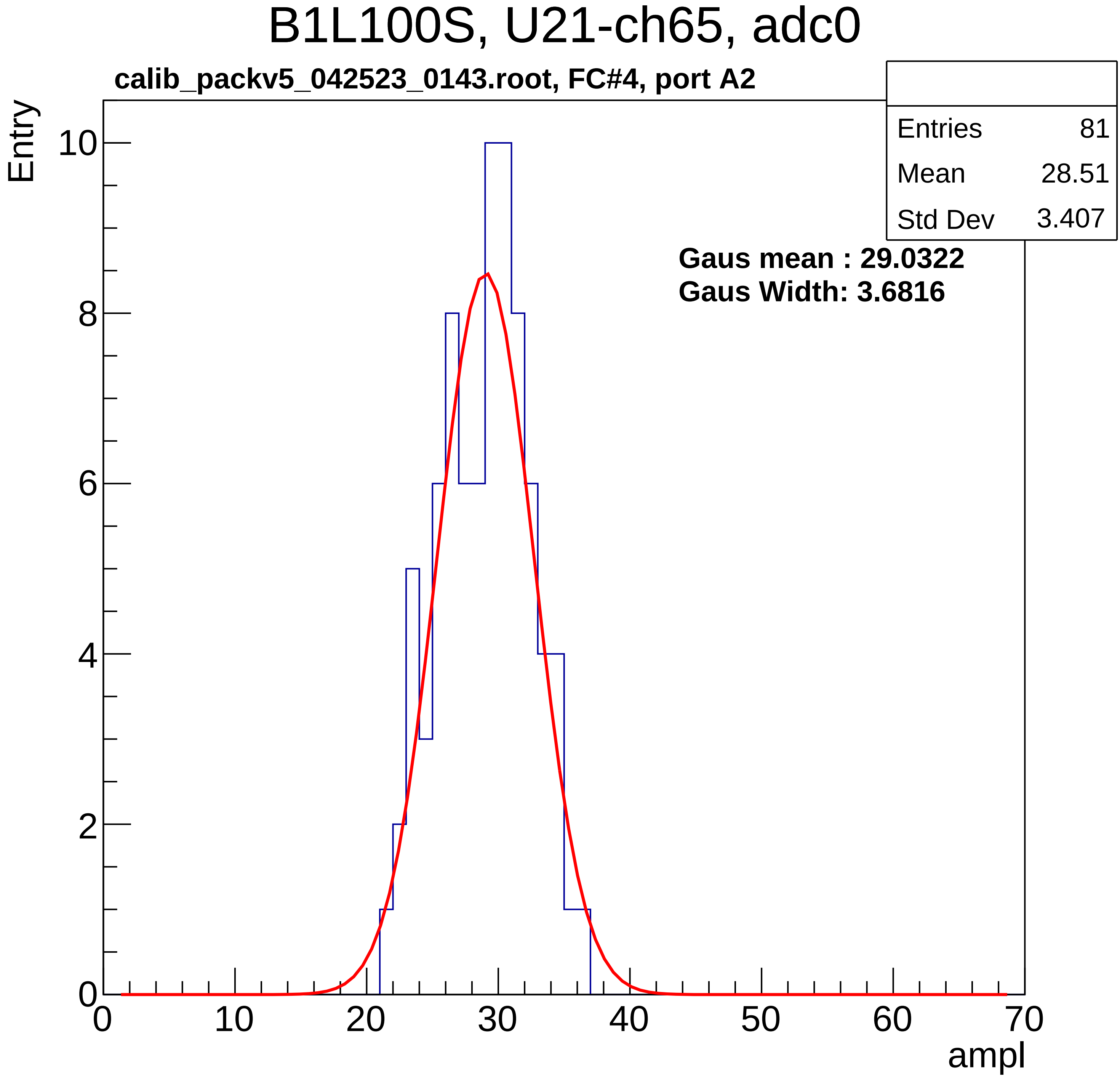


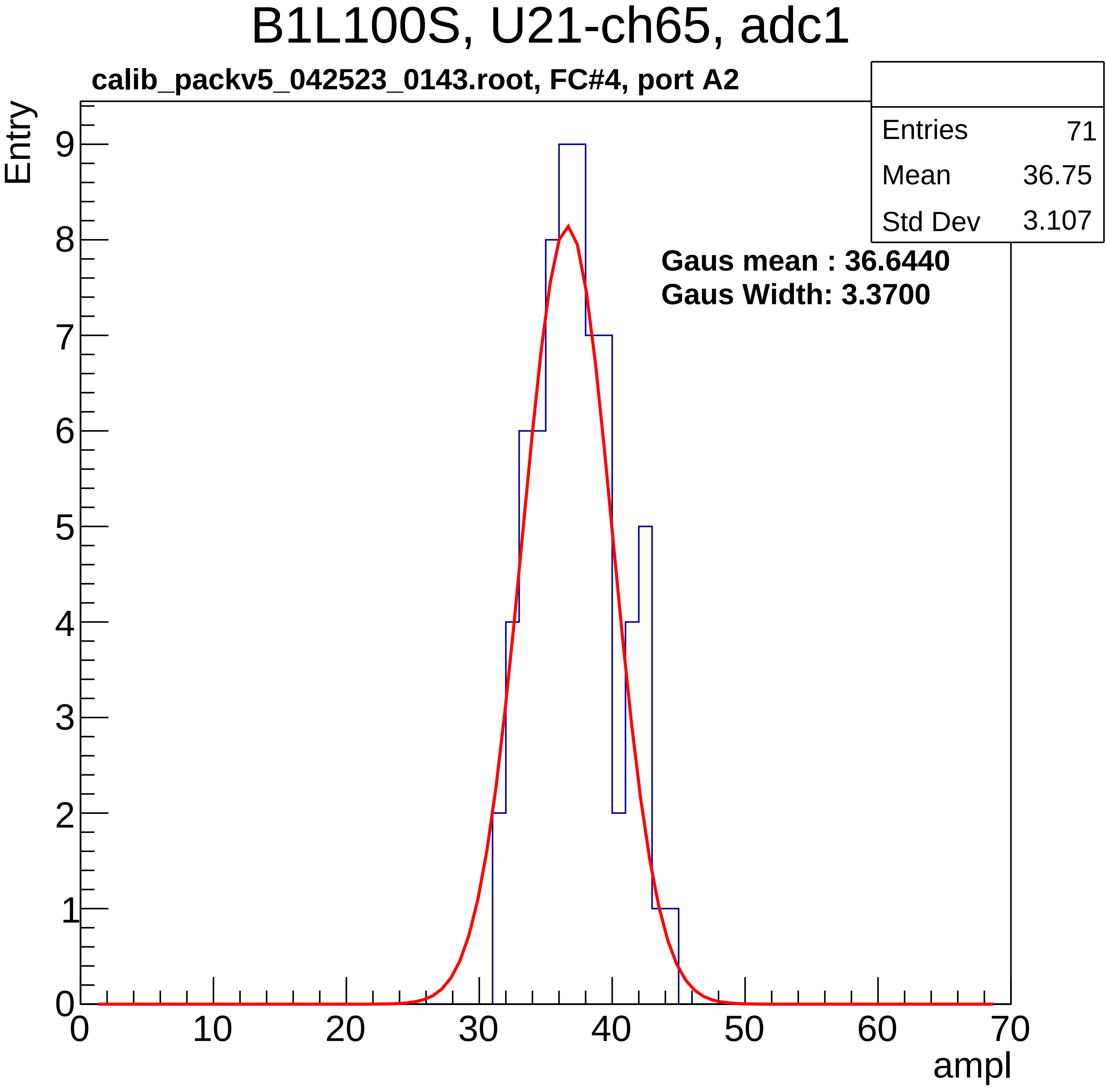

B1L100S, U21-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 33
Mean 59.76
Std Dev 2.839
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

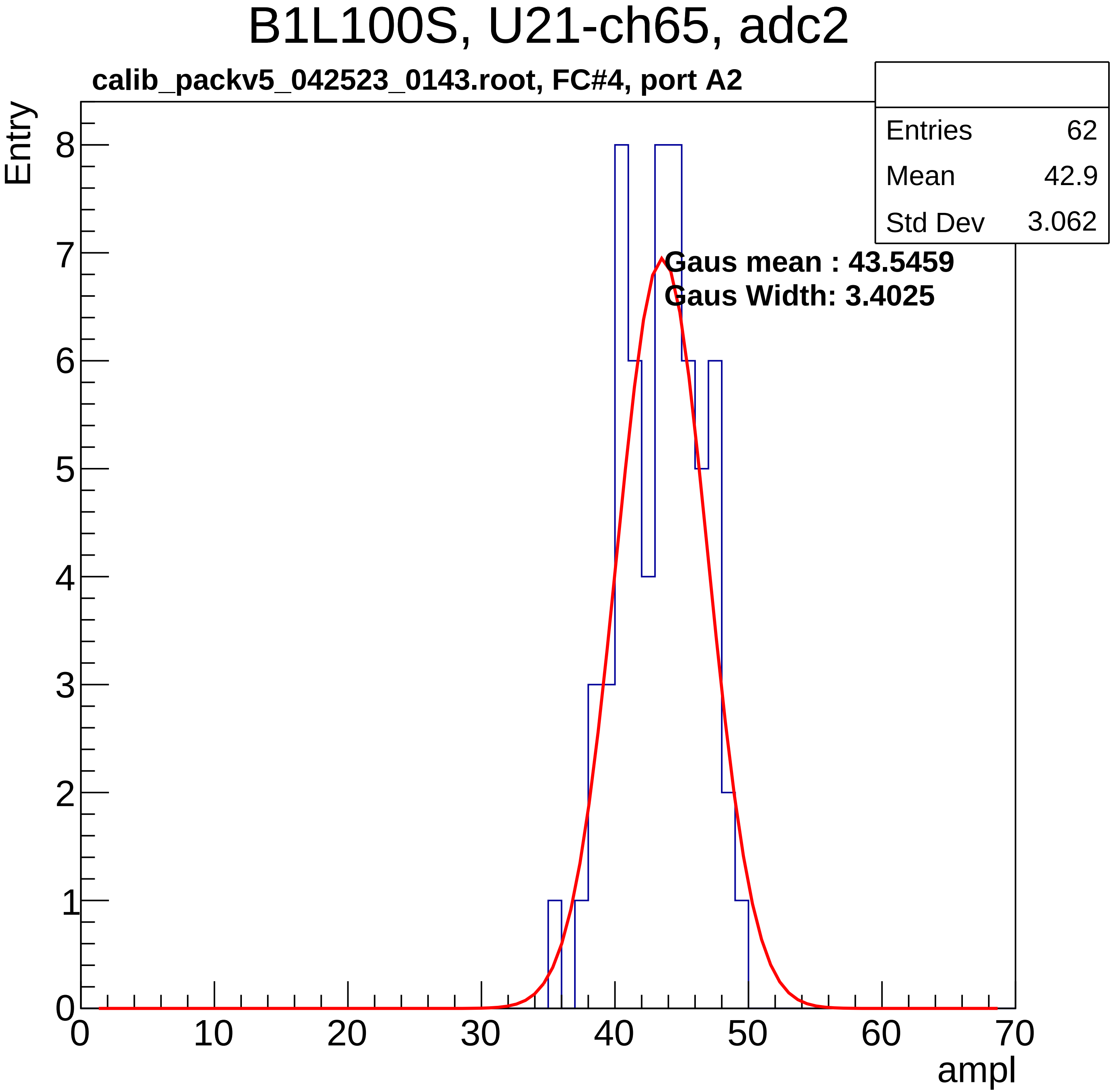

B1L100S, U21-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 9
Mean 54.56
Std Dev 19.36
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

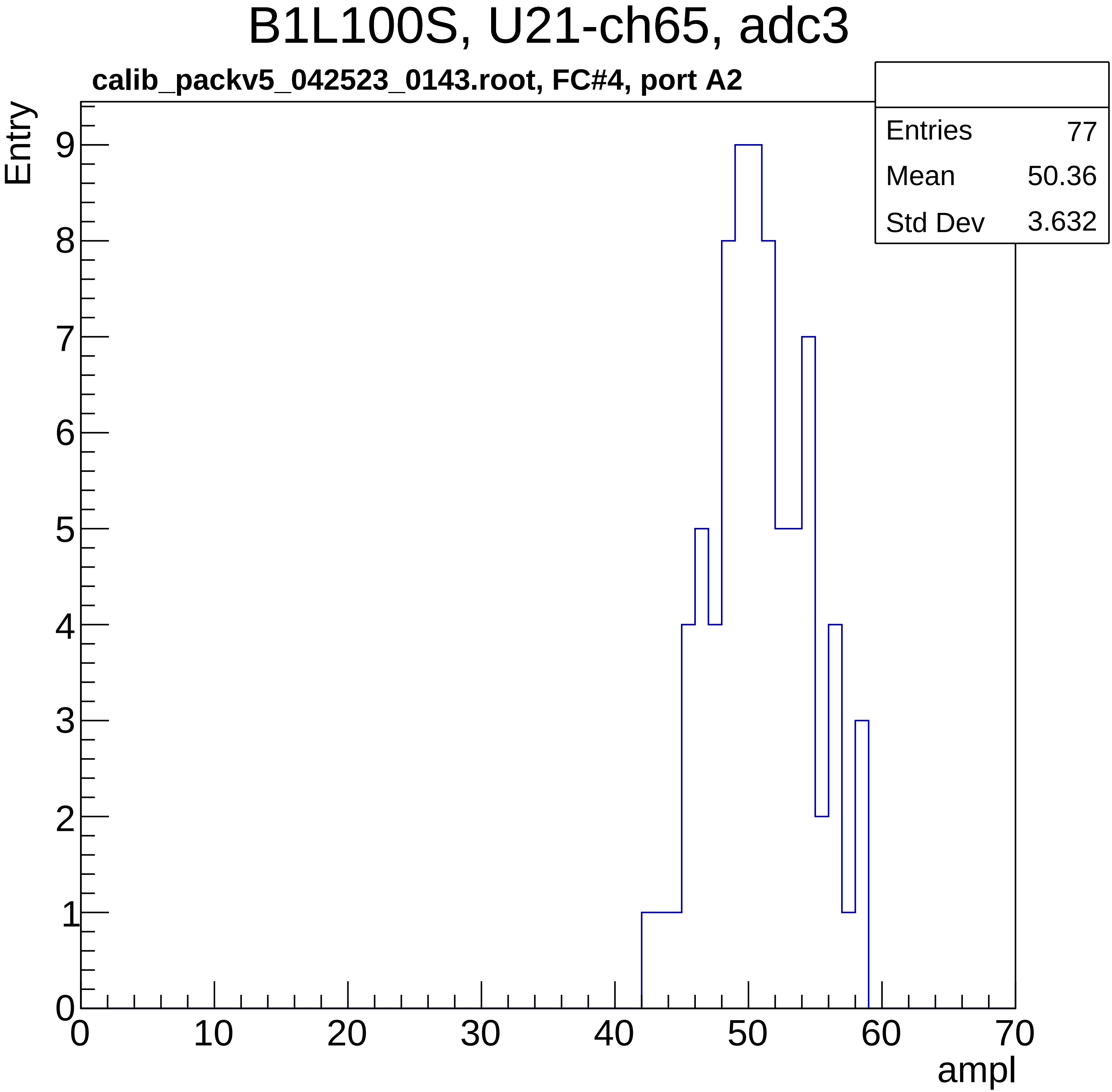
B1L100S, U21-ch64, adc7

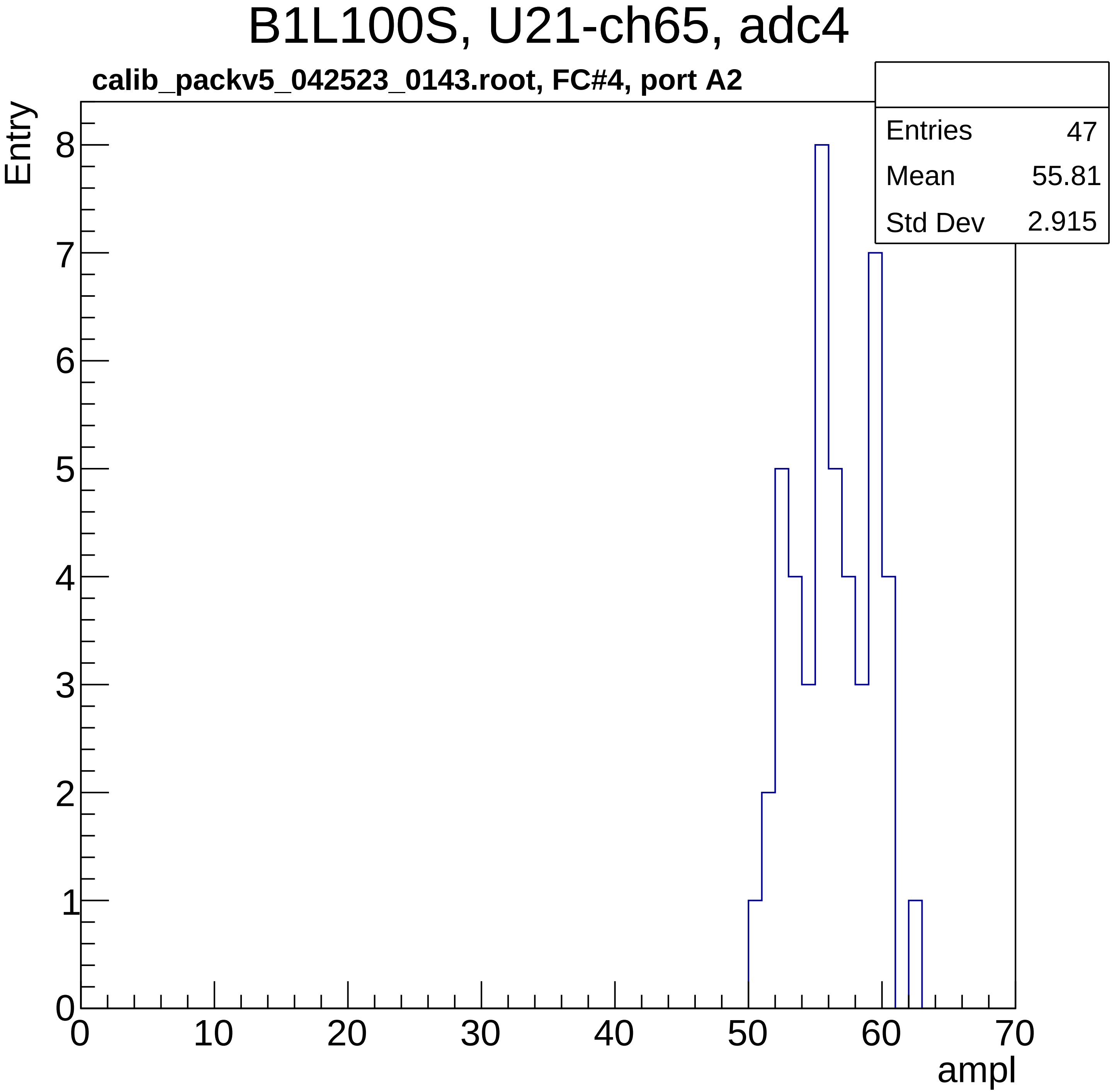



B1L100S, U21-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 28.51
Std Dev 3.407
Gaus mean : 29.0322
Gaus Width: 3.6816
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 36.75
Std Dev 3.107
Gaus mean : 36.6440
Gaus Width: 3.3700
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

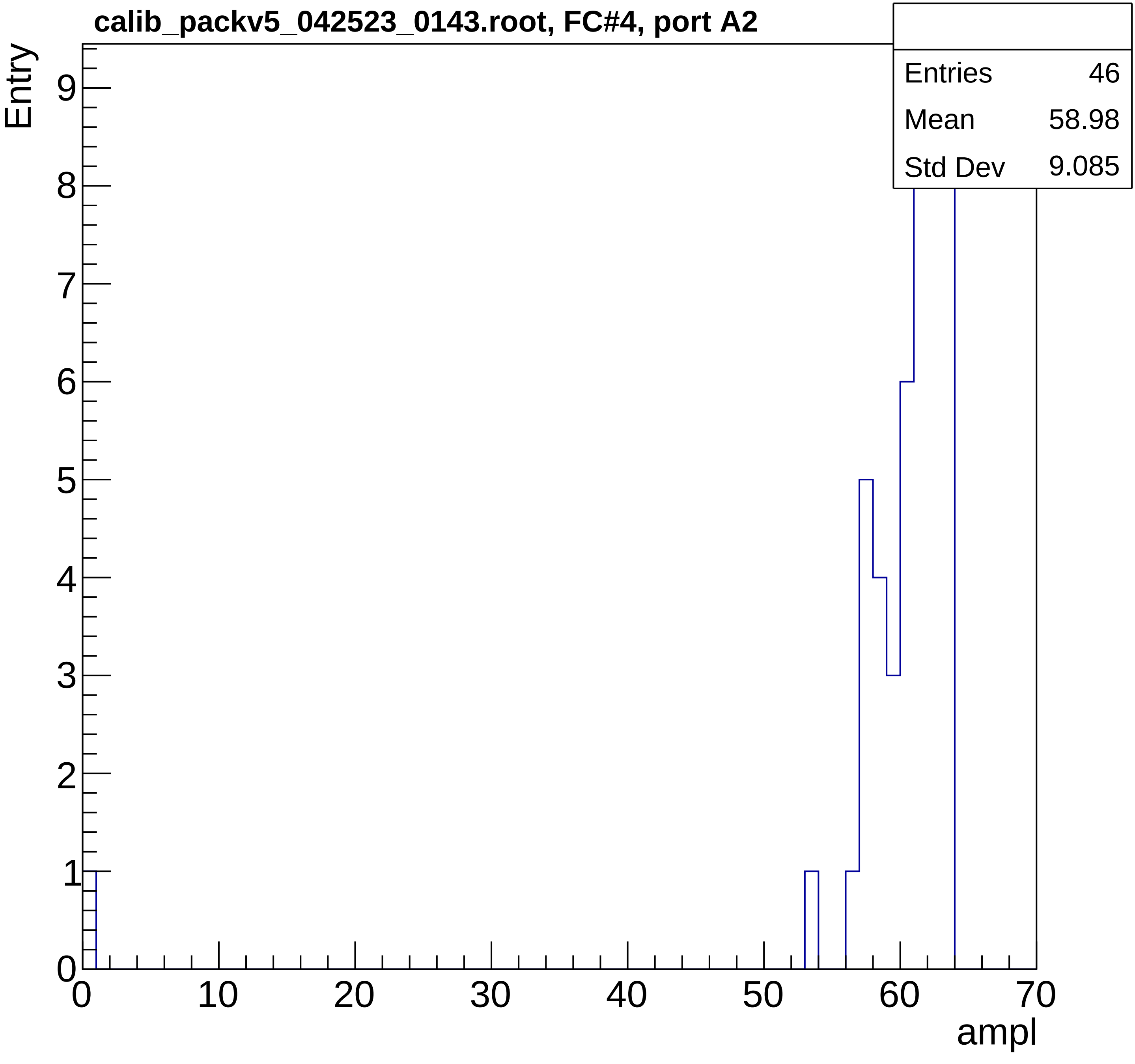

B1L100S, U21-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 62
Mean 42.9
Std Dev 3.062
Gaus mean : 43.5459
Gaus Width: 3.4025
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 50.36
Std Dev 3.632
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

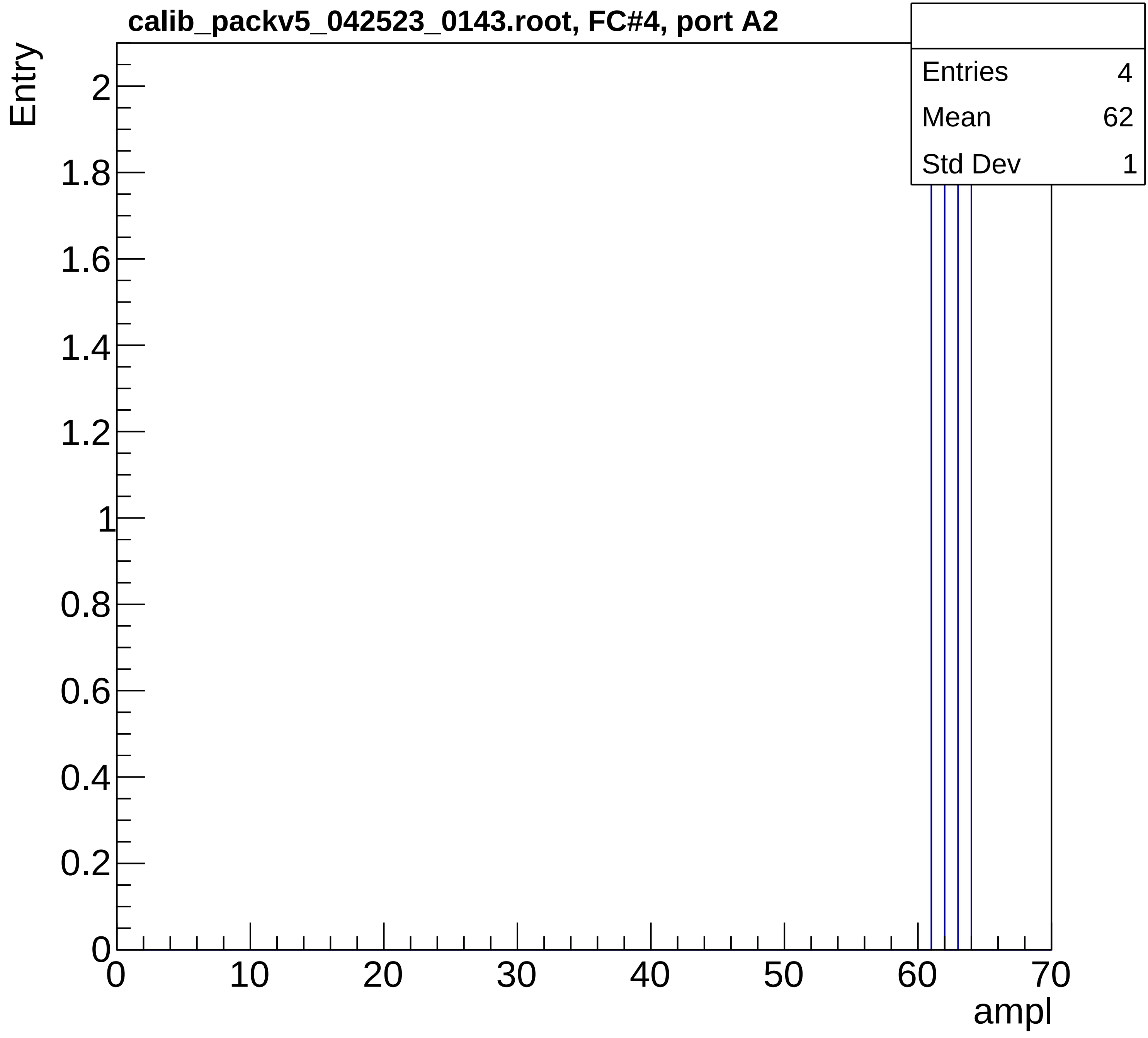

B1L100S, U21-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.81
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

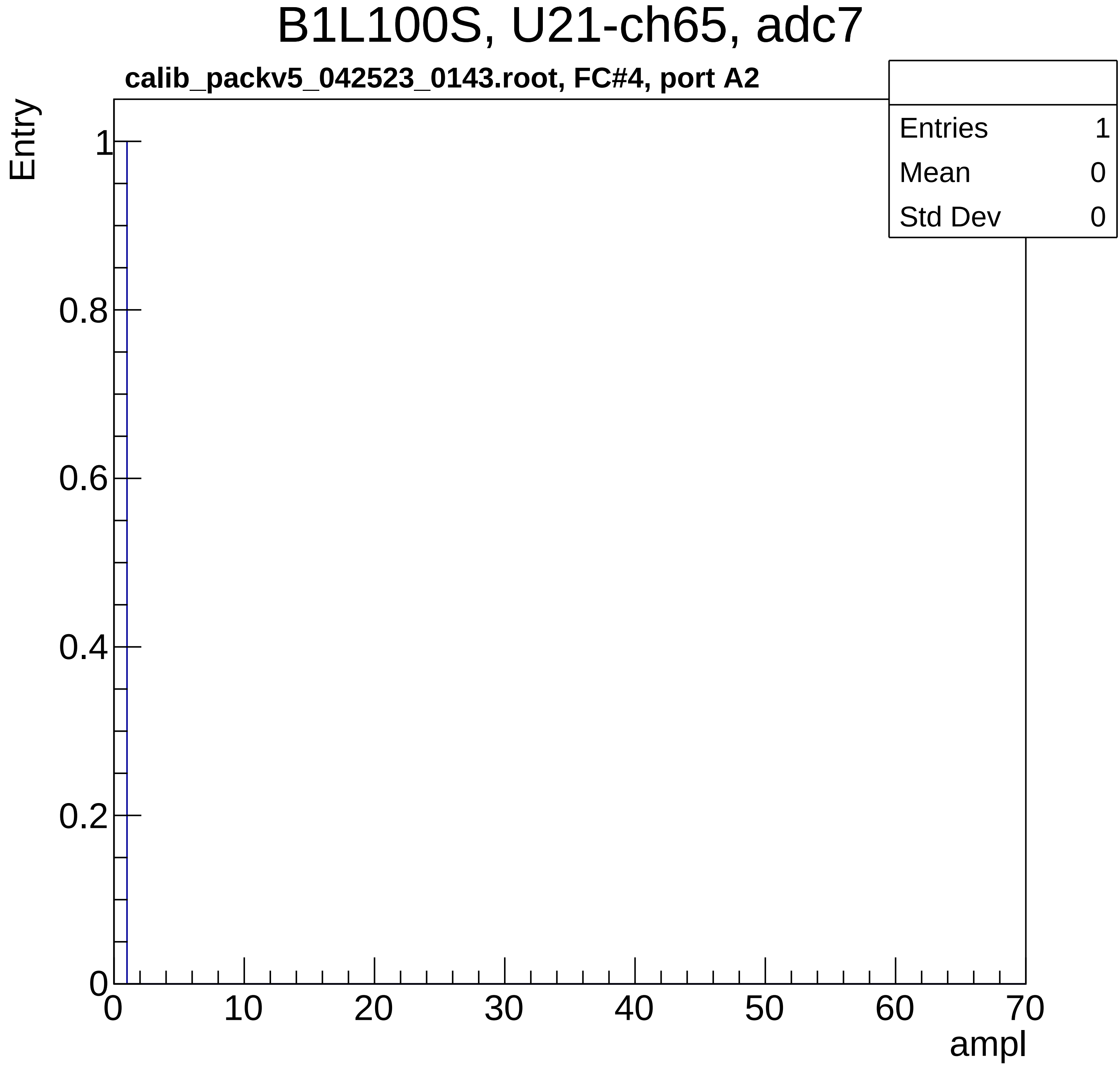
B1L100S, U21-ch65, adc5

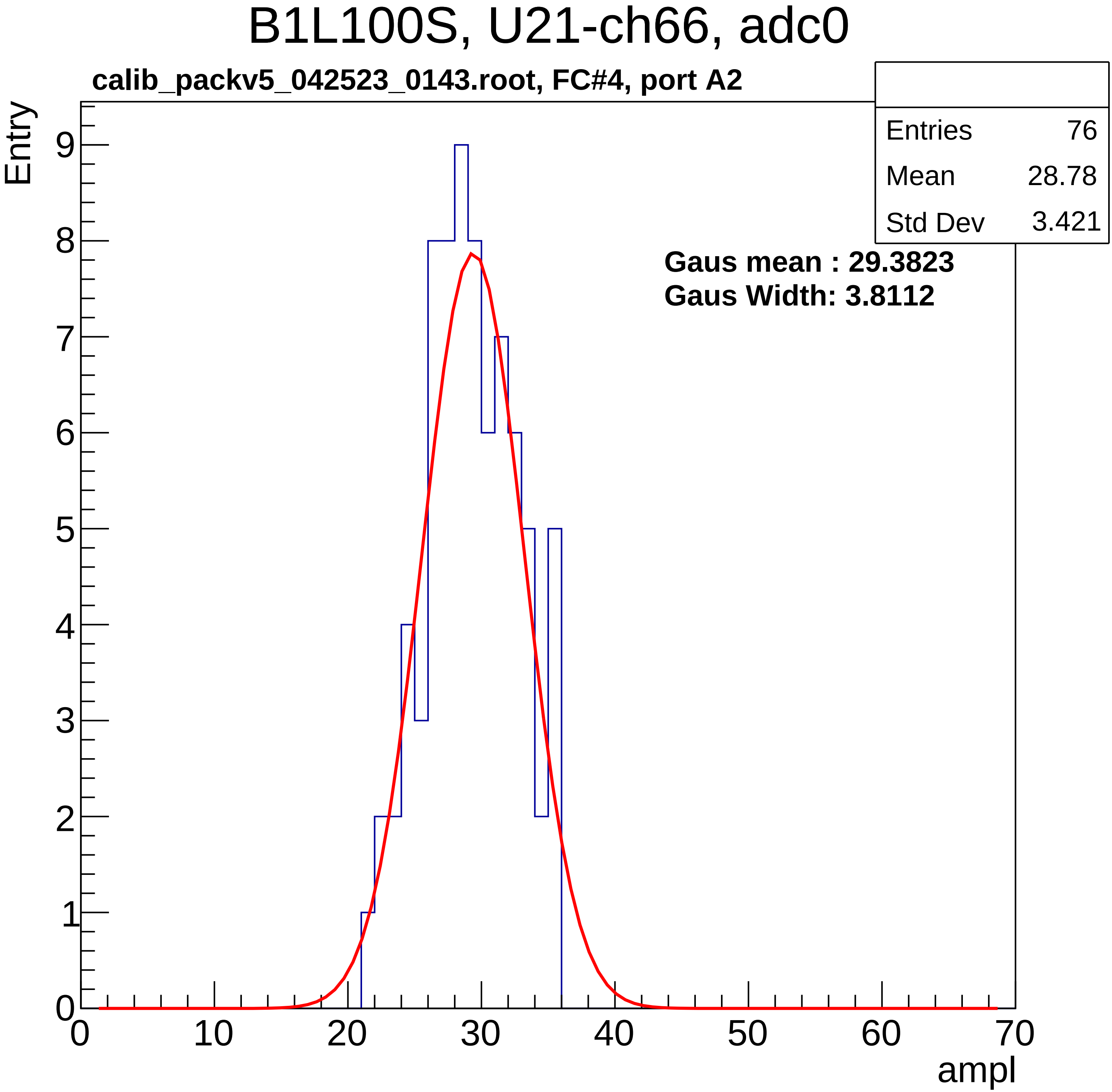
calib_packv5_042523_0143.root, FC#4, port A2



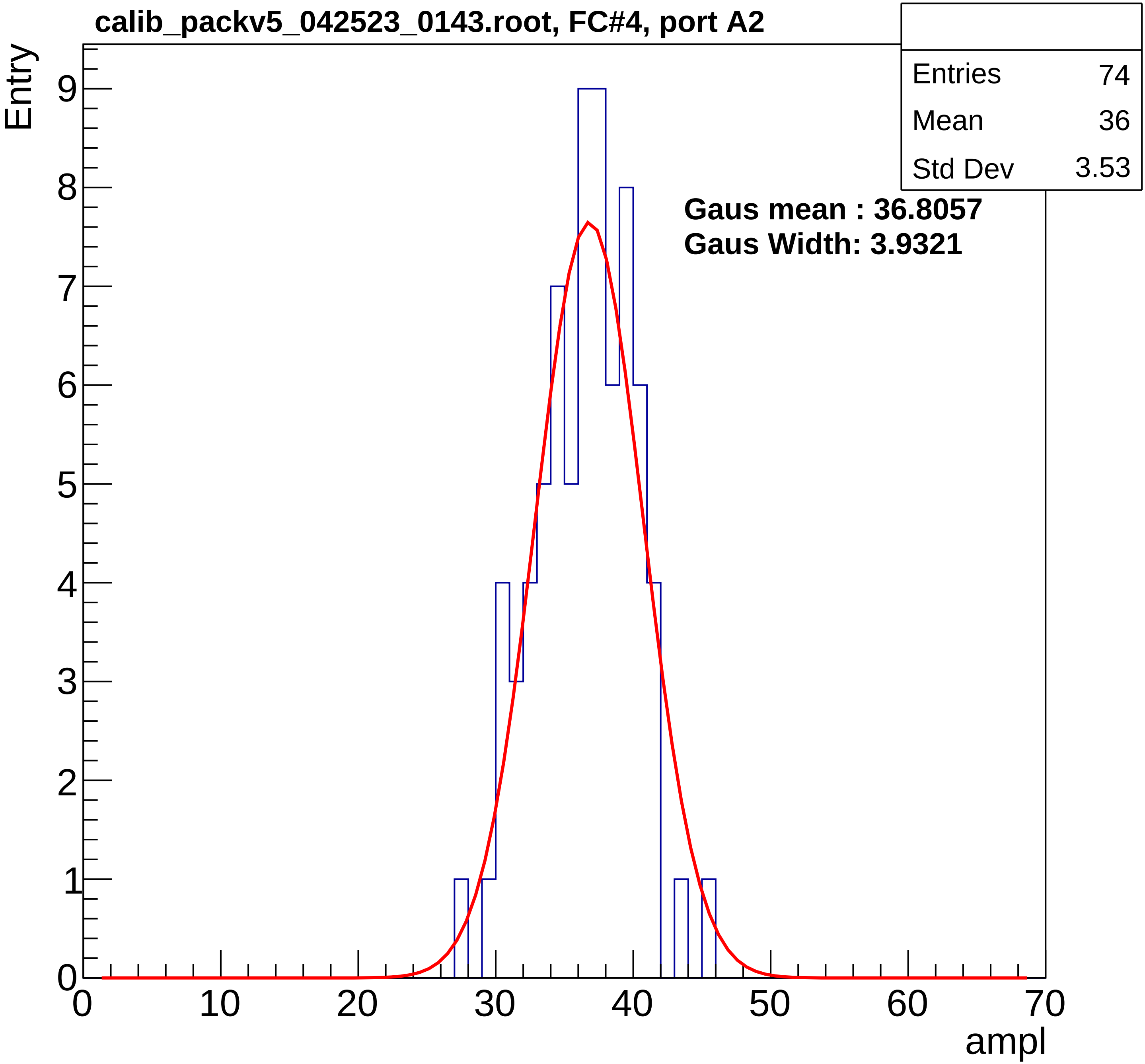
B1L100S, U21-ch65, adc6

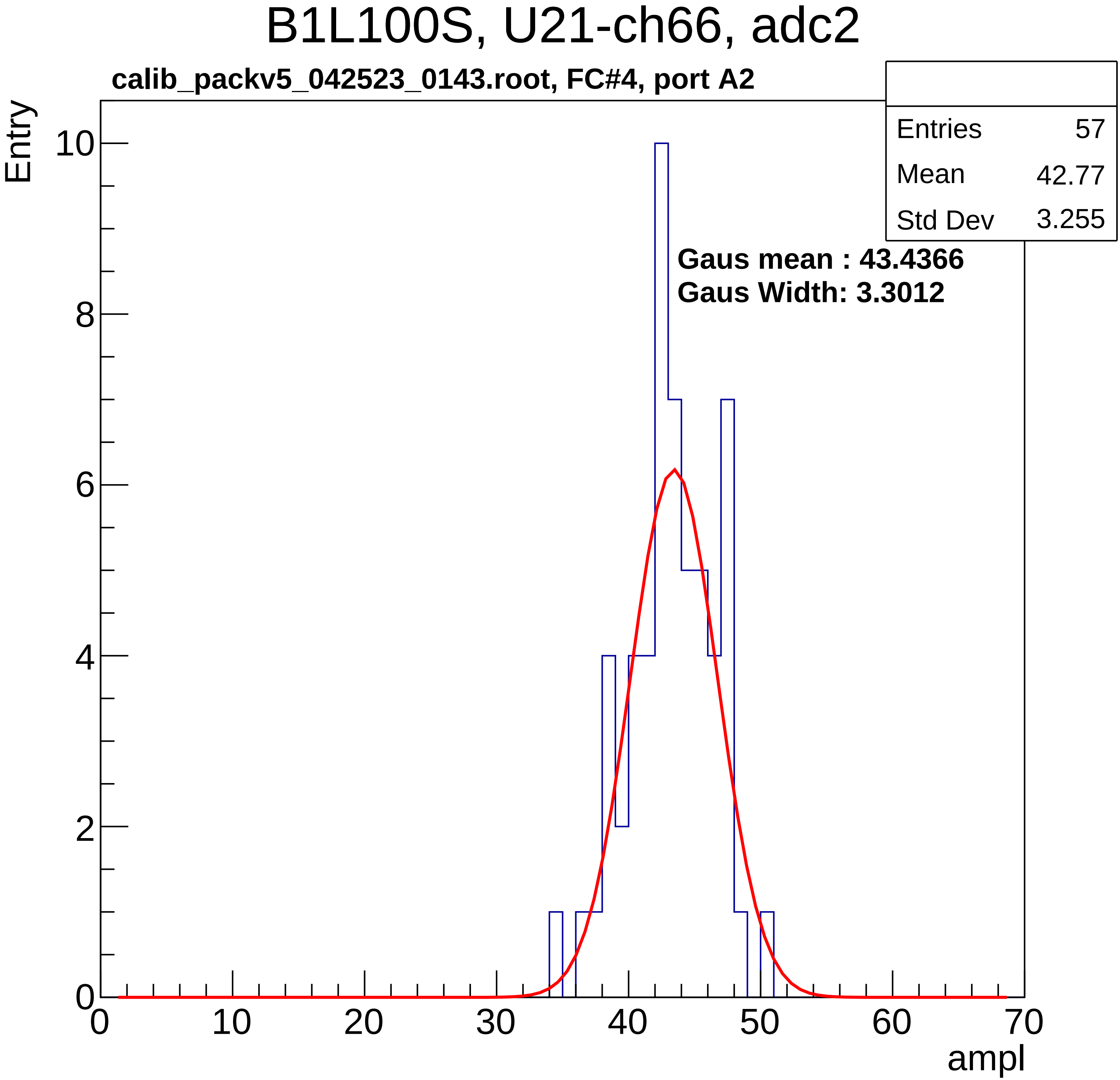



B1L100S, U21-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.78
Std Dev 3.421
Gaus mean : 29.3823
Gaus Width: 3.8112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

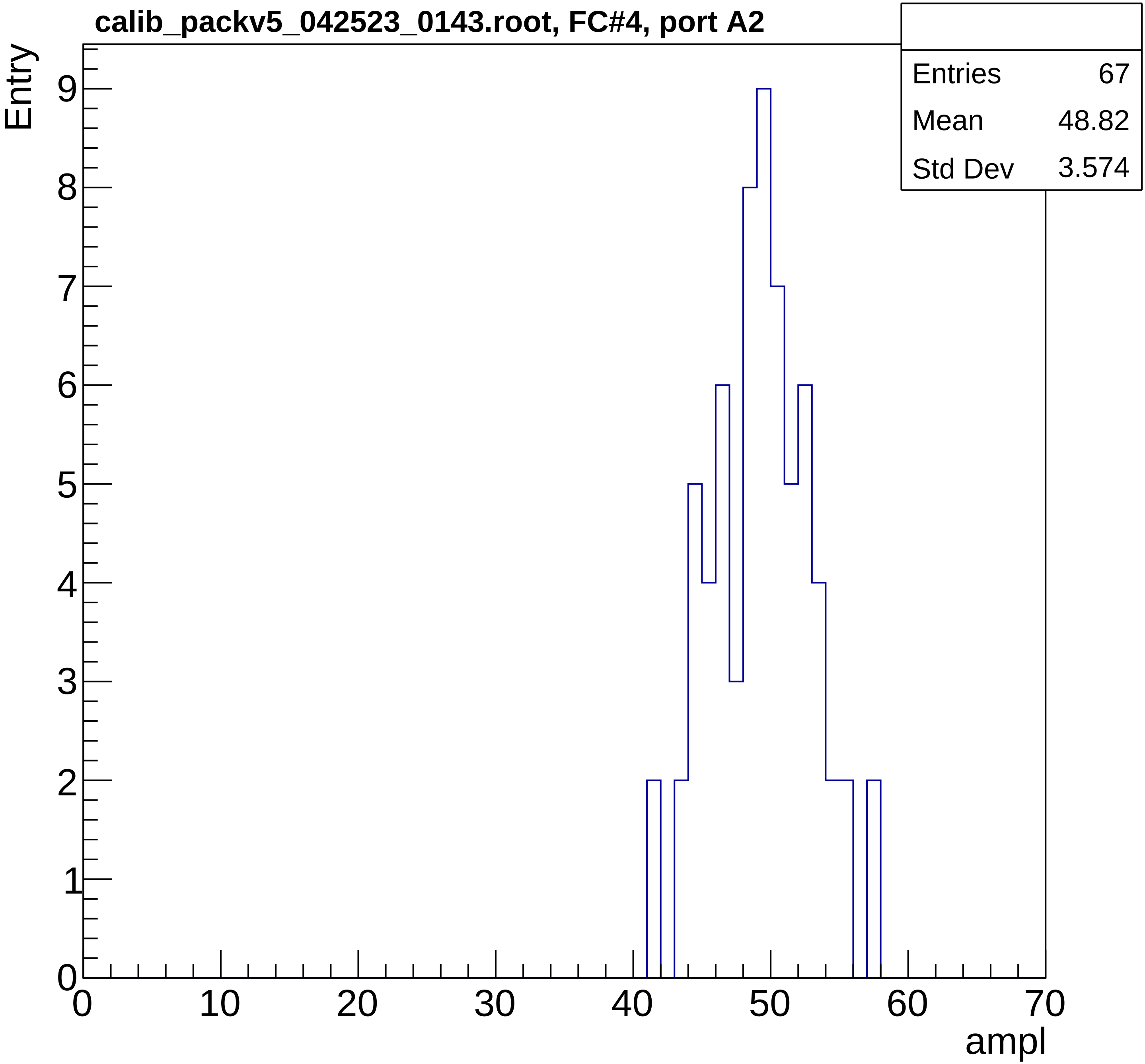
B1L100S, U21-ch66, adc1

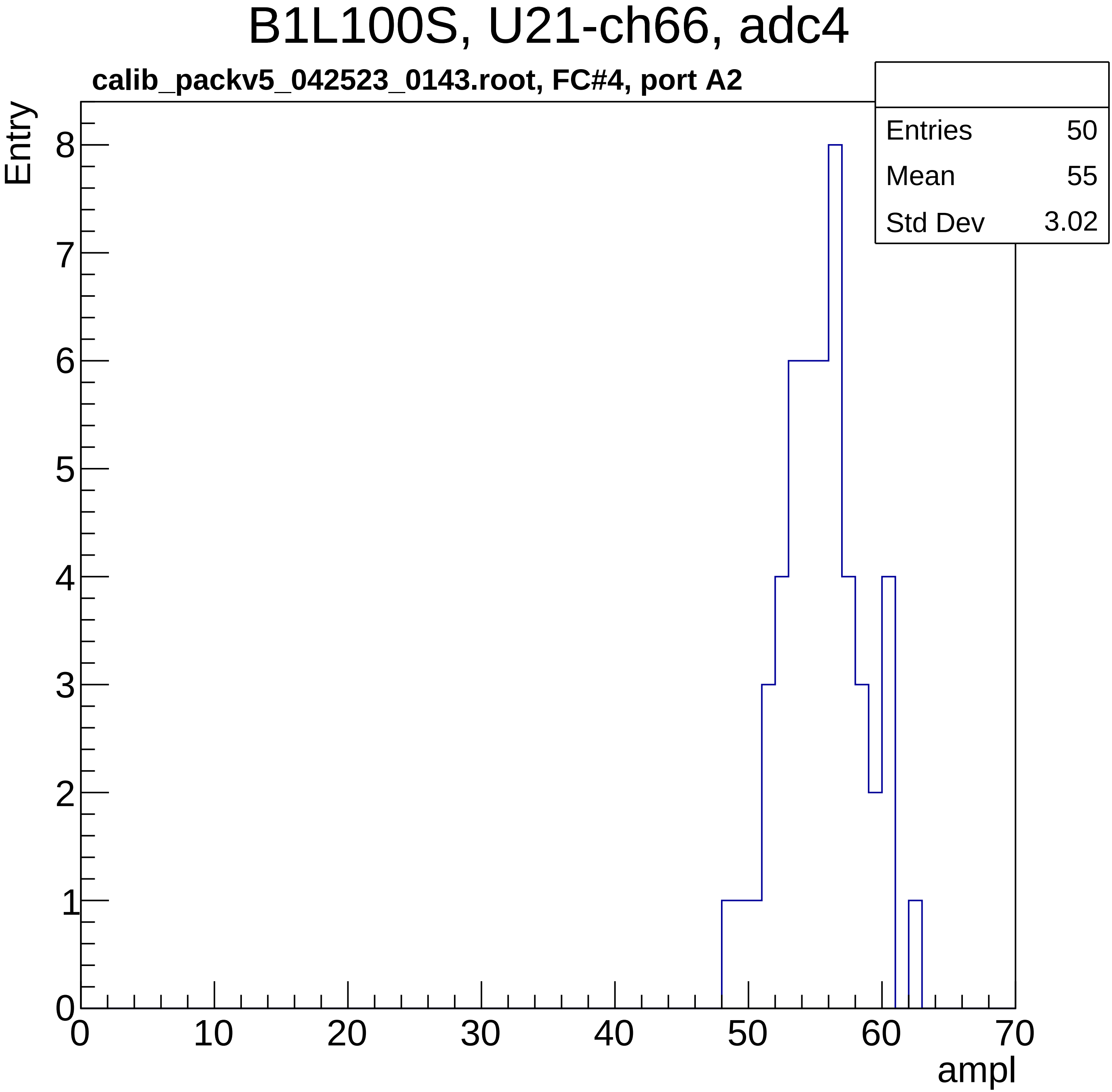



B1L100S, U21-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.77
Std Dev 3.255
Gaus mean : 43.4366
Gaus Width: 3.3012
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

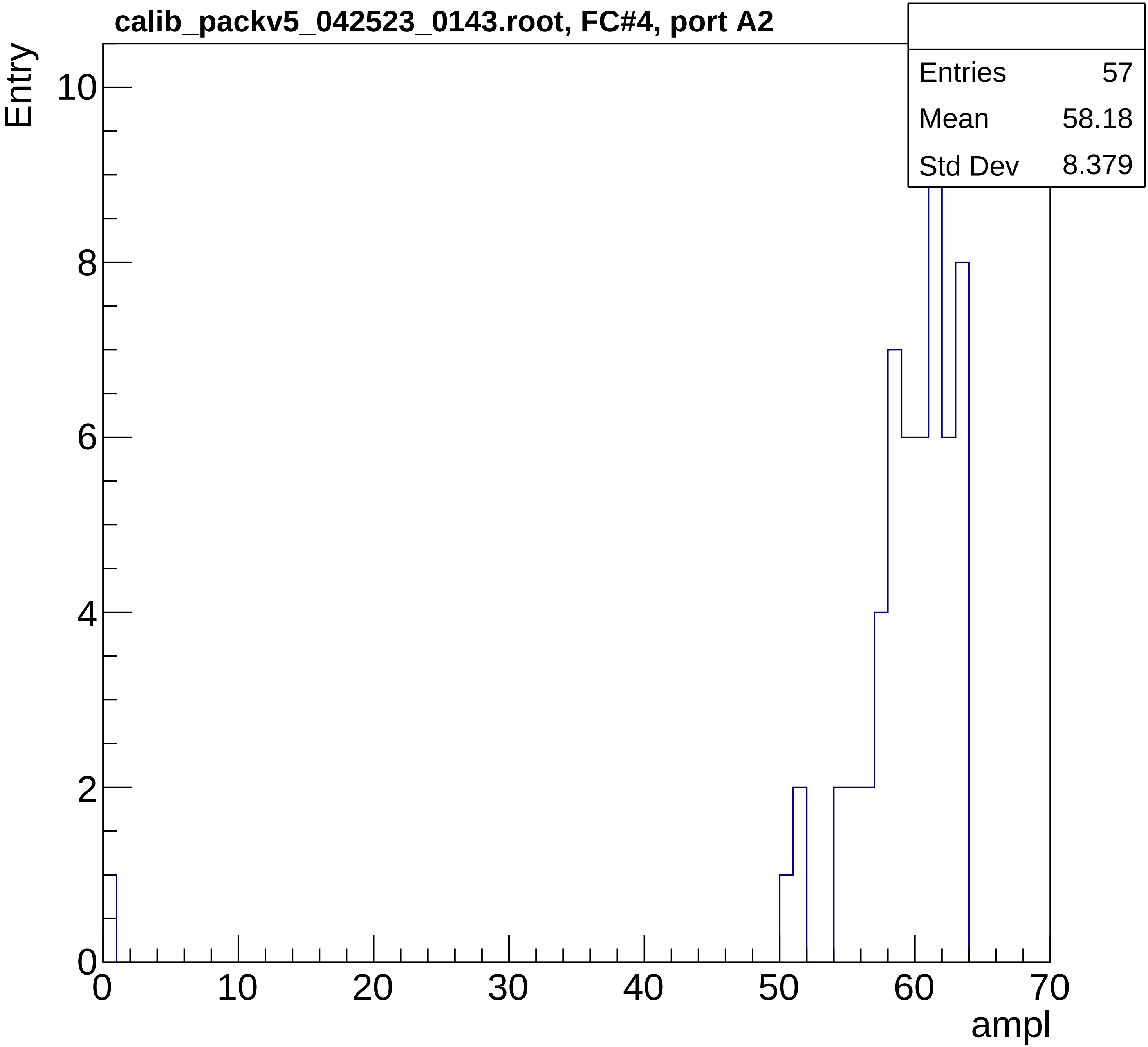
B1L100S, U21-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2

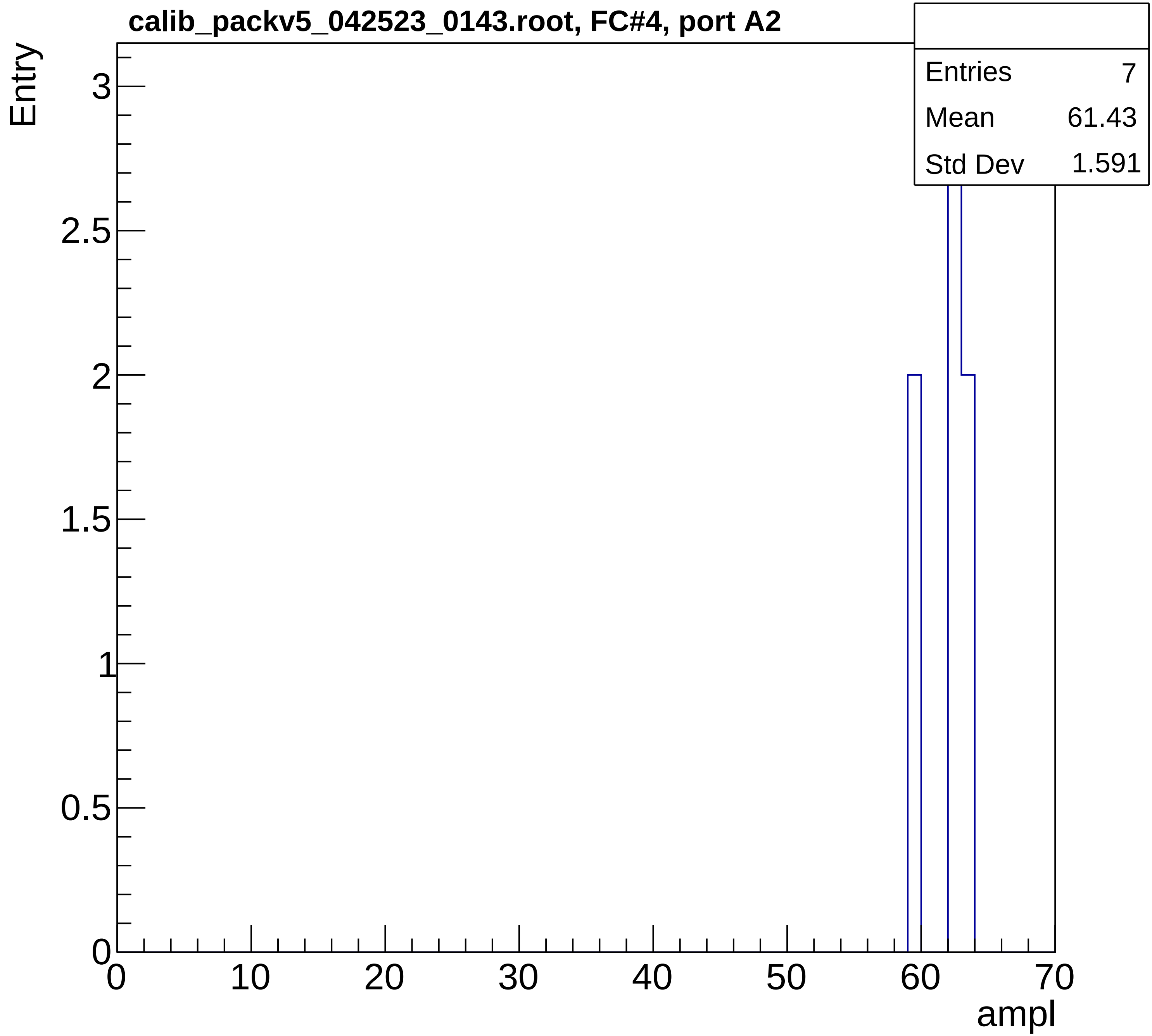


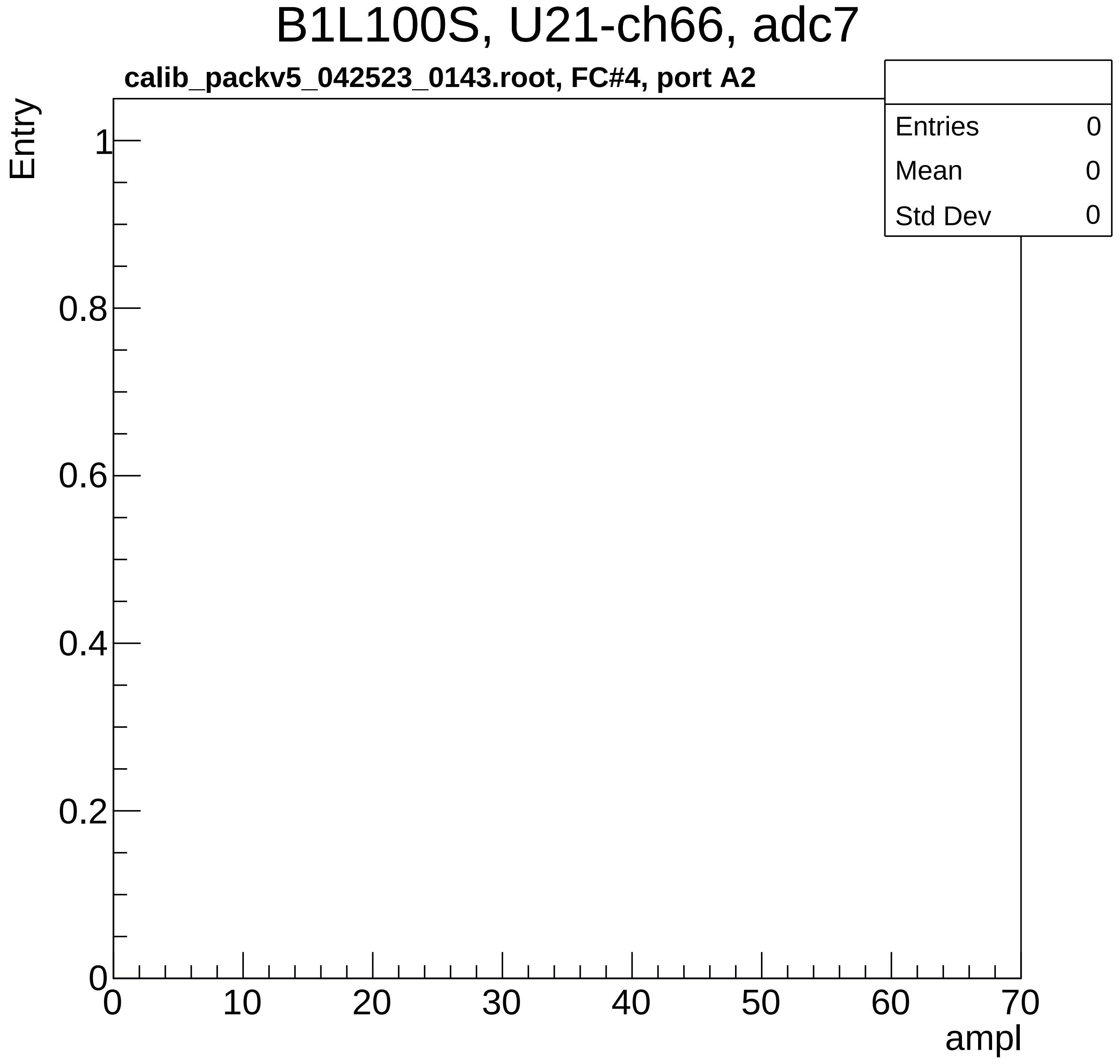

B1L100S, U21-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55
Std Dev 3.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

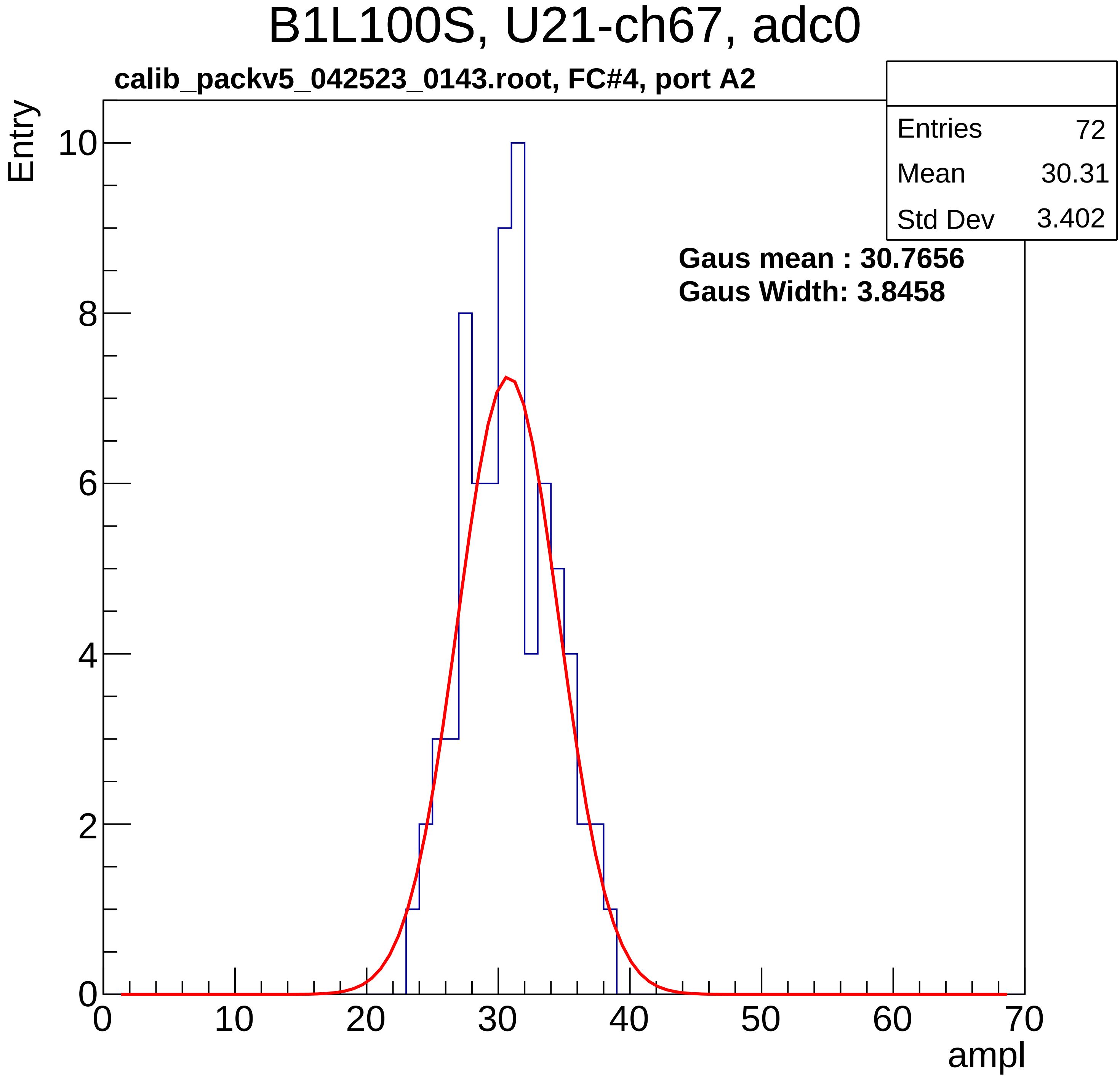
B1L100S, U21-ch66, adc5



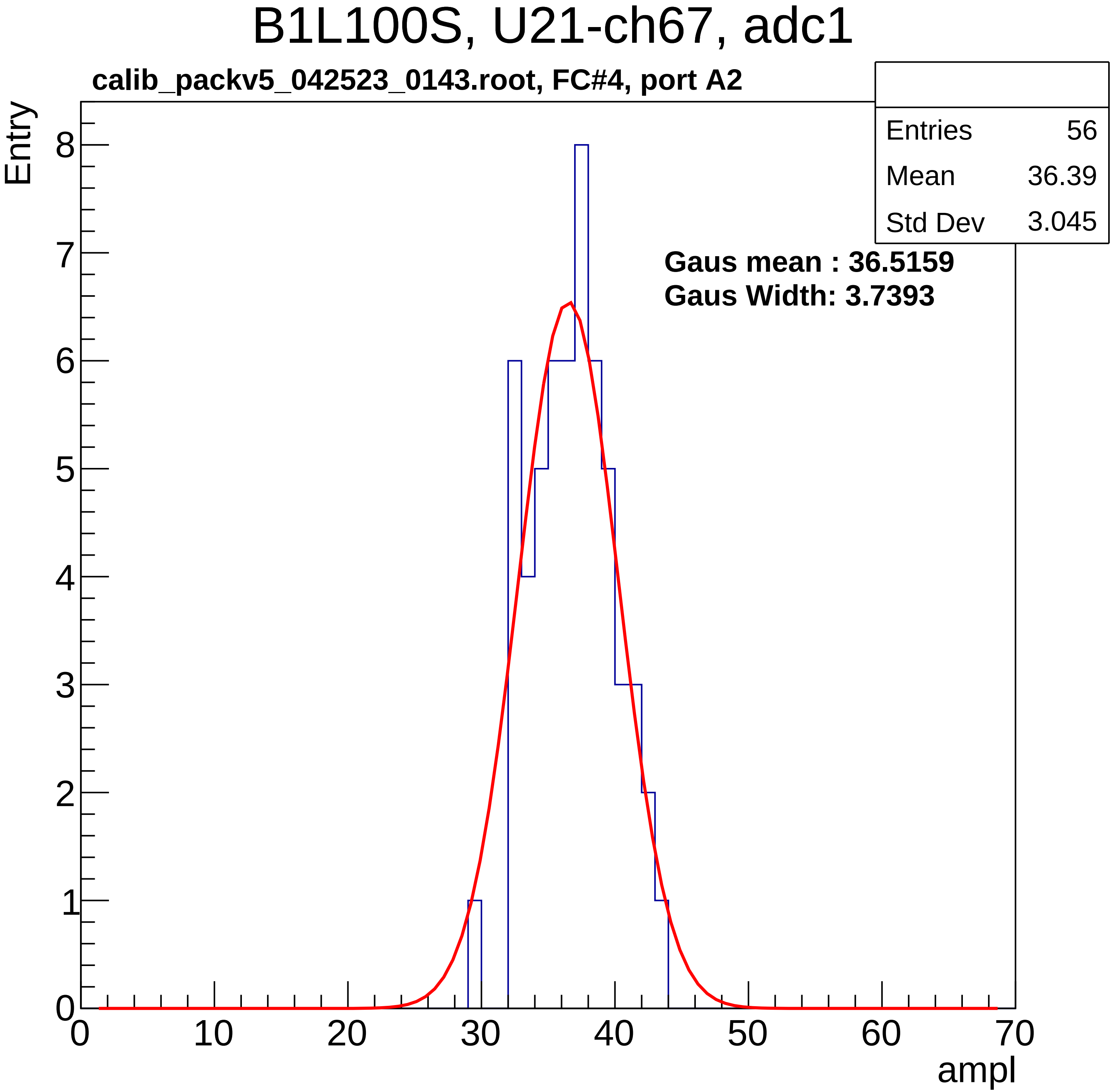
B1L100S, U21-ch66, adc6

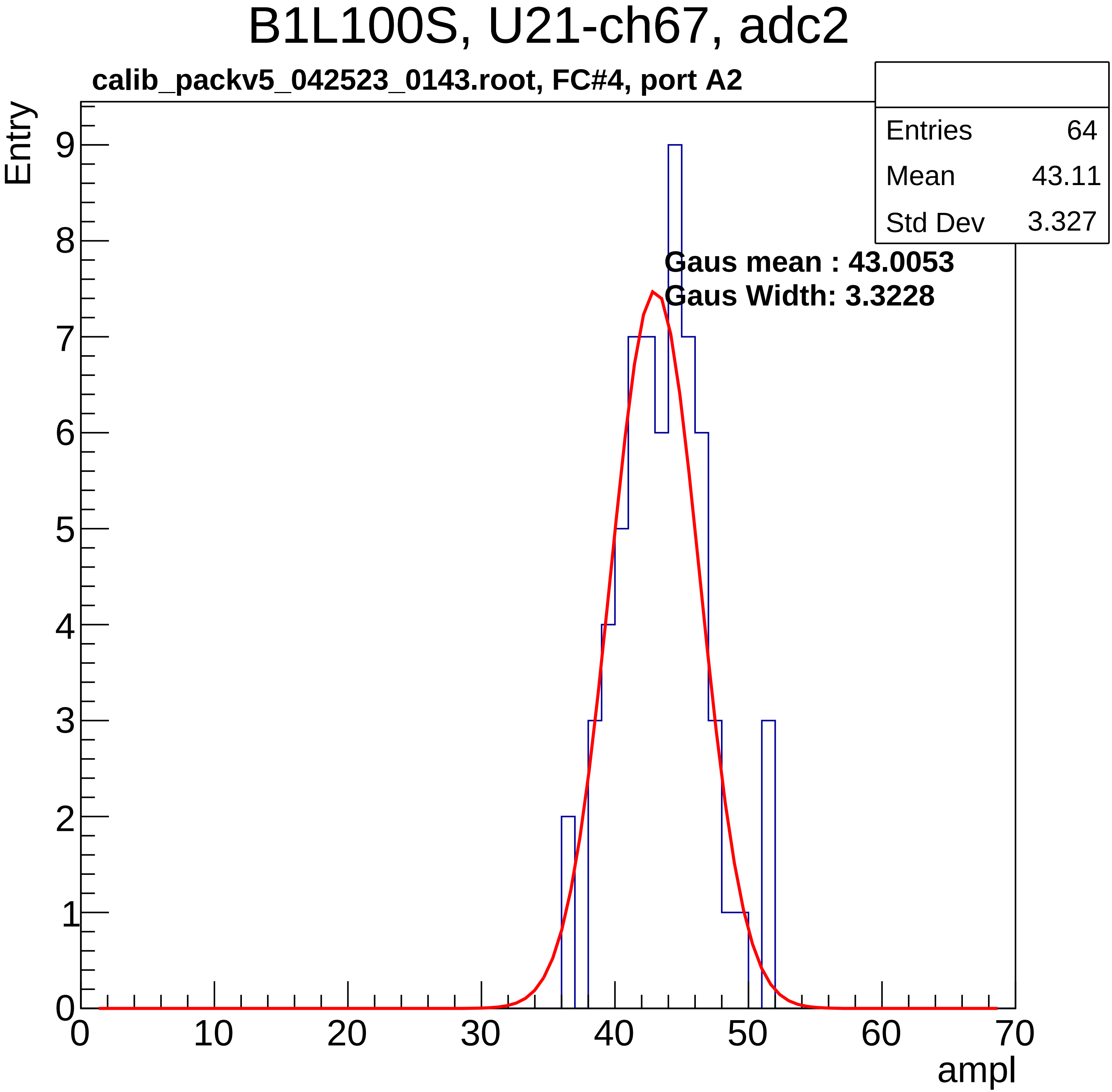


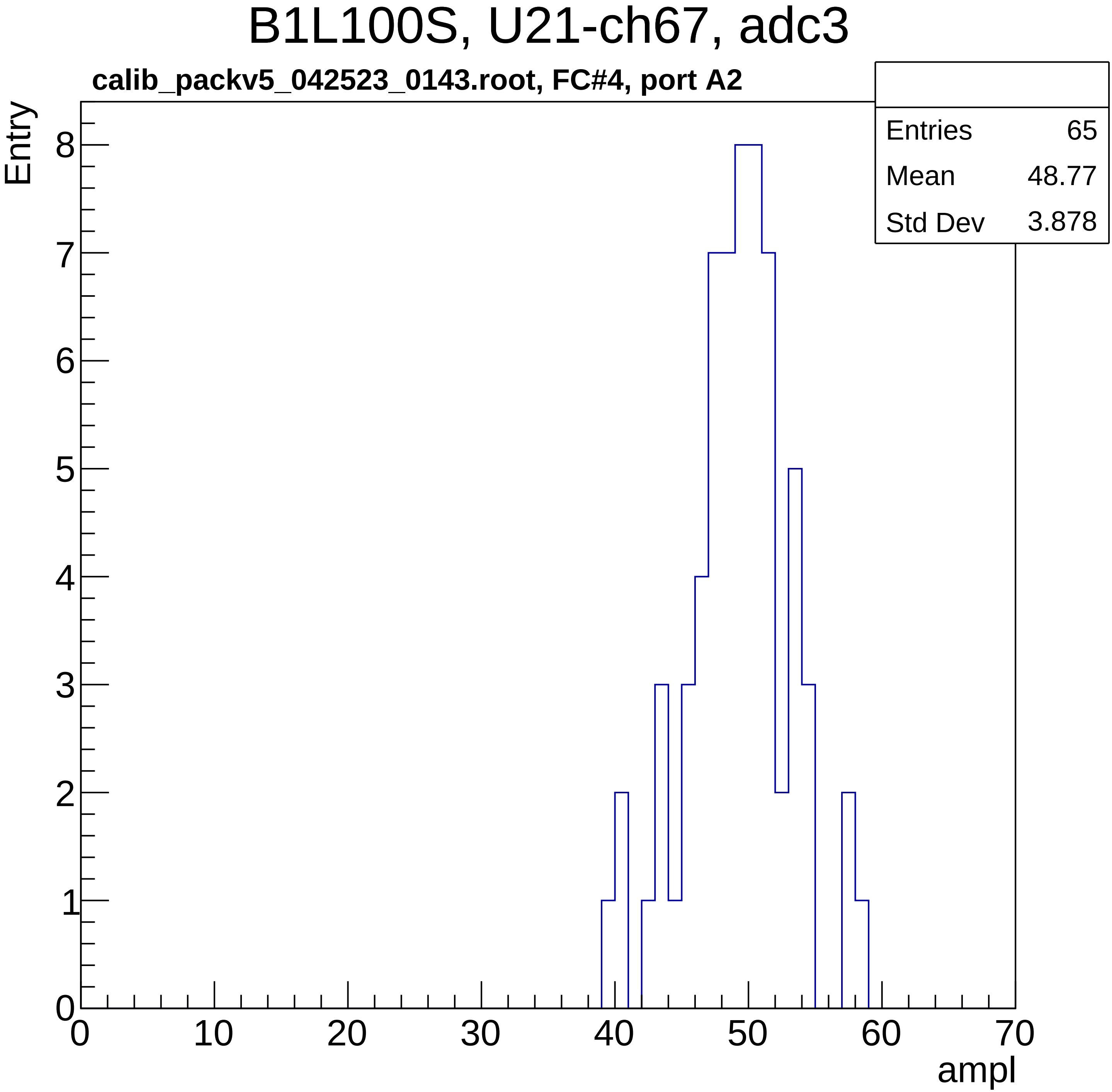

B1L100S, U21-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl



B1L100S, U21-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.31
Std Dev 3.402
Gaus mean : 30.7656
Gaus Width: 3.8458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

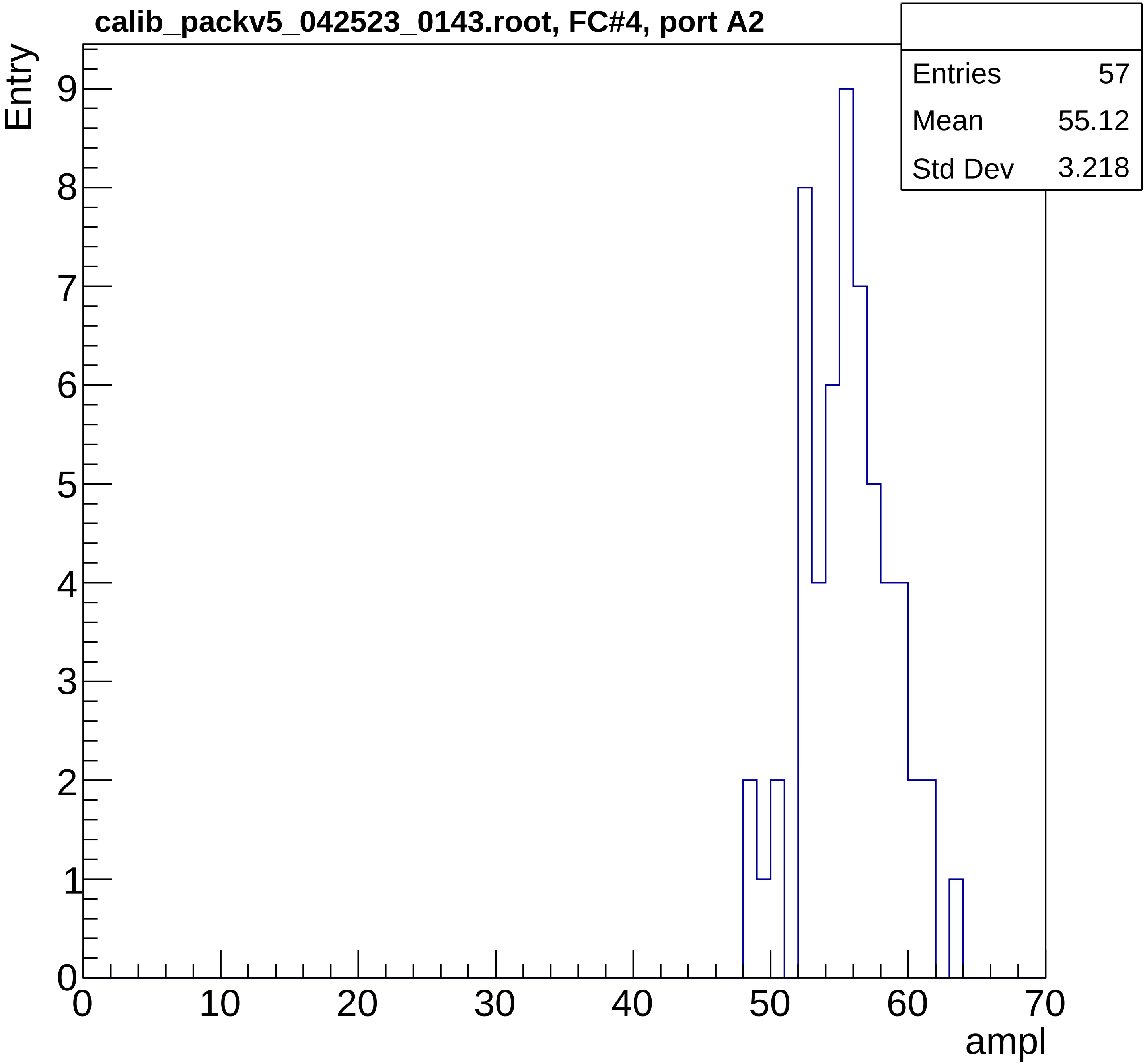

B1L100S, U21-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 56
Mean 36.39
Std Dev 3.045
Gaus mean : 36.5159
Gaus Width: 3.7393
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

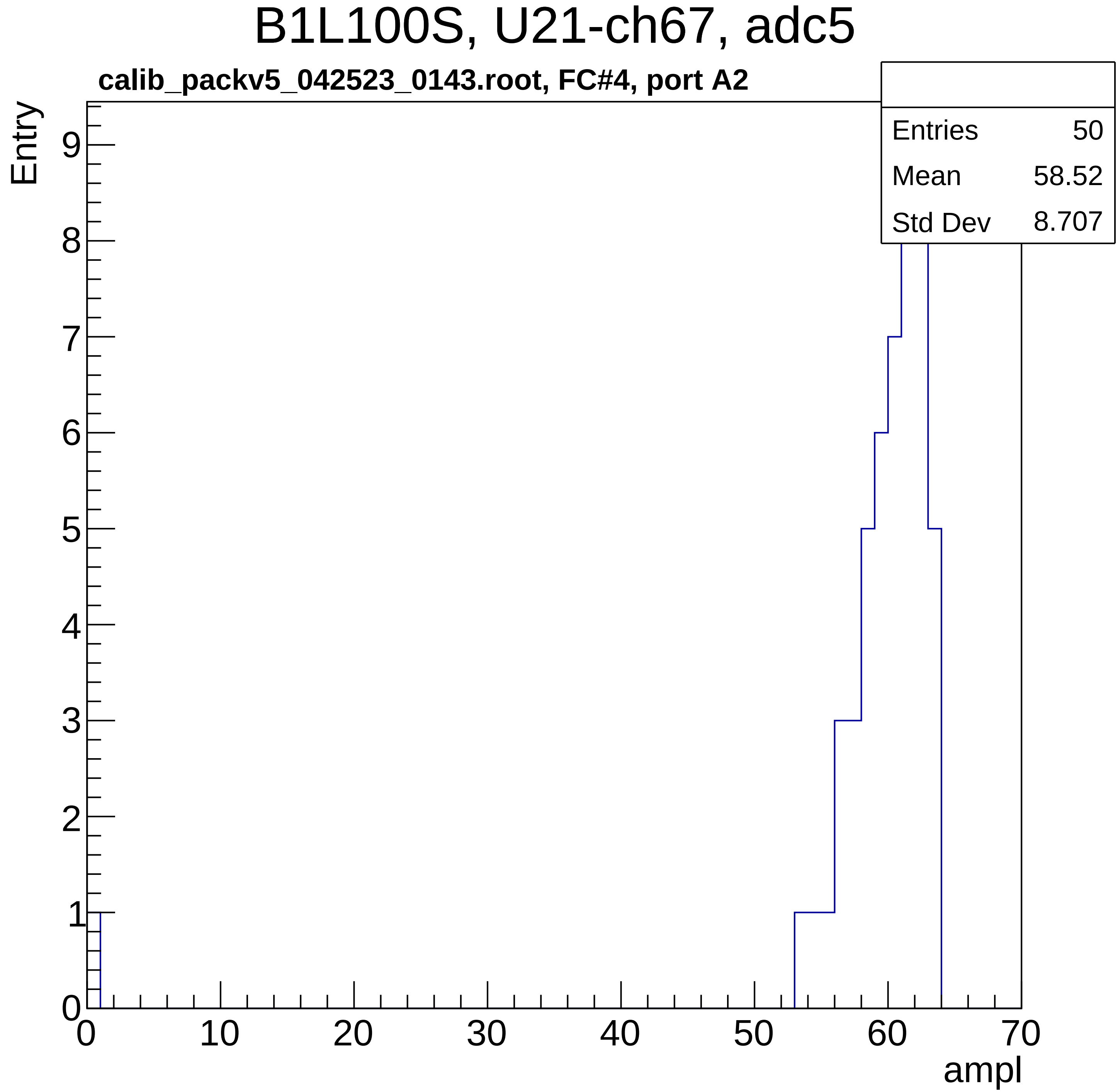

B1L100S, U21-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 64
Mean 43.11
Std Dev 3.327
Gaus mean : 43.0053
Gaus Width: 3.3228
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 48.77
Std Dev 3.878
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

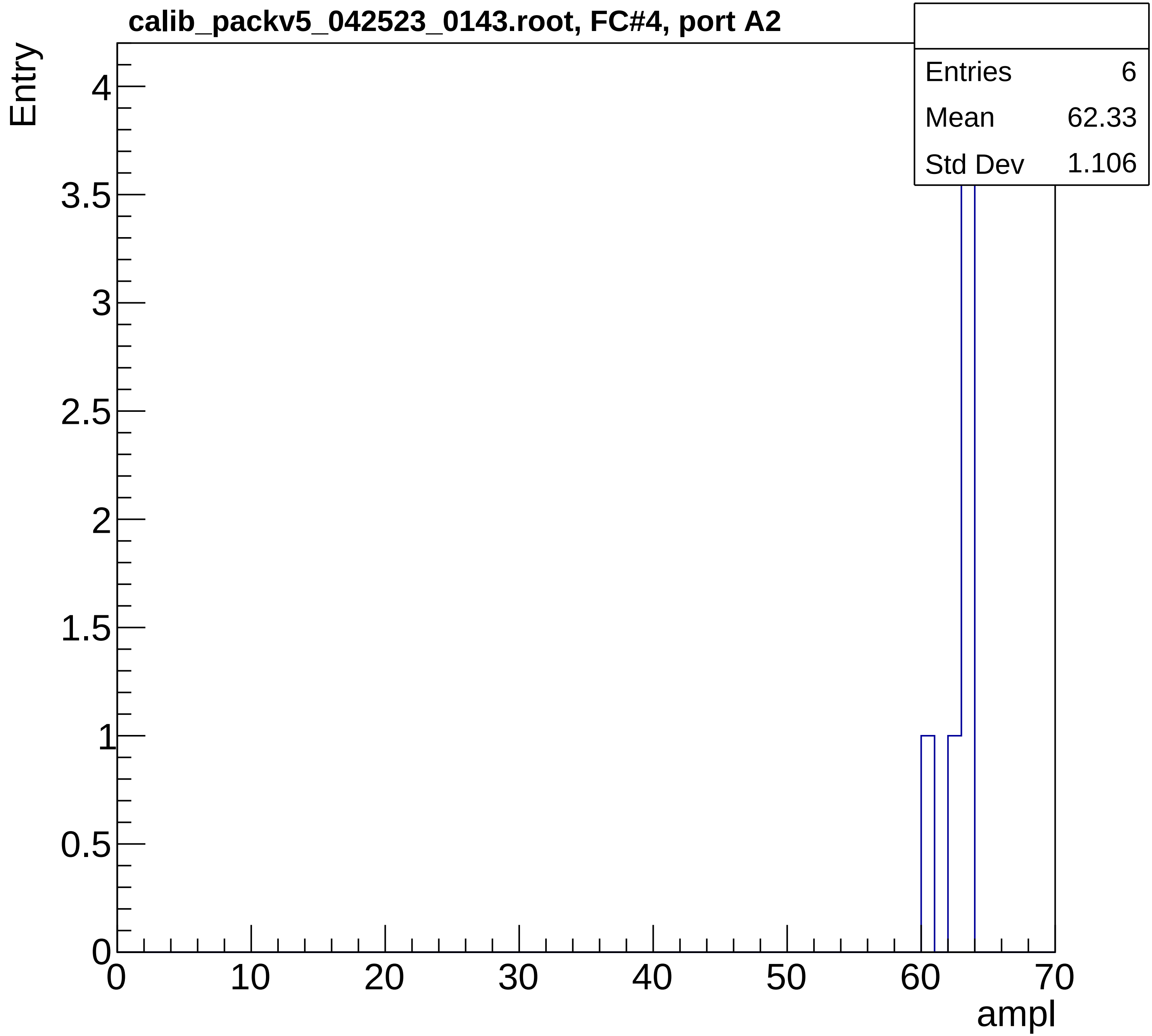
B1L100S, U21-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2

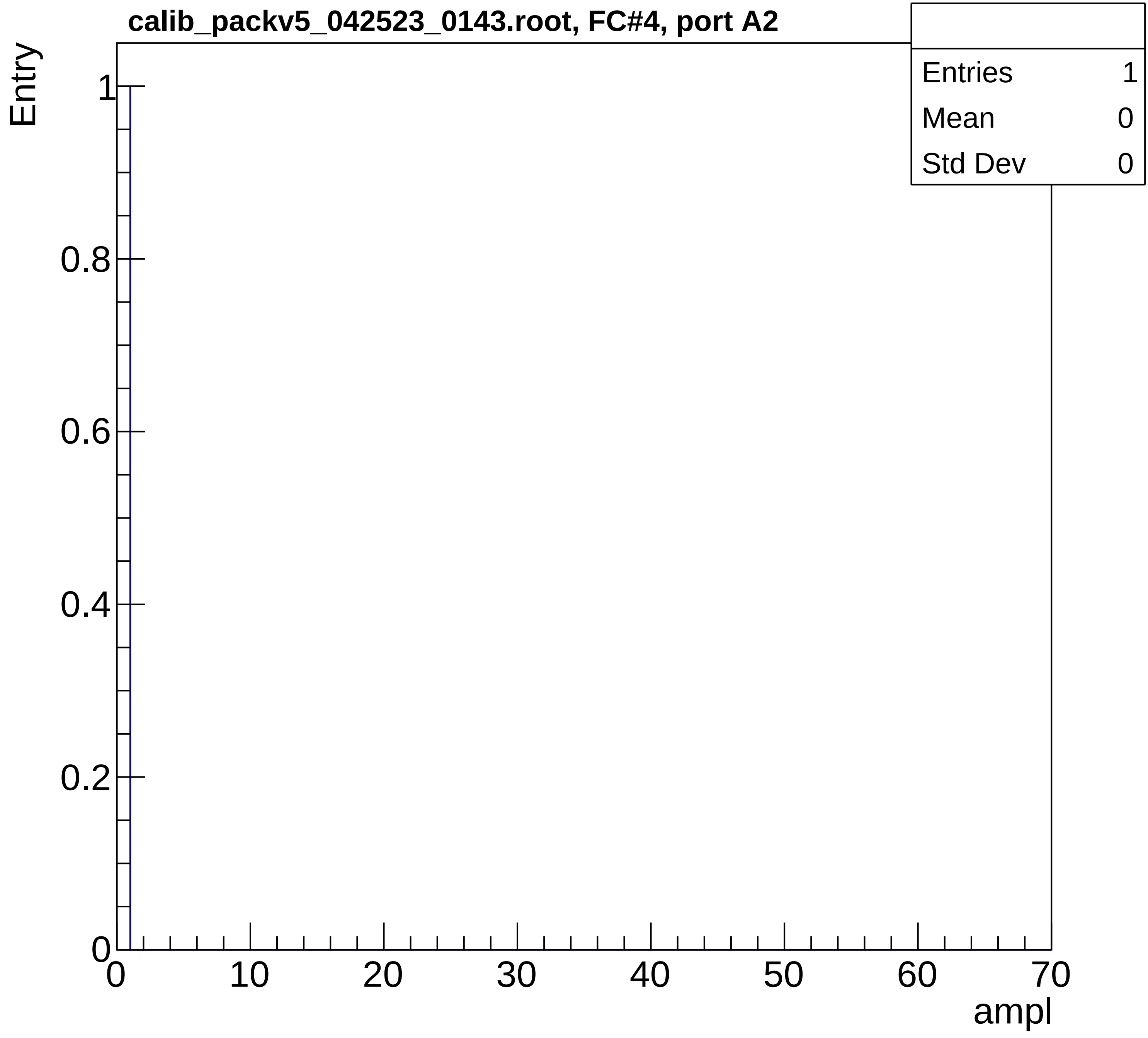


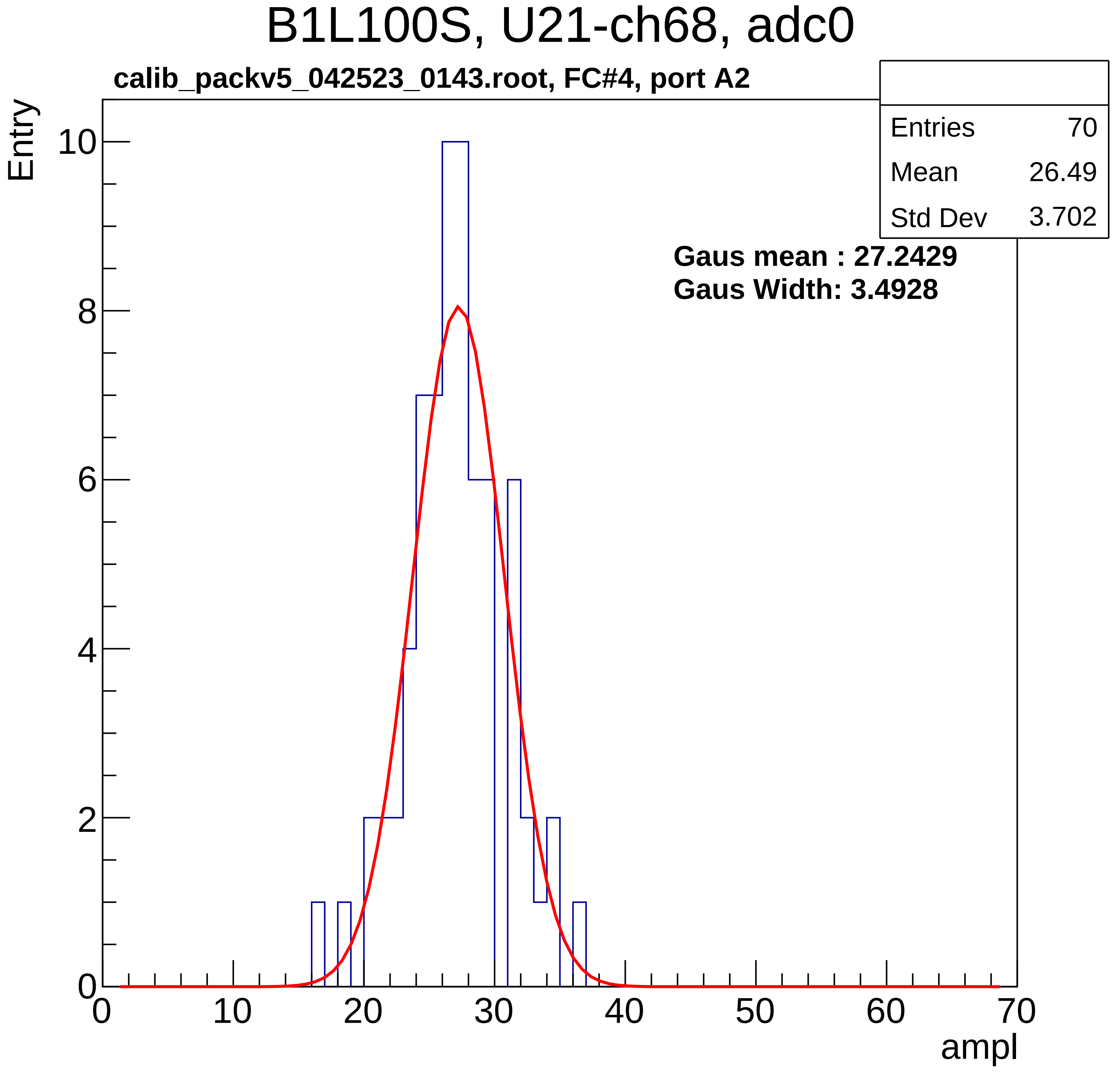

B1L100S, U21-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 50
Mean 58.52
Std Dev 8.707
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

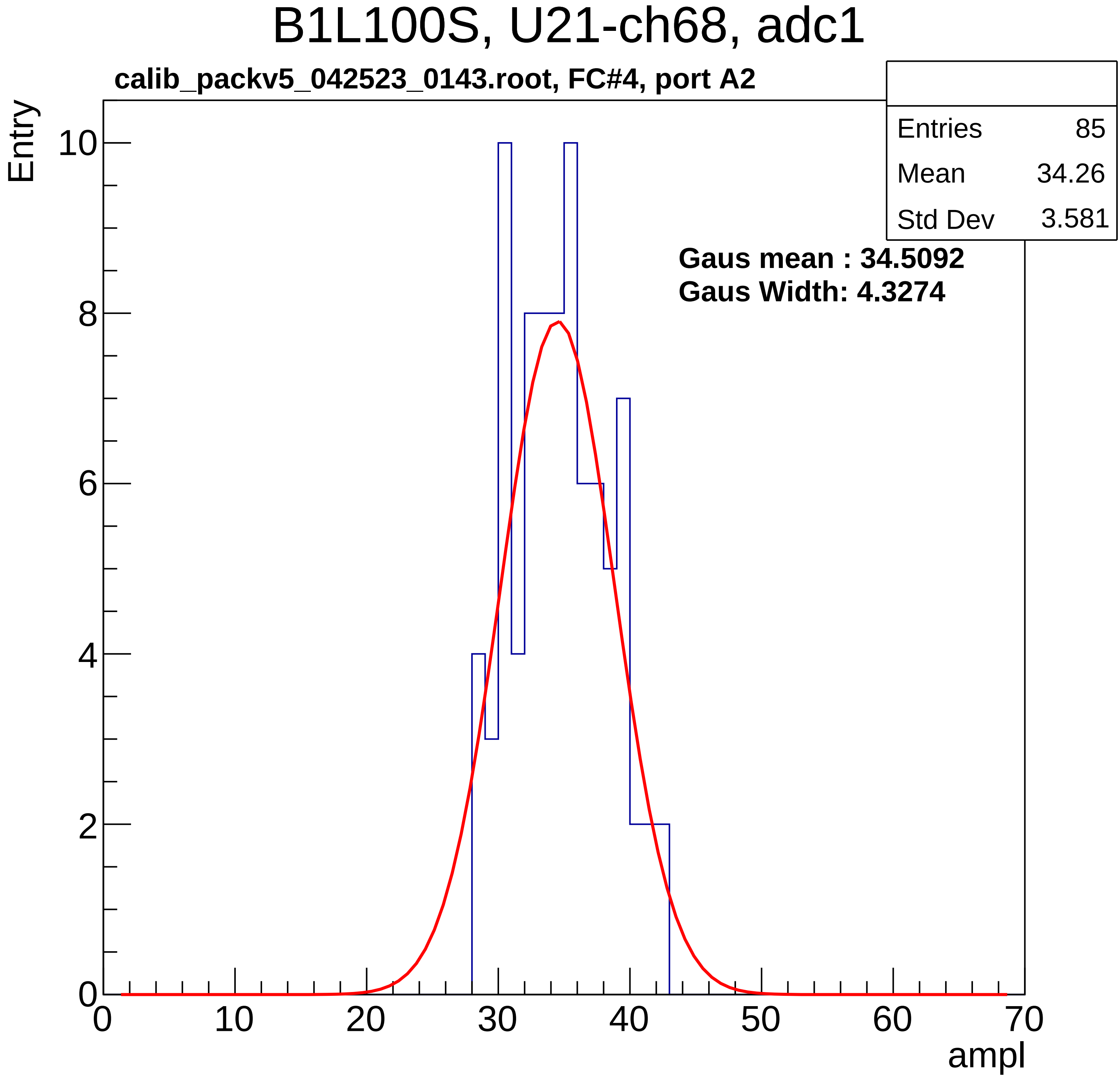
B1L100S, U21-ch67, adc6

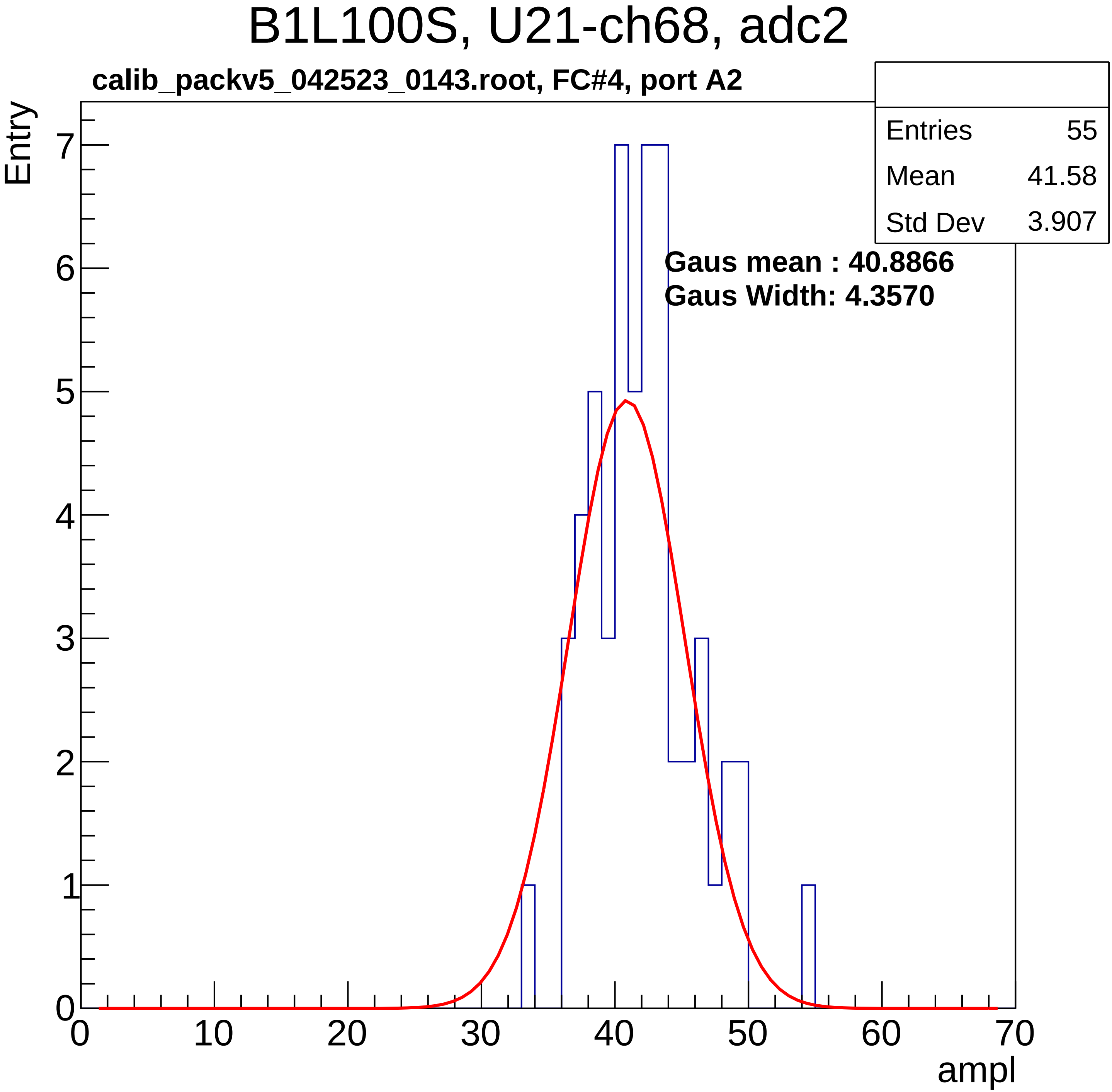


B1L100S, U21-ch67, adc7

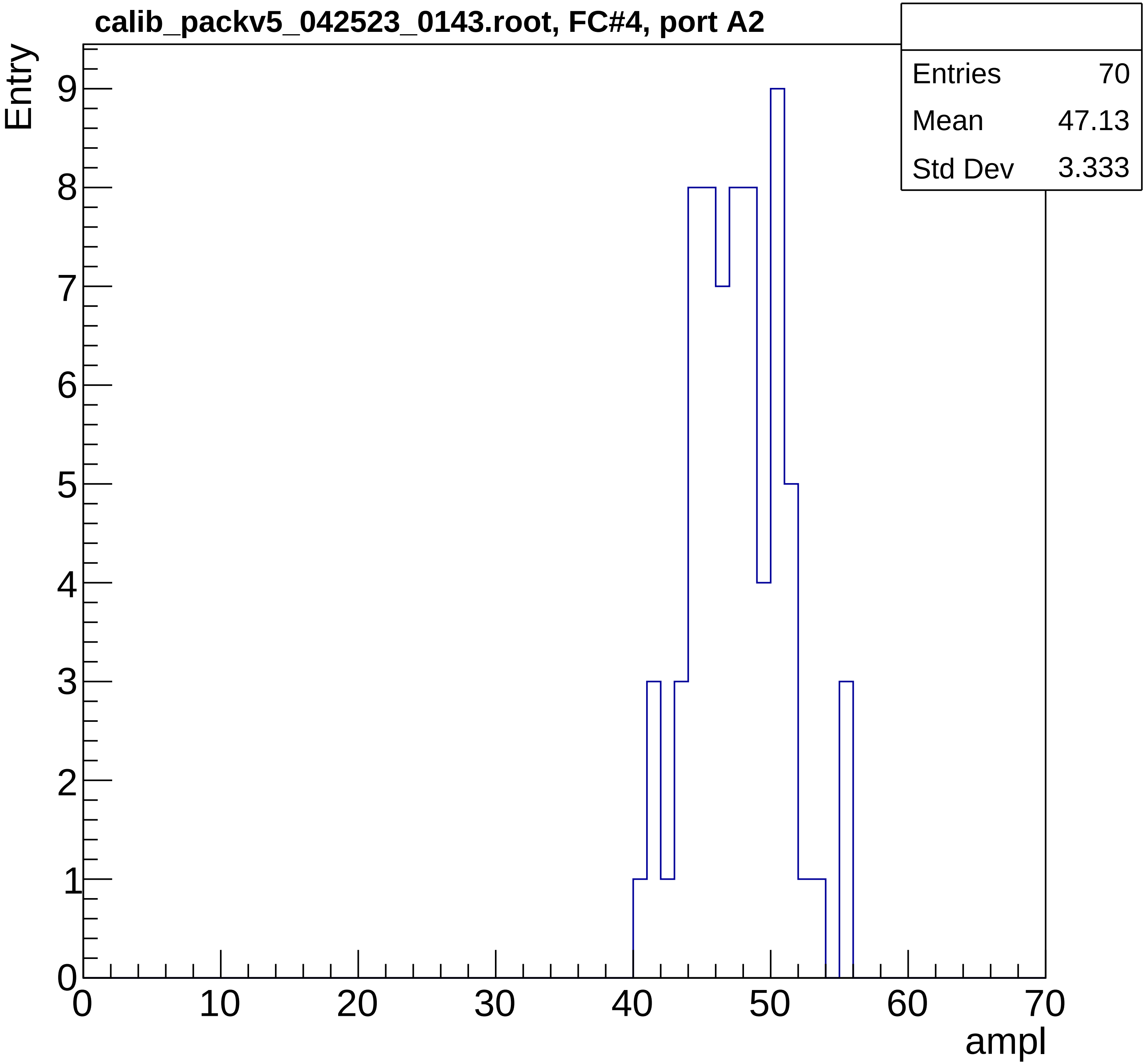



B1L100S, U21-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 26.49
Std Dev 3.702
Gaus mean : 27.2429
Gaus Width: 3.4928
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

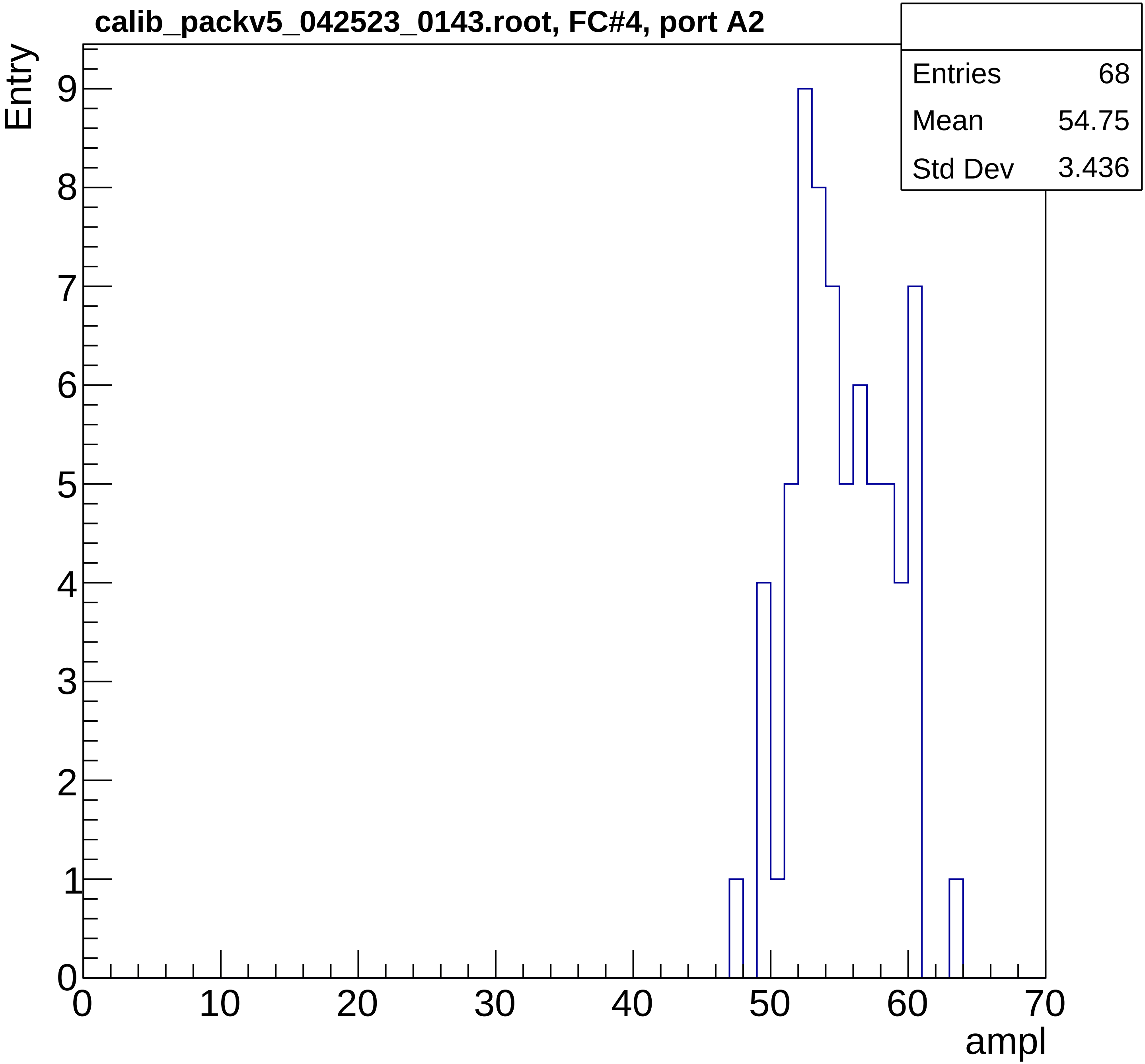

B1L100S, U21-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 34.26
Std Dev 3.581
Gaus mean : 34.5092
Gaus Width: 4.3274
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 41.58
Std Dev 3.907
Gaus mean : 40.8866
Gaus Width: 4.3570
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

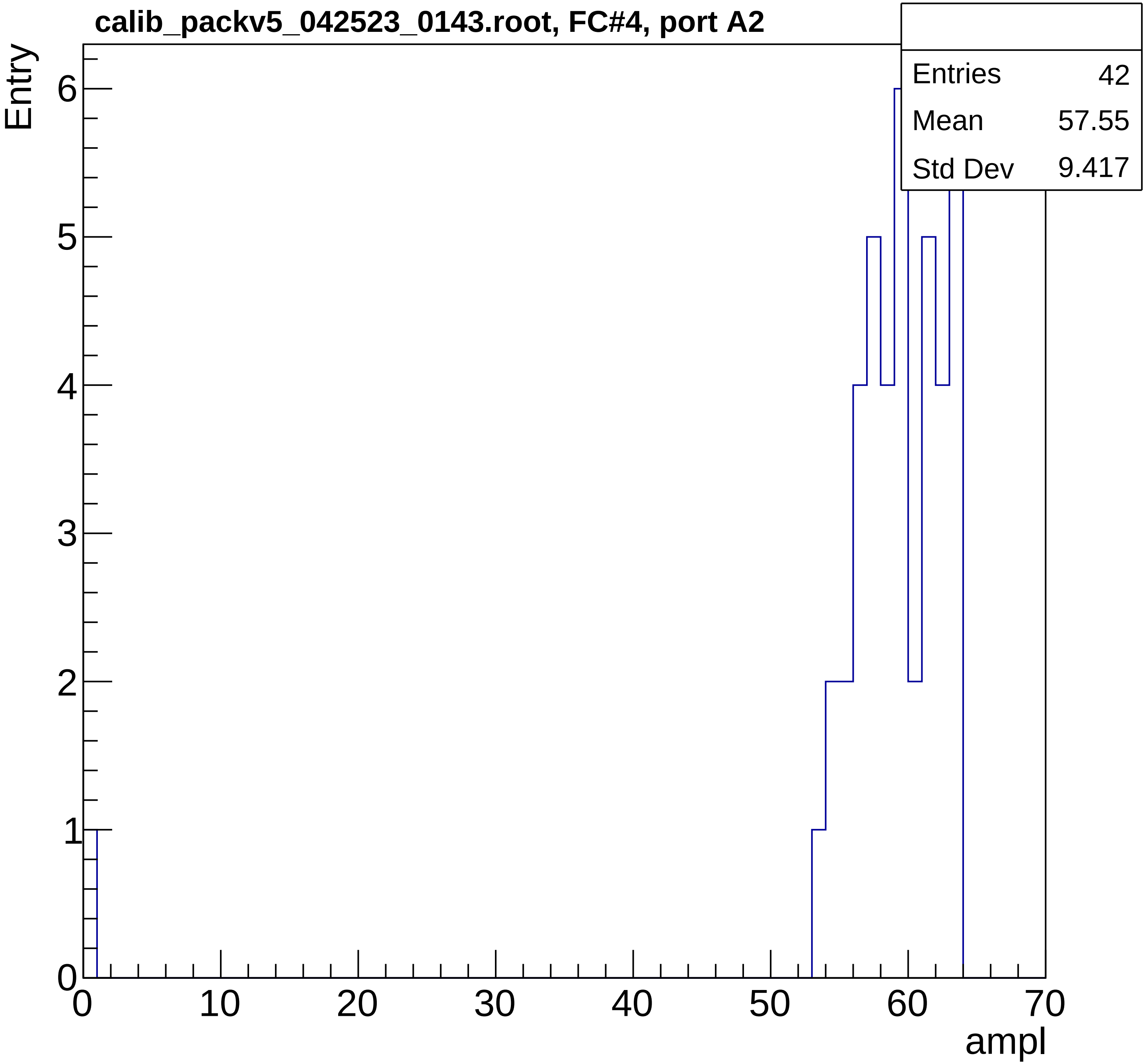
B1L100S, U21-ch68, adc3

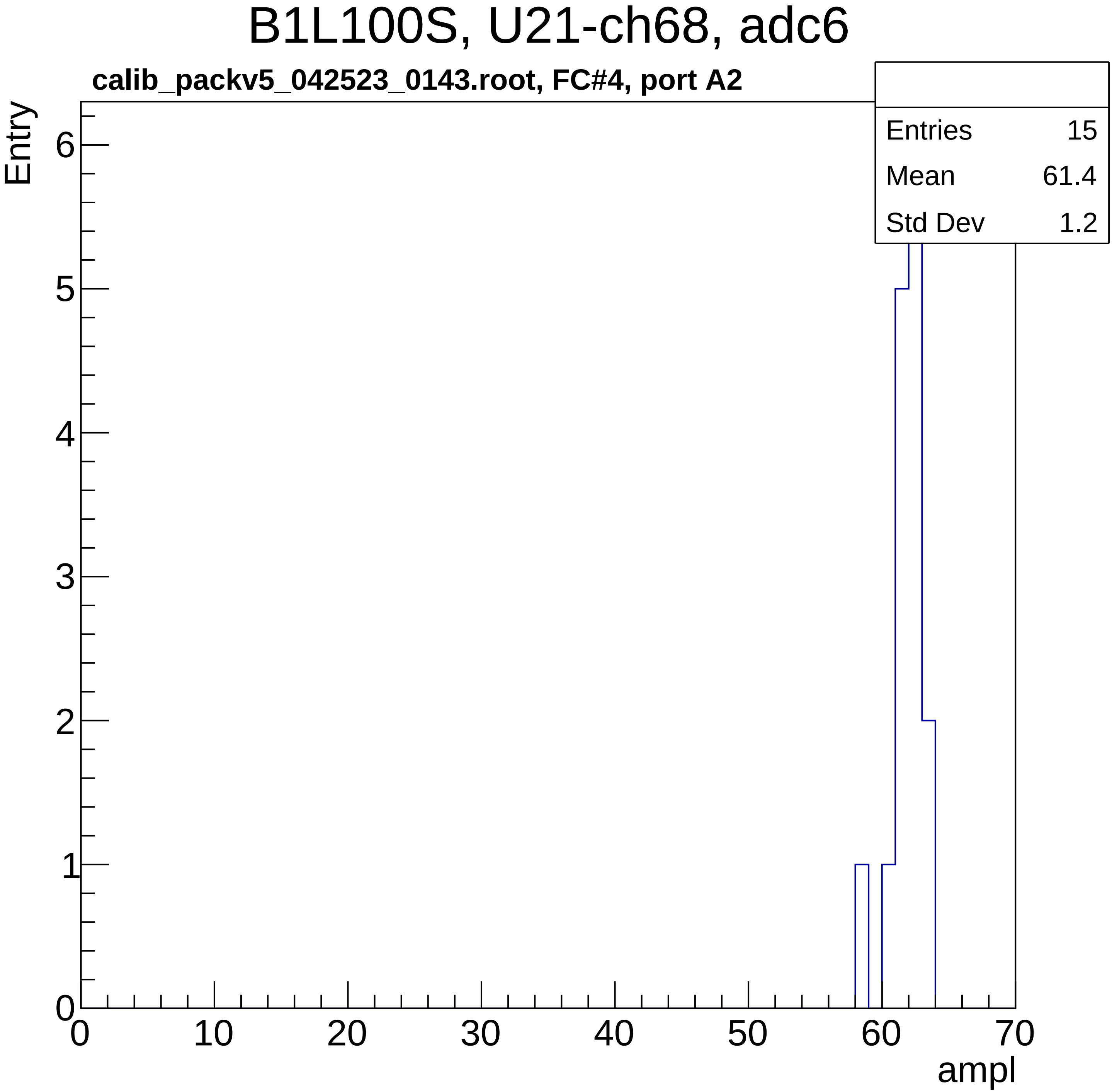


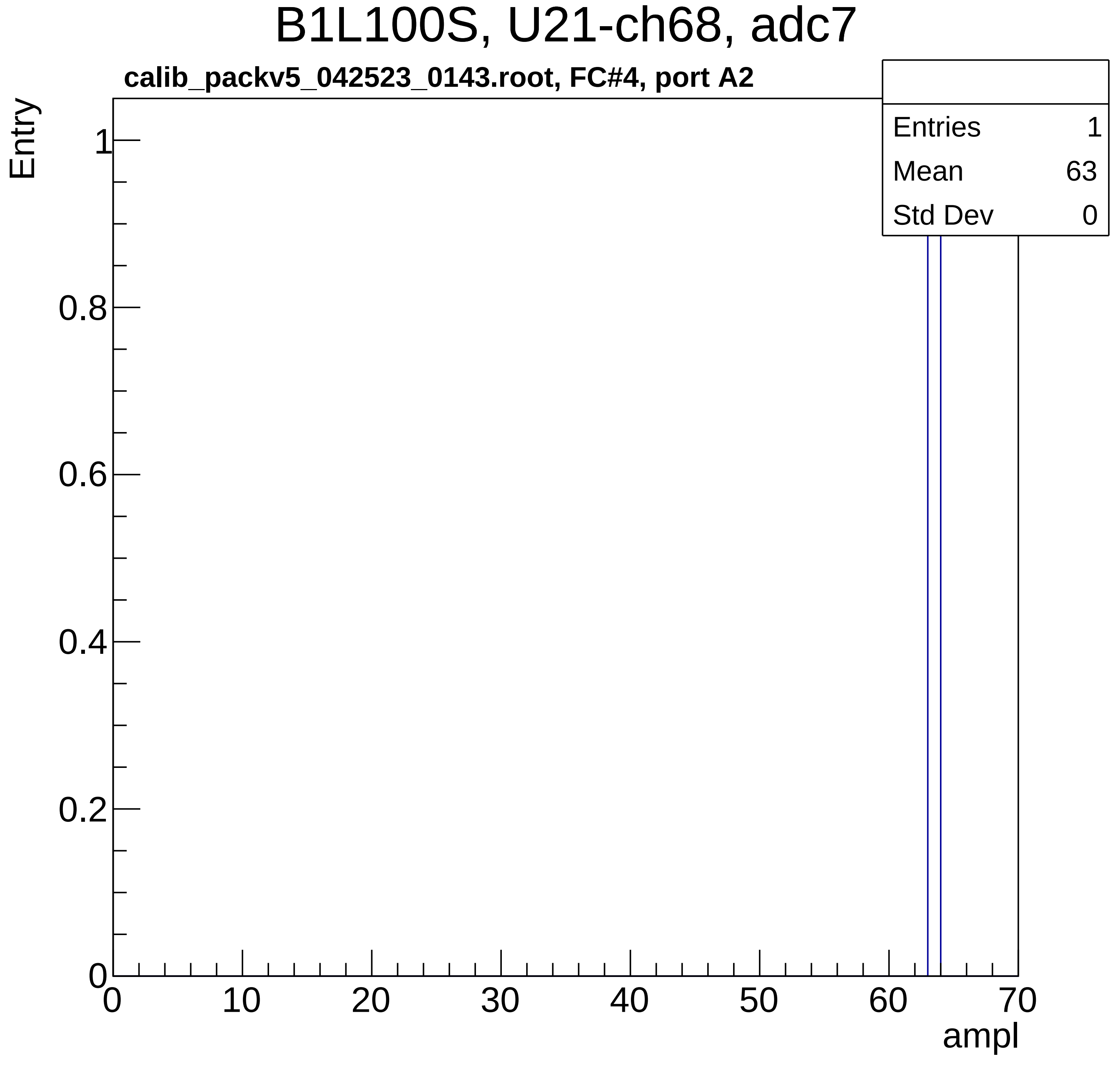
B1L100S, U21-ch68, adc4

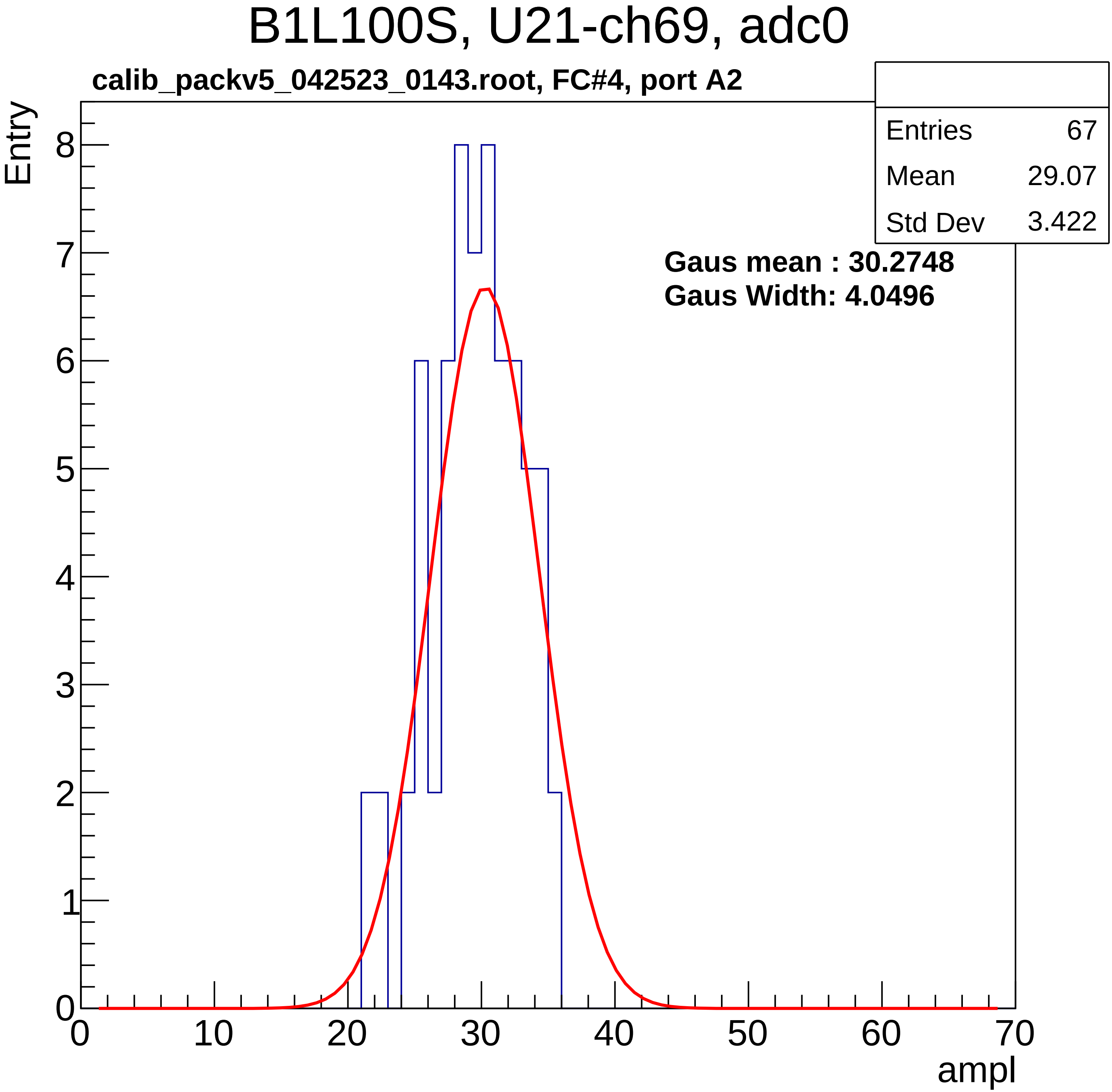


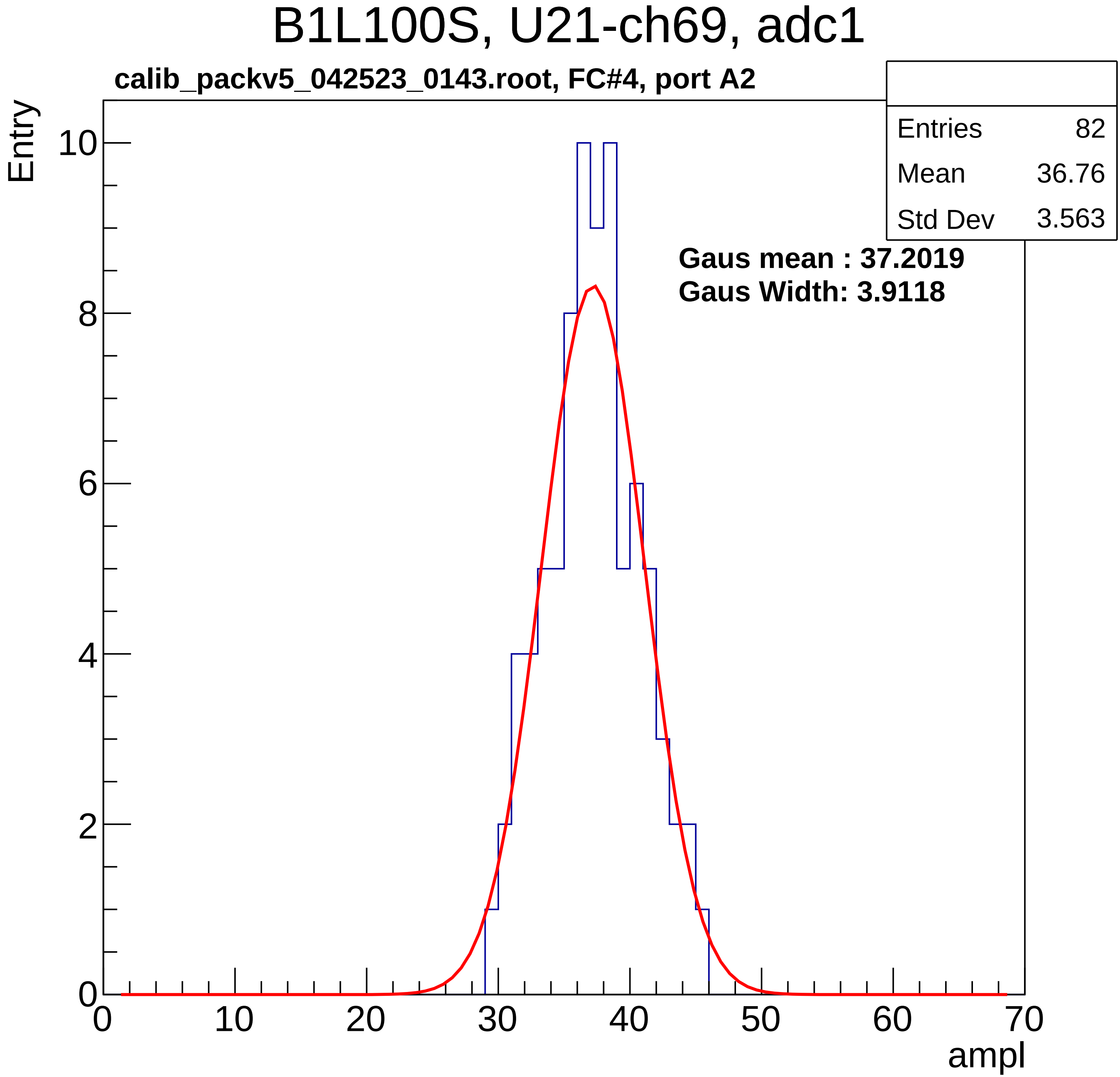
B1L100S, U21-ch68, adc5

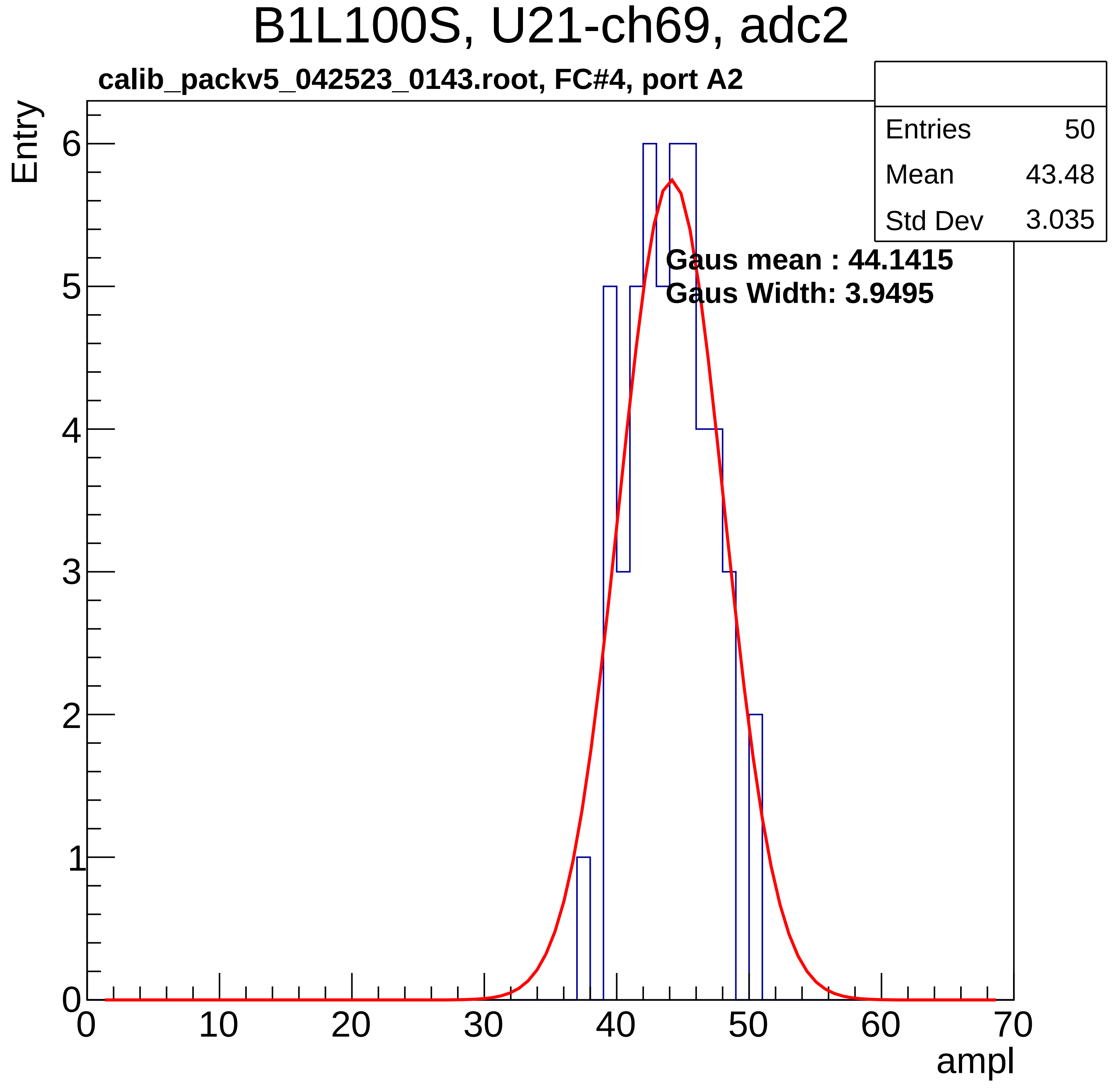



B1L100S, U21-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 15
Mean 61.4
Std Dev 1.2
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

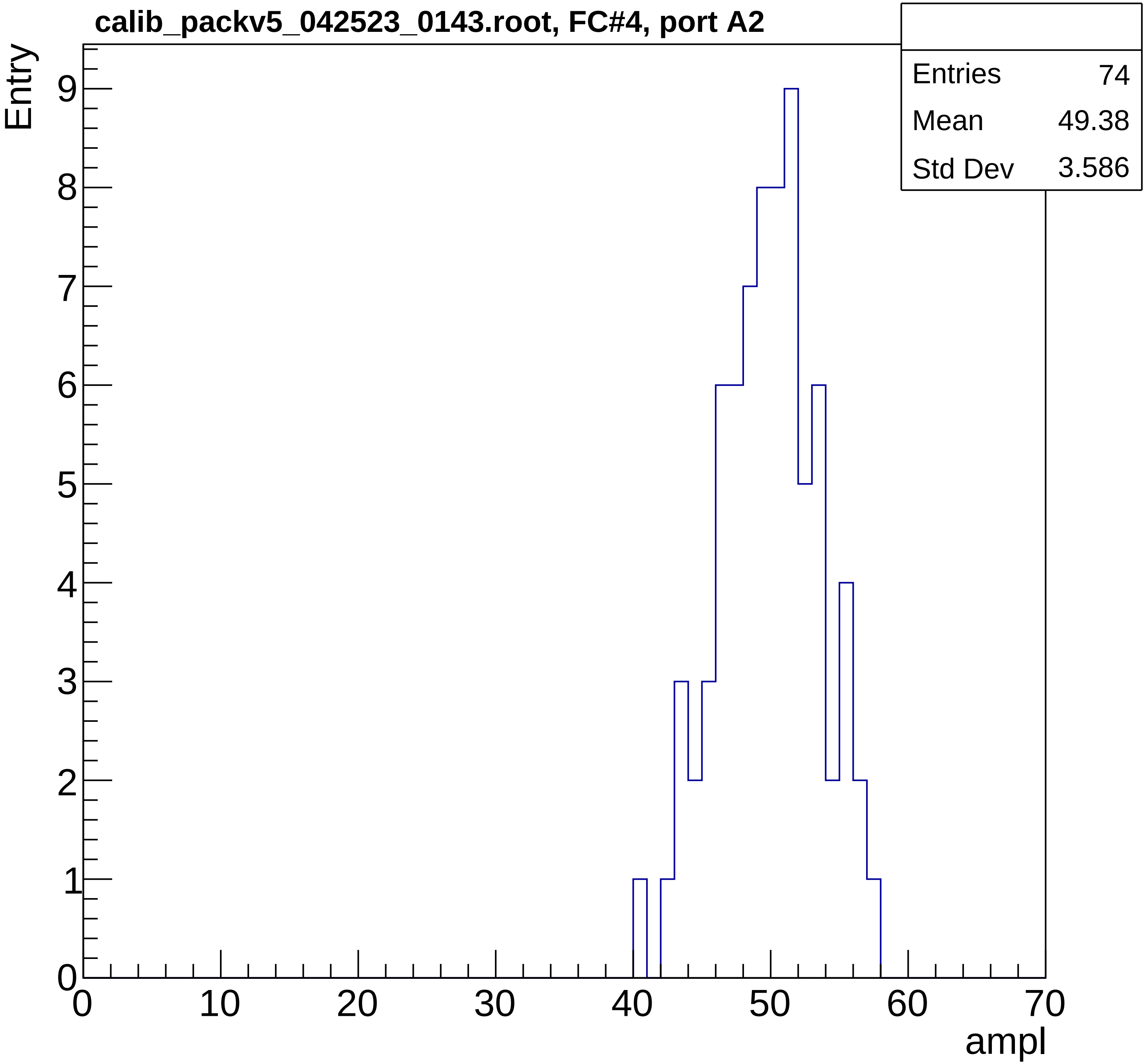

B1L100S, U21-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 67
Mean 29.07
Std Dev 3.422
Gaus mean : 30.2748
Gaus Width: 4.0496
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 36.76
Std Dev 3.563
Gaus mean : 37.2019
Gaus Width: 3.9118
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

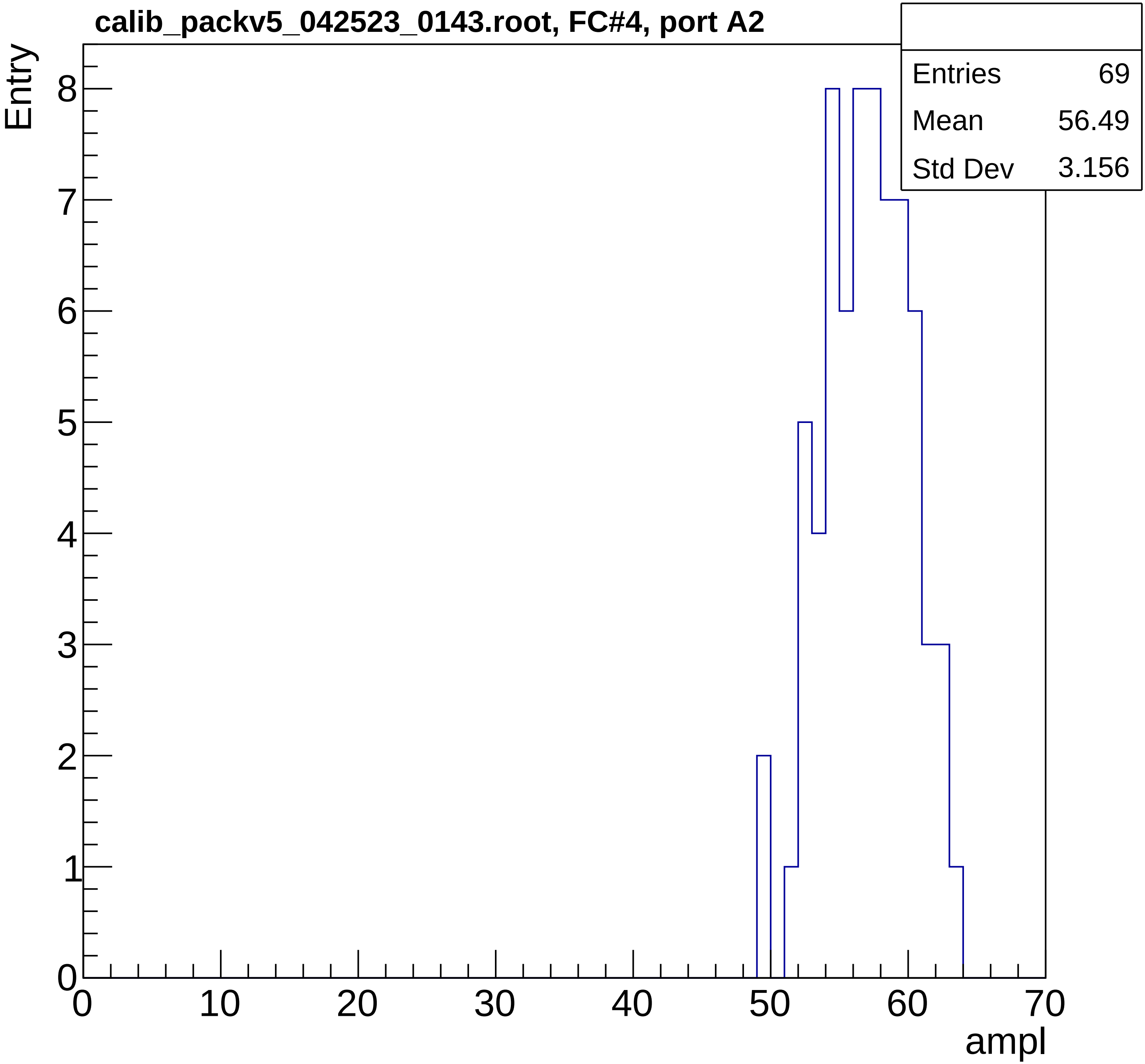

B1L100S, U21-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 43.48
Std Dev 3.035
Gaus mean : 44.1415
Gaus Width: 3.9495
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

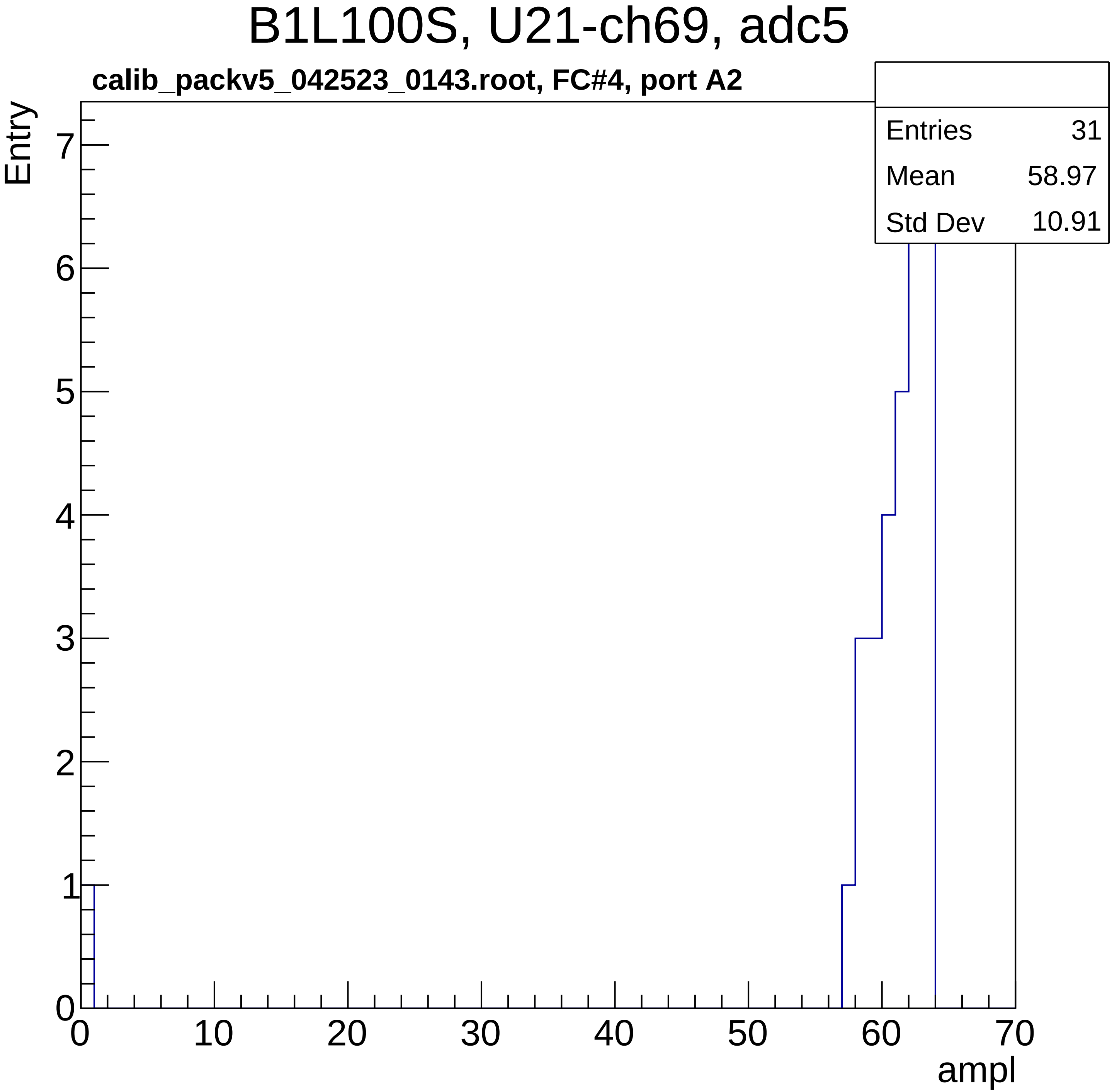
B1L100S, U21-ch69, adc3



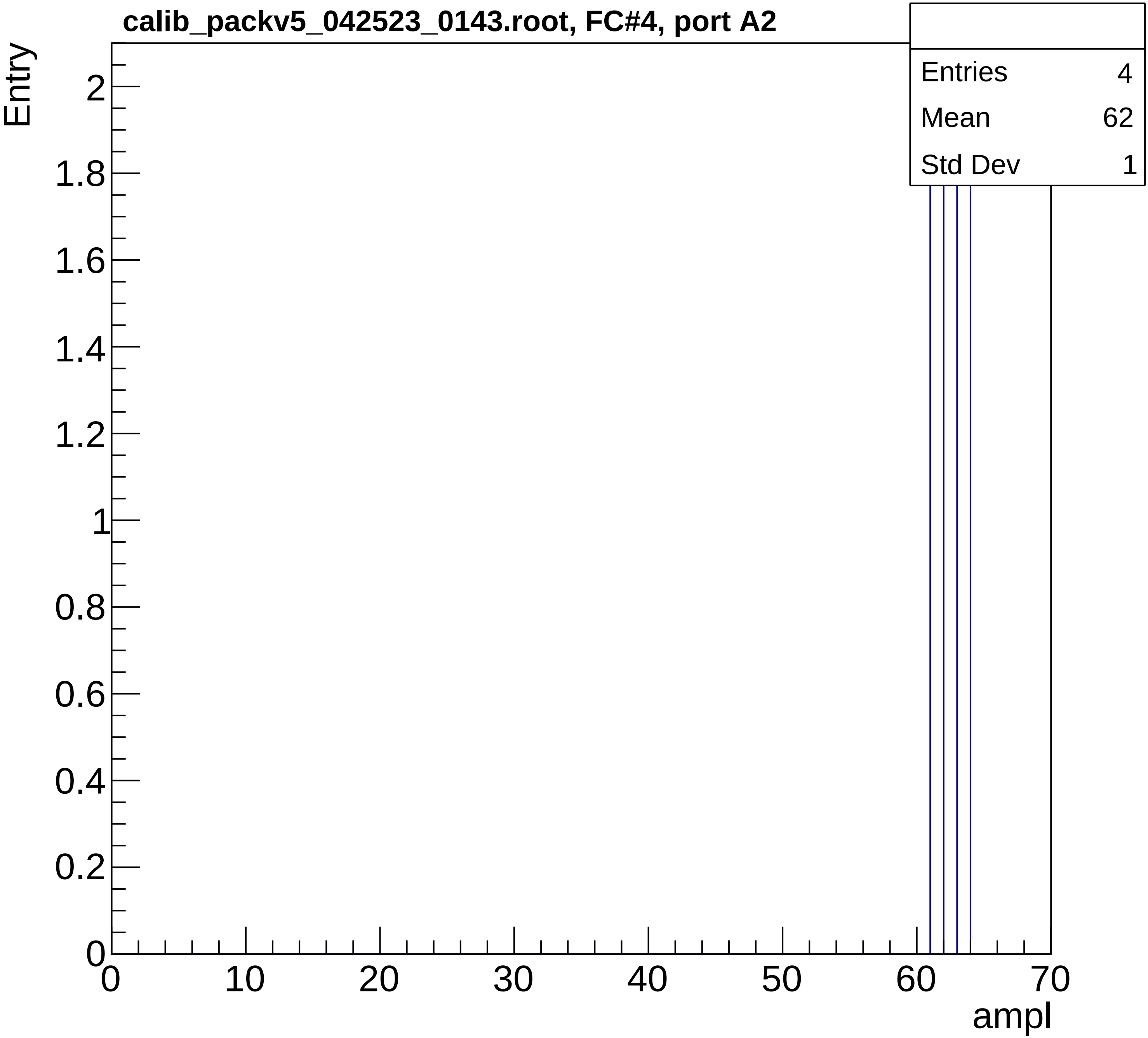
B1L100S, U21-ch69, adc4

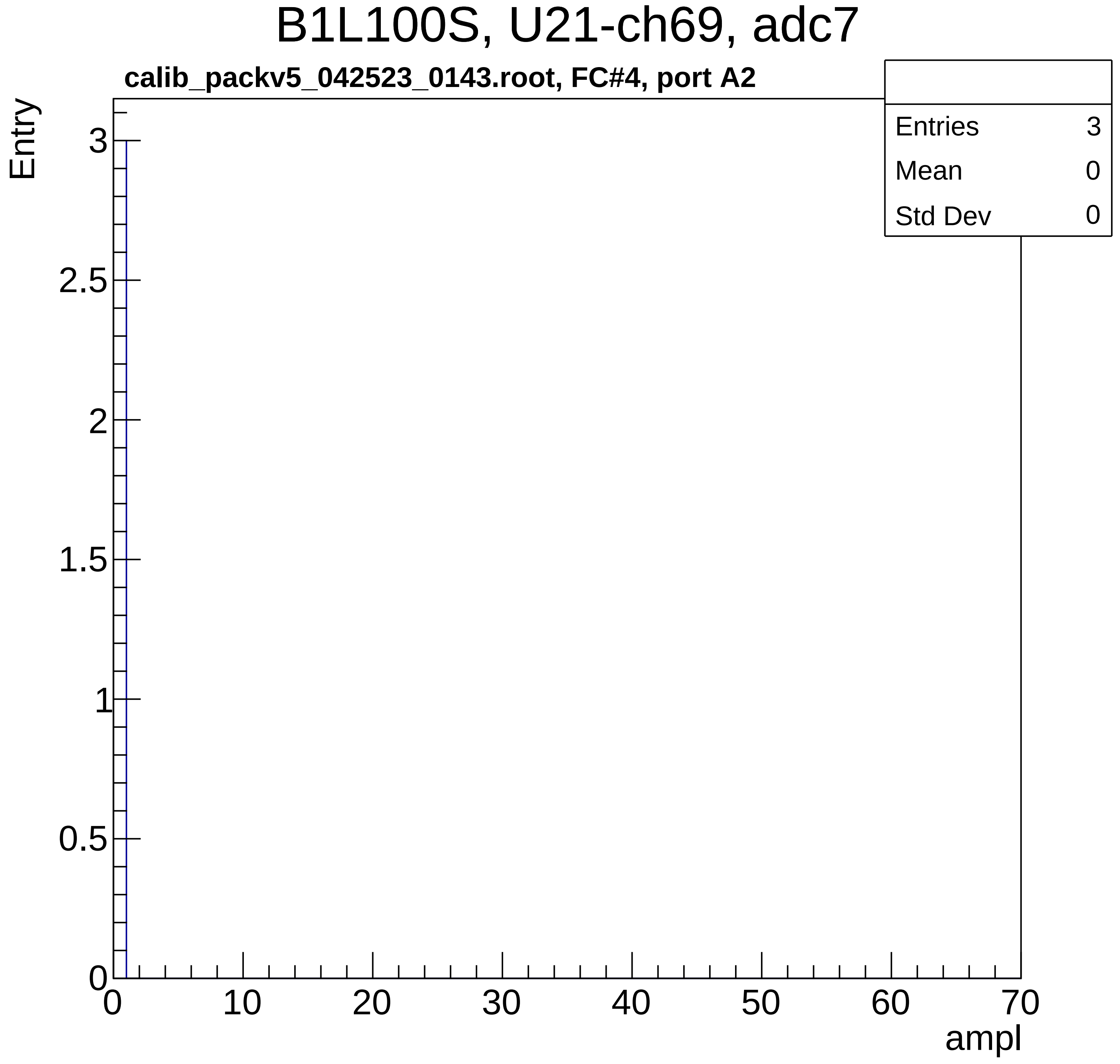
calib_packv5_042523_0143.root, FC#4, port A2

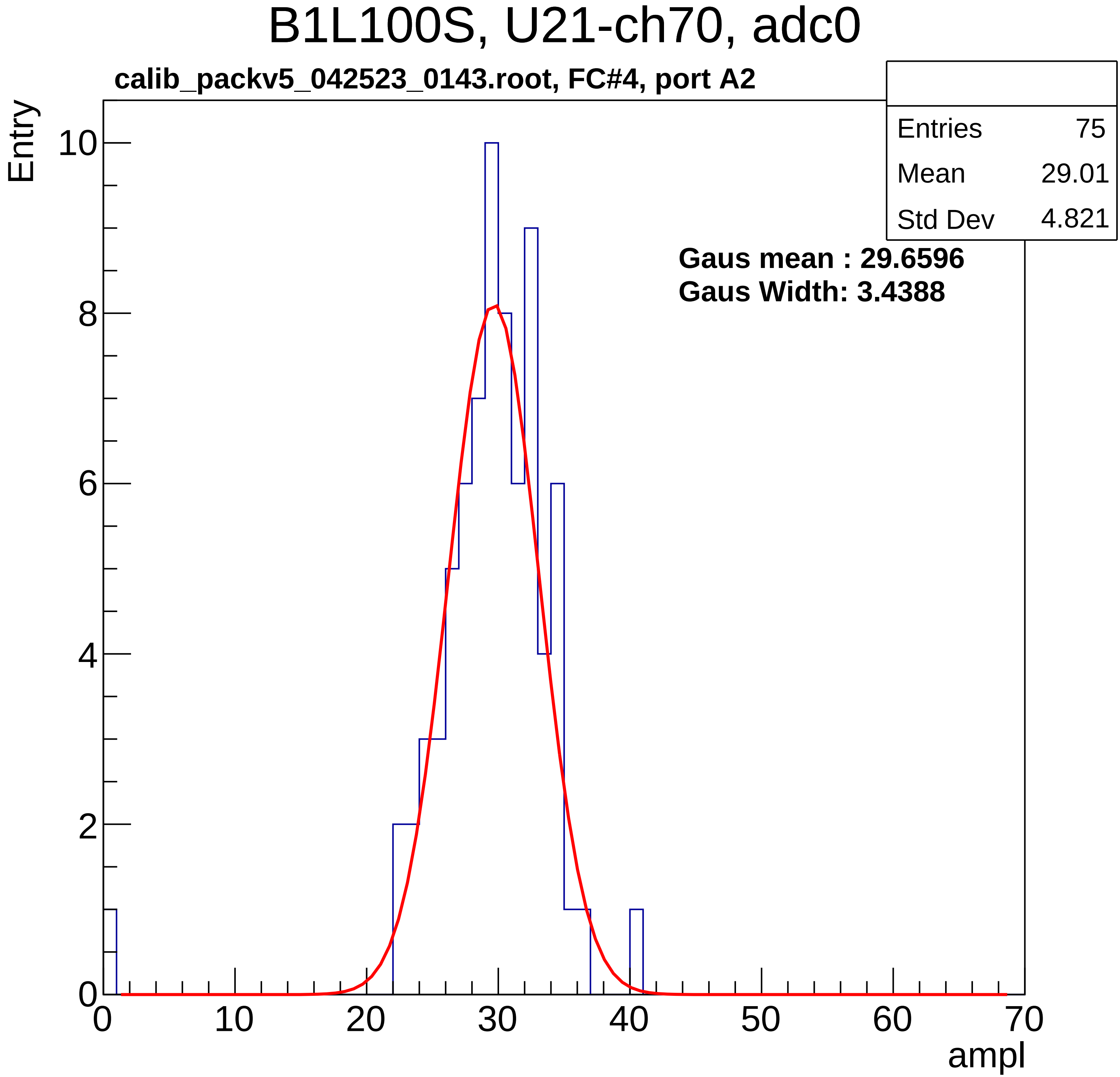


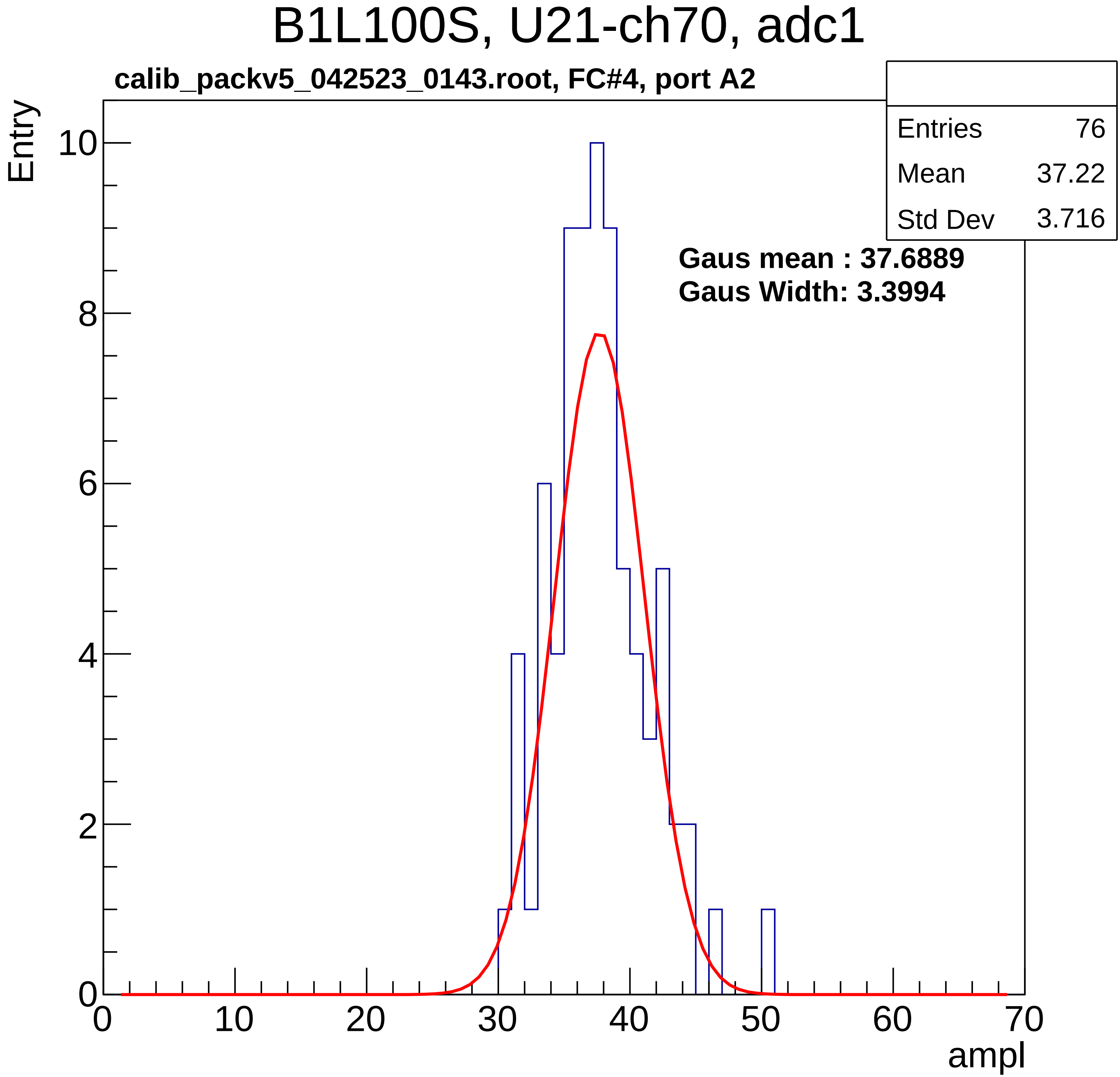

B1L100S, U21-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.97
Std Dev 10.91
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

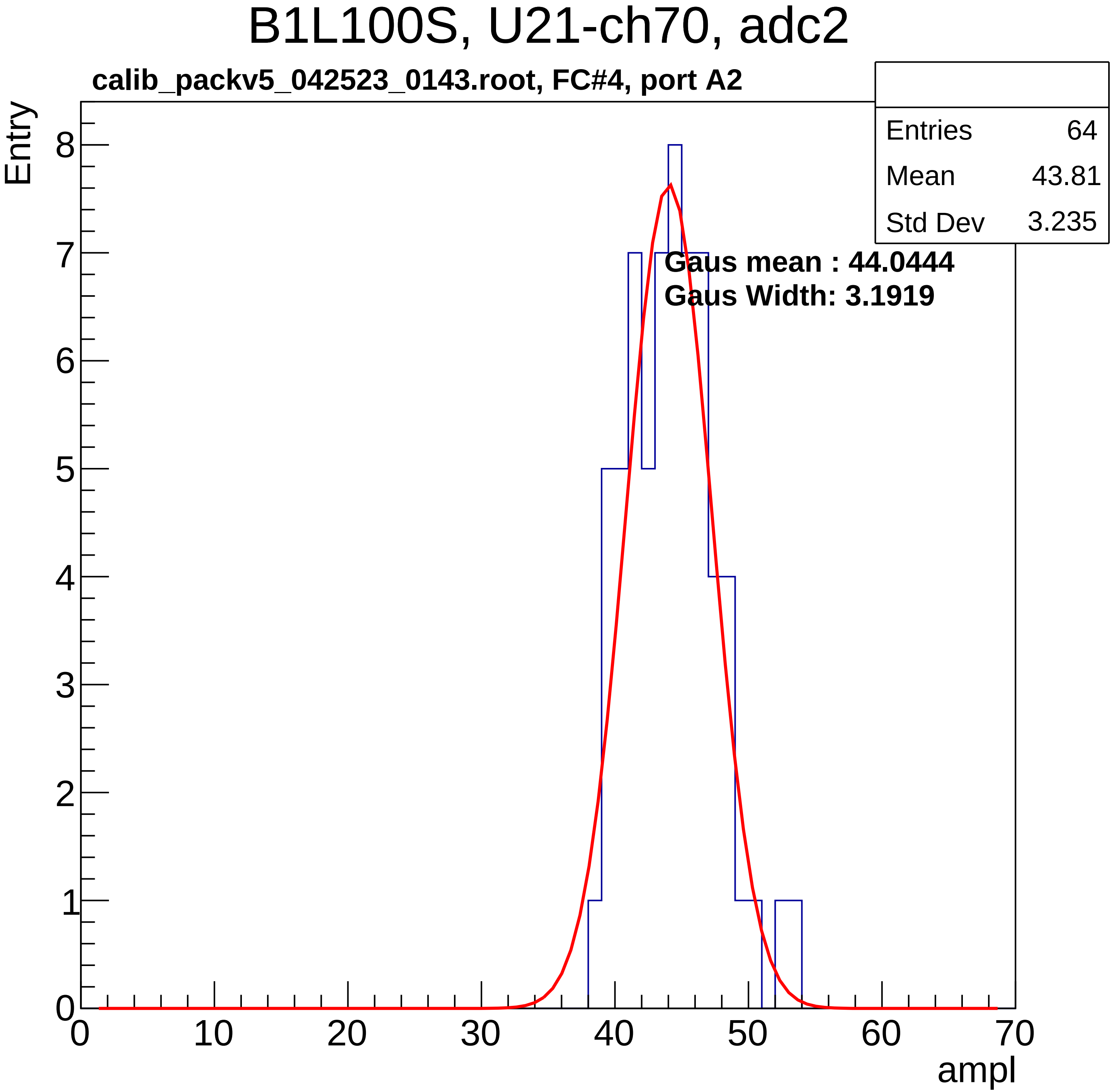
B1L100S, U21-ch69, adc6




B1L100S, U21-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

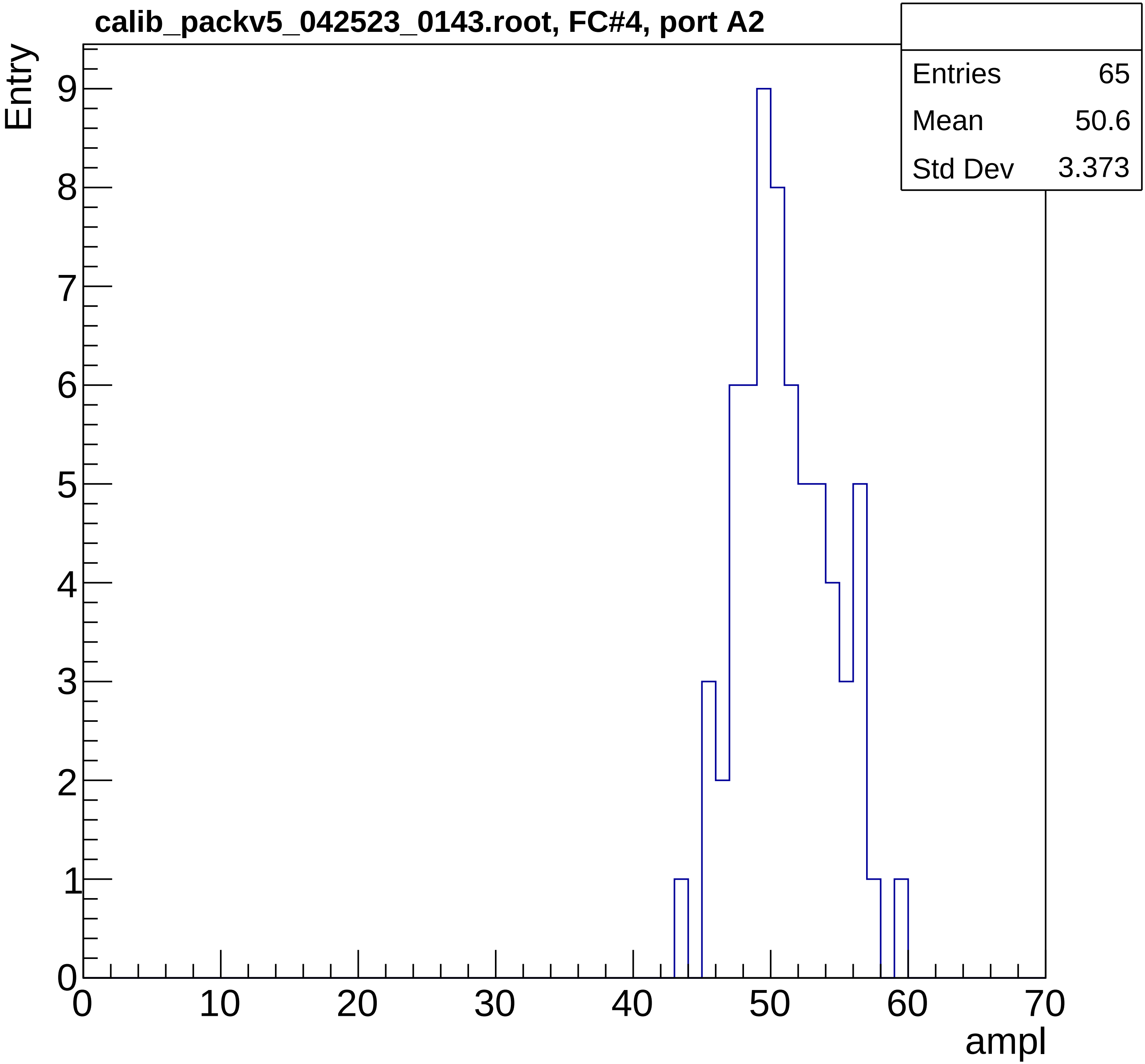

B1L100S, U21-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.01
Std Dev 4.821
Gaus mean : 29.6596
Gaus Width: 3.4388
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.22
Std Dev 3.716
Gaus mean : 37.6889
Gaus Width: 3.3994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

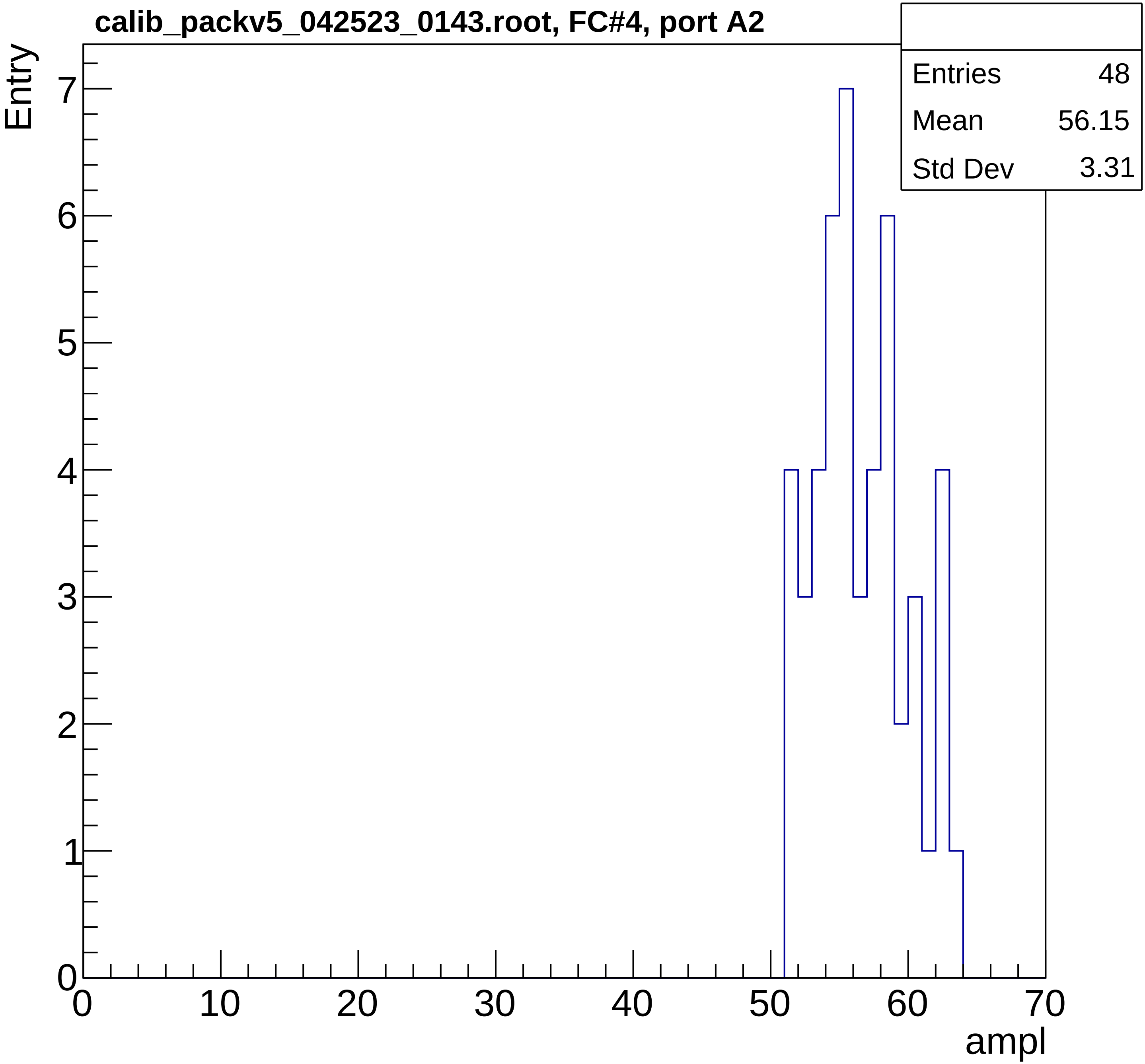

B1L100S, U21-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 64
Mean 43.81
Std Dev 3.235
Gaus mean : 44.0444
Gaus Width: 3.1919
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

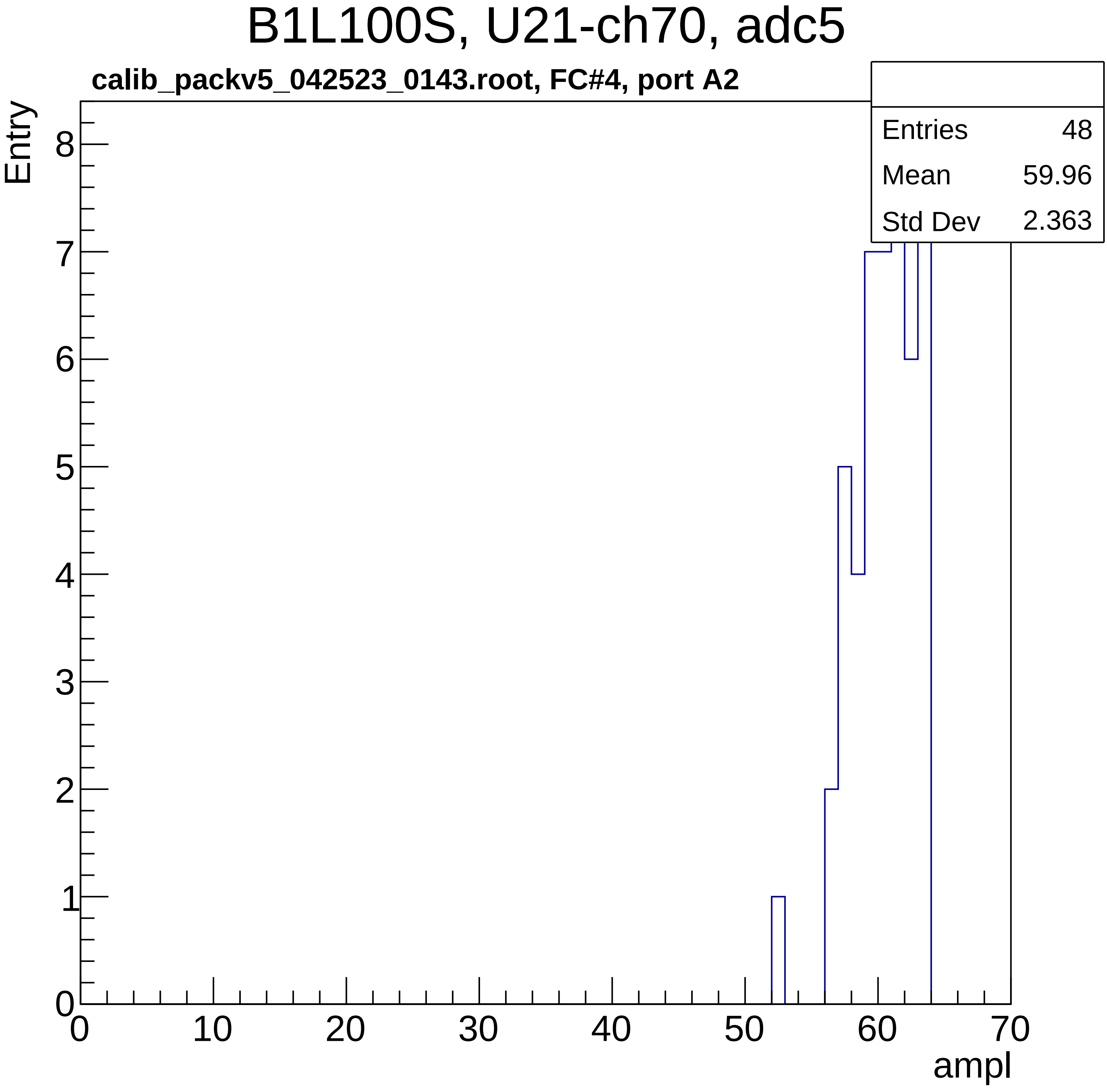
B1L100S, U21-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

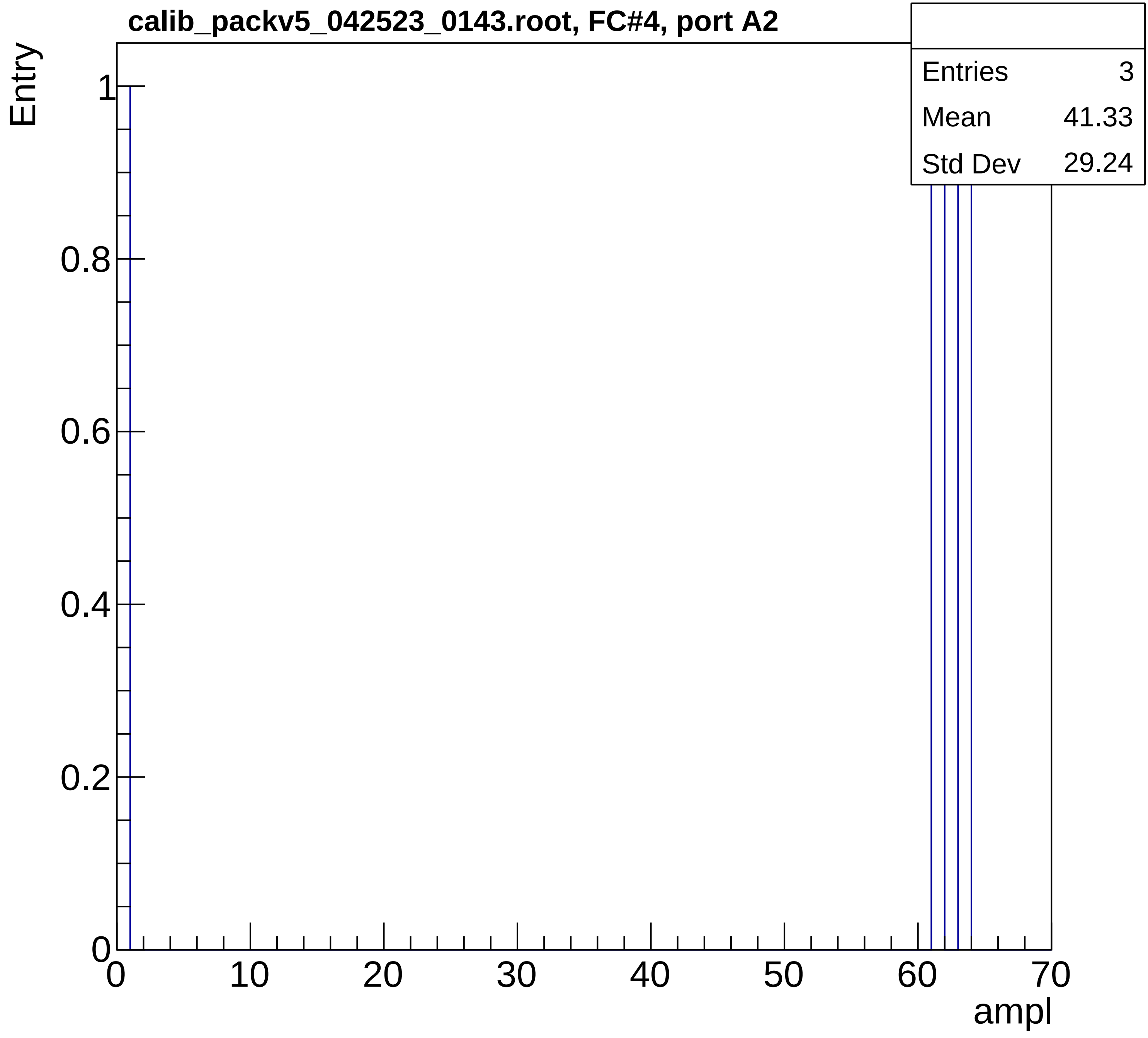


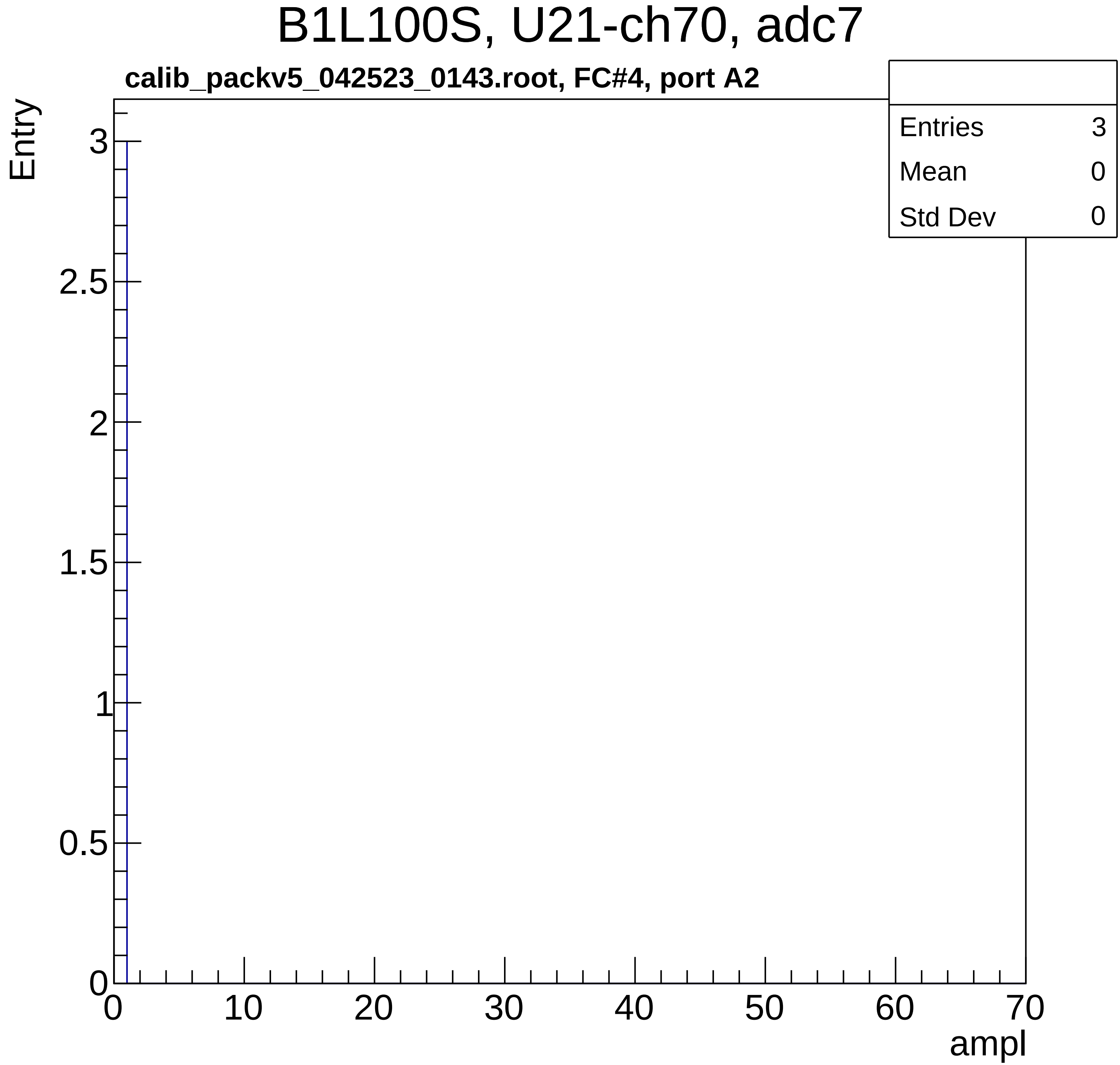
B1L100S, U21-ch70, adc4

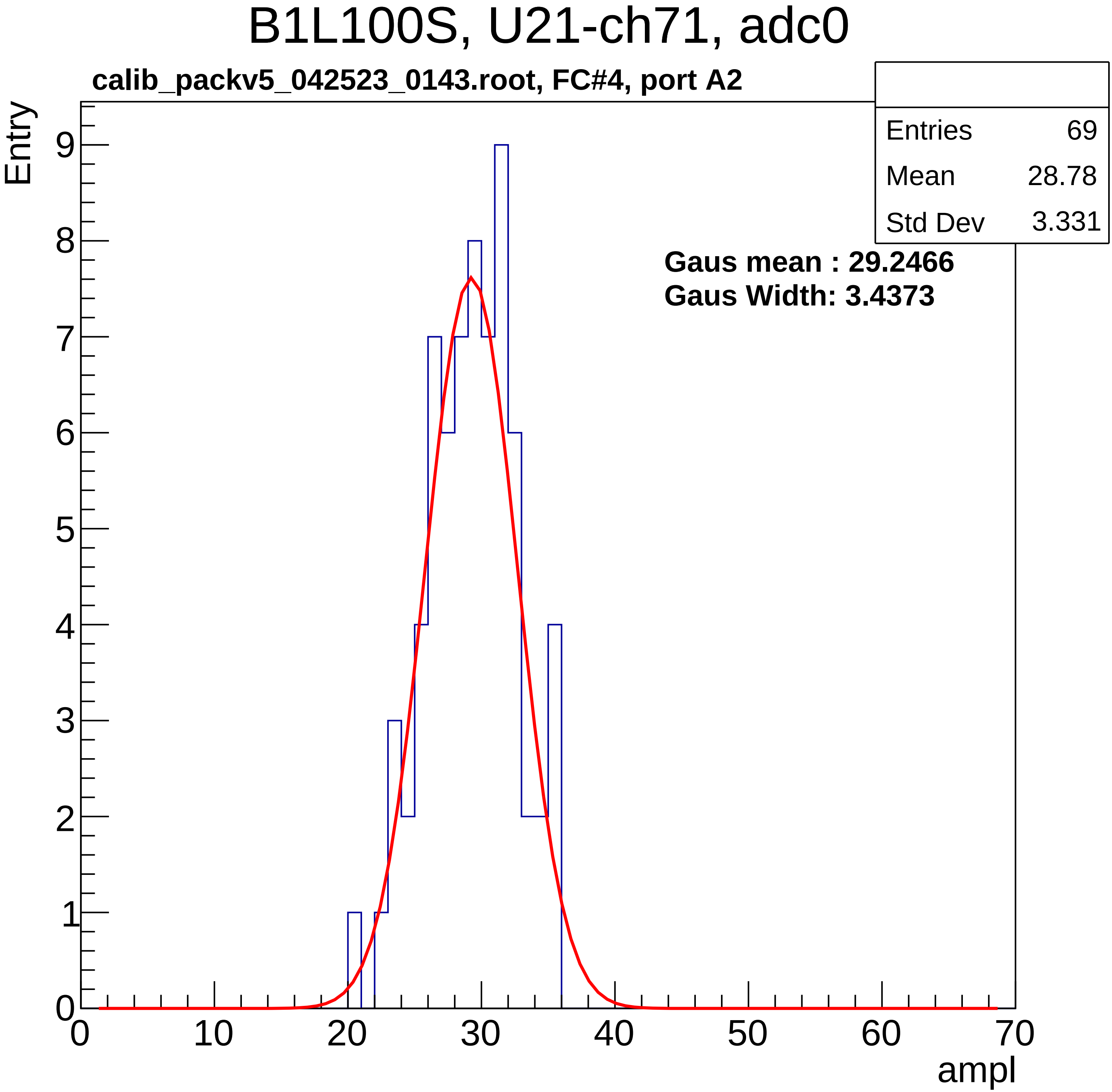


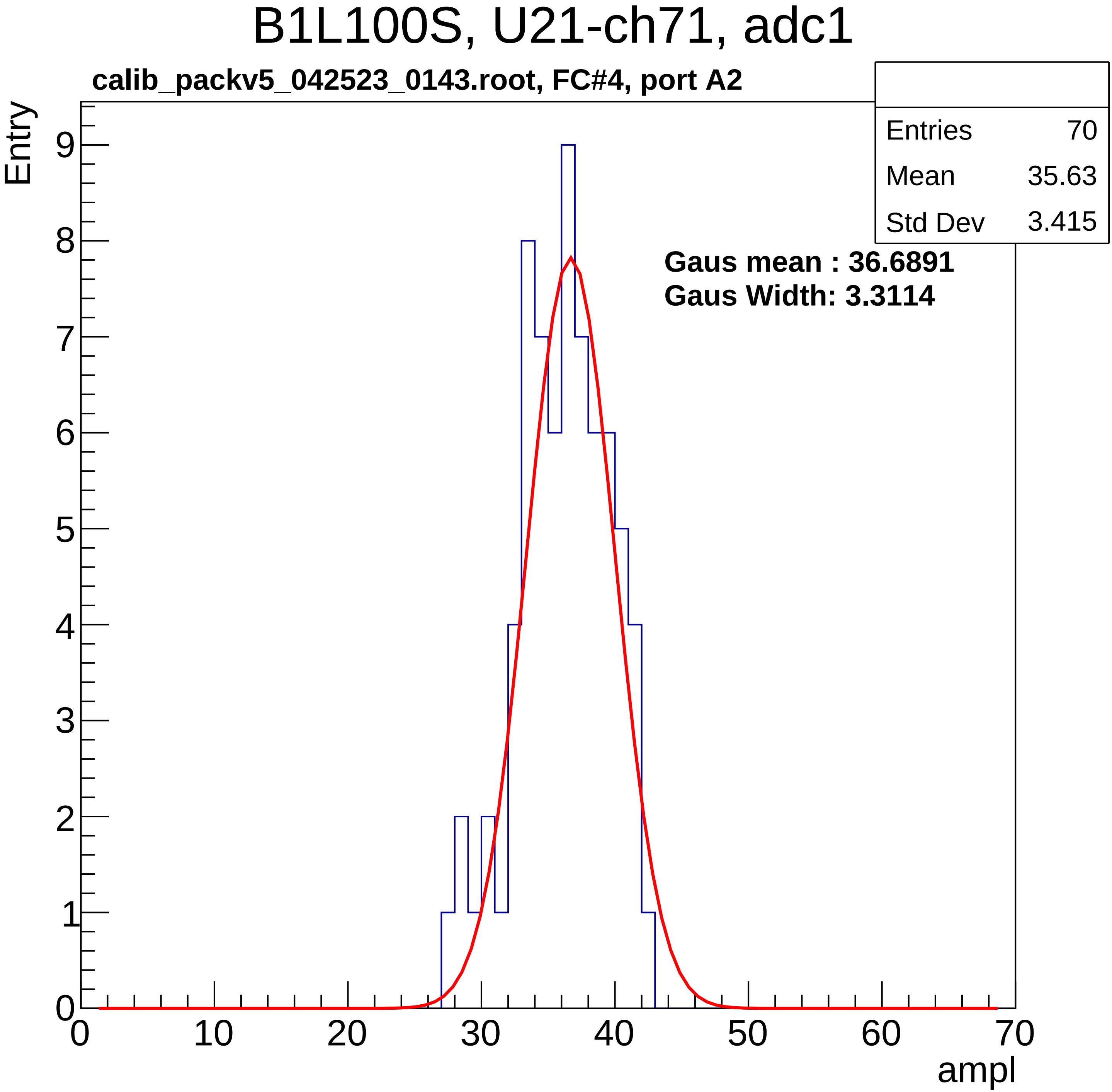

B1L100S, U21-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.96
Std Dev 2.363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

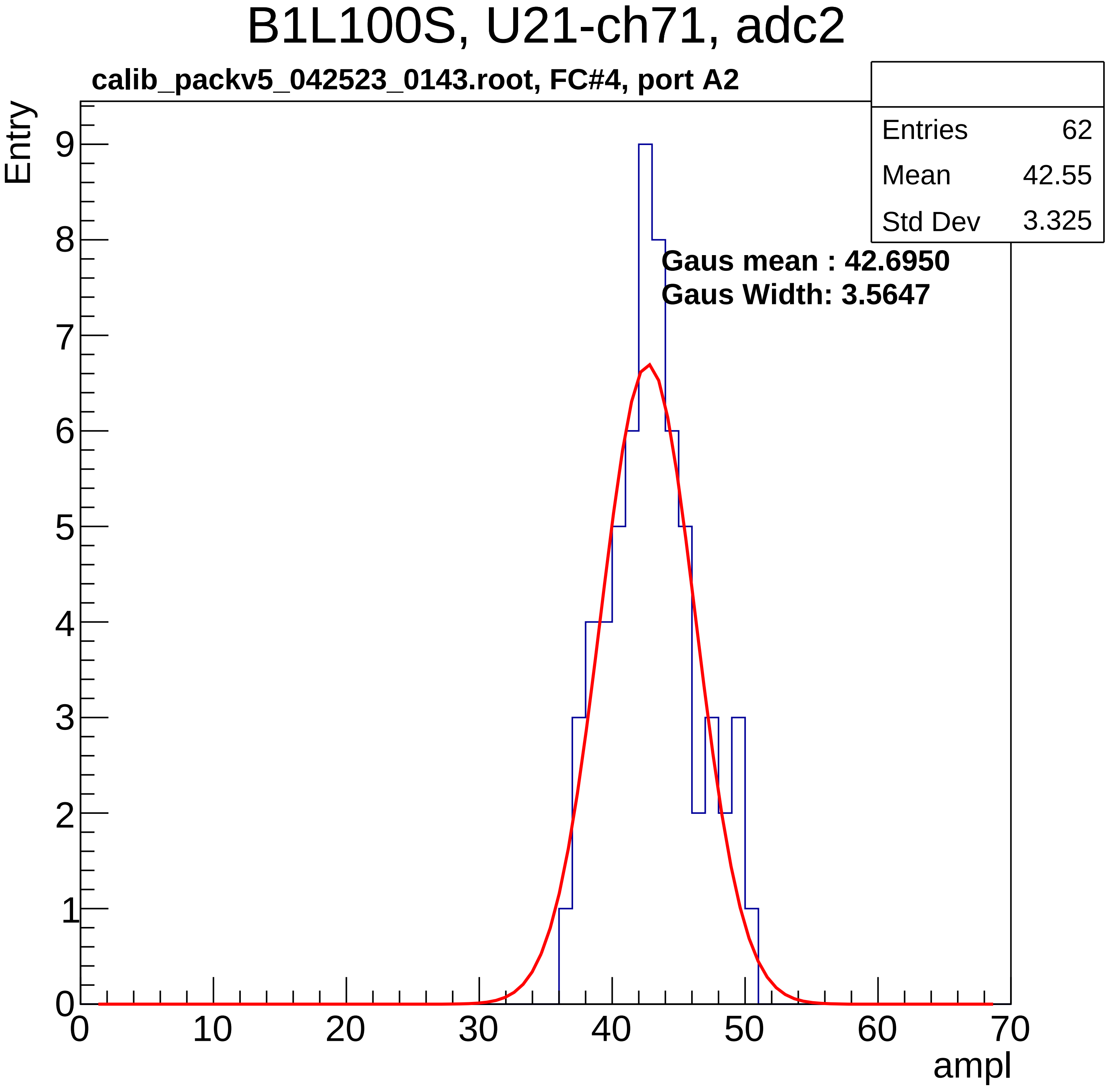
B1L100S, U21-ch70, adc6

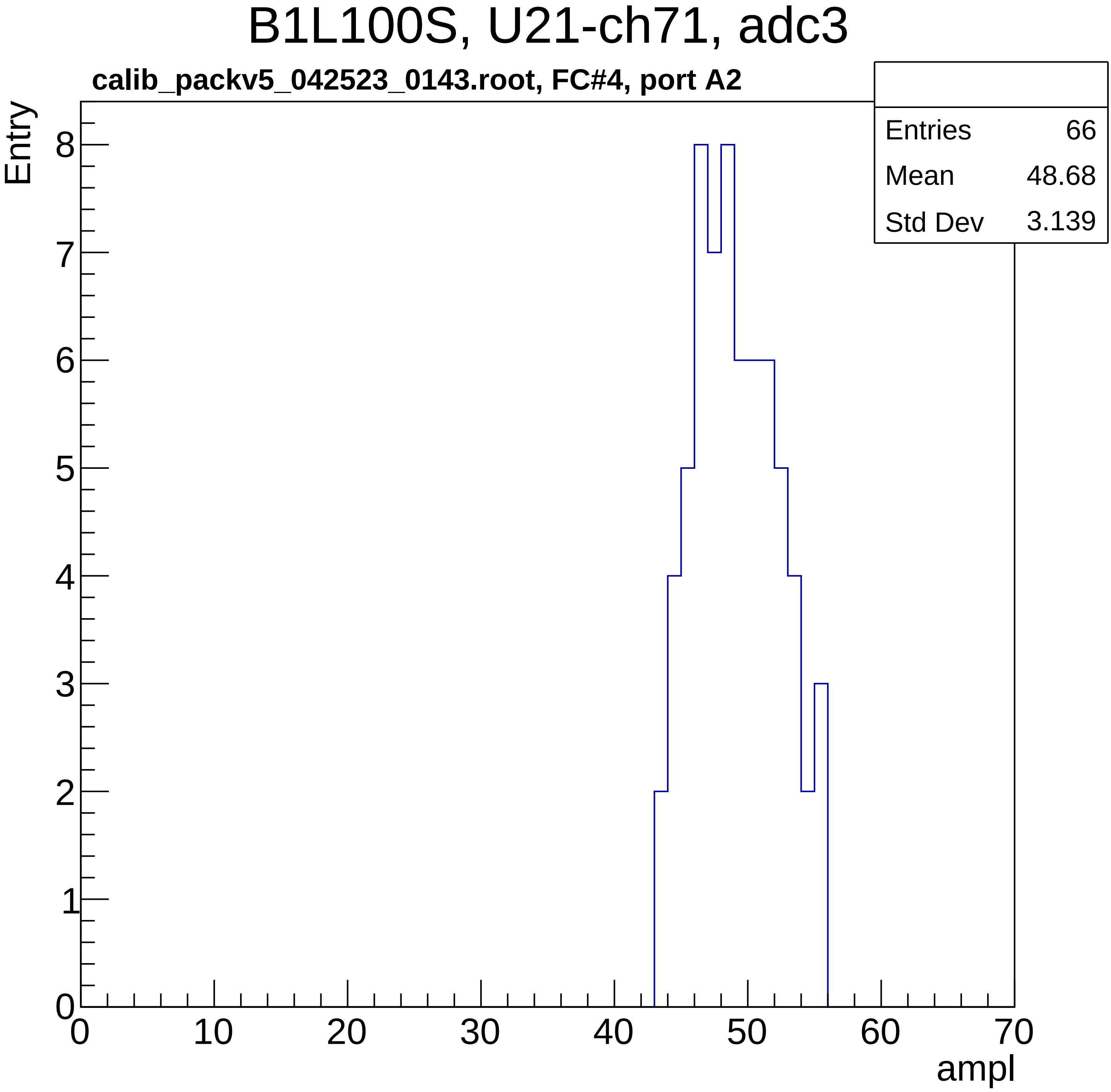



B1L100S, U21-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

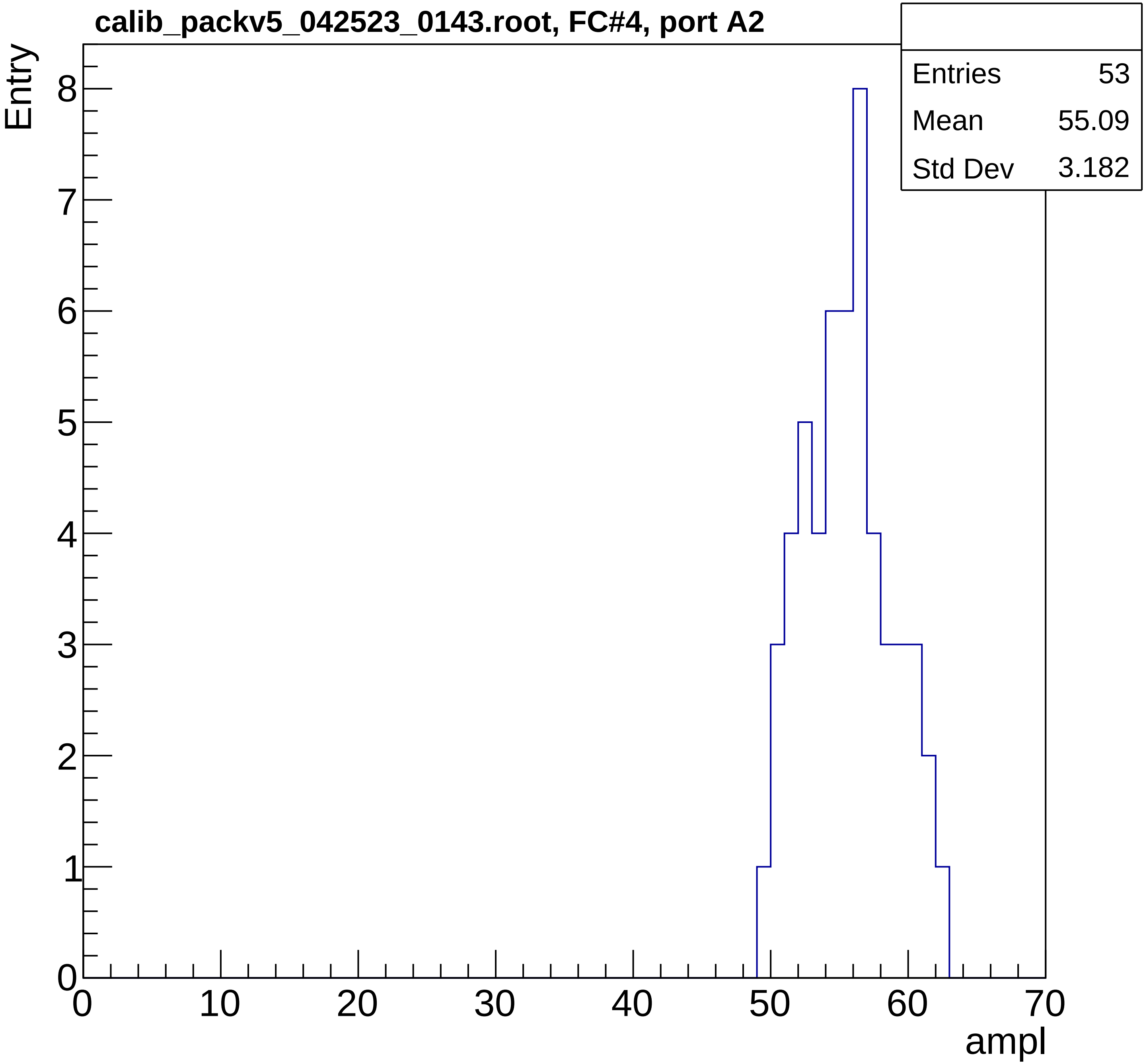

B1L100S, U21-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 69
Mean 28.78
Std Dev 3.331
Gaus mean : 29.2466
Gaus Width: 3.4373
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

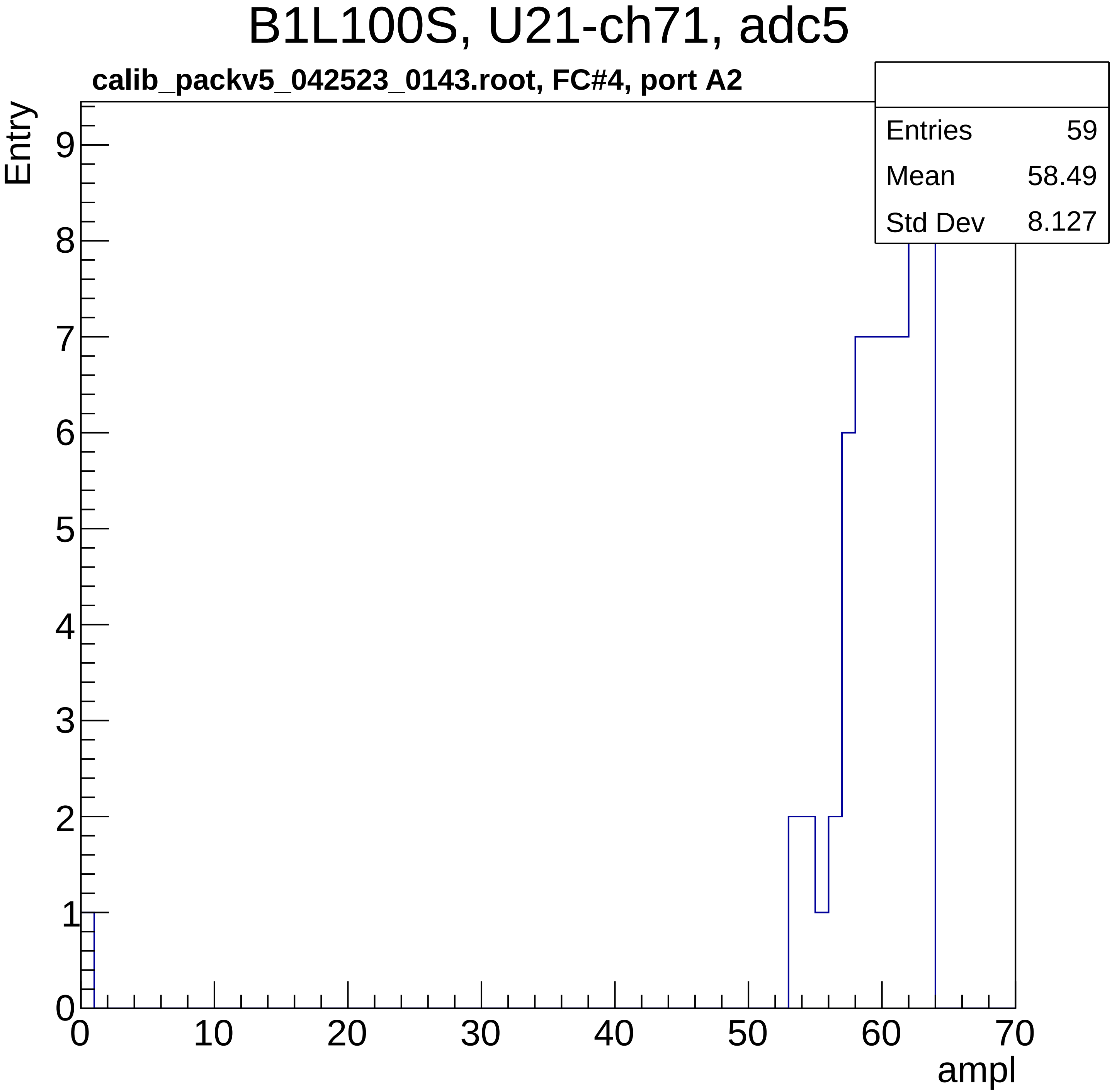

B1L100S, U21-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.63
Std Dev 3.415
Gaus mean : 36.6891
Gaus Width: 3.3114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

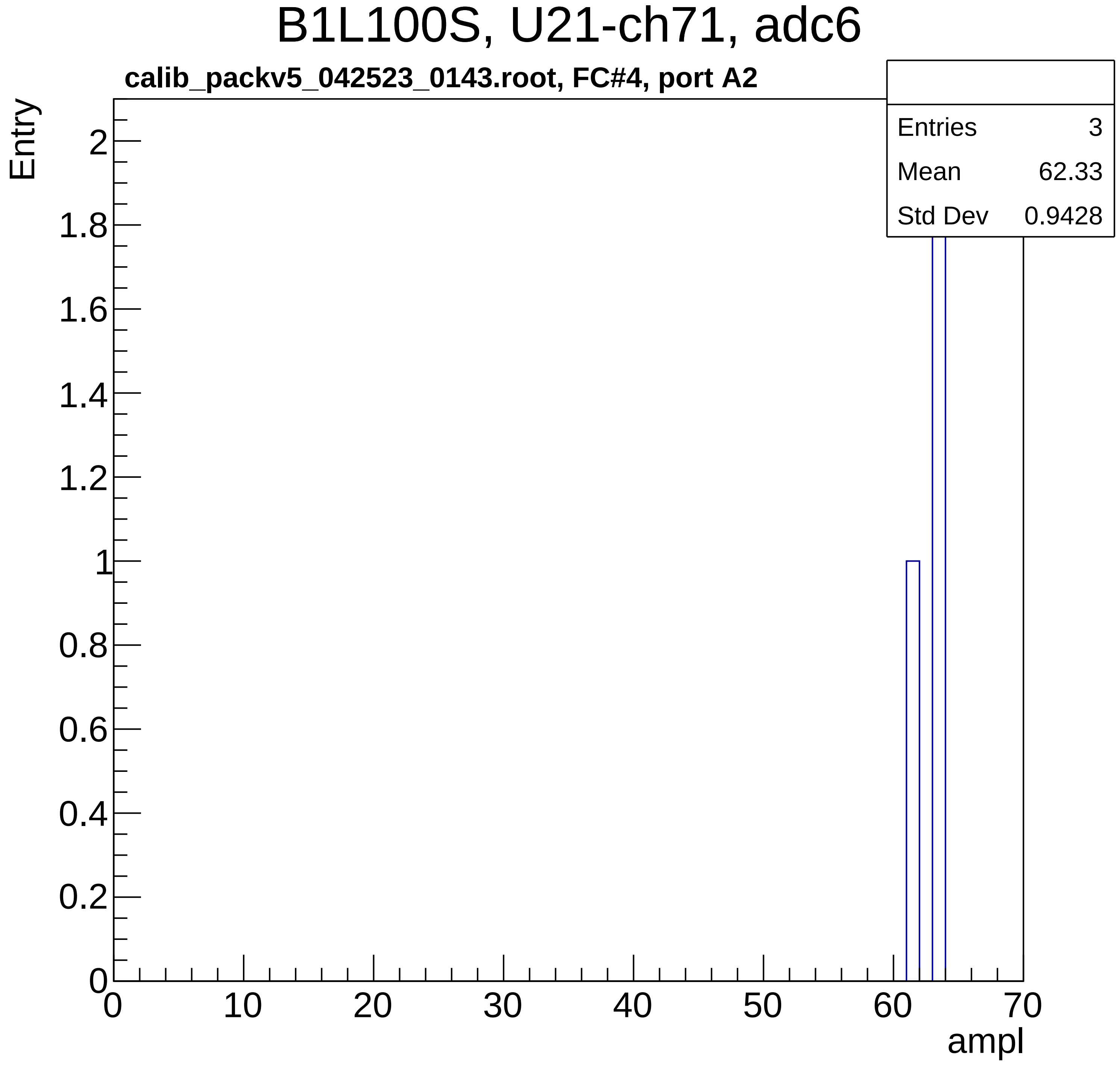

B1L100S, U21-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.55
Std Dev 3.325
Gaus mean : 42.6950
Gaus Width: 3.5647
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 48.68
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

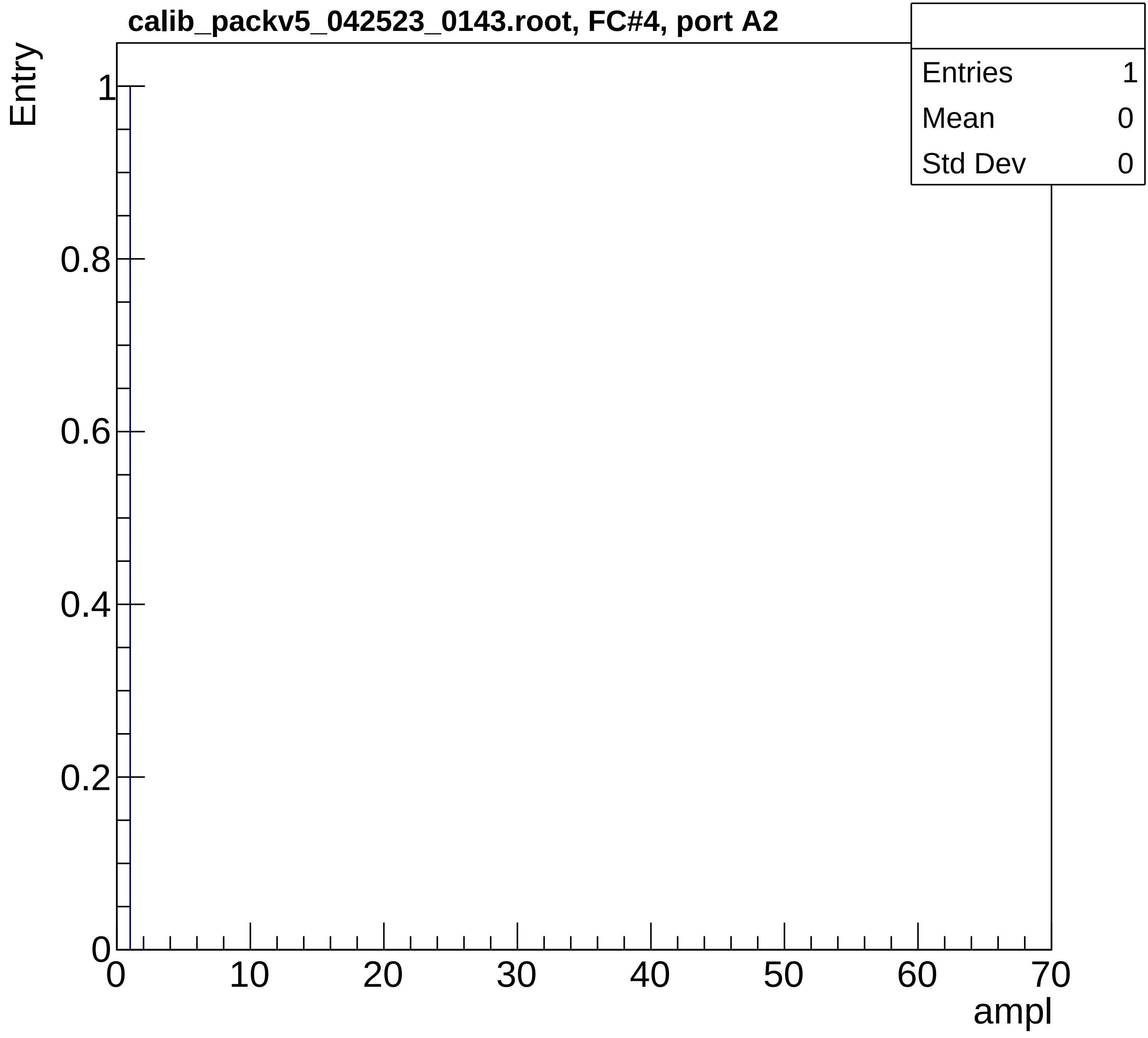
B1L100S, U21-ch71, adc4

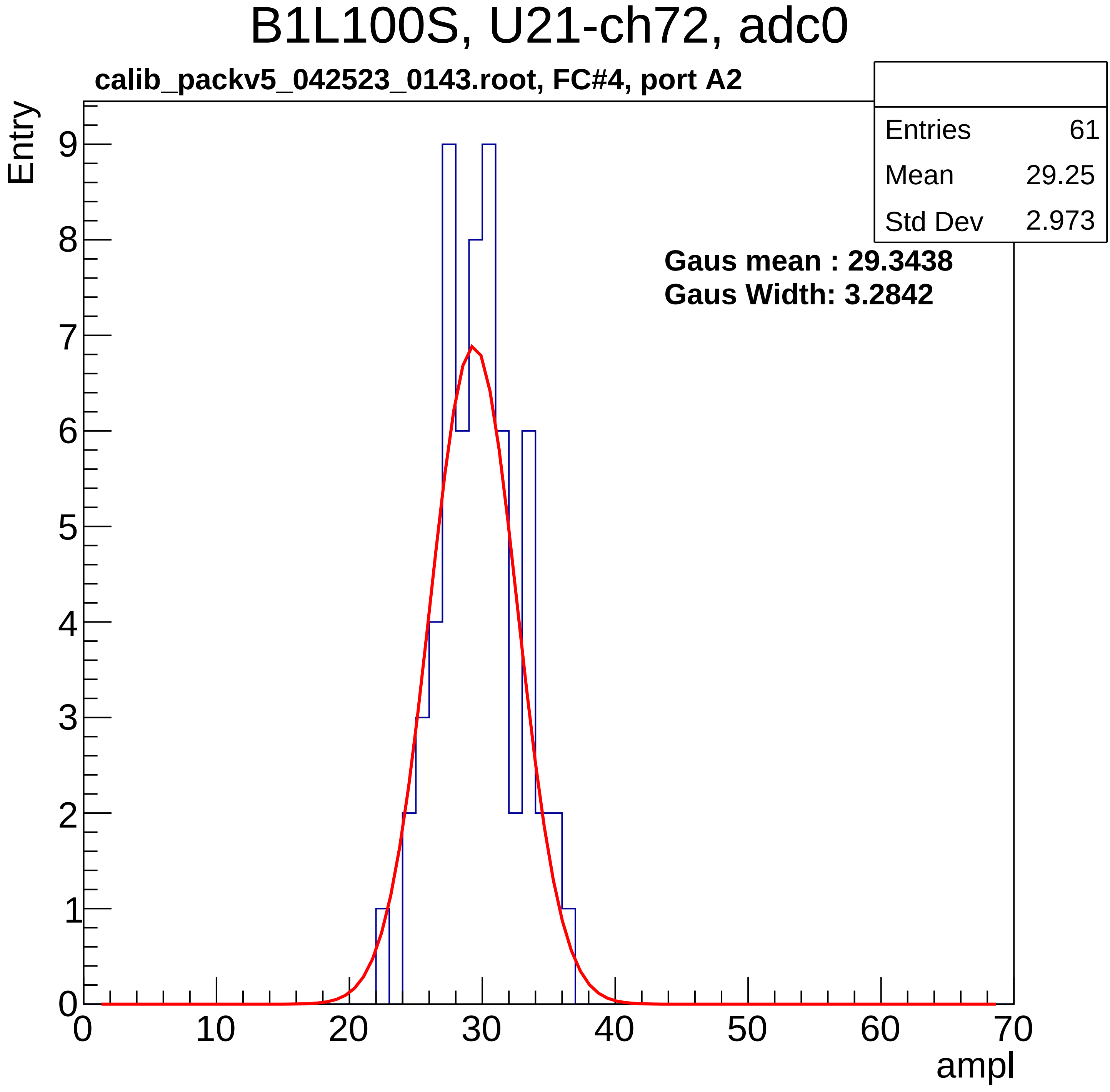


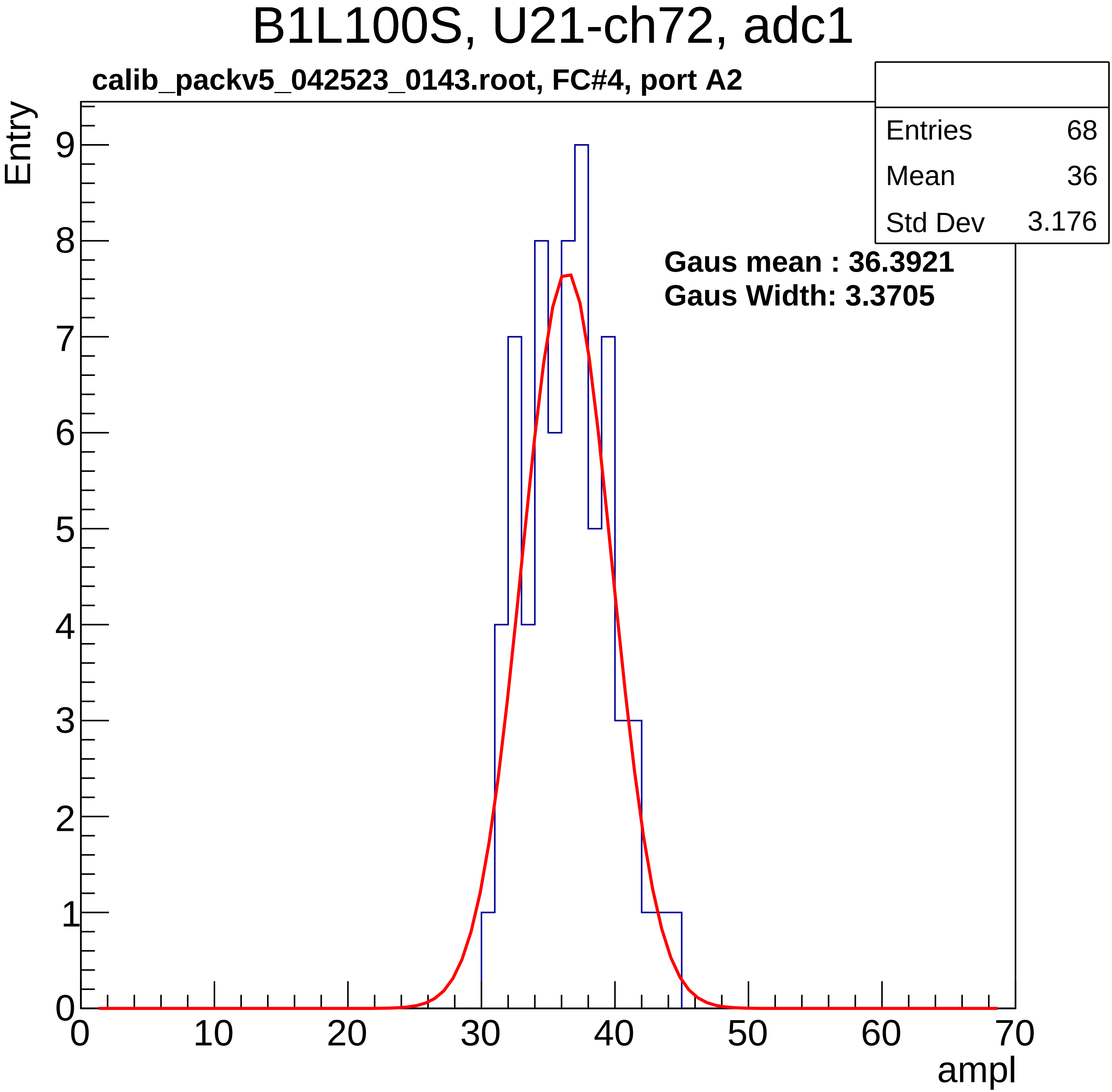

B1L100S, U21-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.49
Std Dev 8.127
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

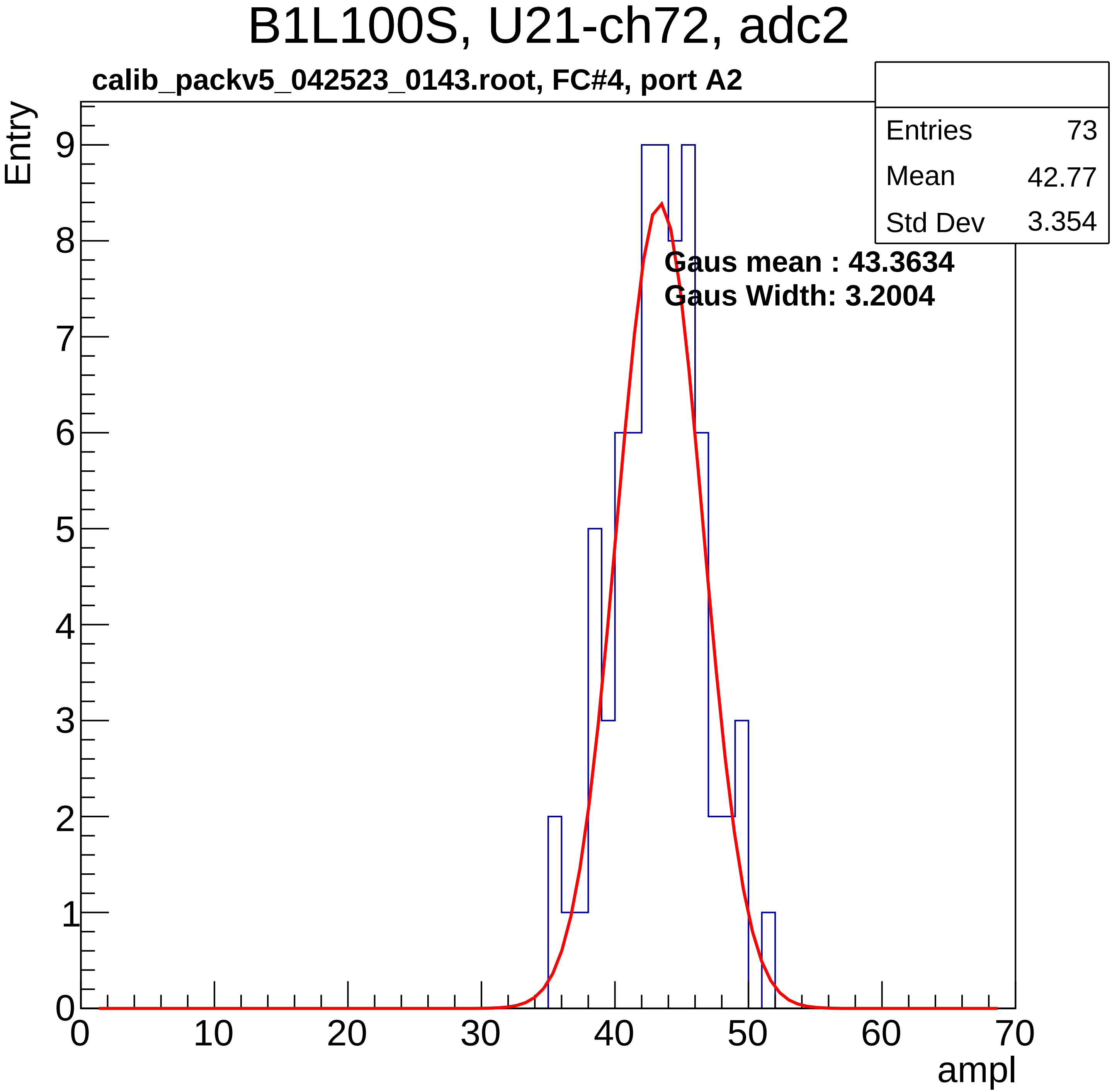

B1L100S, U21-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch71, adc7

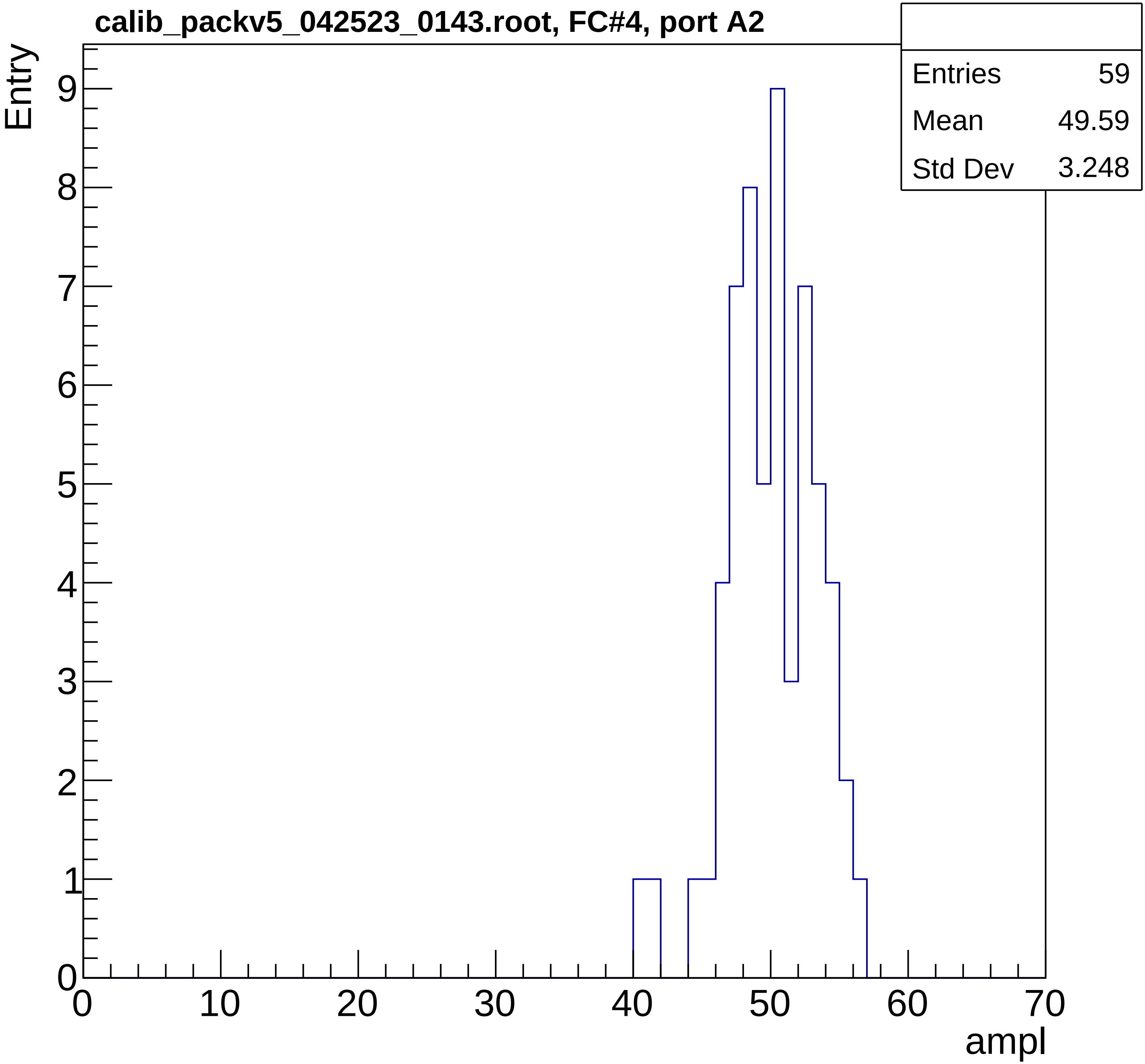


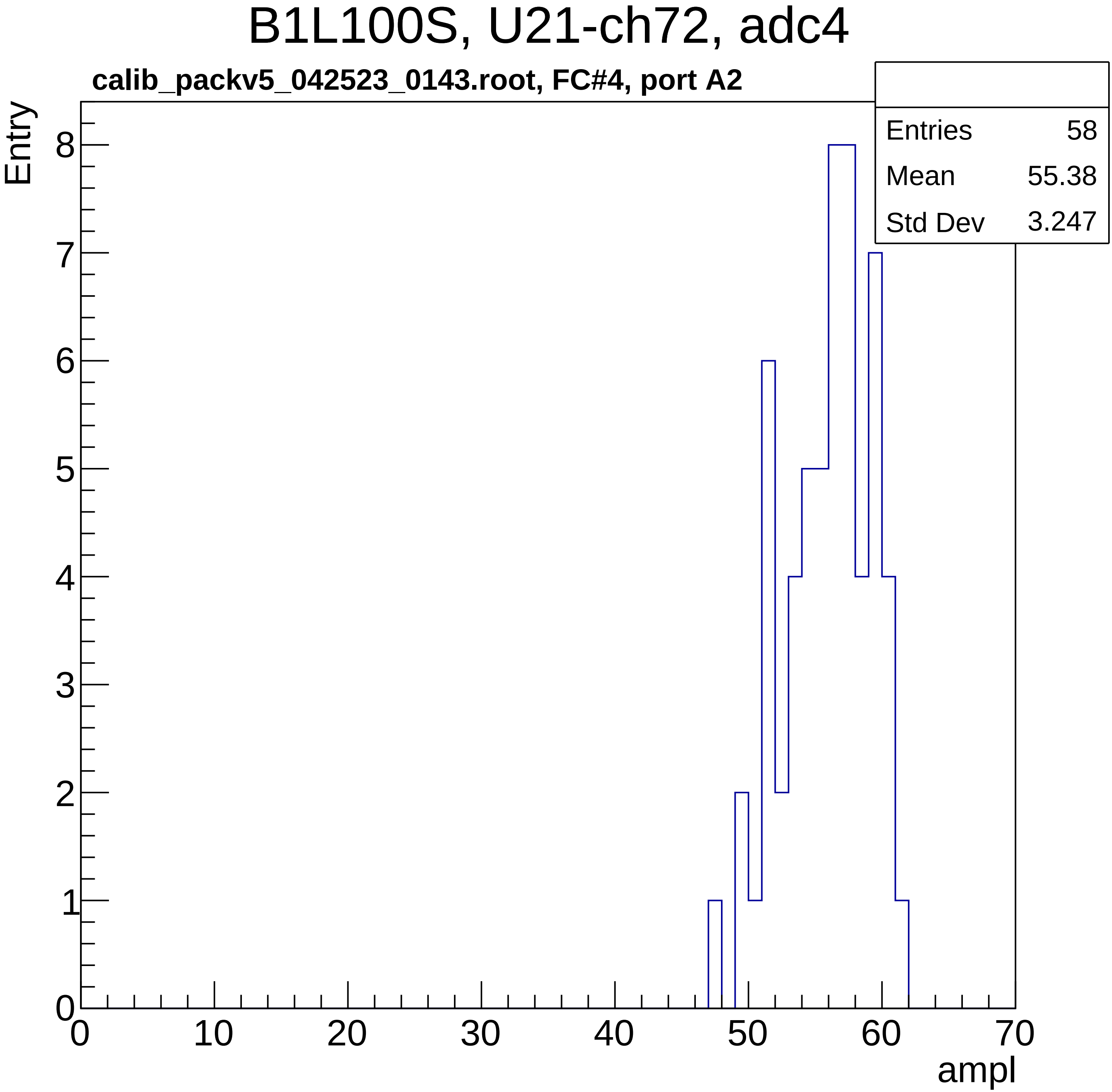

B1L100S, U21-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.25
Std Dev 2.973
Gaus mean : 29.3438
Gaus Width: 3.2842
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

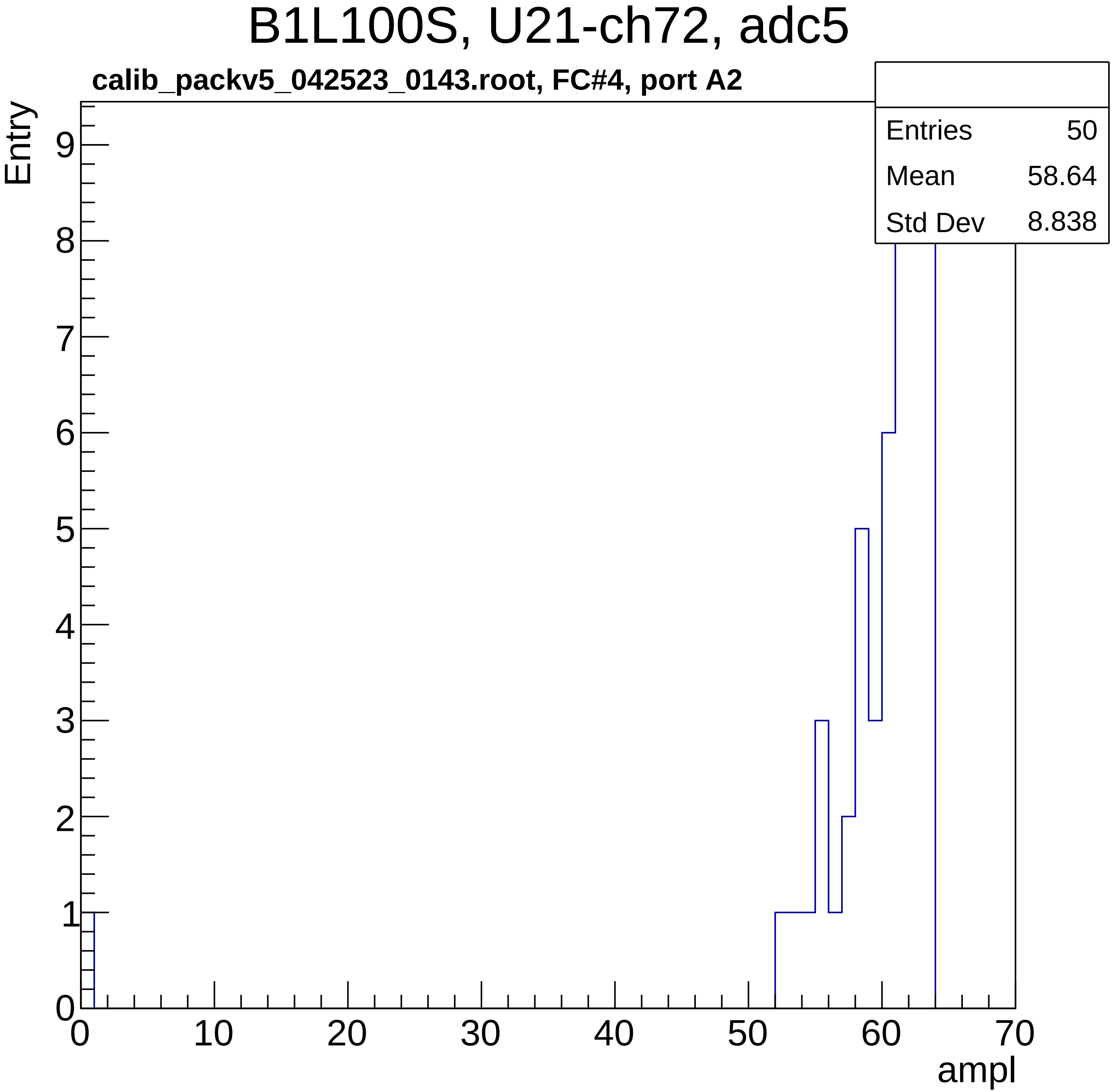

B1L100S, U21-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36
Std Dev 3.176
Gaus mean : 36.3921
Gaus Width: 3.3705
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 73
Mean 42.77
Std Dev 3.354
Gaus mean : 43.3634
Gaus Width: 3.2004
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

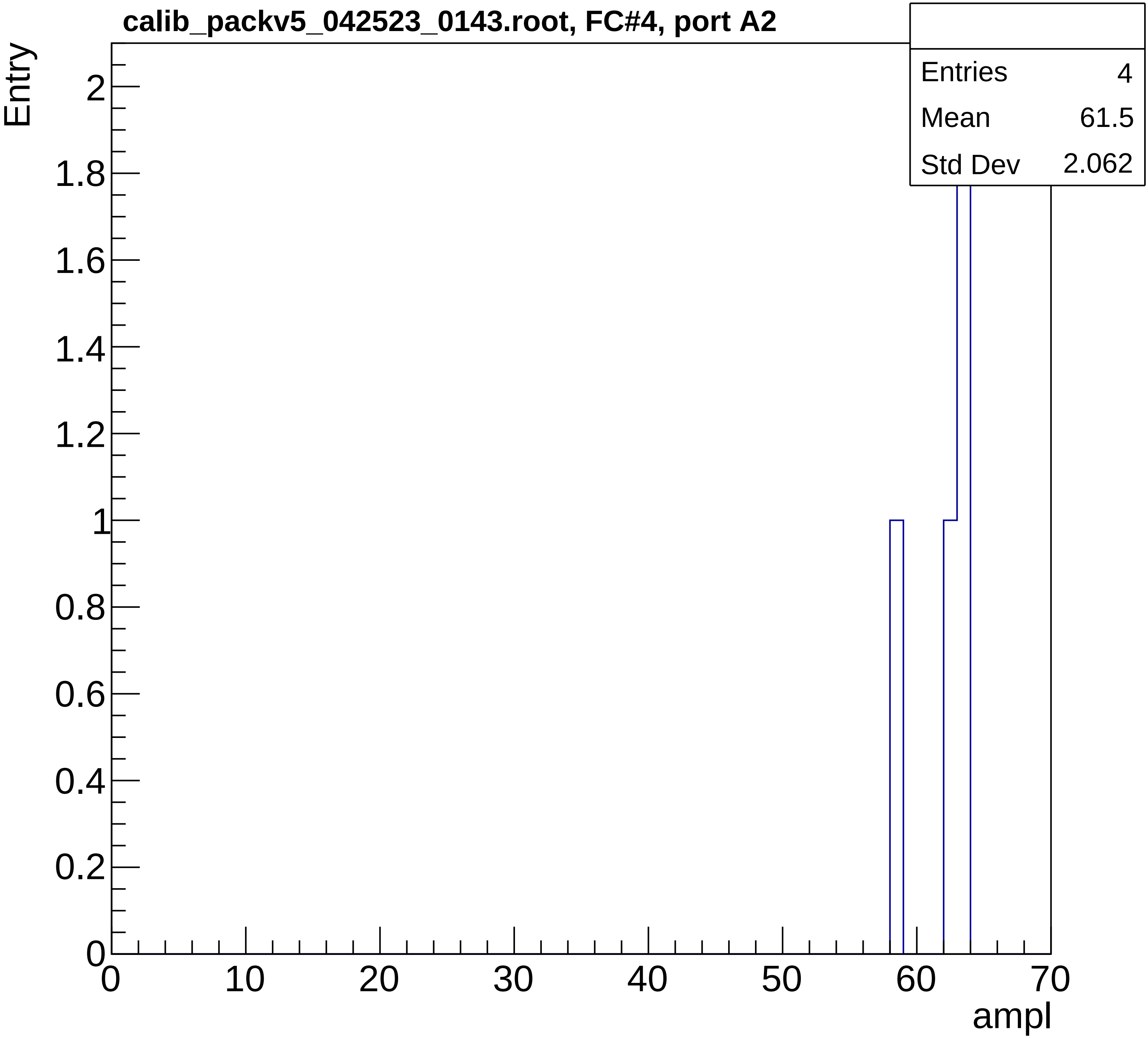
B1L100S, U21-ch72, adc3



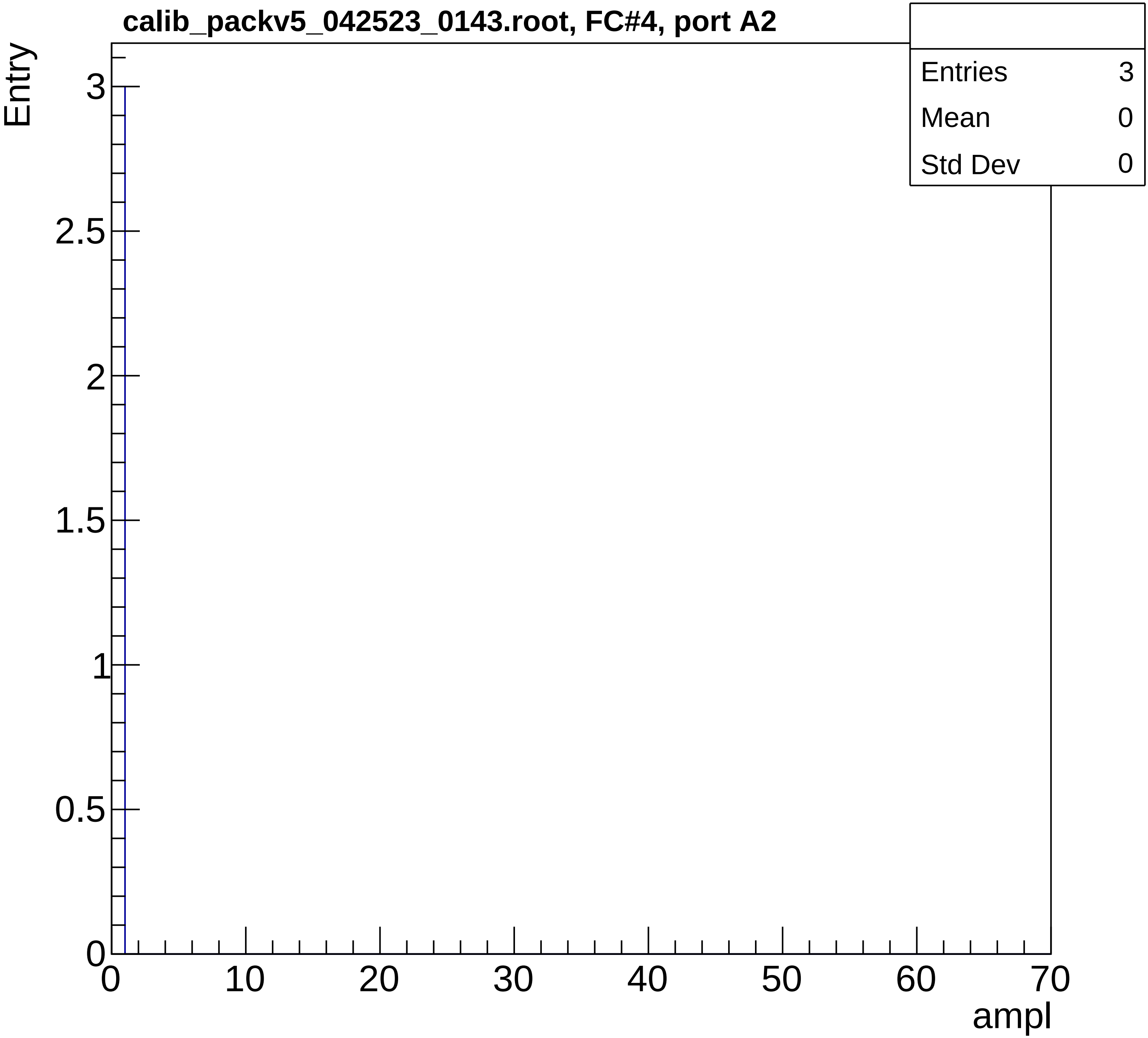

B1L100S, U21-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 58
Mean 55.38
Std Dev 3.247
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

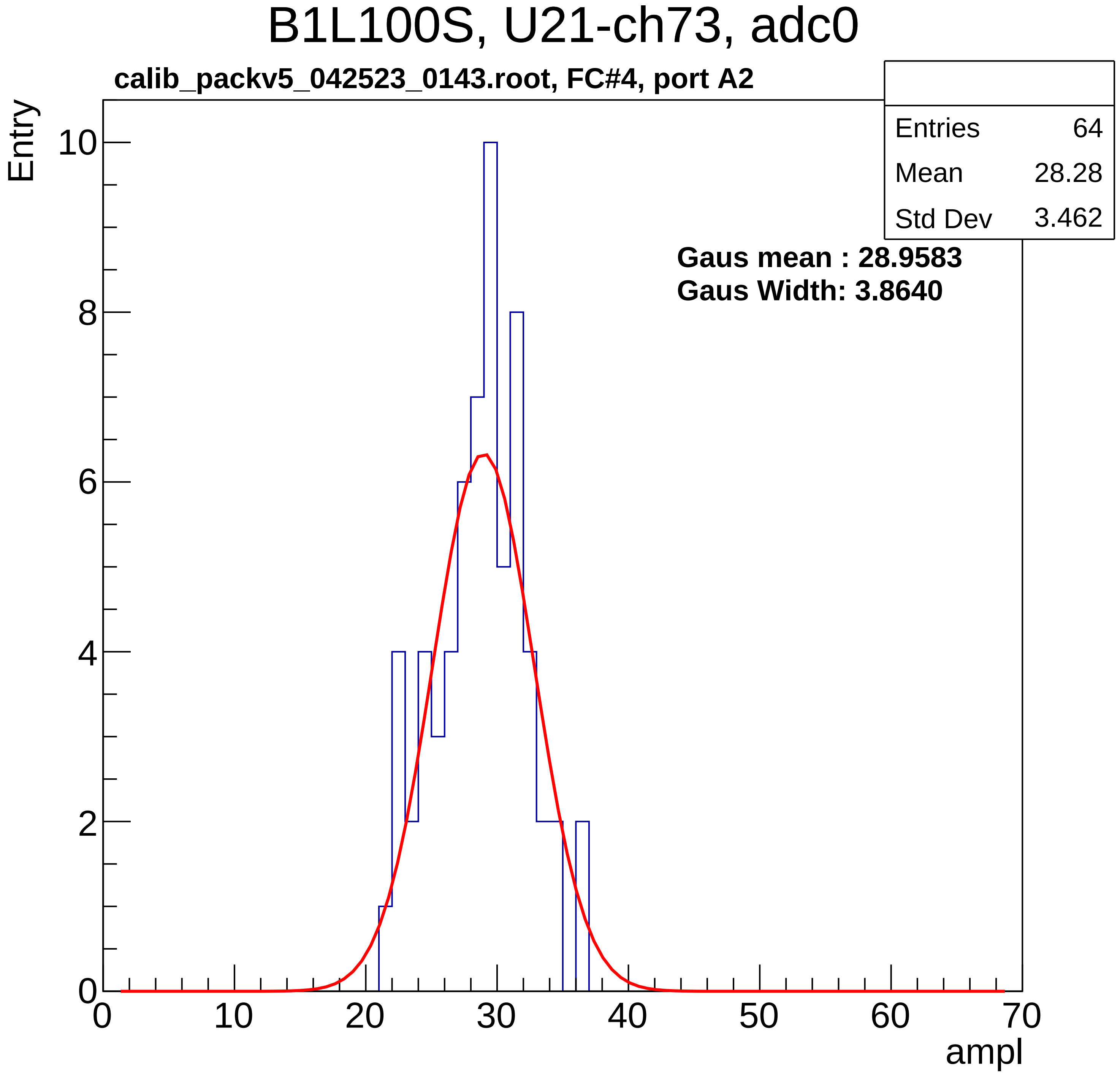

B1L100S, U21-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.64
Std Dev 8.838
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

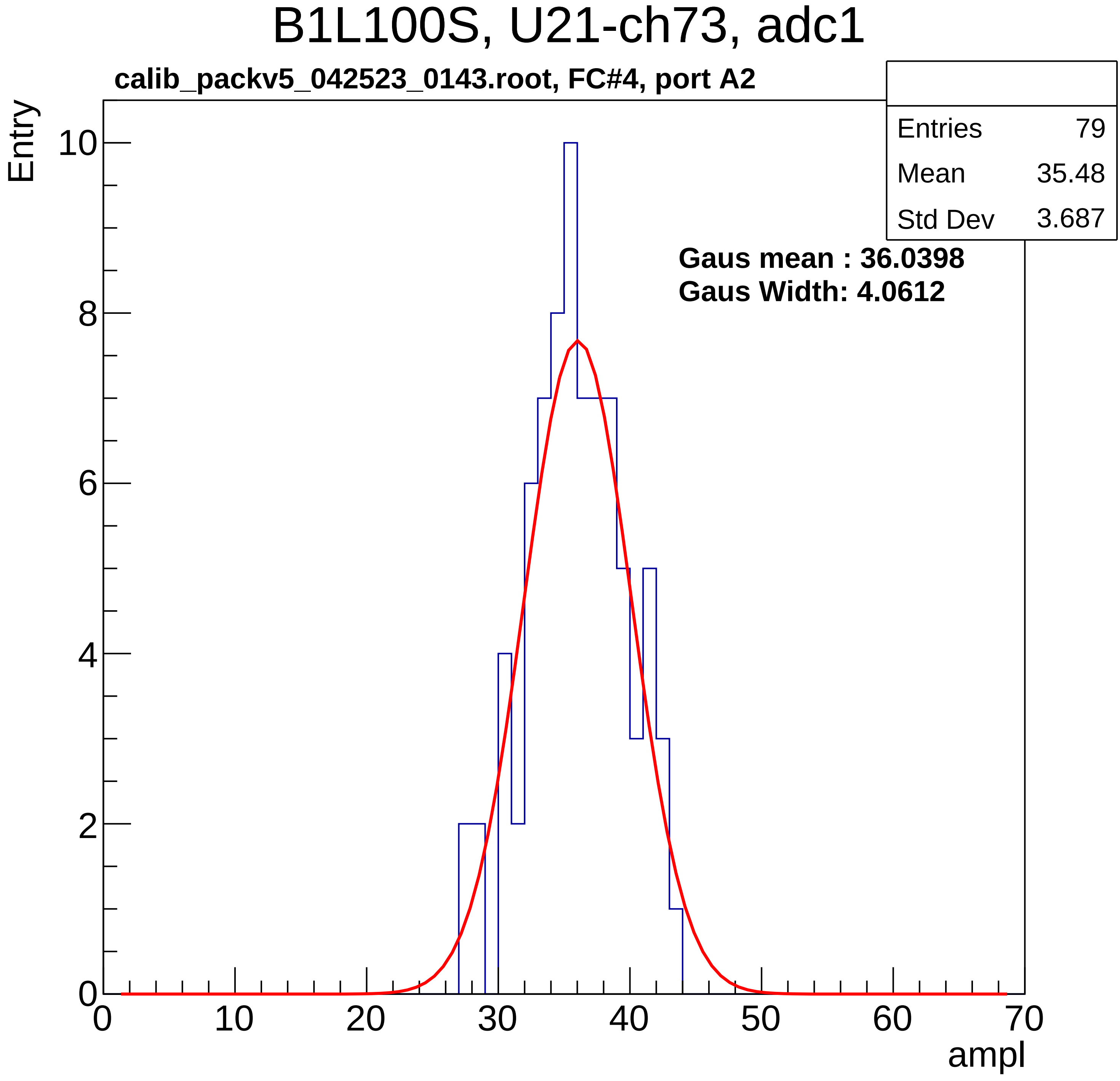
B1L100S, U21-ch72, adc6

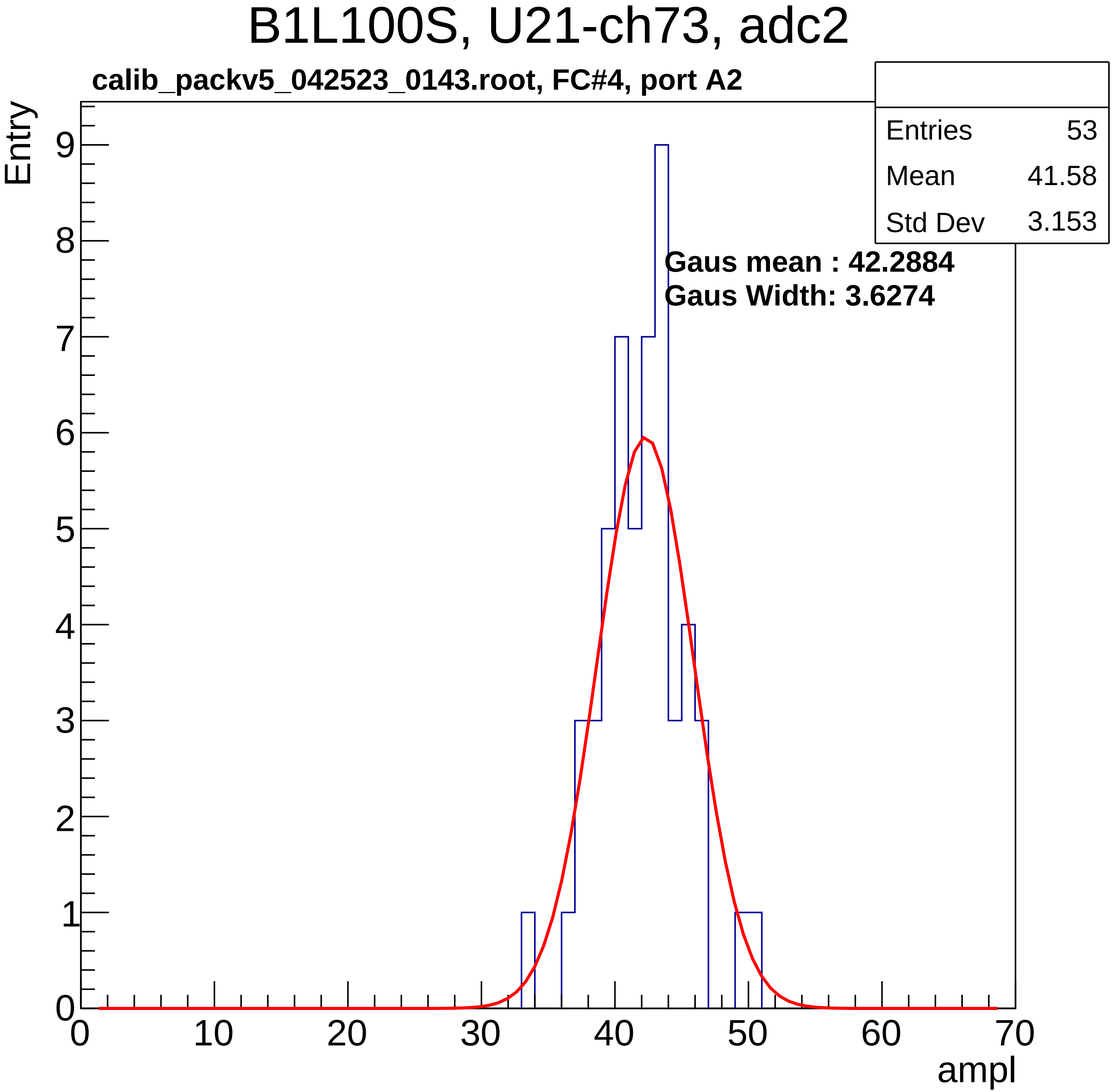


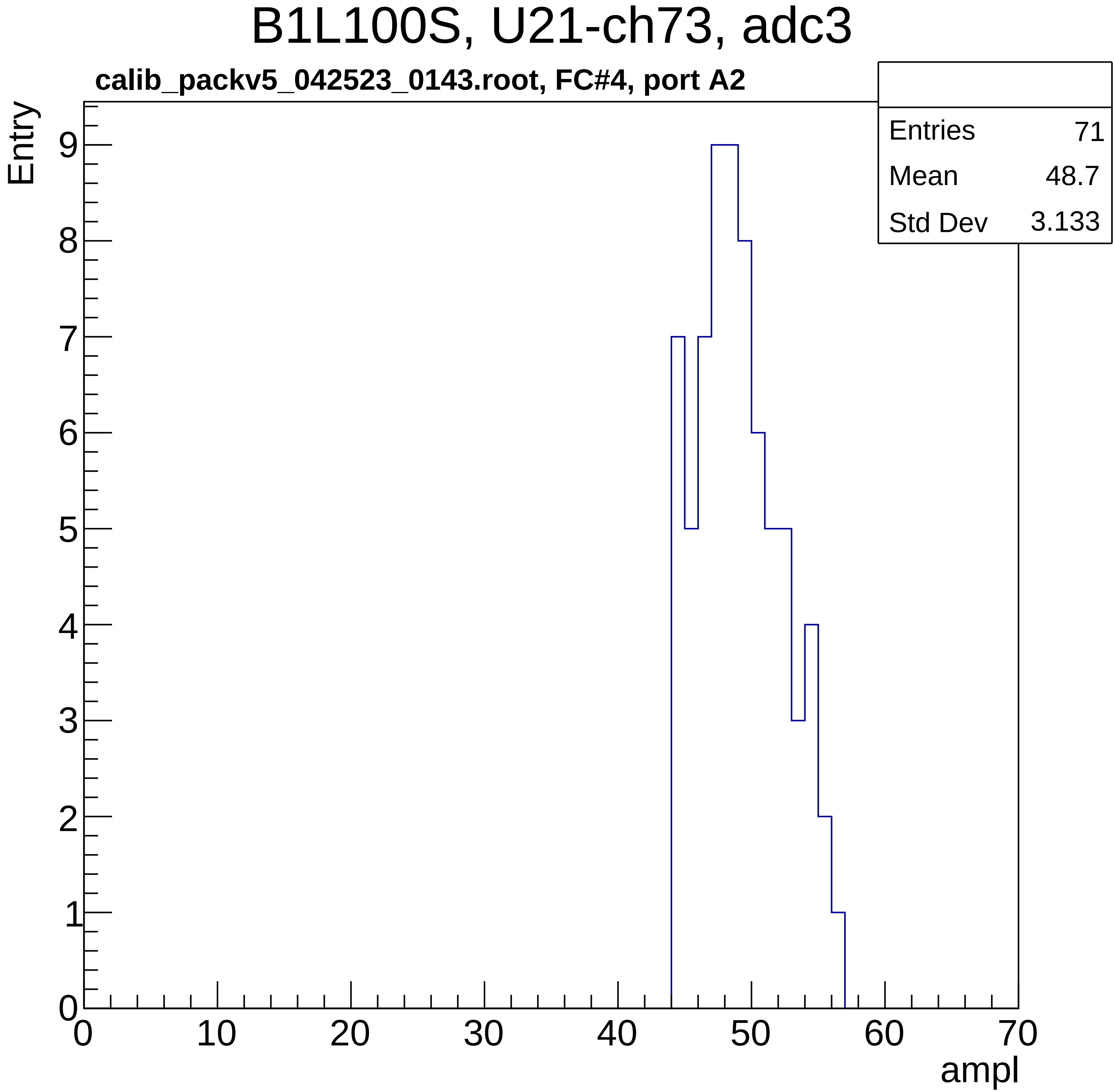
B1L100S, U21-ch72, adc7

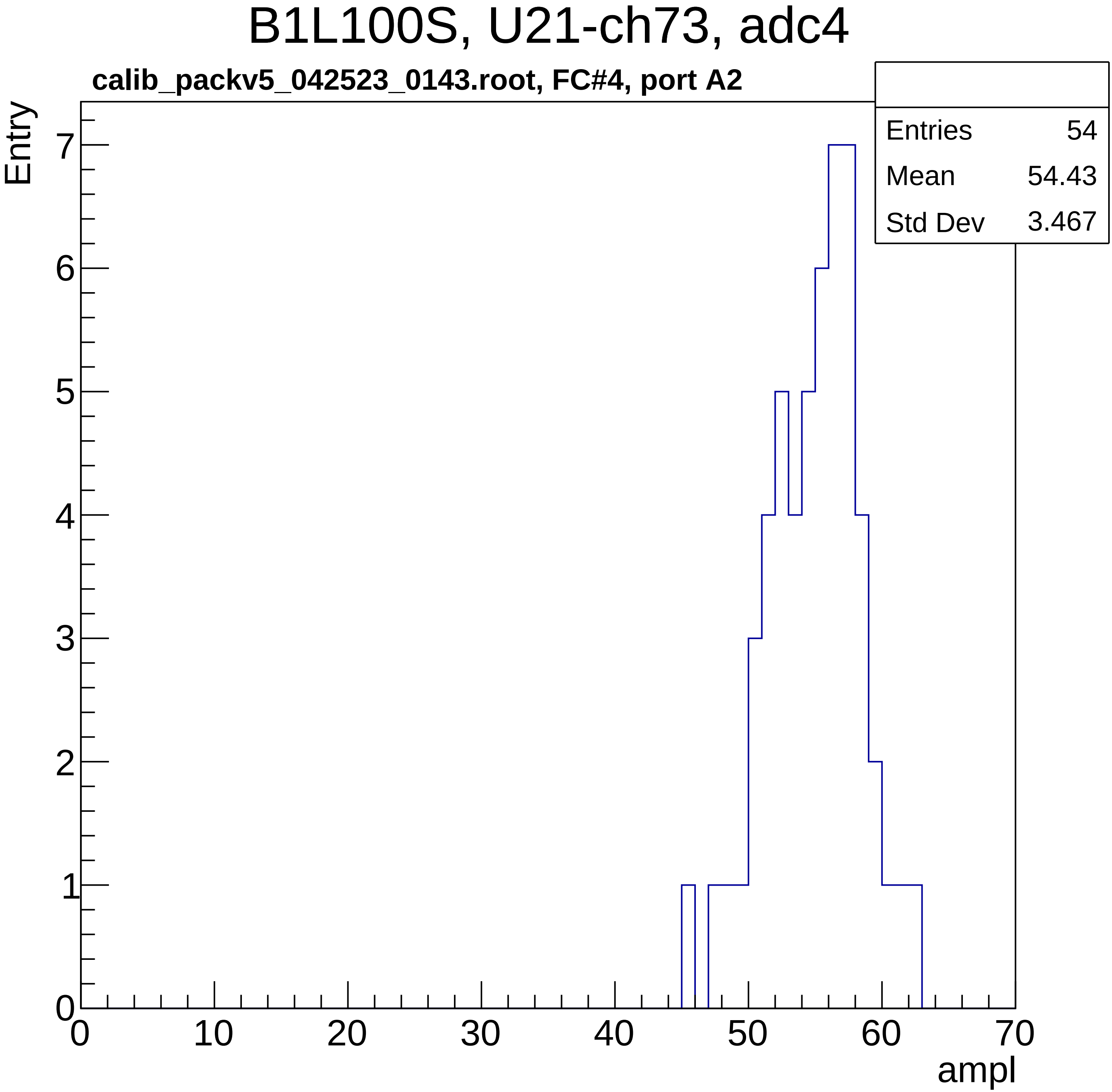


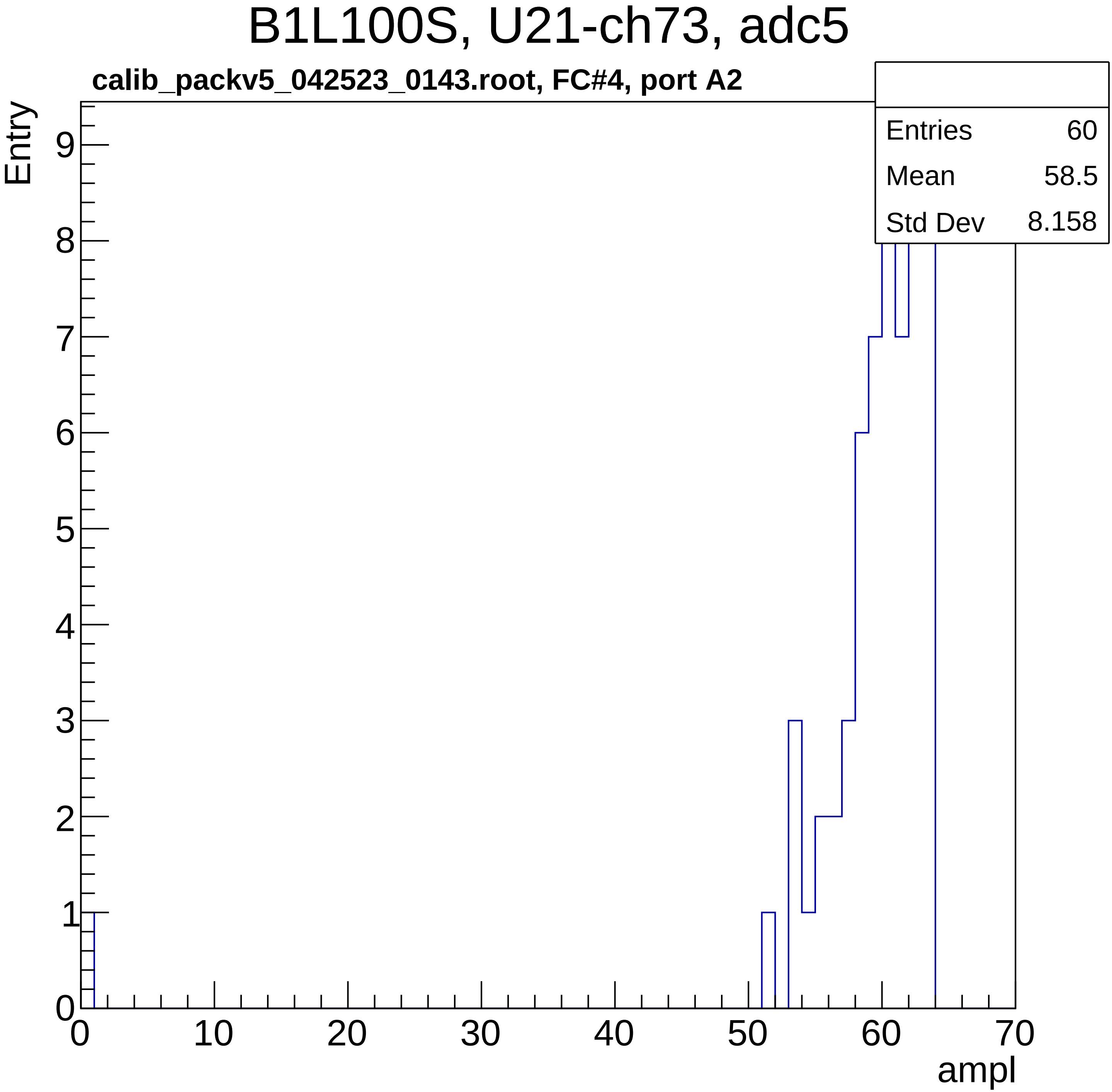

B1L100S, U21-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 28.28
Std Dev 3.462
Gaus mean : 28.9583
Gaus Width: 3.8640
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

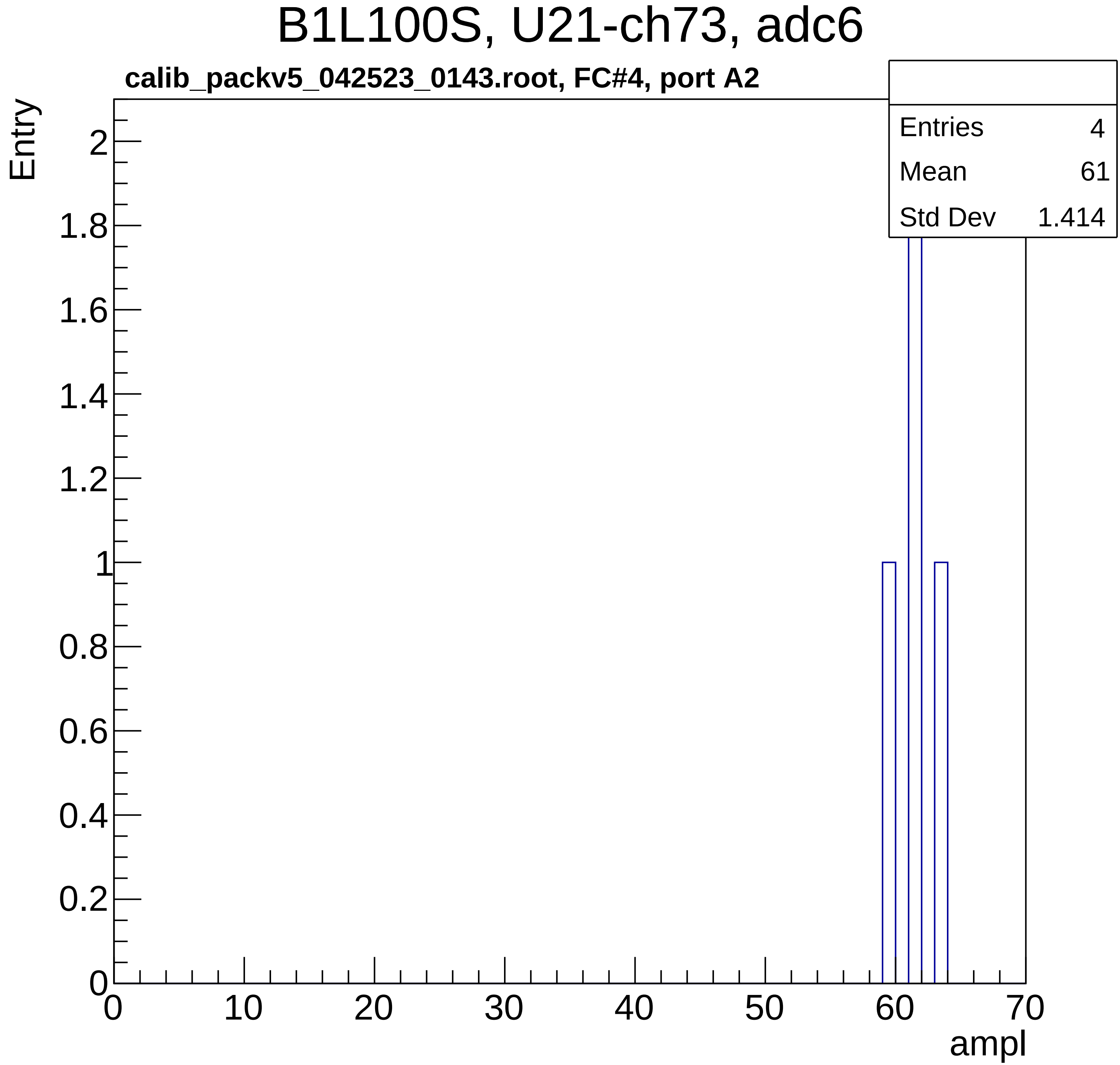

B1L100S, U21-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.48
Std Dev 3.687
Gaus mean : 36.0398
Gaus Width: 4.0612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 41.58
Std Dev 3.153
Gaus mean : 42.2884
Gaus Width: 3.6274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

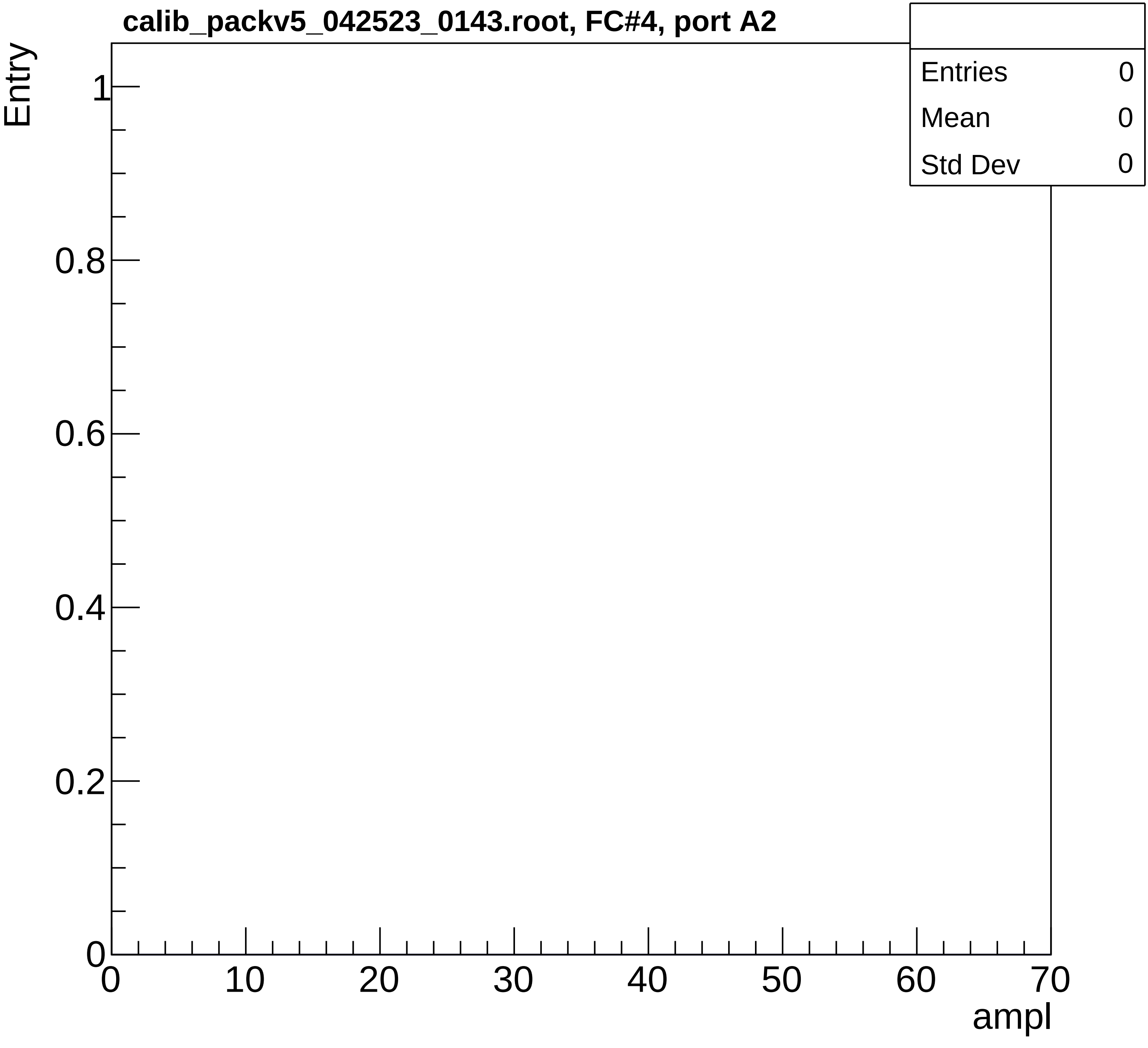

B1L100S, U21-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.7
Std Dev 3.133
Entry
9
8
7
6
5
4
3
2
1
0
0
10
20
30
40
50
60
70
ampl

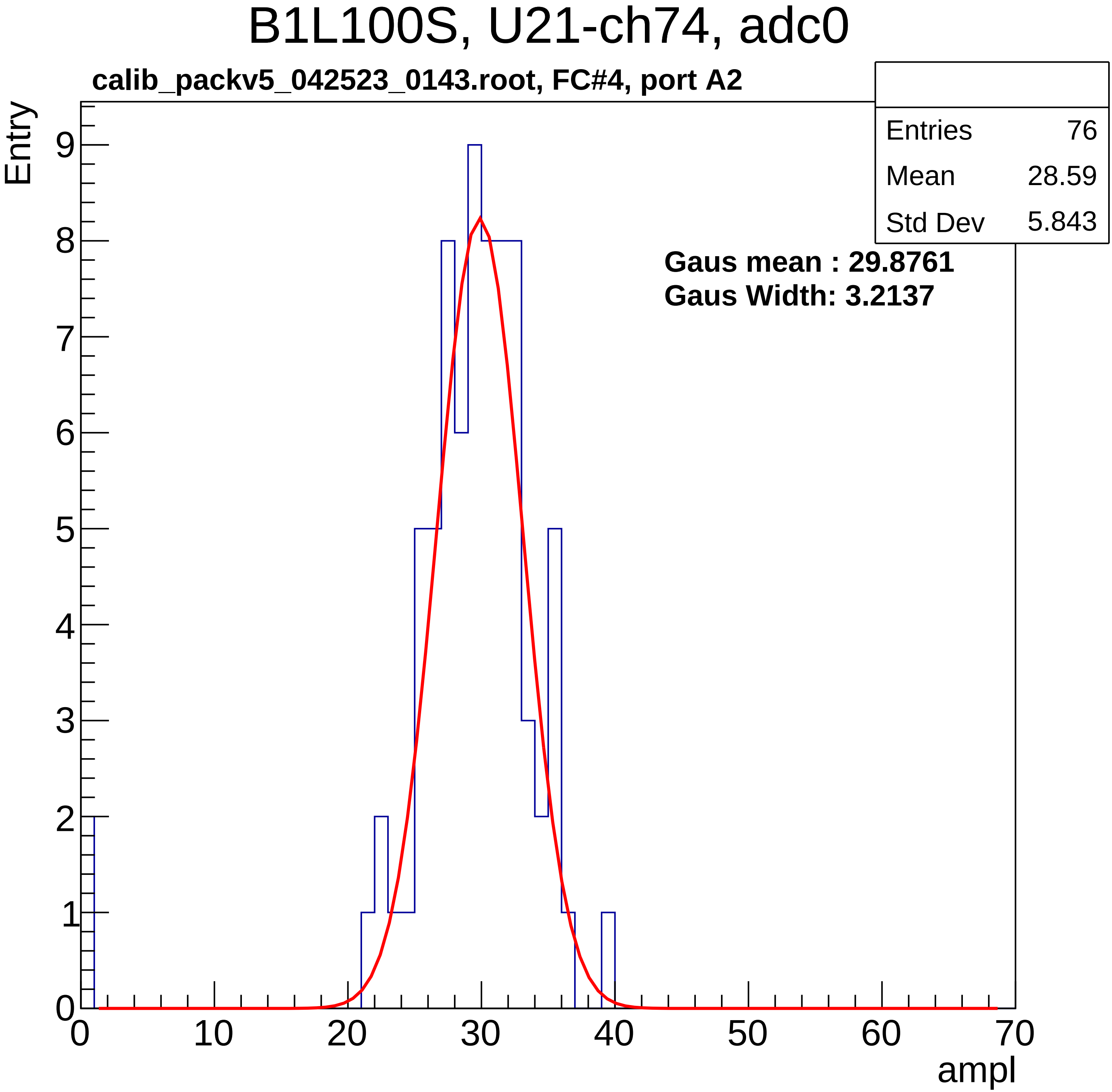

B1L100S, U21-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 54.43
Std Dev 3.467
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

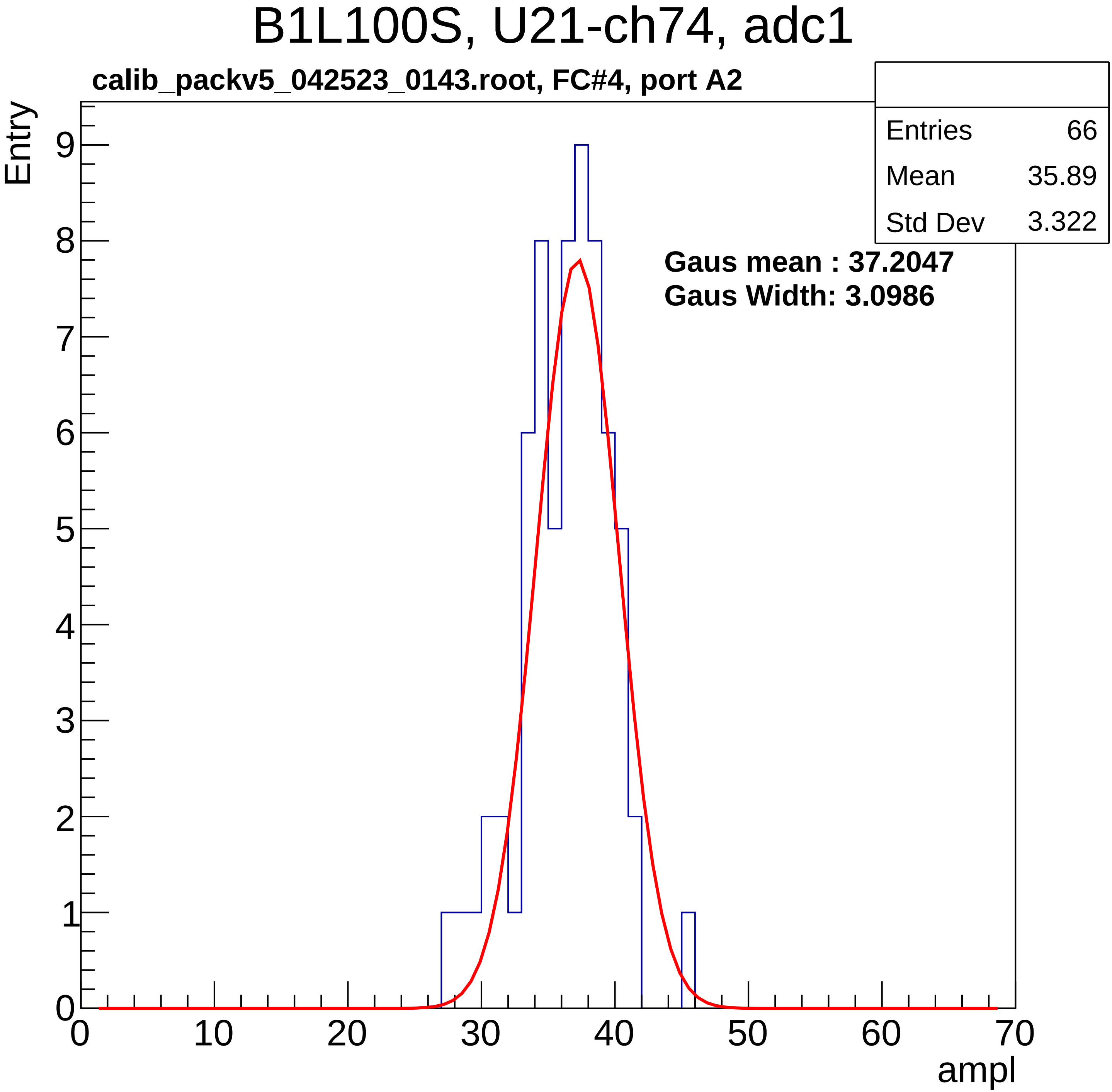

B1L100S, U21-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 58.5
Std Dev 8.158
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

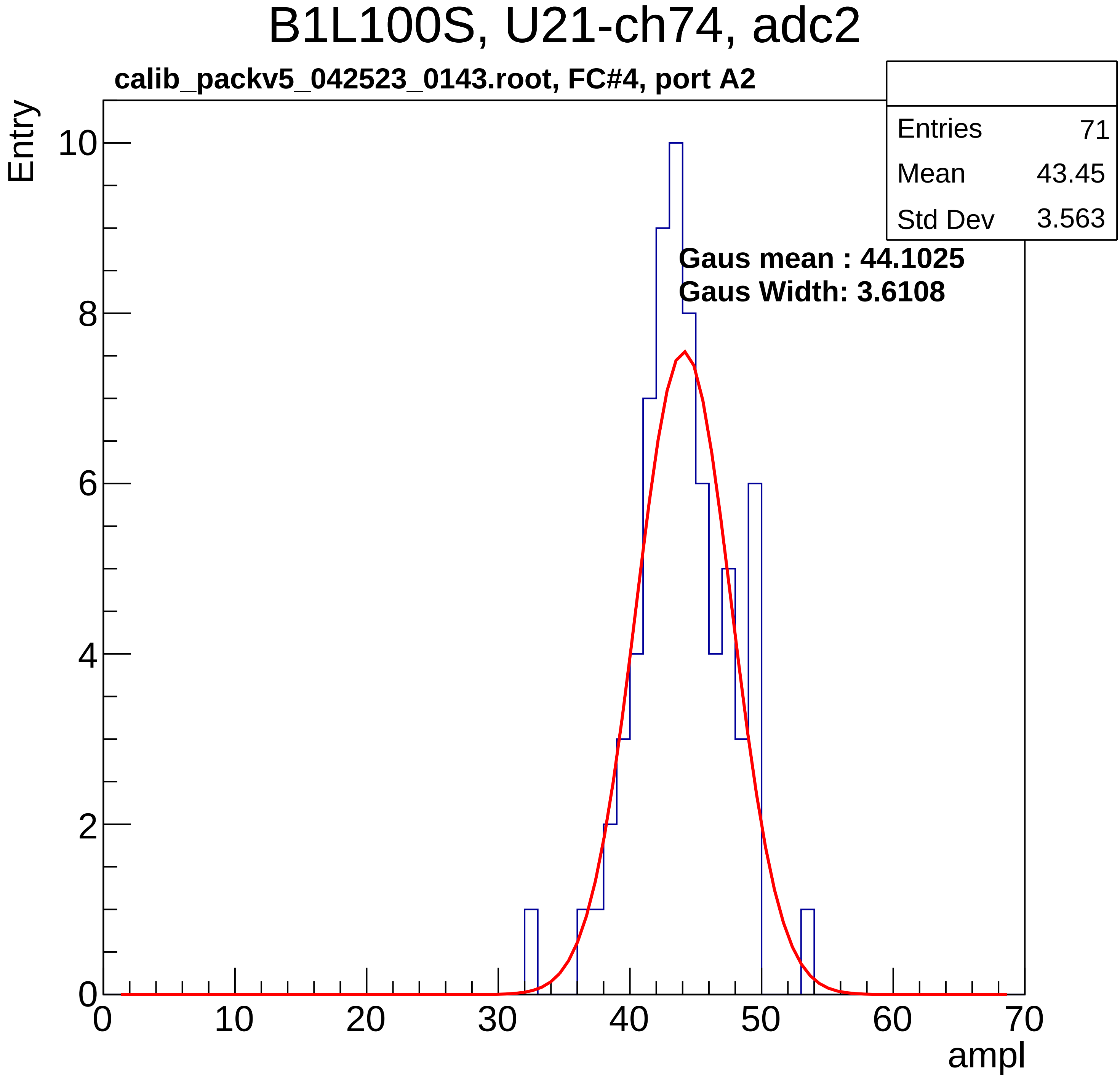

B1L100S, U21-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61
Std Dev 1.414
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch73, adc7

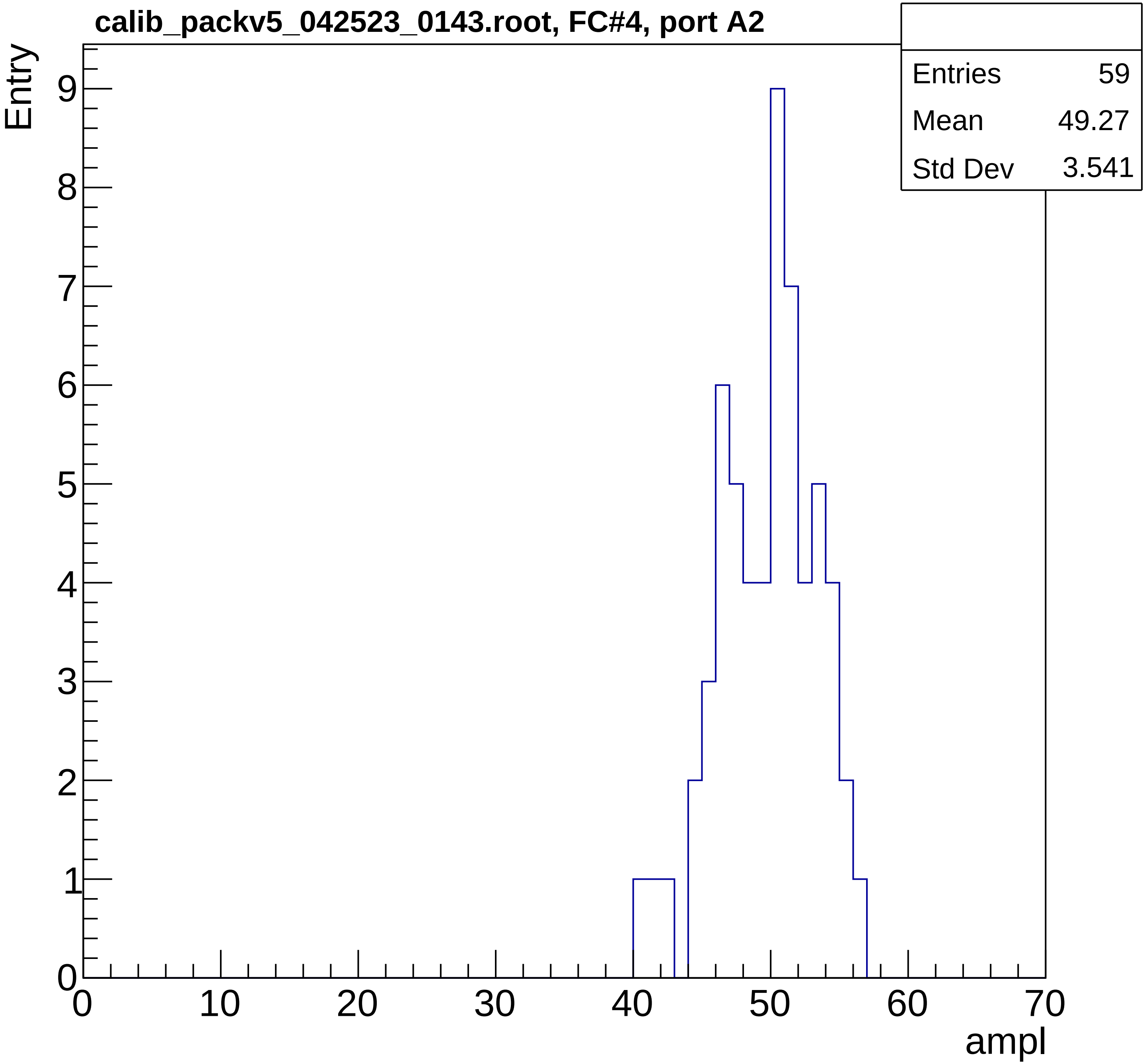


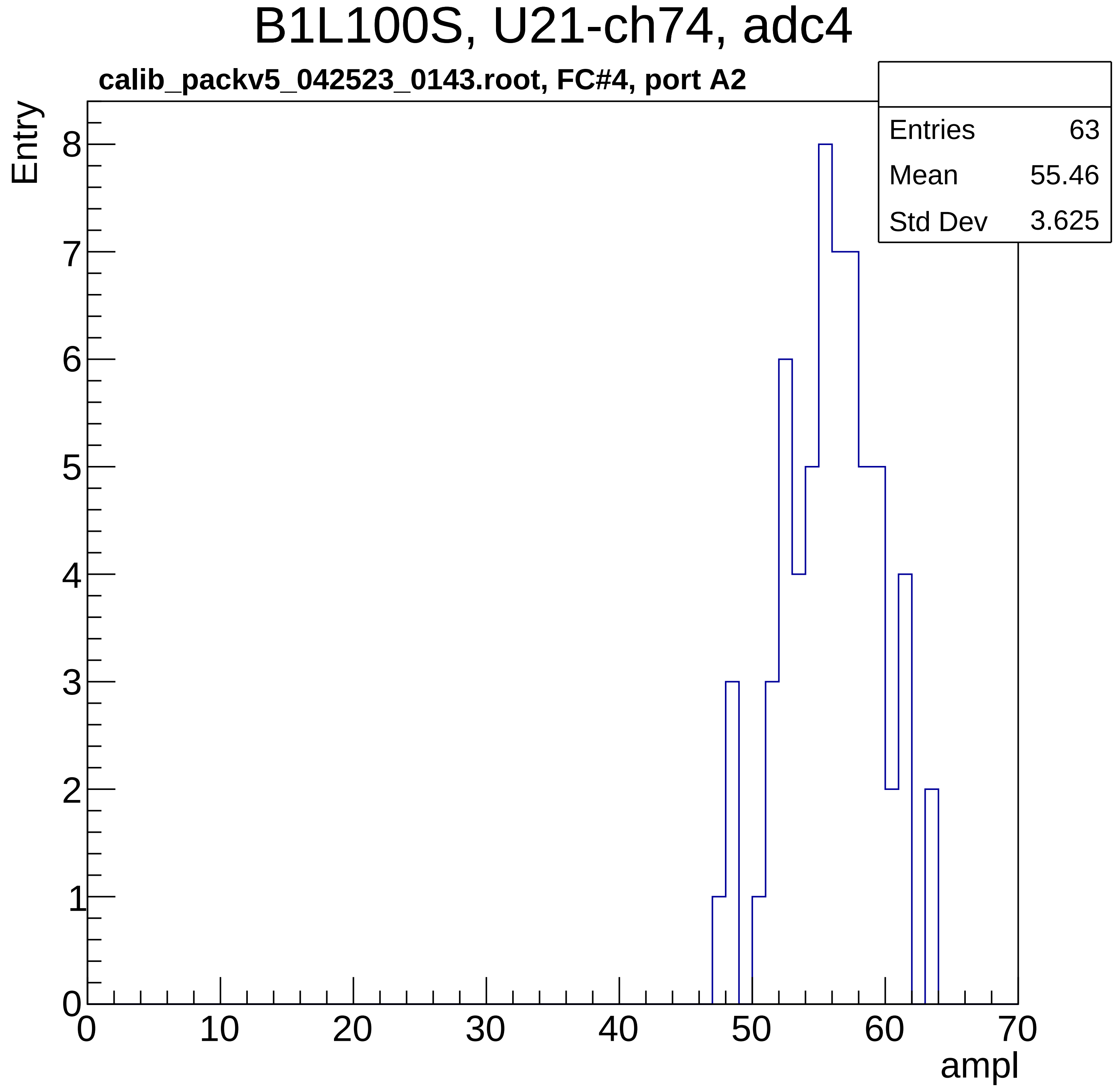

B1L100S, U21-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.59
Std Dev 5.843
Gaus mean : 29.8761
Gaus Width: 3.2137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

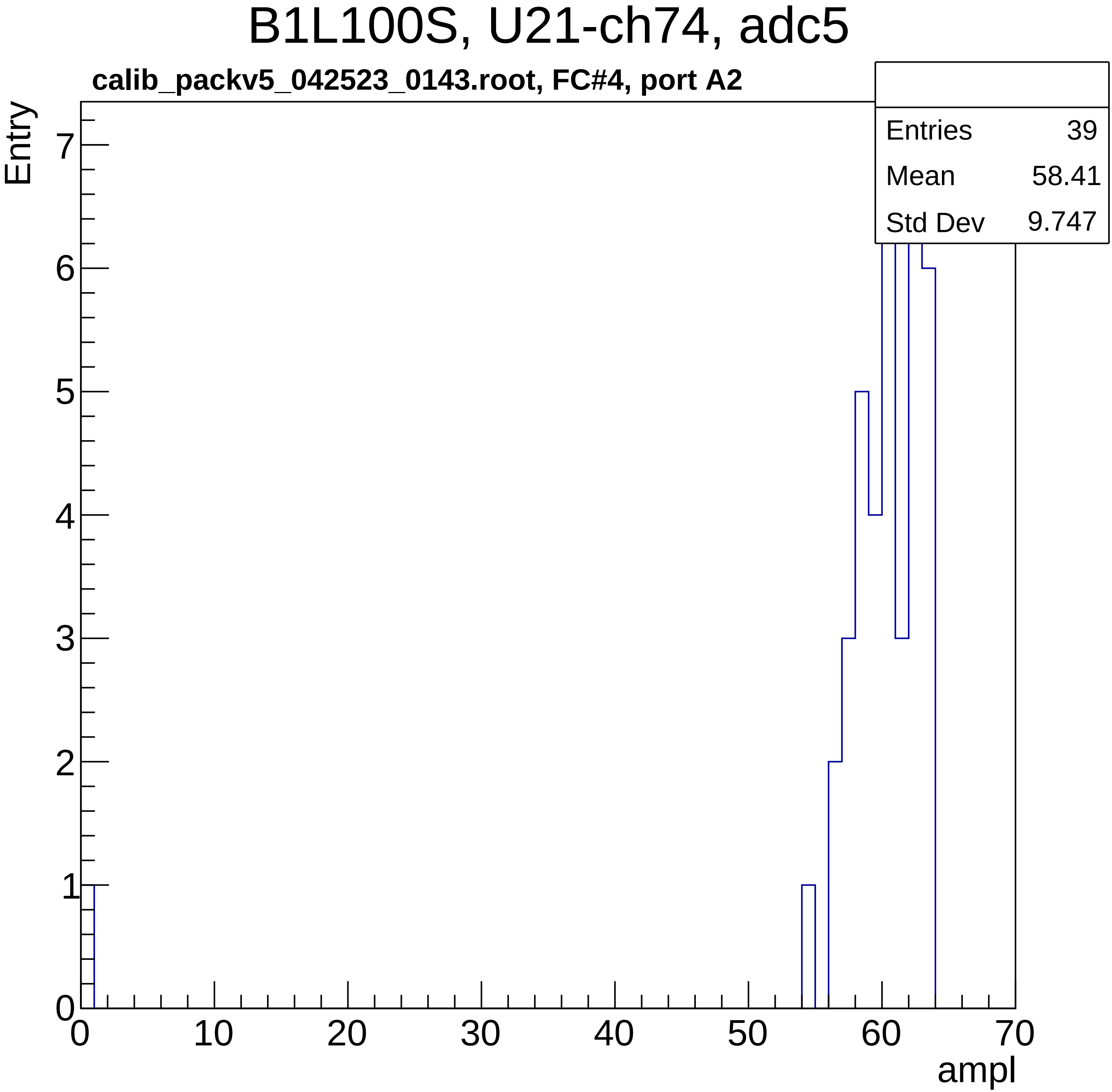

B1L100S, U21-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 66
Mean 35.89
Std Dev 3.322
Gaus mean : 37.2047
Gaus Width: 3.0986
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

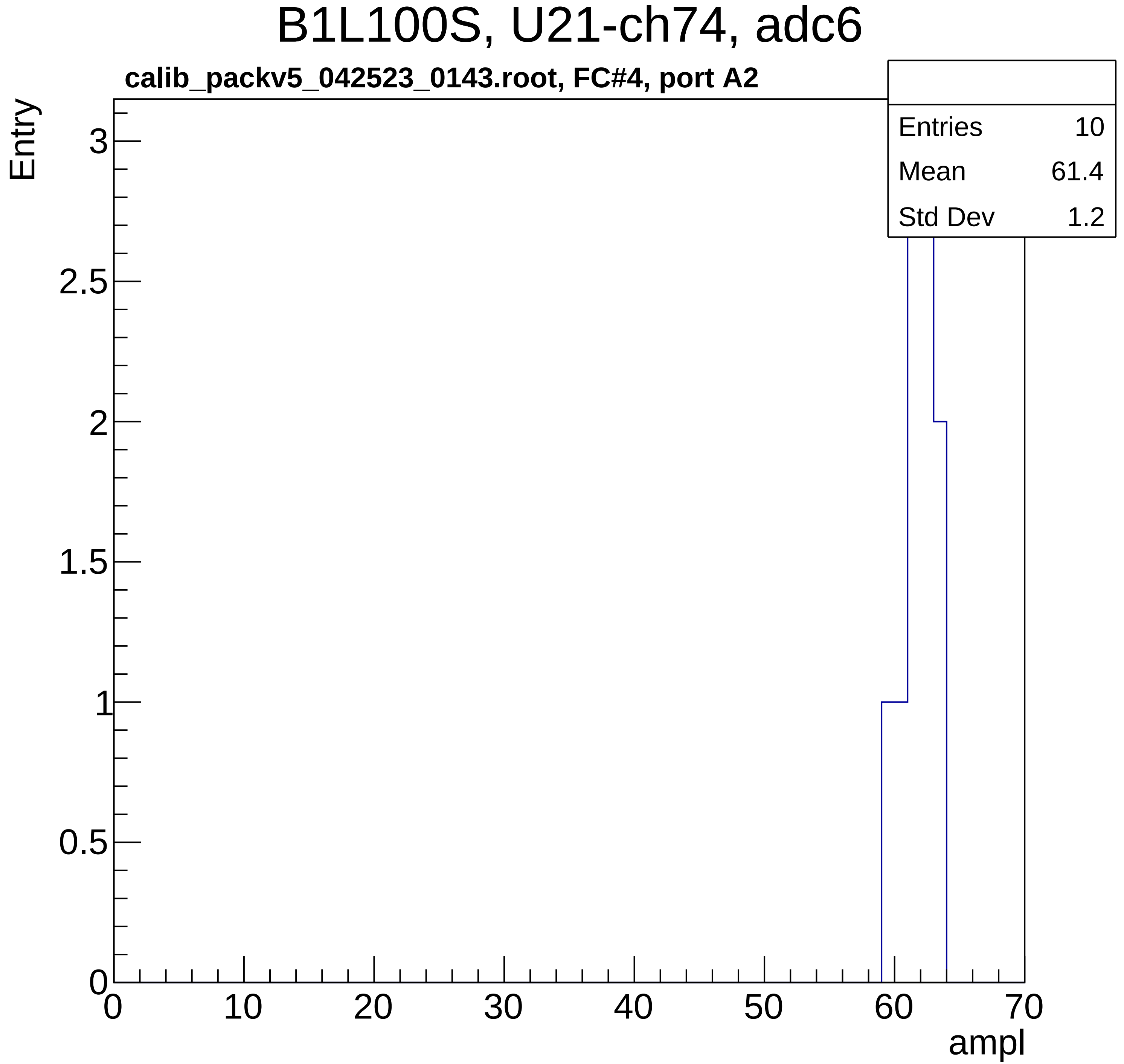

B1L100S, U21-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 43.45
Std Dev 3.563
Gaus mean : 44.1025
Gaus Width: 3.6108
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

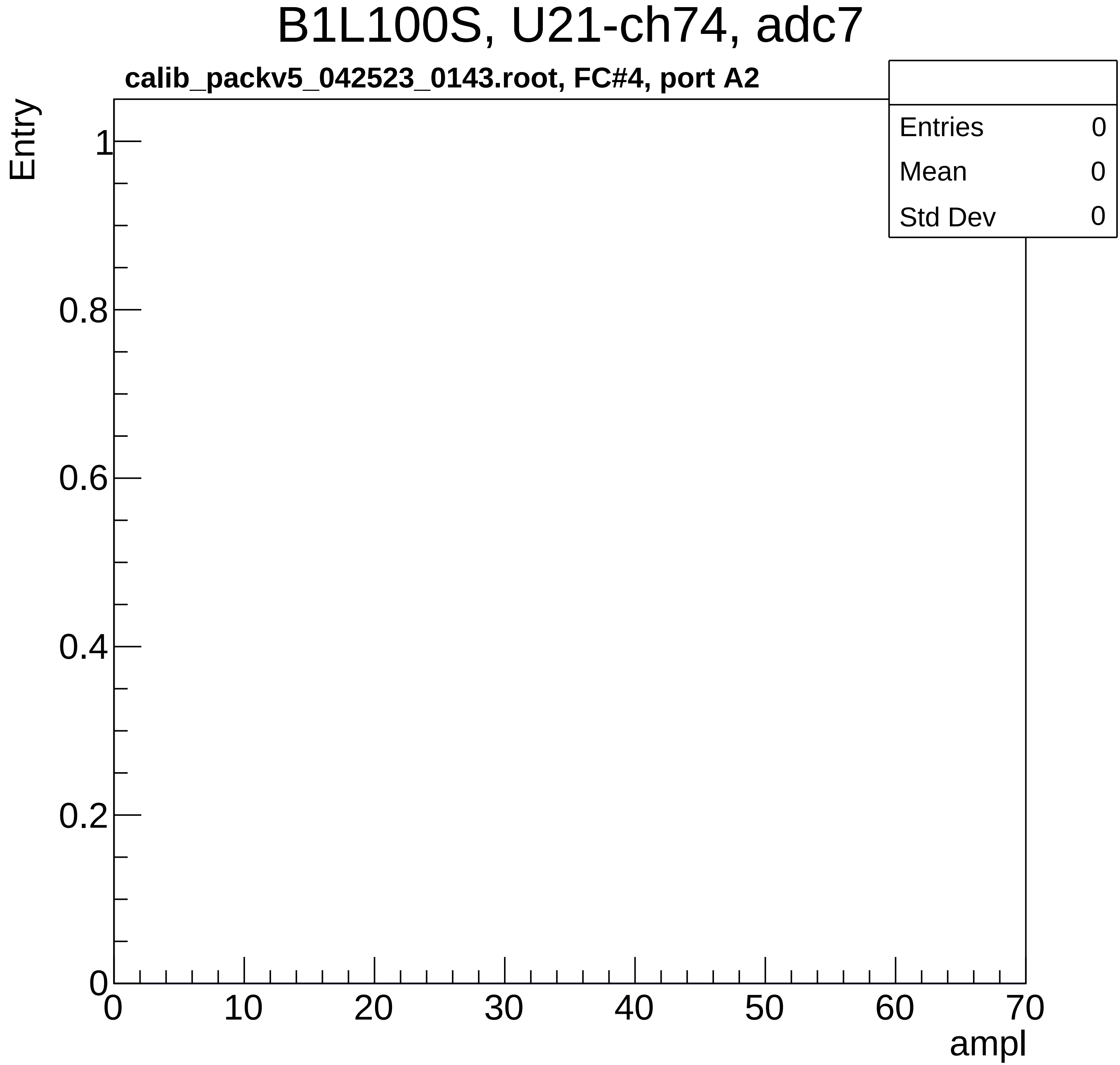
B1L100S, U21-ch74, adc3

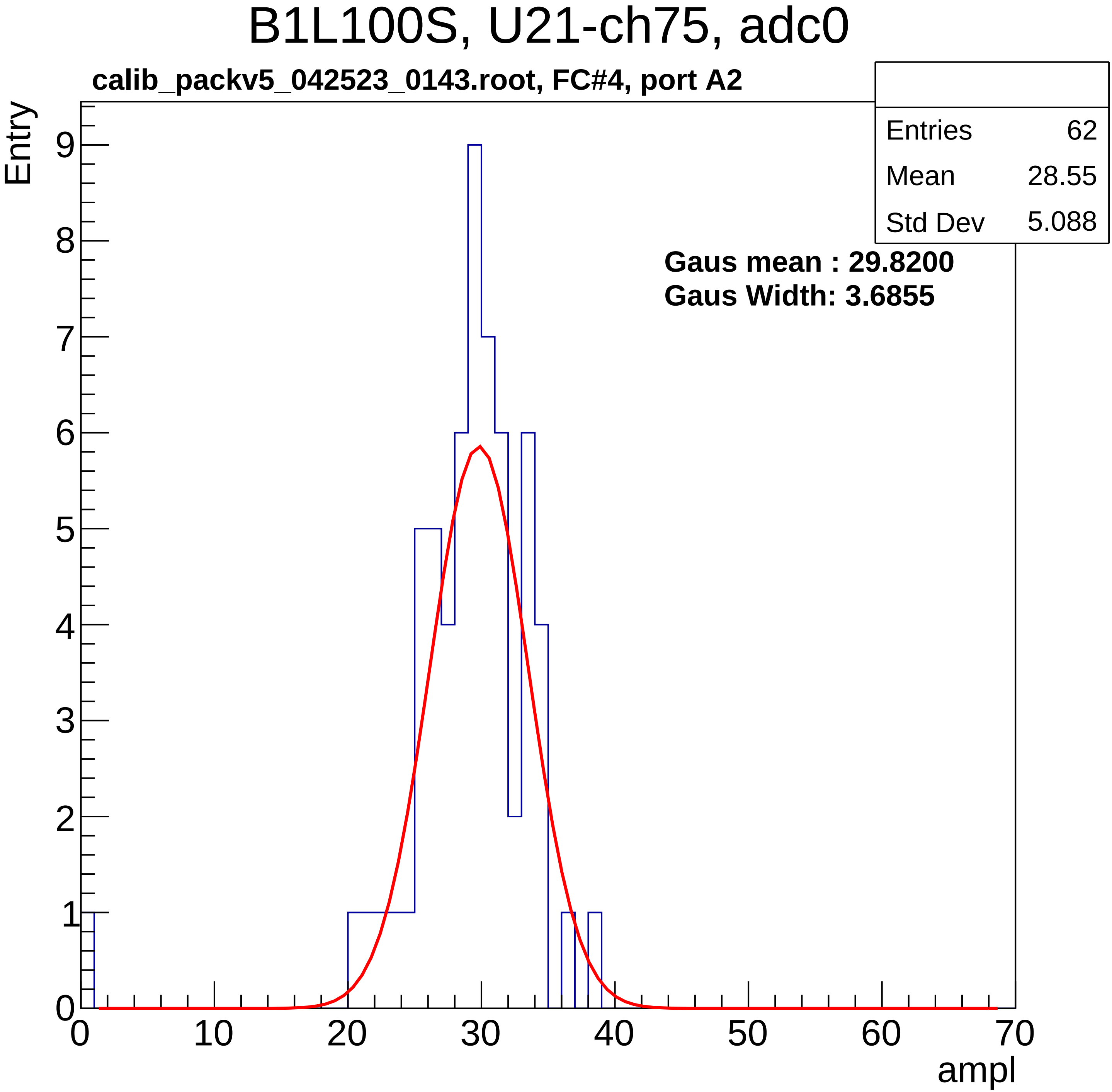


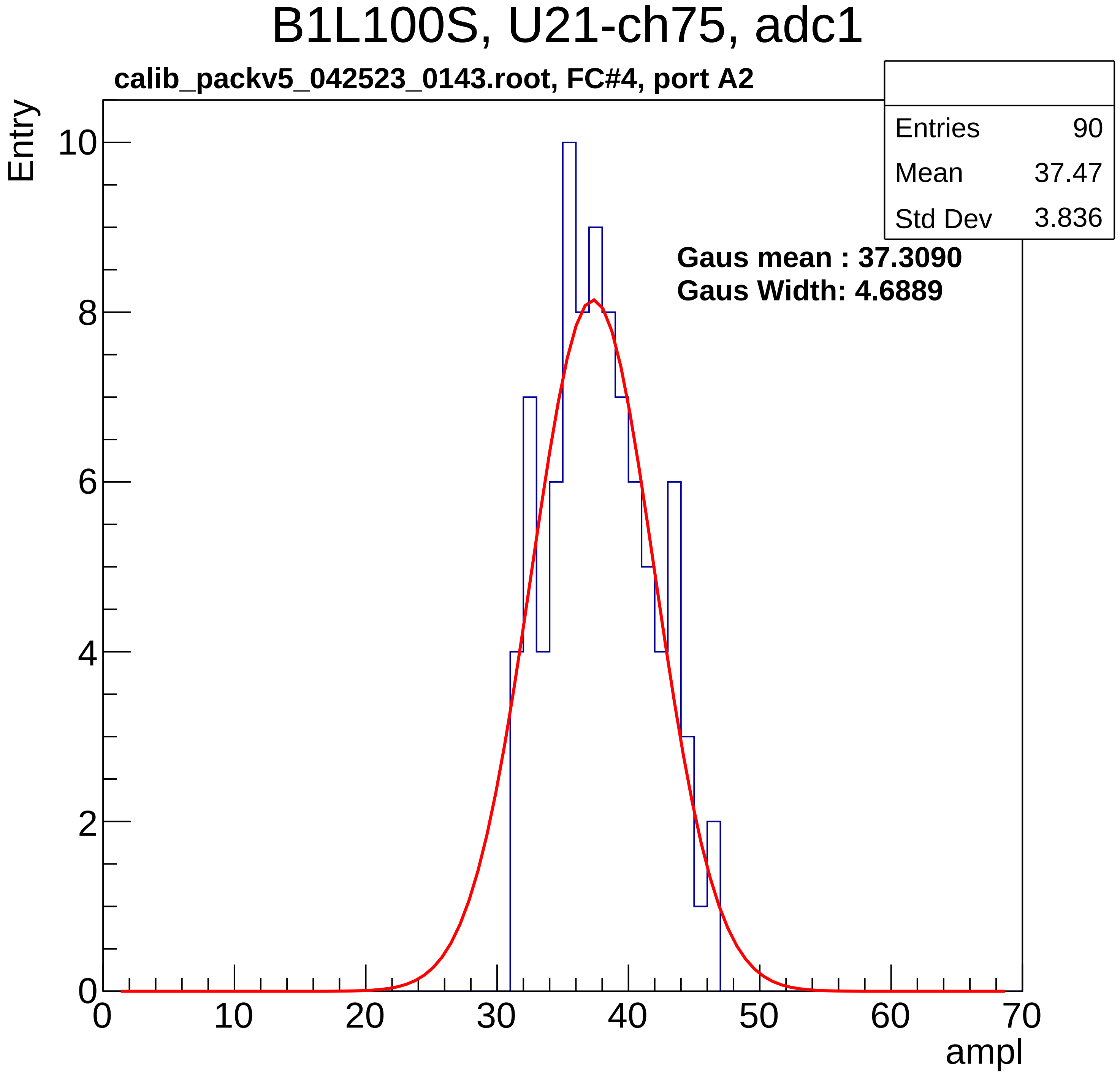

B1L100S, U21-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.46
Std Dev 3.625
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

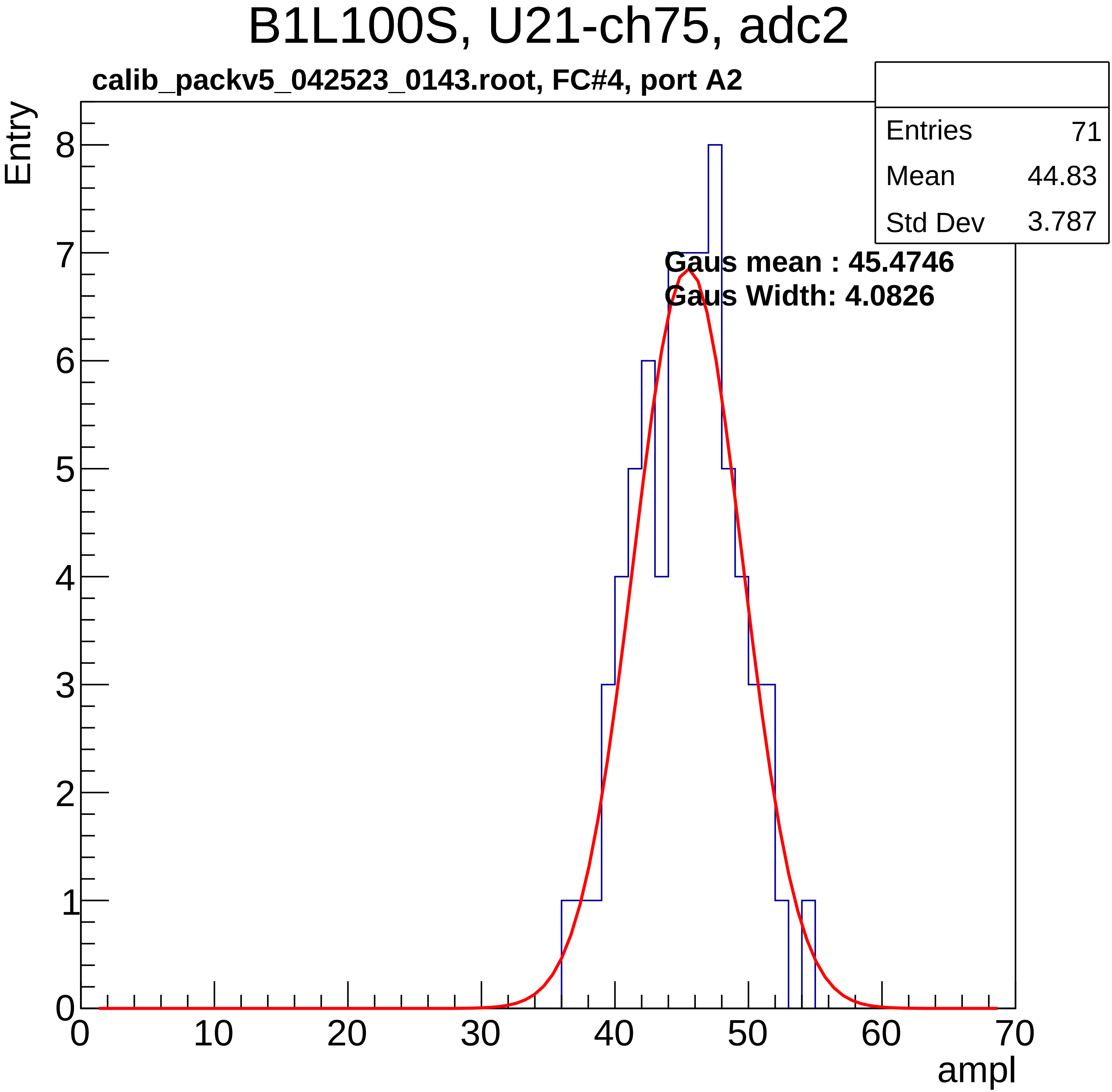

B1L100S, U21-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.41
Std Dev 9.747
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 10
Mean 61.4
Std Dev 1.2
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U21-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

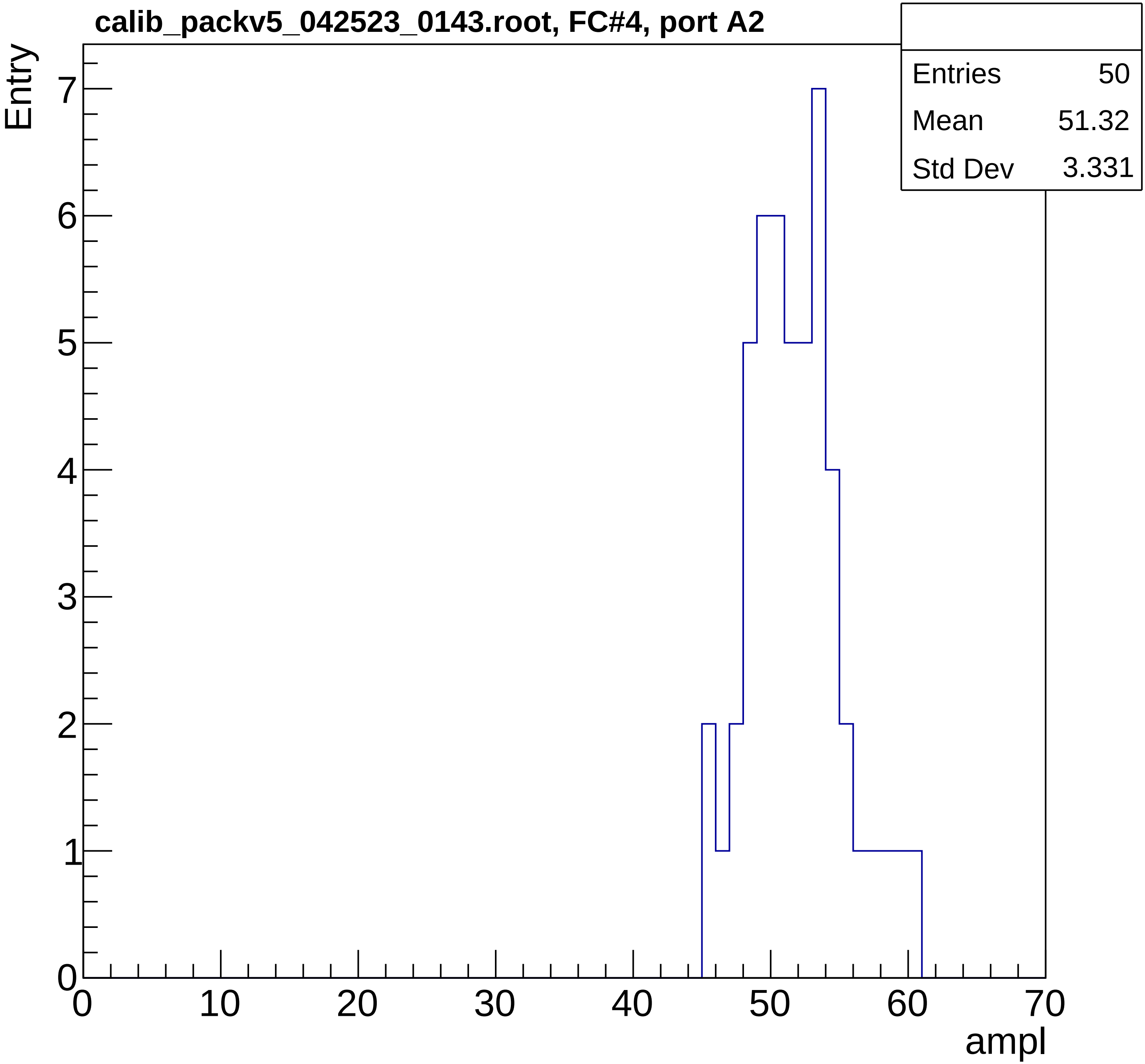

B1L100S, U21-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.55
Std Dev 5.088
Gaus mean : 29.8200
Gaus Width: 3.6855
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

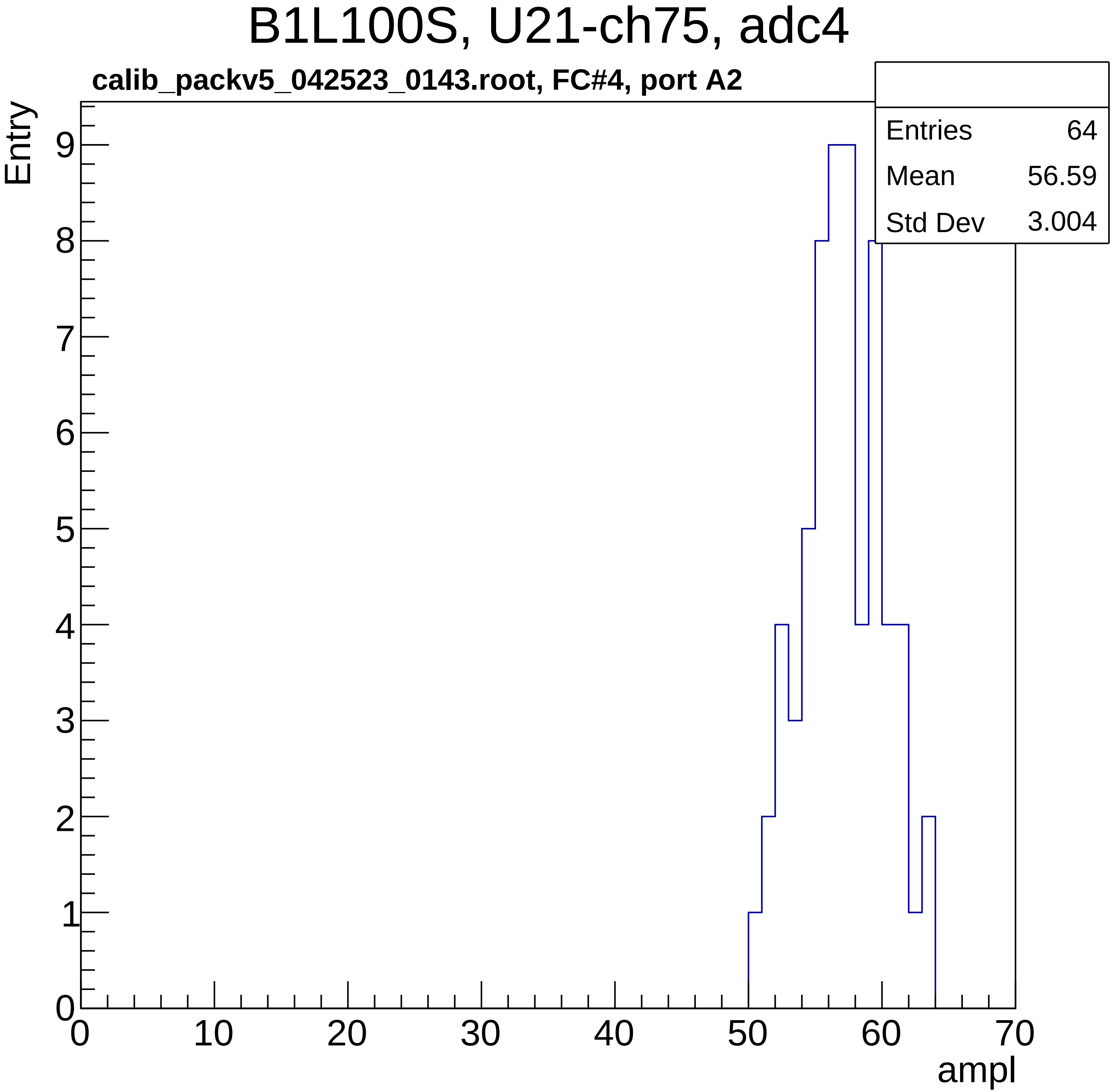

B1L100S, U21-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 37.47
Std Dev 3.836
Gaus mean : 37.3090
Gaus Width: 4.6889
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

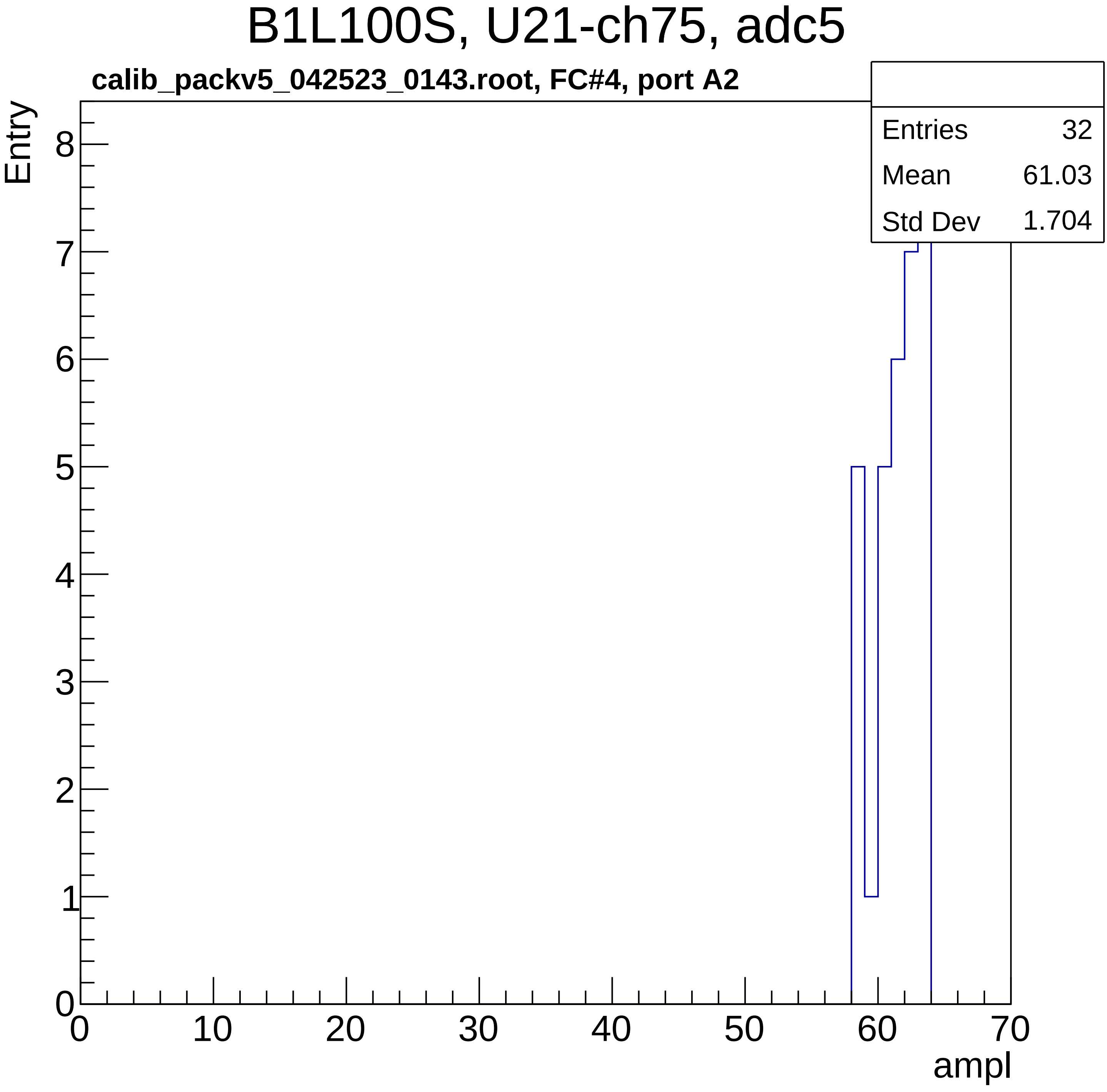

B1L100S, U21-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 44.83
Std Dev 3.787
Gaus mean : 45.4746
Gaus Width: 4.0826
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

B1L100S, U21-ch75, adc3

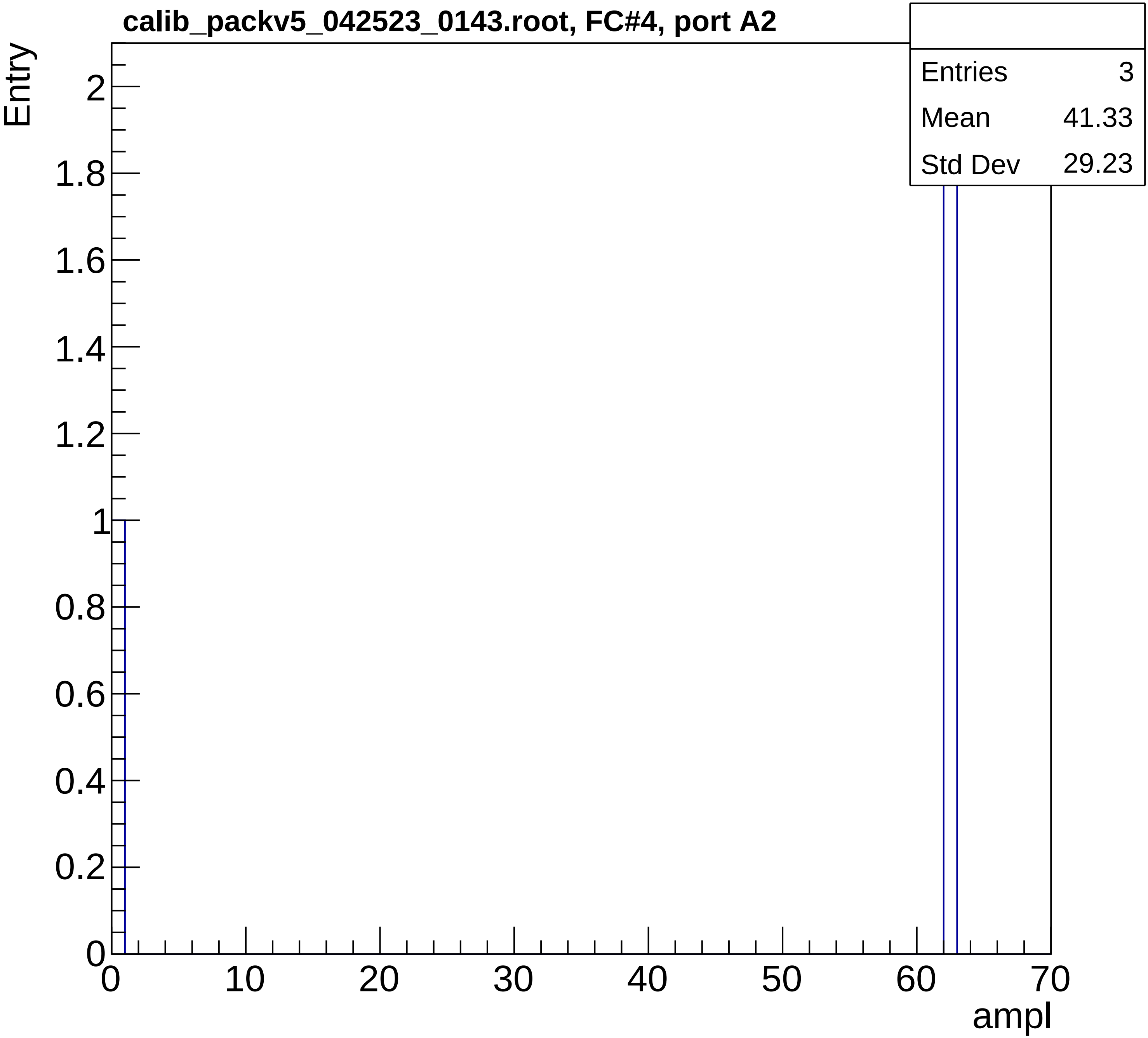
calib_packv5_042523_0143.root, FC#4, port A2

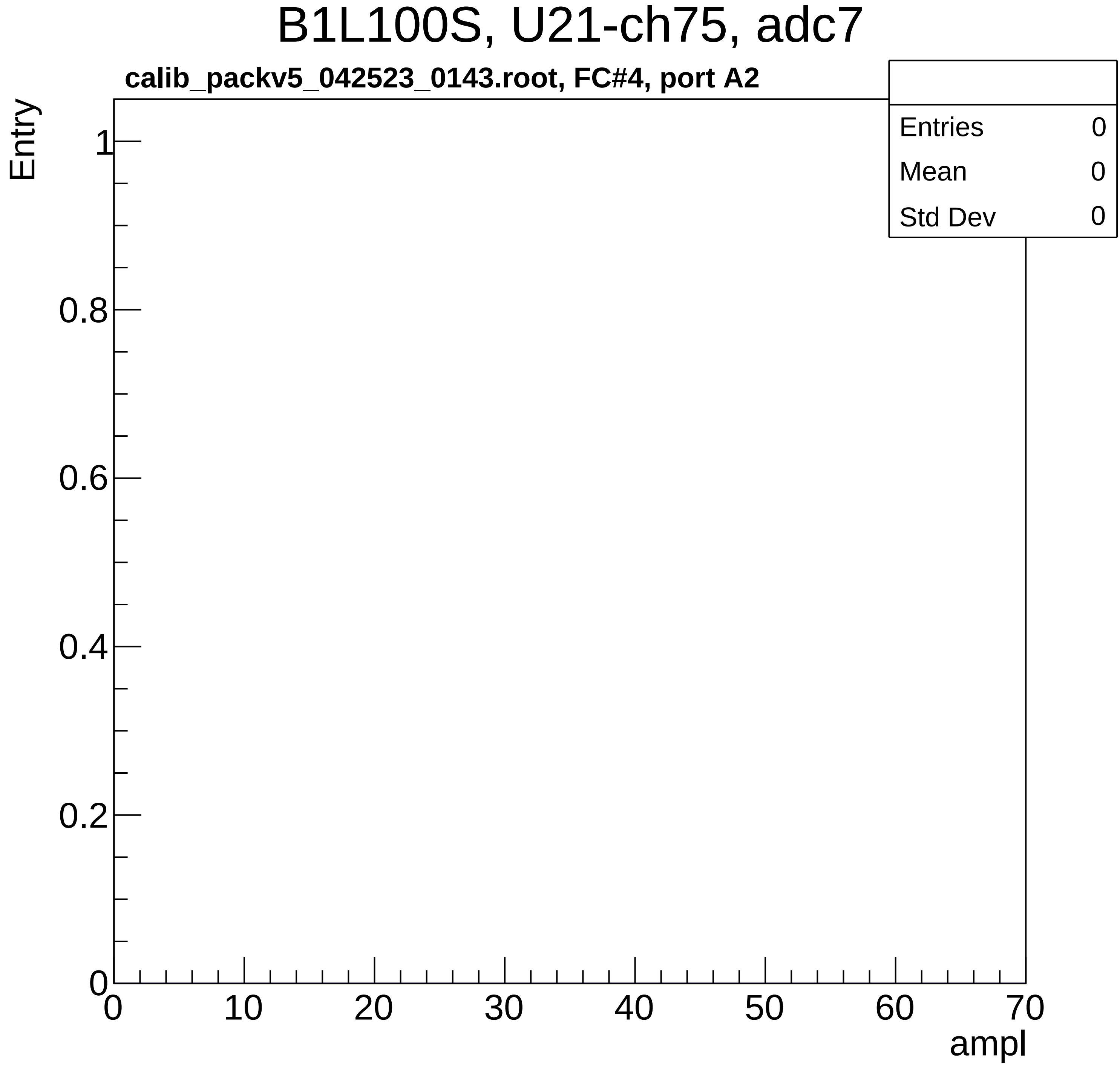


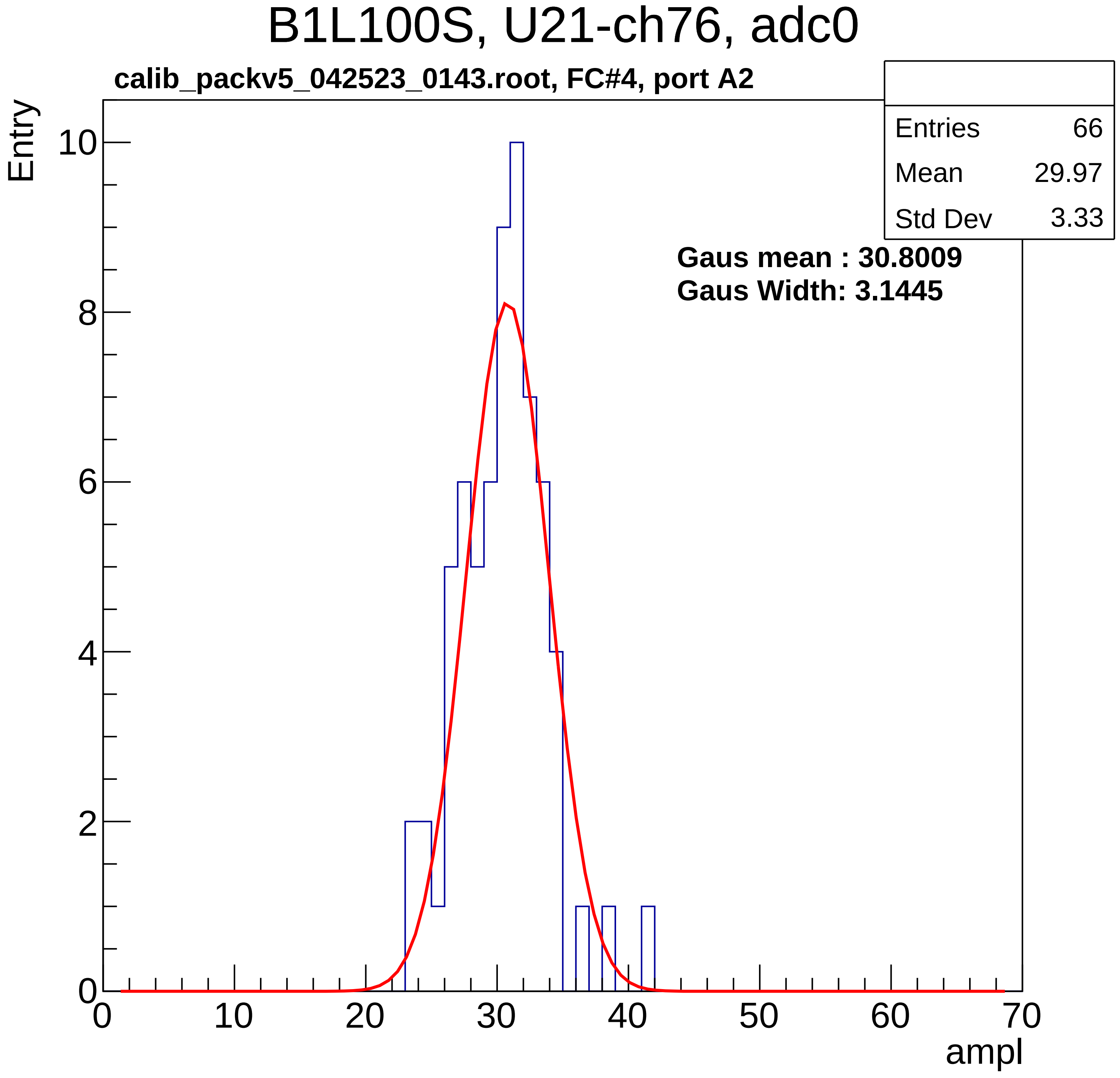

B1L100S, U21-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 56.59
Std Dev 3.004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

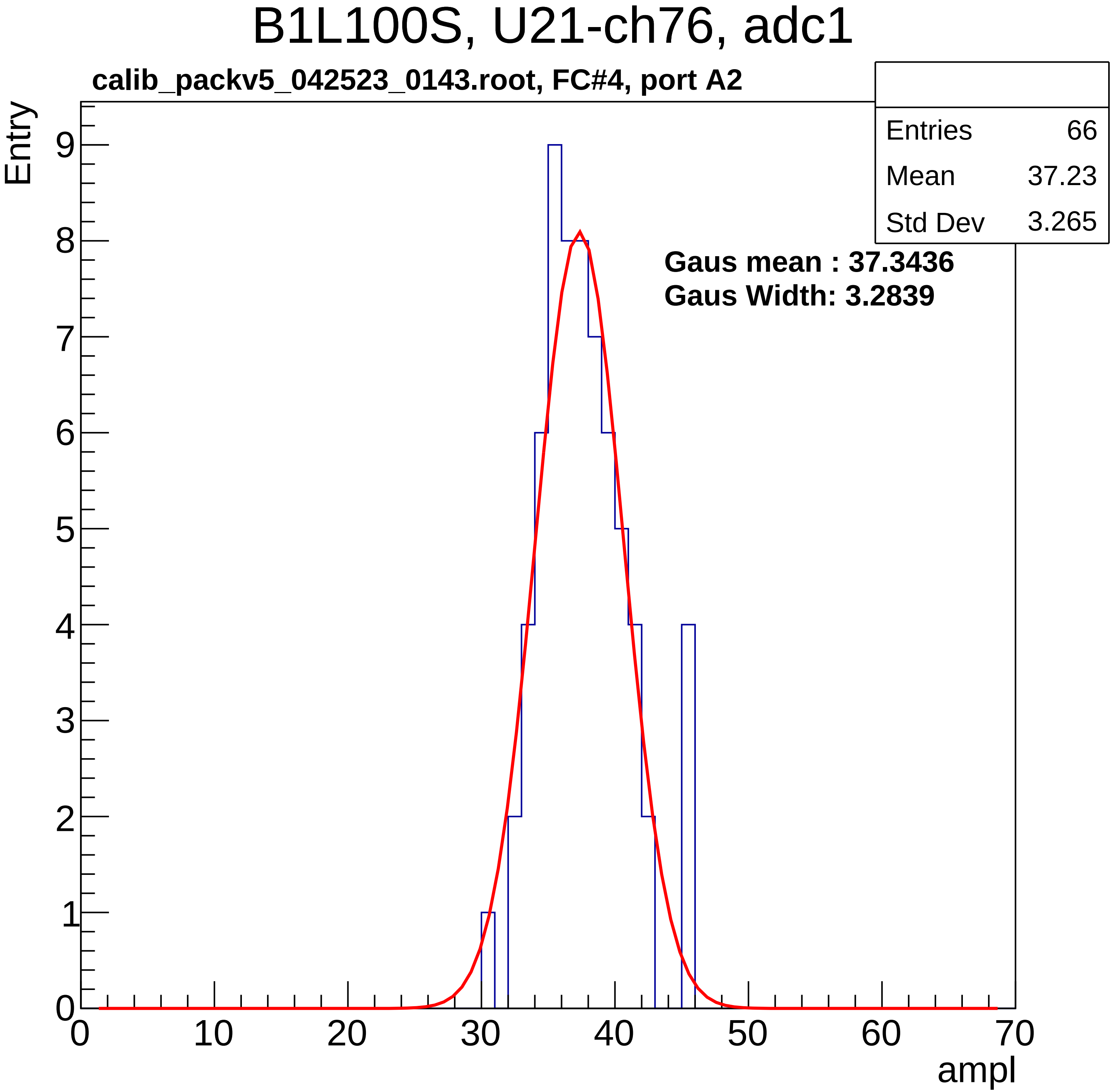

B1L100S, U21-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 61.03
Std Dev 1.704
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

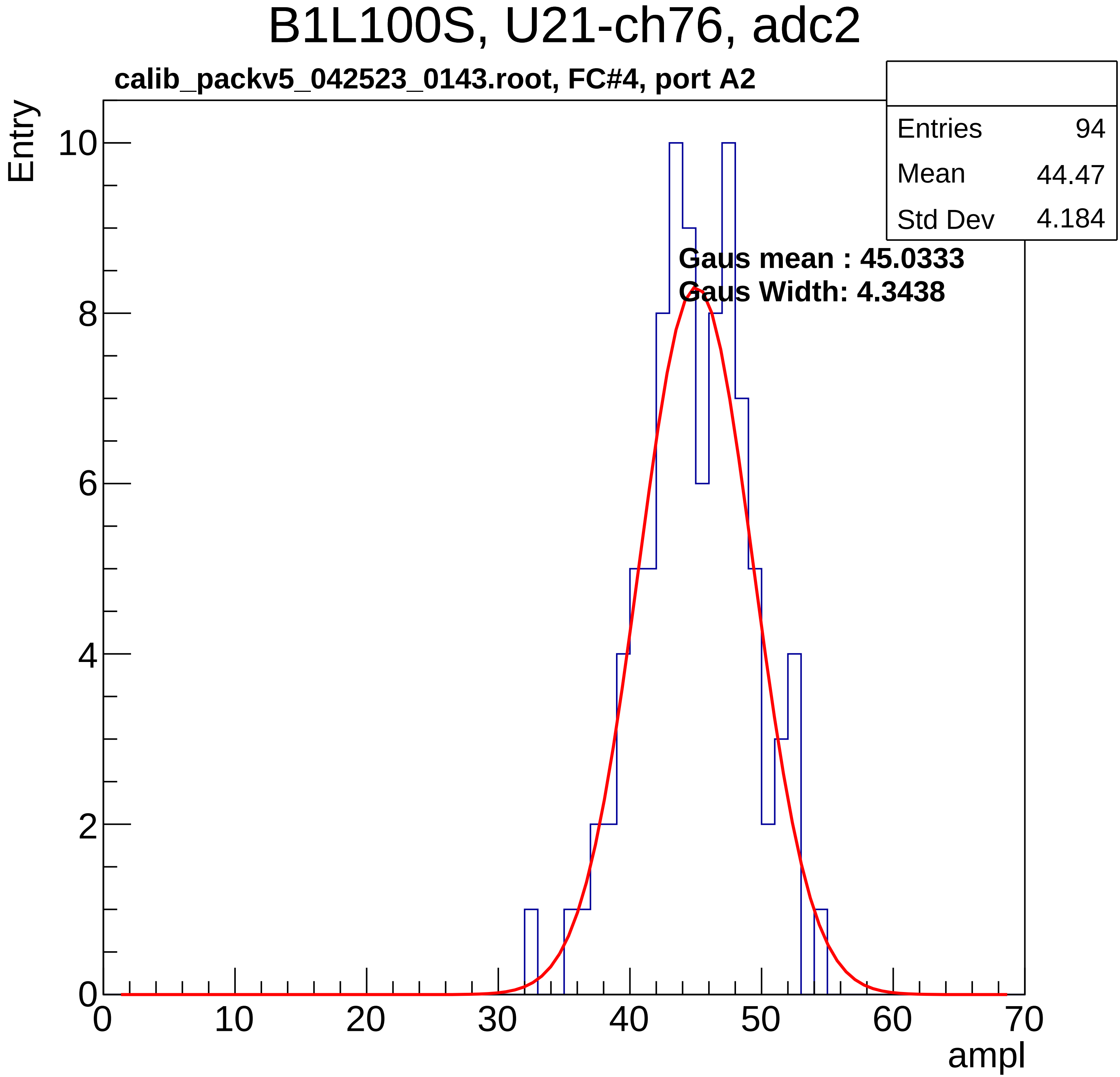
B1L100S, U21-ch75, adc6

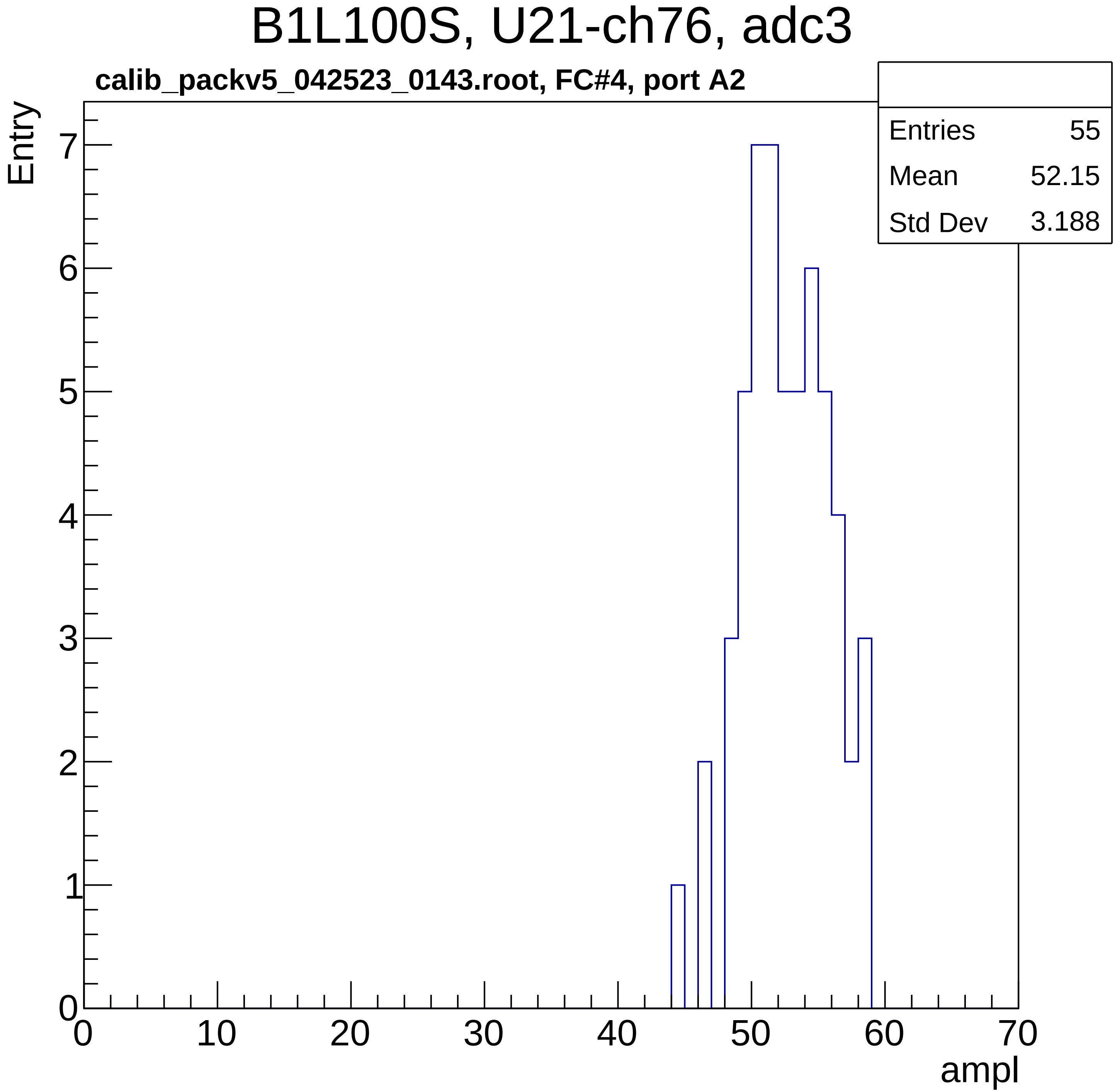



B1L100S, U21-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.97
Std Dev 3.33
Gaus mean : 30.8009
Gaus Width: 3.1445
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

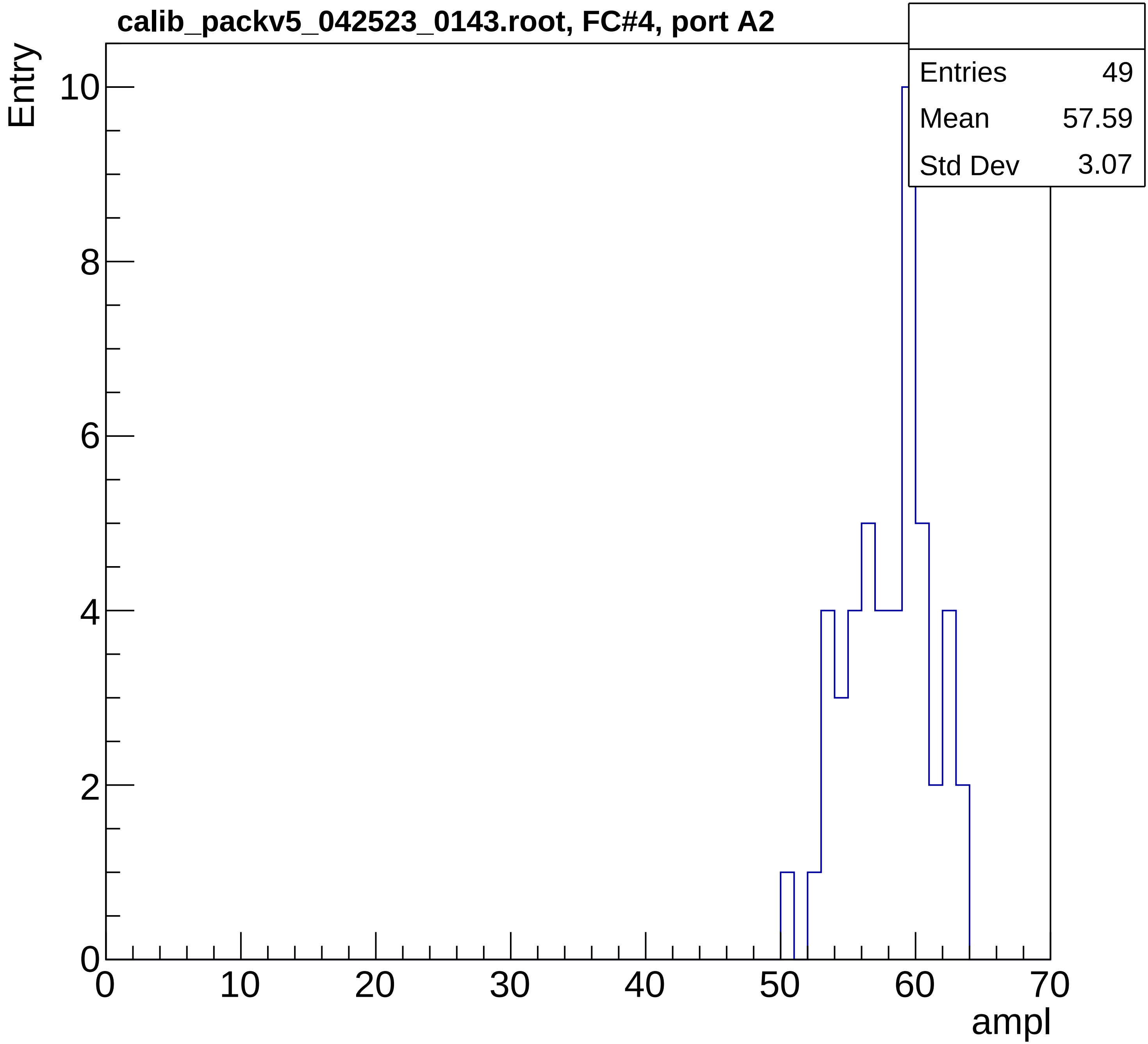

B1L100S, U21-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 37.23
Std Dev 3.265
Gaus mean : 37.3436
Gaus Width: 3.2839
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 44.47
Std Dev 4.184
Gaus mean : 45.0333
Gaus Width: 4.3438
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

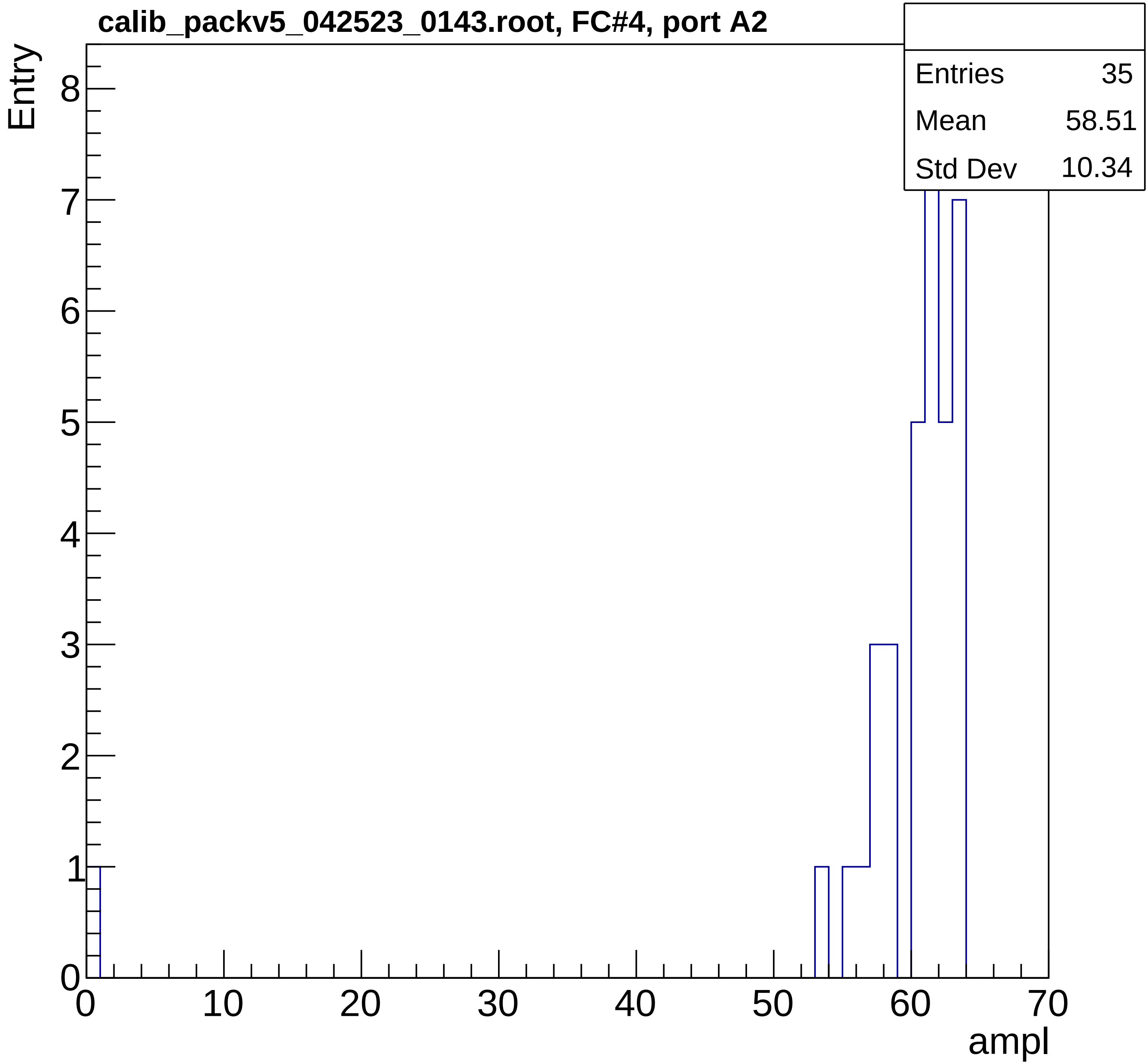

B1L100S, U21-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 55
Mean 52.15
Std Dev 3.188
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

B1L100S, U21-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2

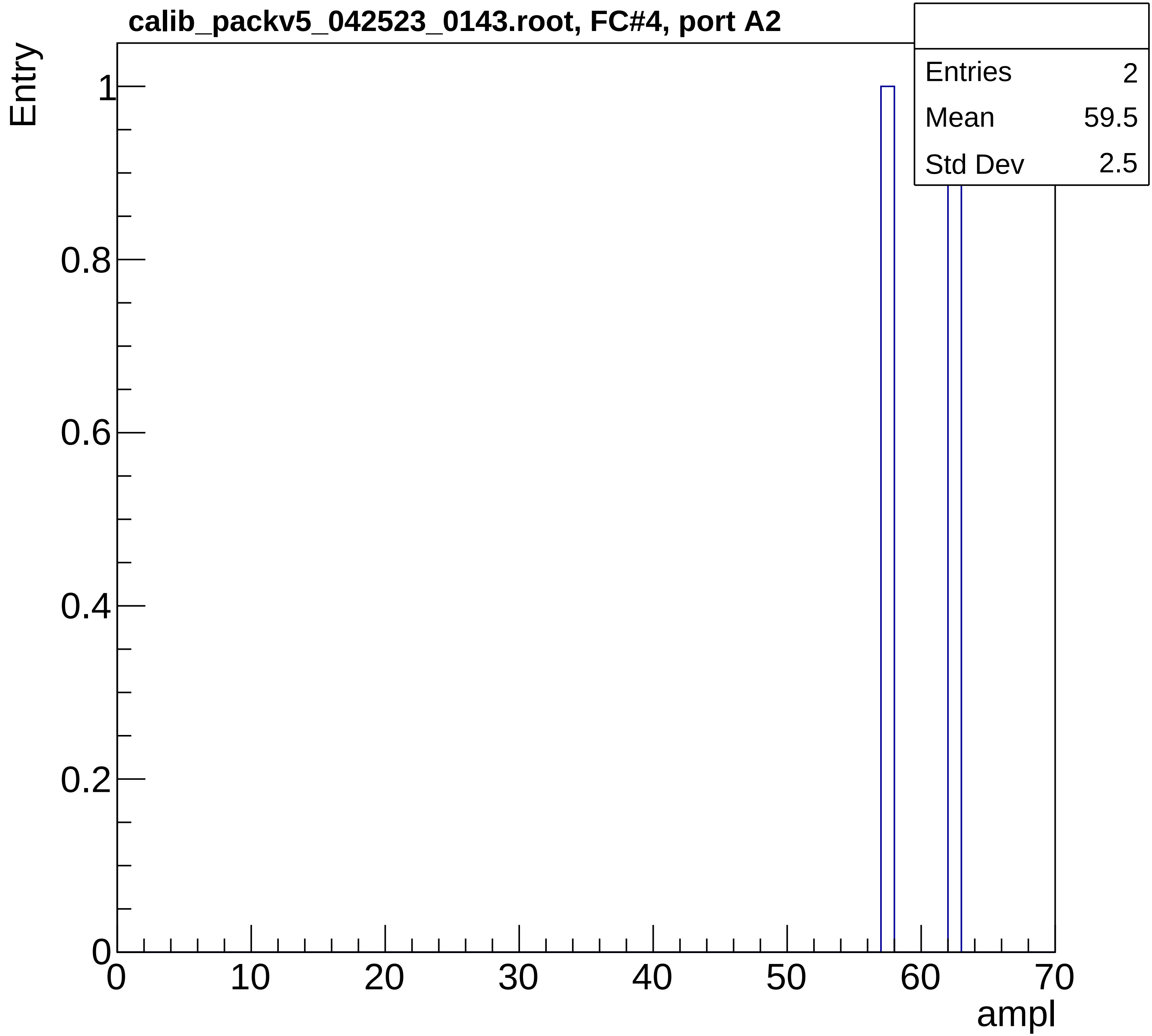


B1L100S, U21-ch76, adc5

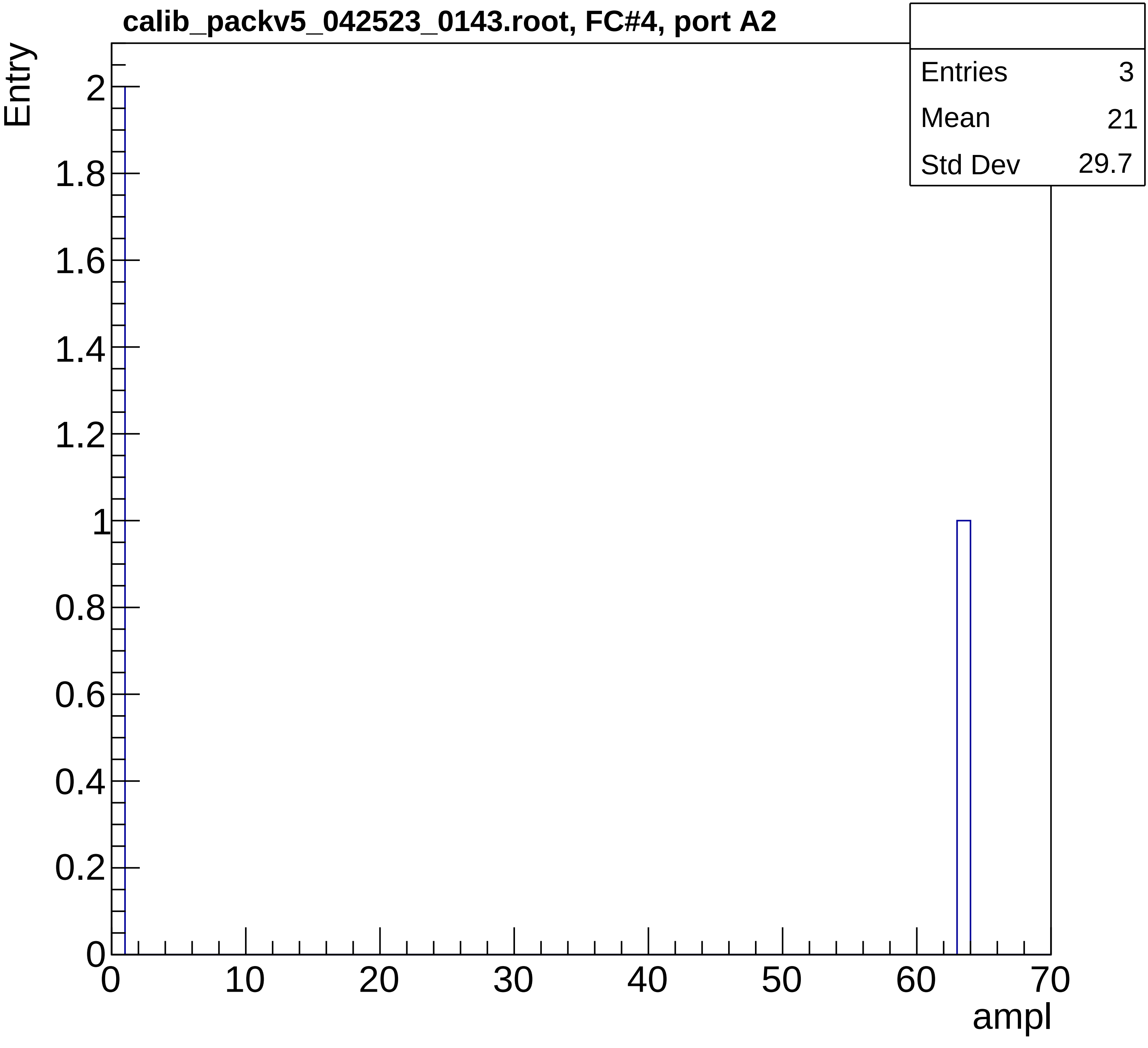


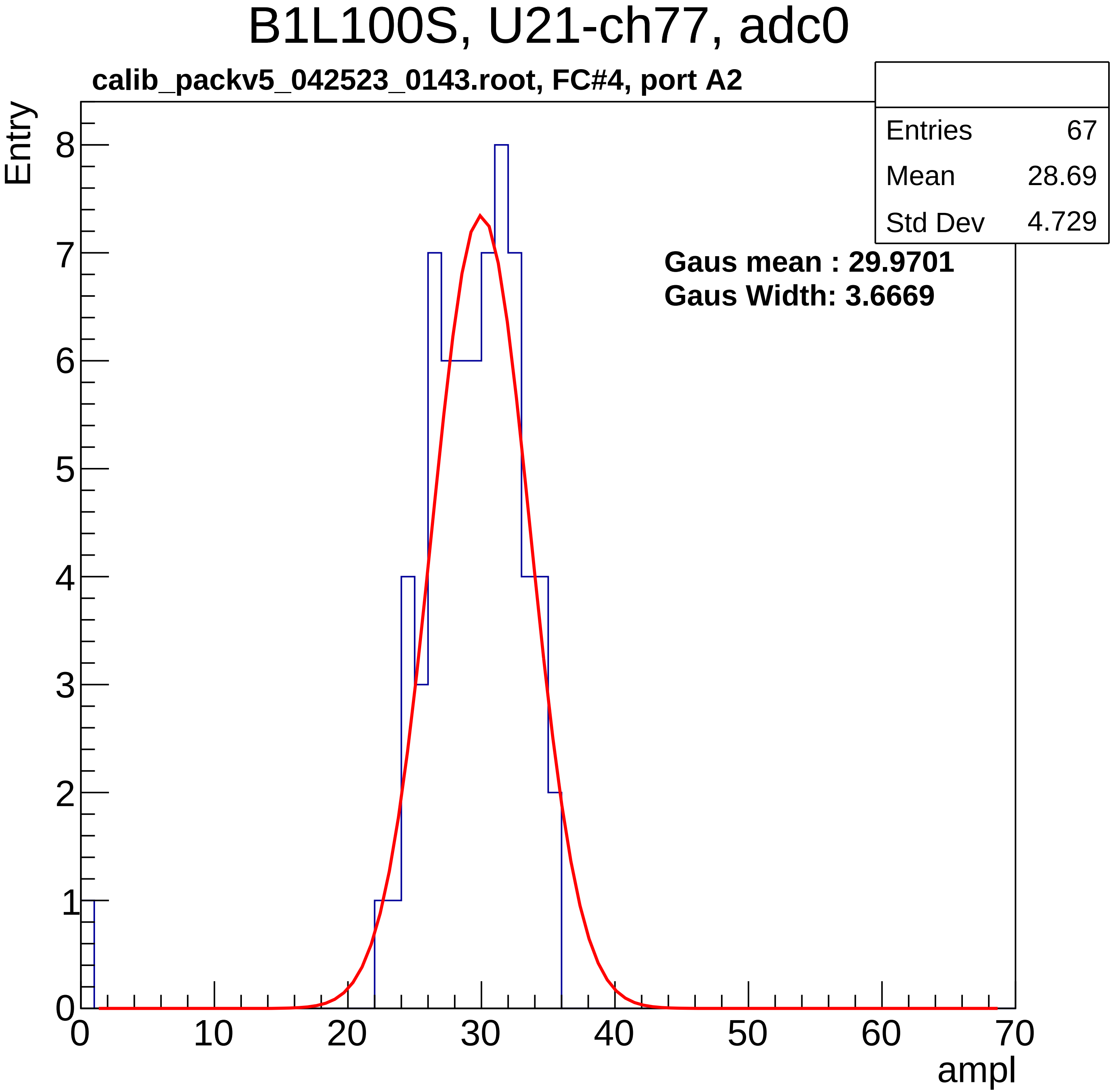
B1L100S, U21-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

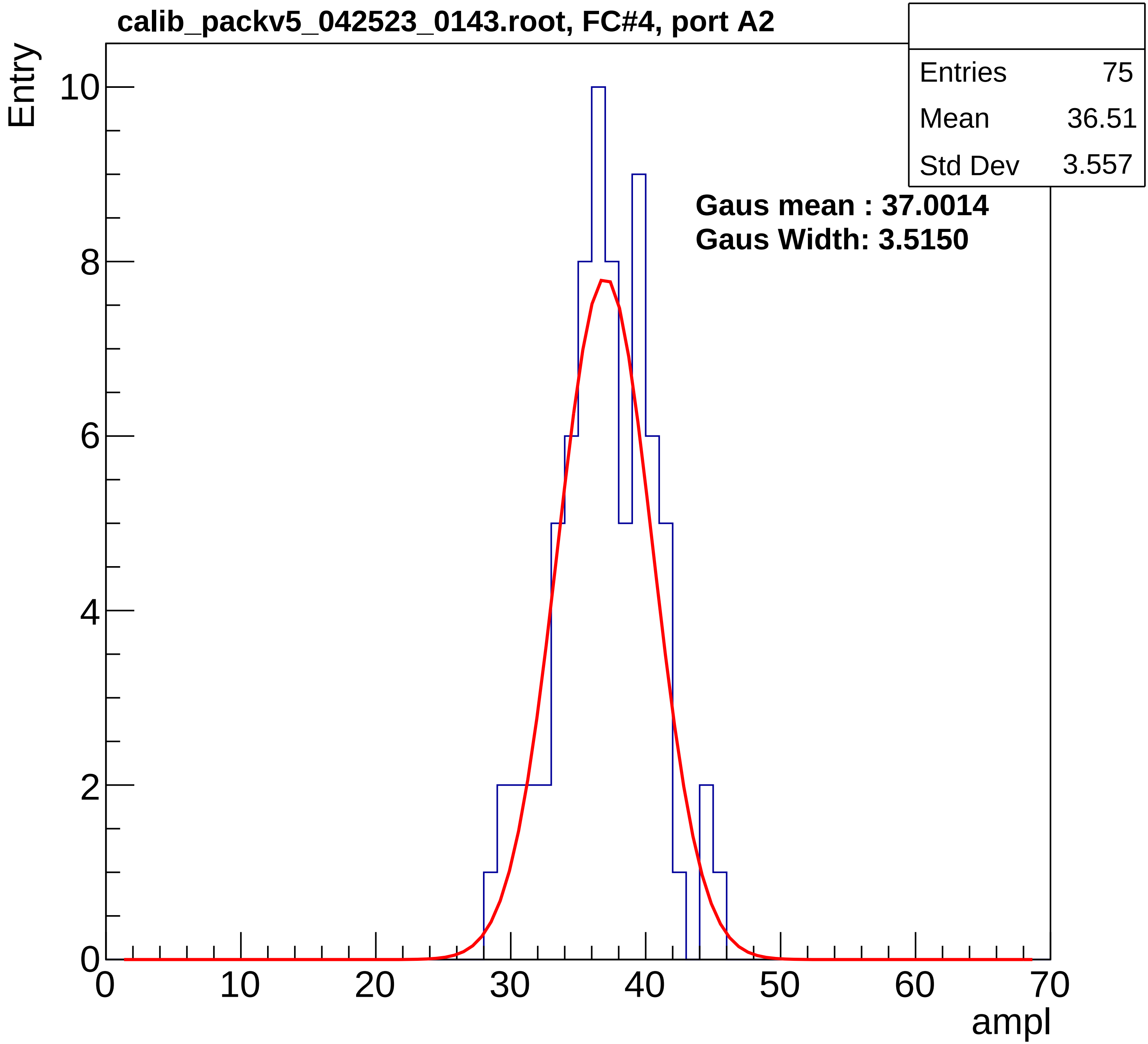


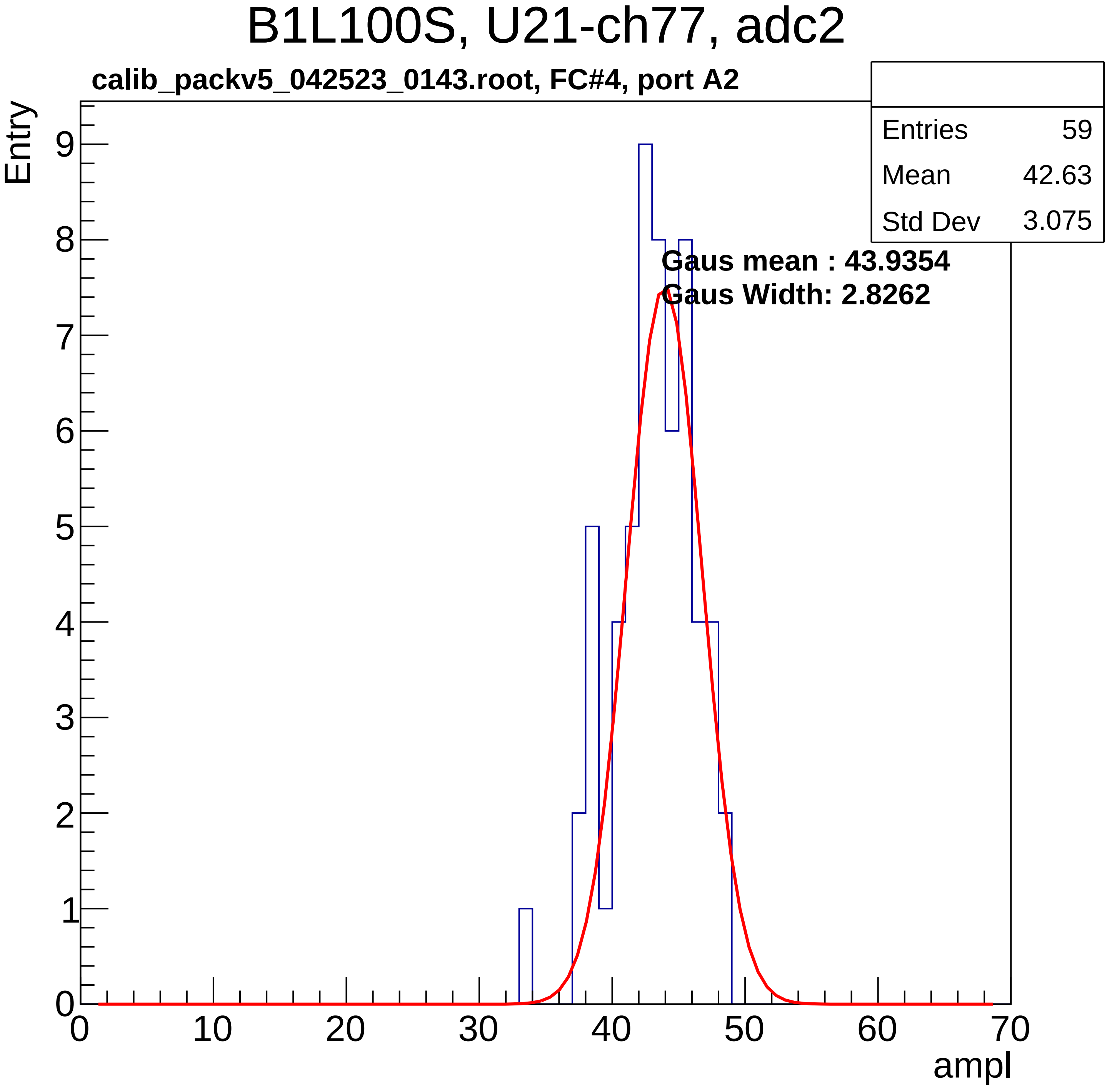
B1L100S, U21-ch76, adc7



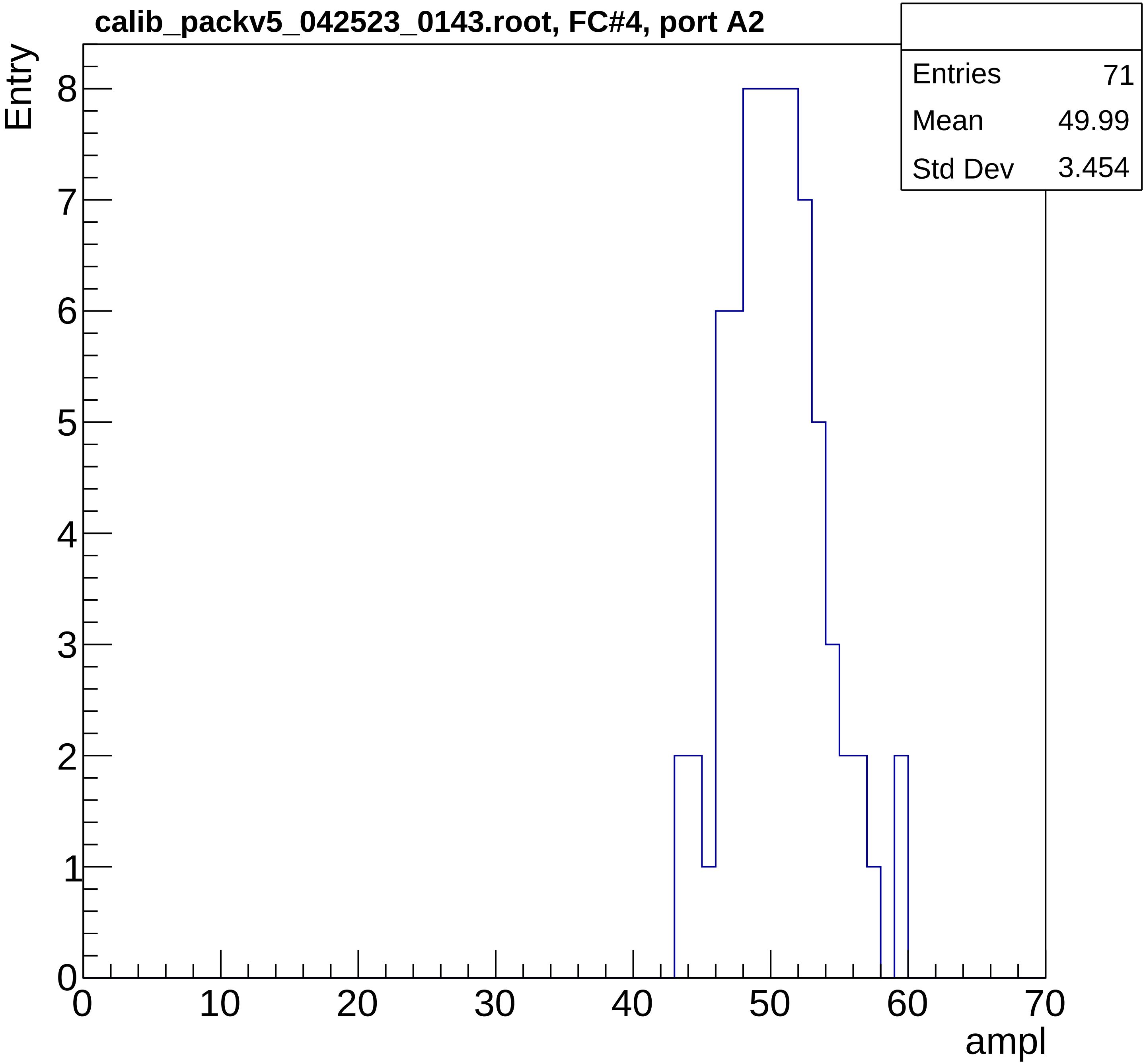

B1L100S, U21-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.69
Std Dev 4.729
Gaus mean : 29.9701
Gaus Width: 3.6669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

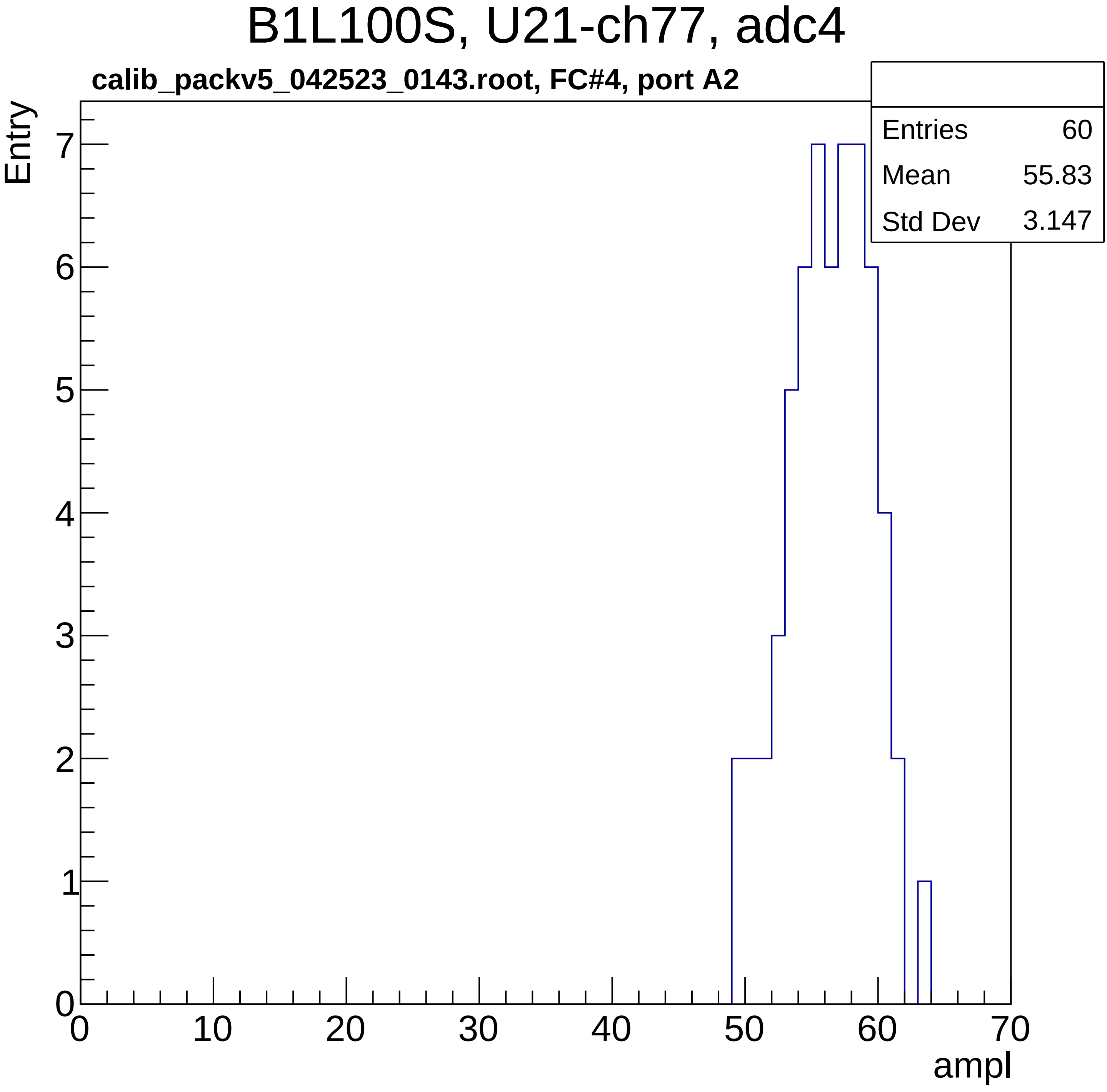
B1L100S, U21-ch77, adc1




B1L100S, U21-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.63
Std Dev 3.075
Gaus mean : 43.9354
Gaus Width: 2.8262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

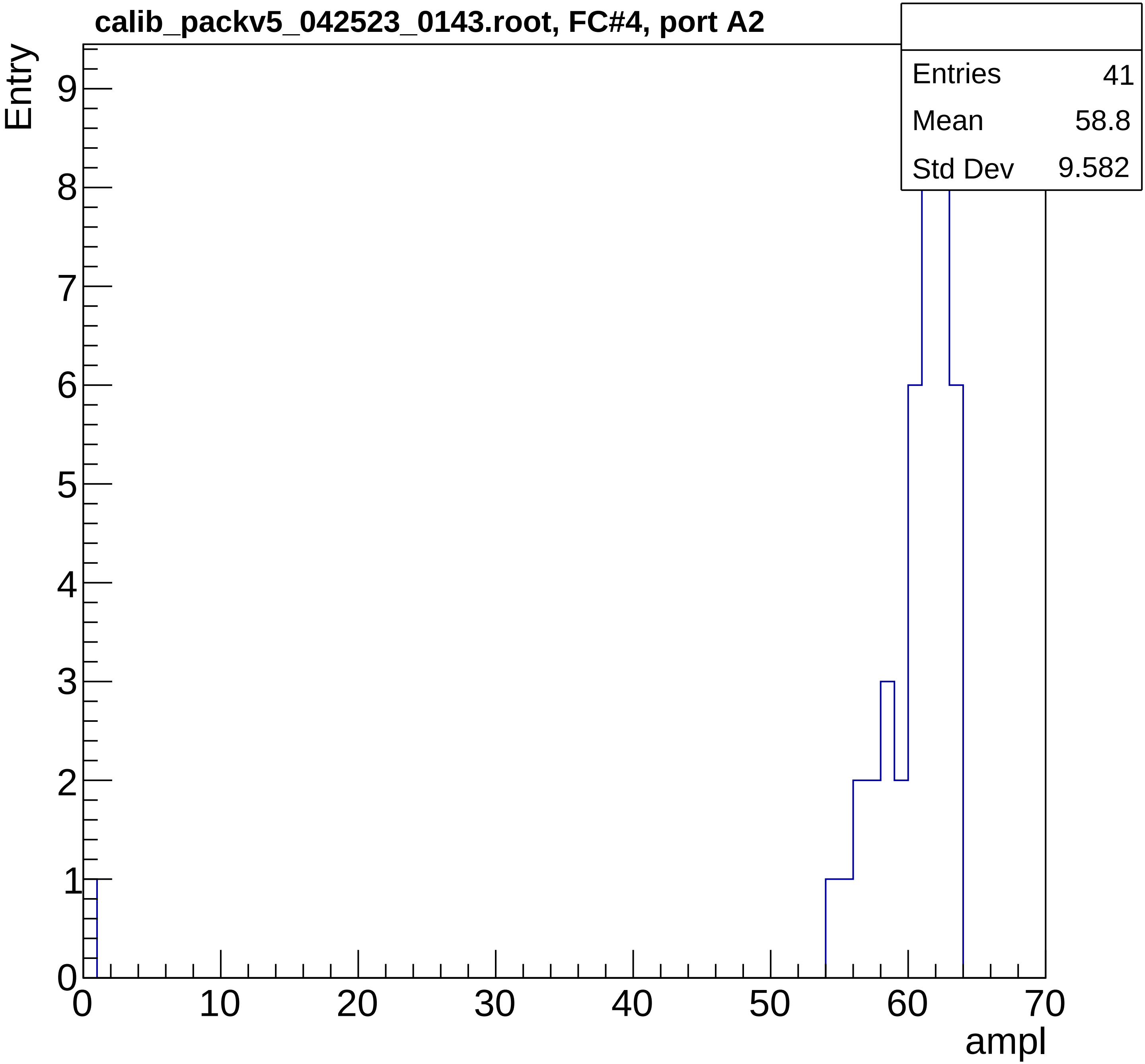
B1L100S, U21-ch77, adc3

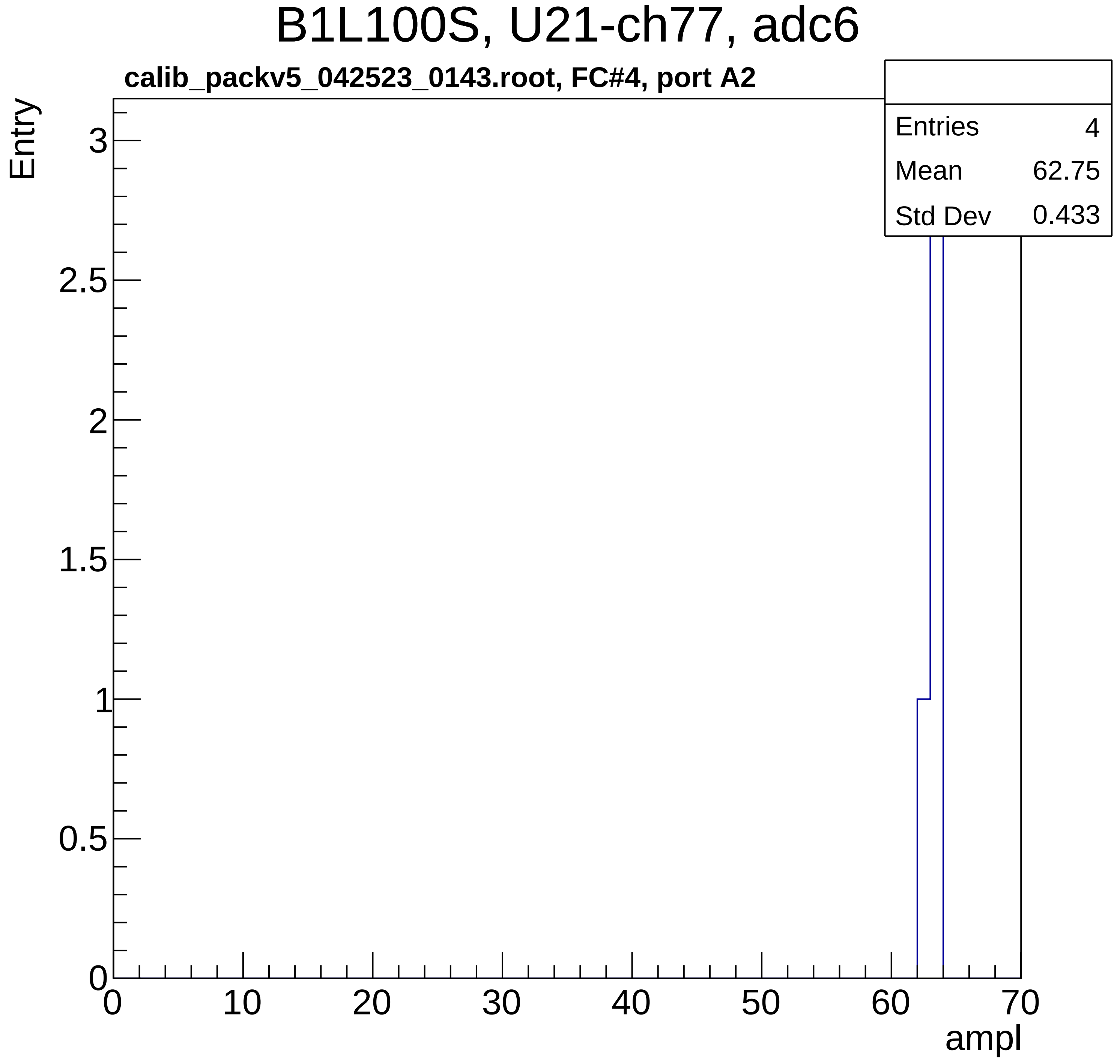


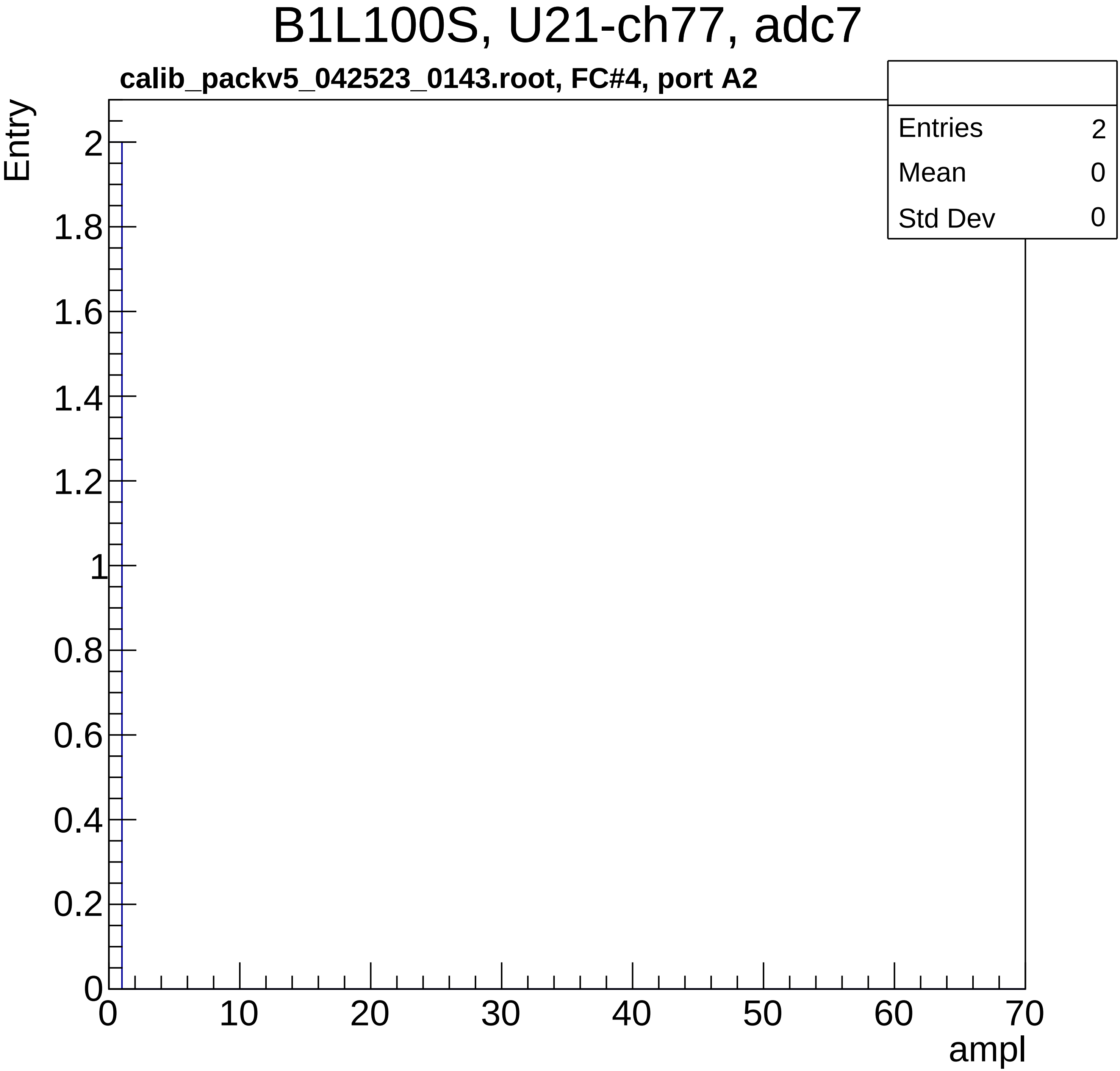

B1L100S, U21-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.83
Std Dev 3.147
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

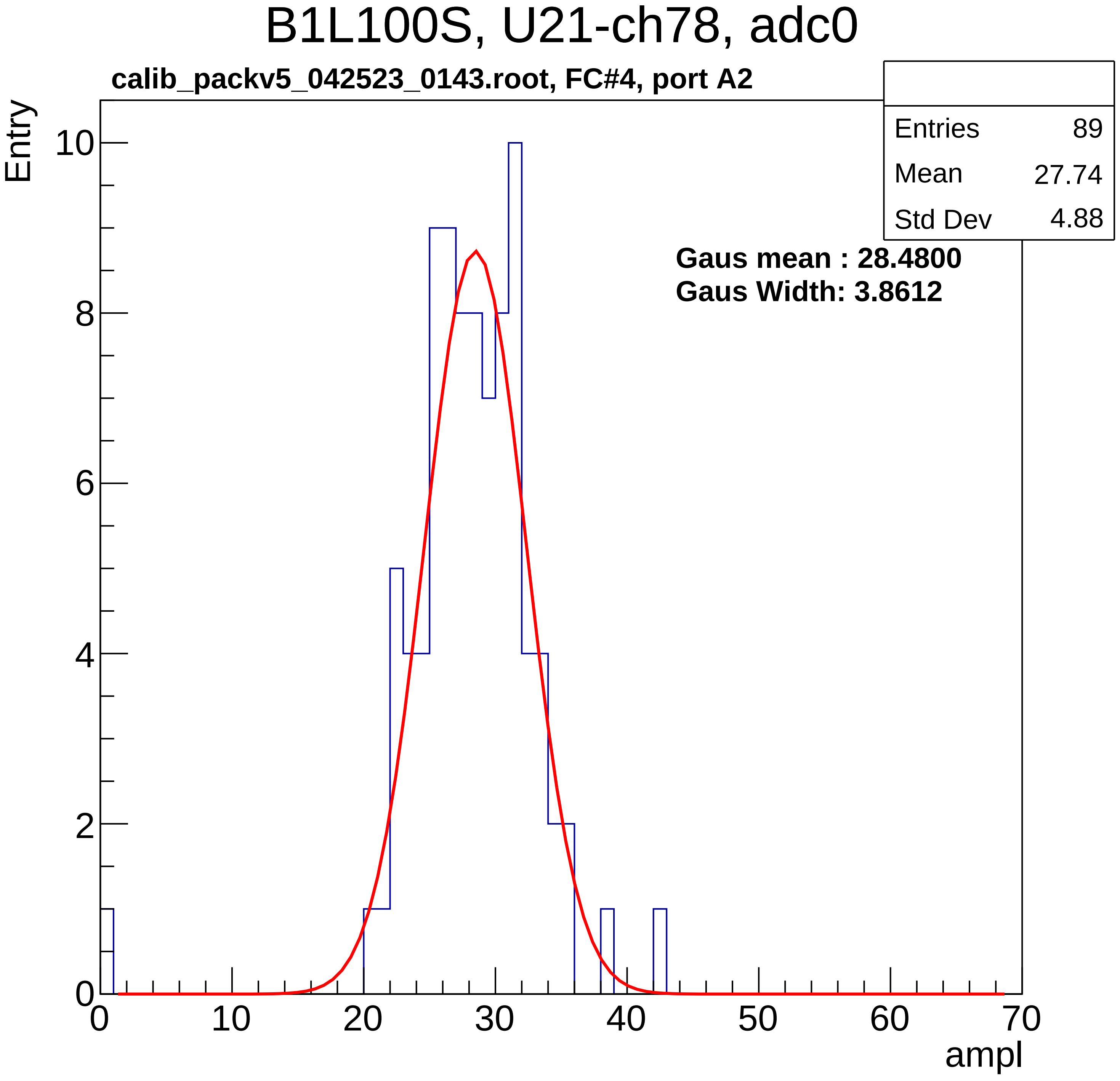
B1L100S, U21-ch77, adc5

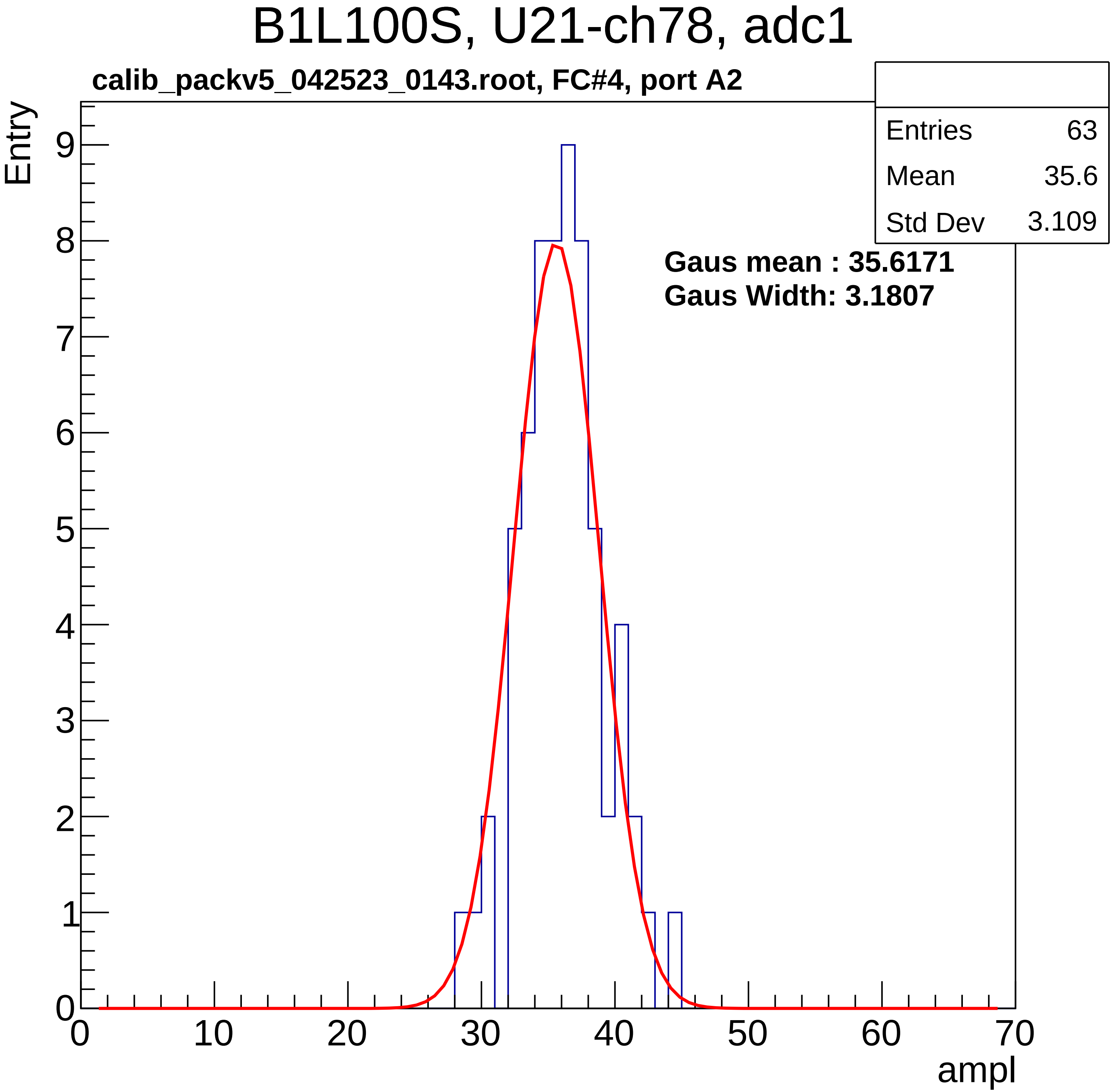
calib_packv5_042523_0143.root, FC#4, port A2

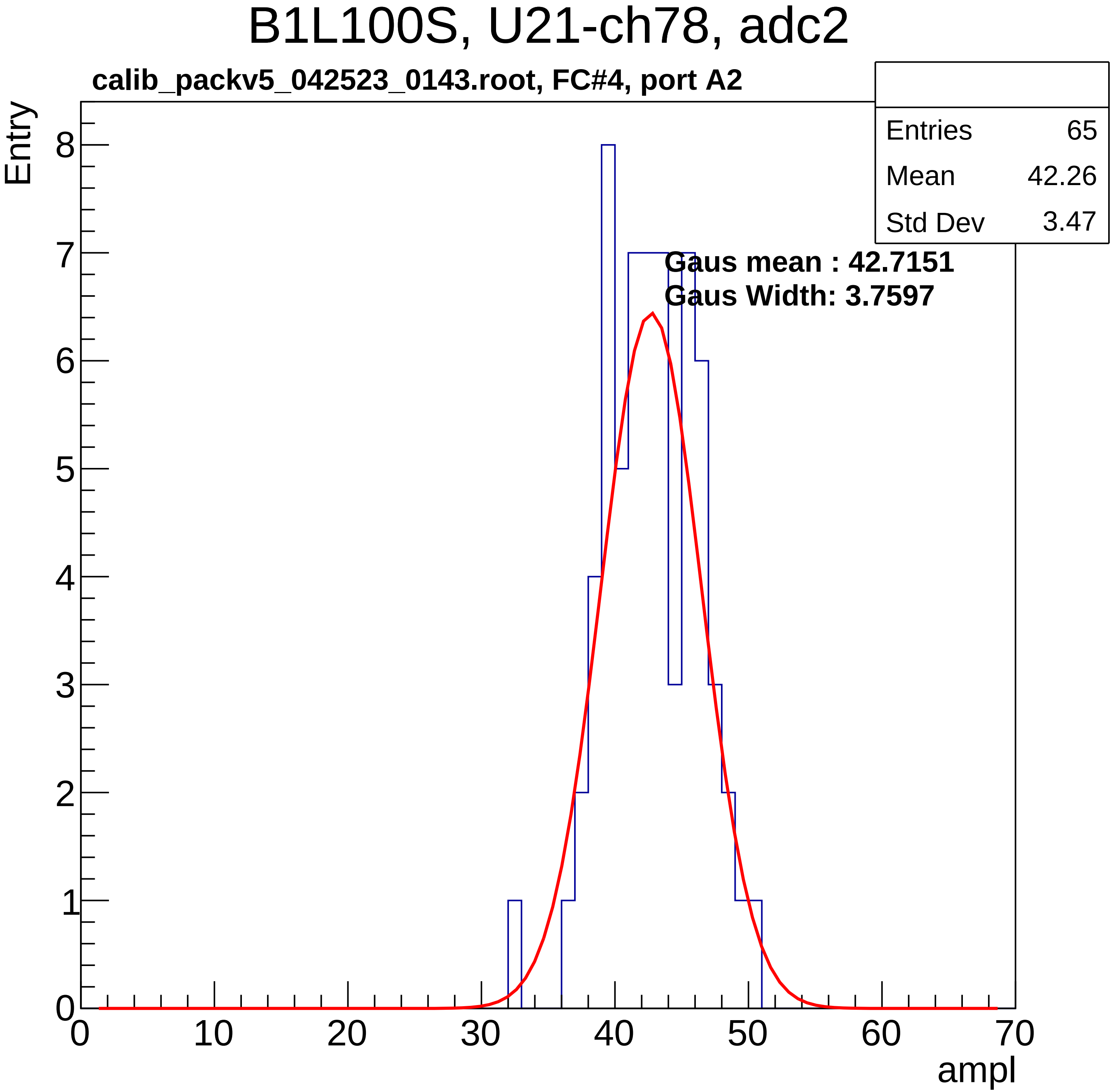



B1L100S, U21-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

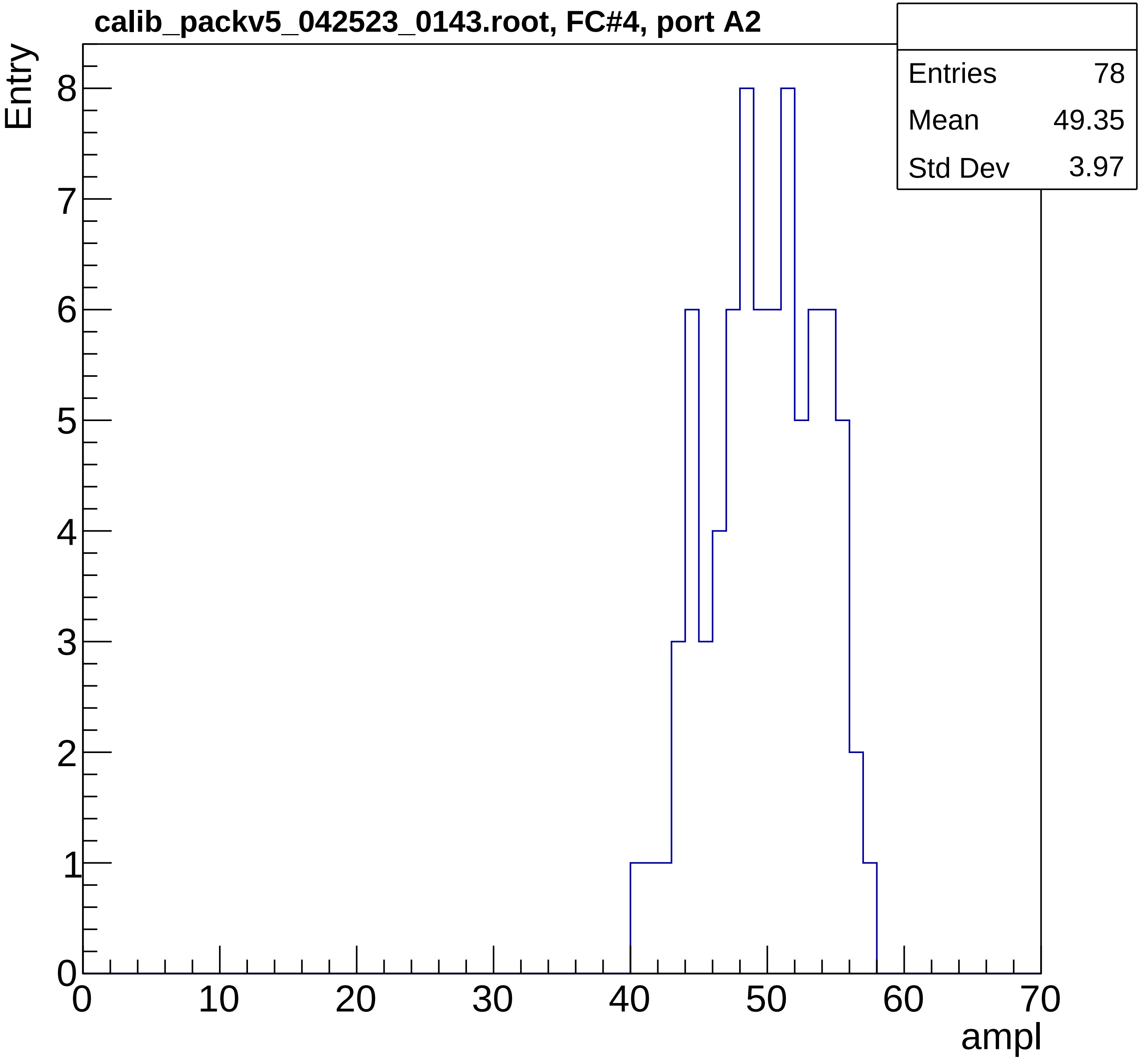

B1L100S, U21-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

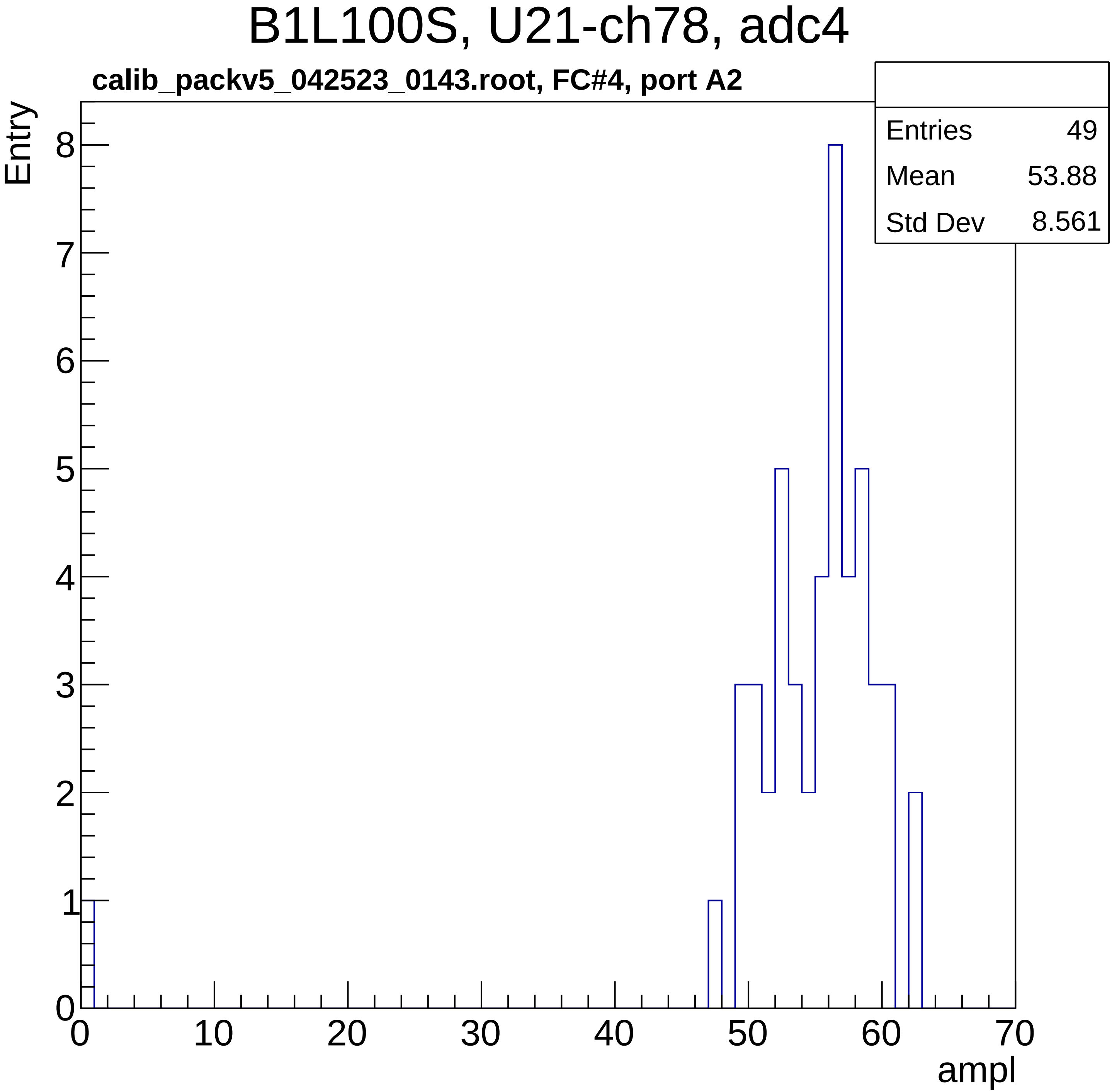

B1L100S, U21-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 27.74
Std Dev 4.88
Gaus mean : 28.4800
Gaus Width: 3.8612
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

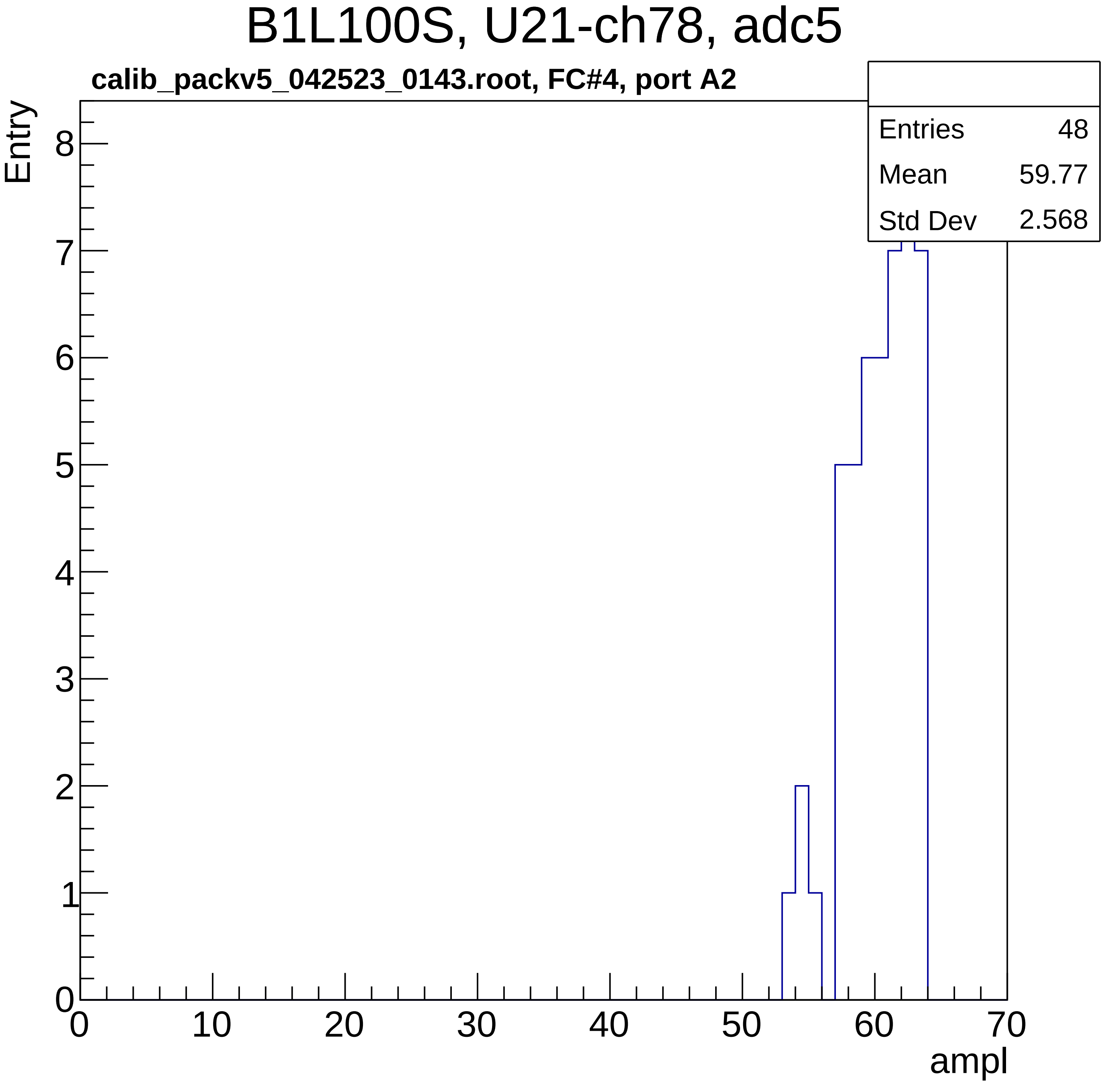

B1L100S, U21-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.6
Std Dev 3.109
Gaus mean : 35.6171
Gaus Width: 3.1807
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

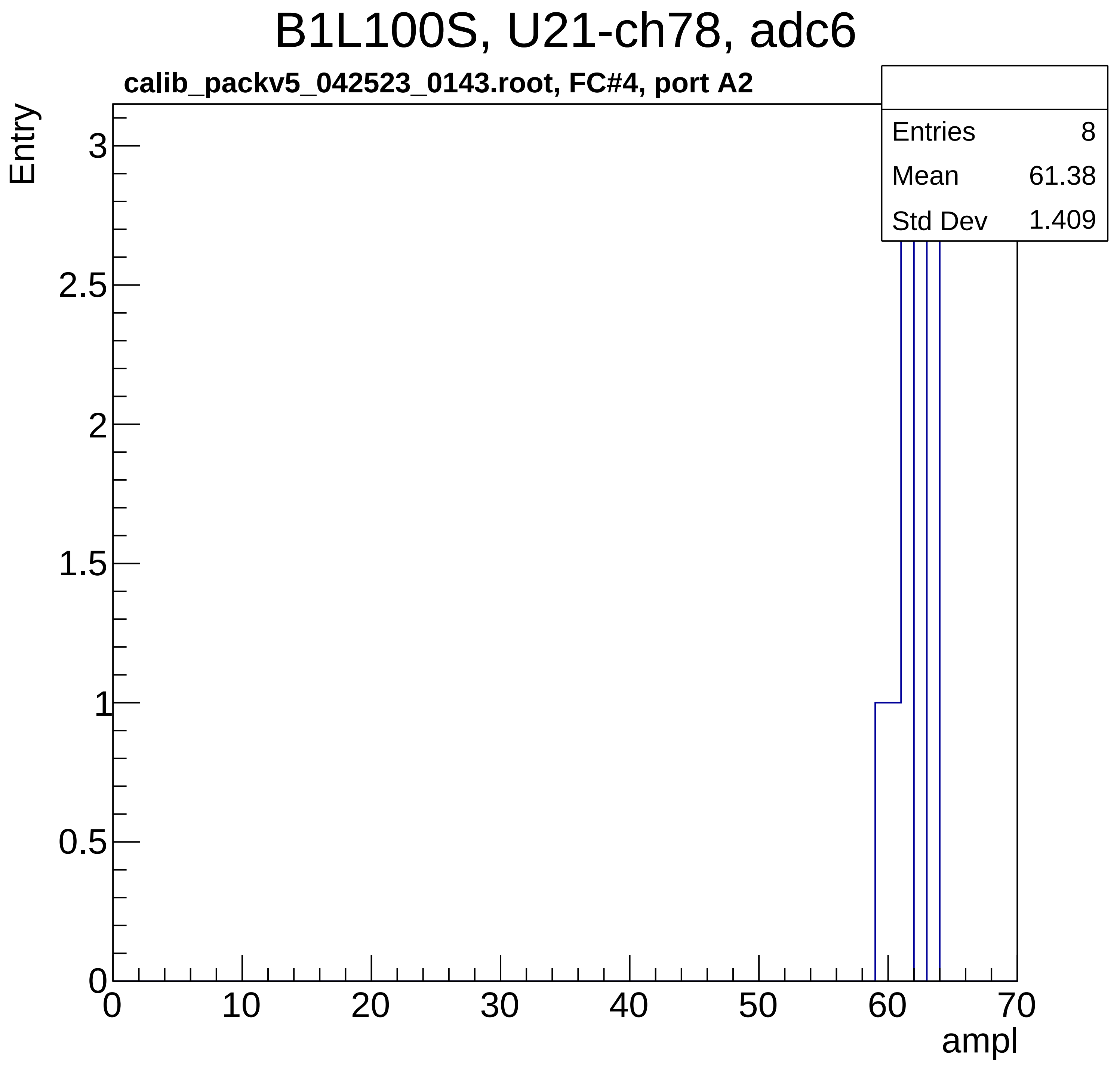

B1L100S, U21-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.26
Std Dev 3.47
Gaus mean : 42.7151
Gaus Width: 3.7597
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

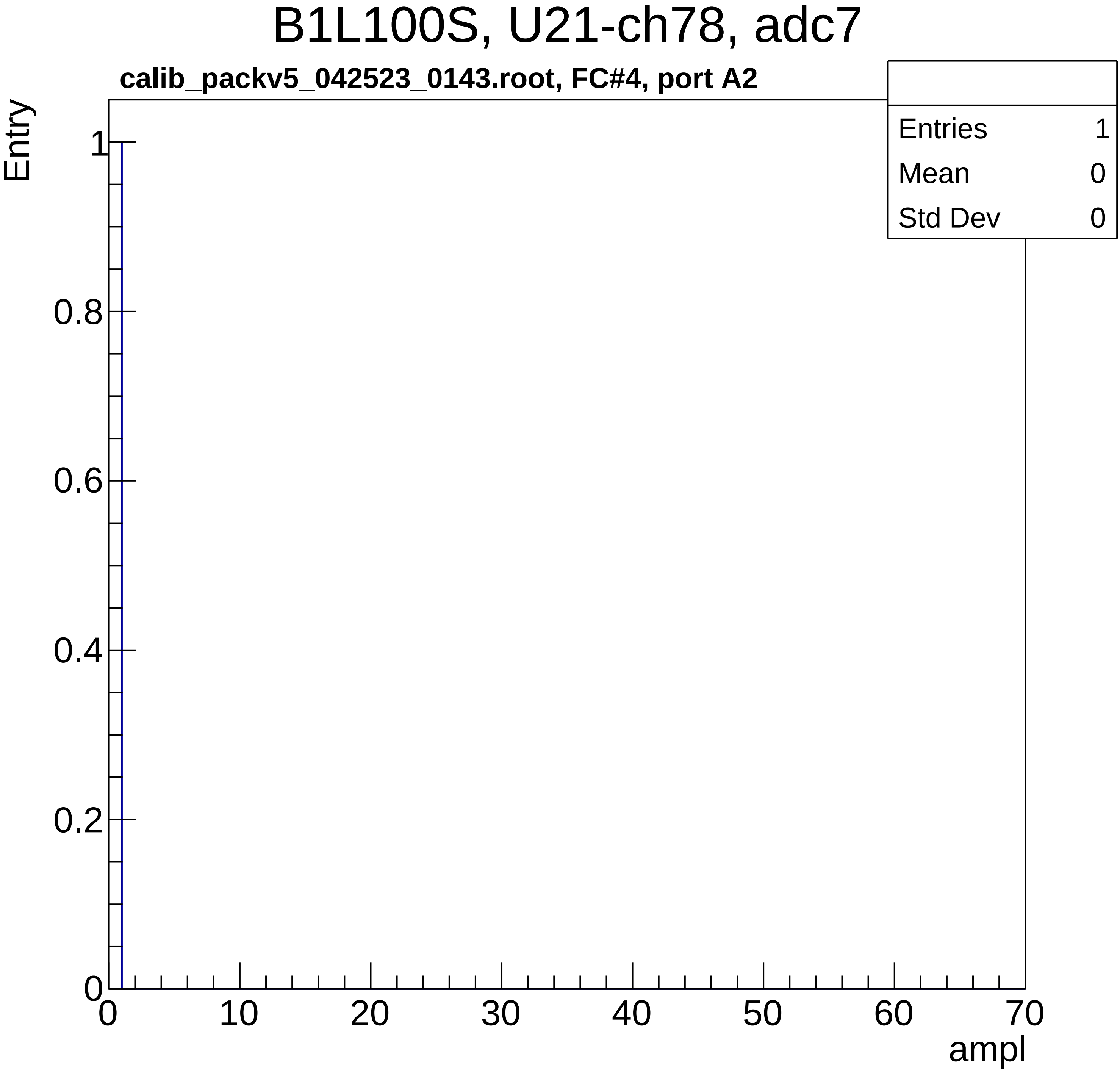
B1L100S, U21-ch78, adc3

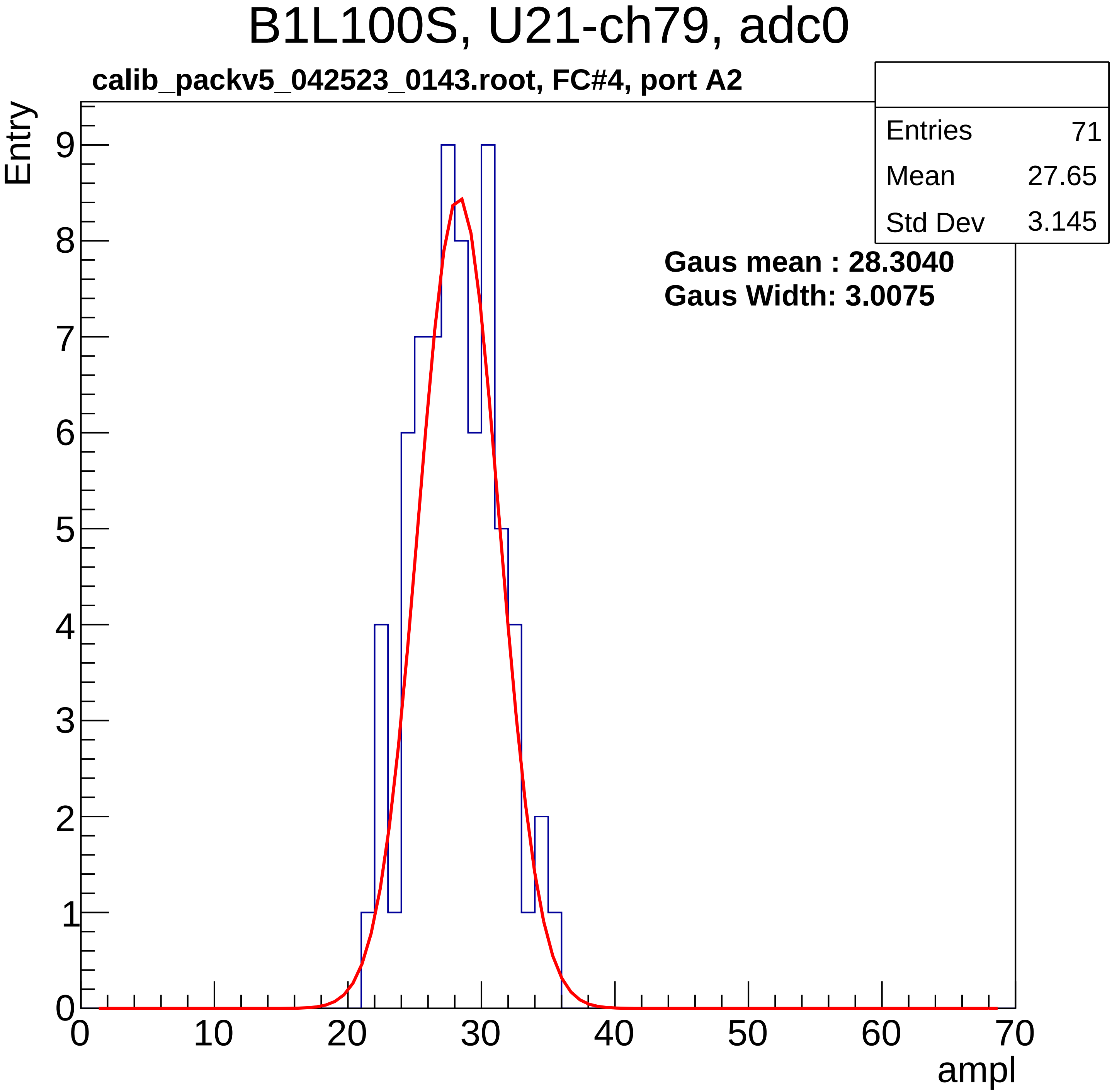


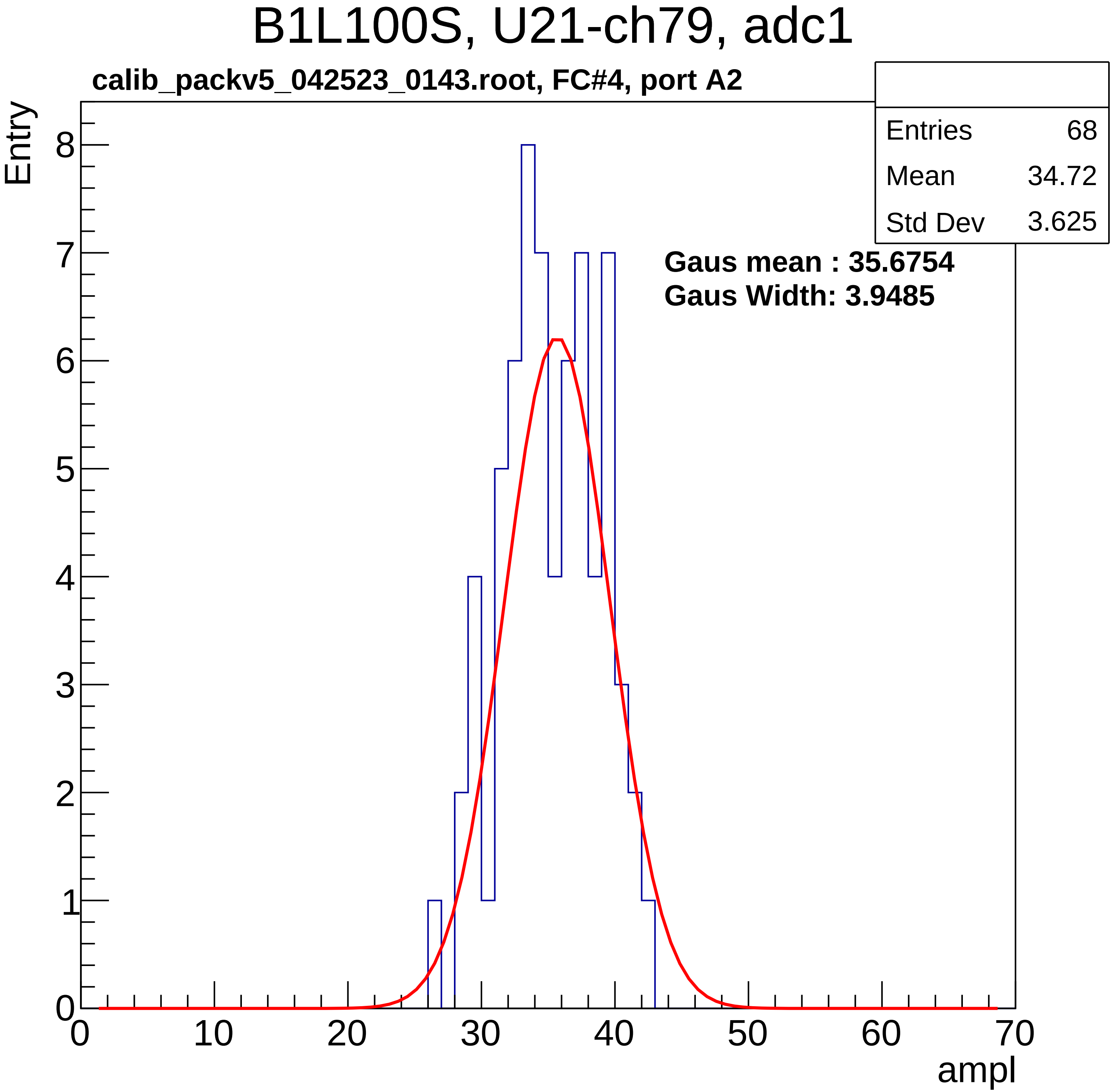

B1L100S, U21-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 53.88
Std Dev 8.561
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

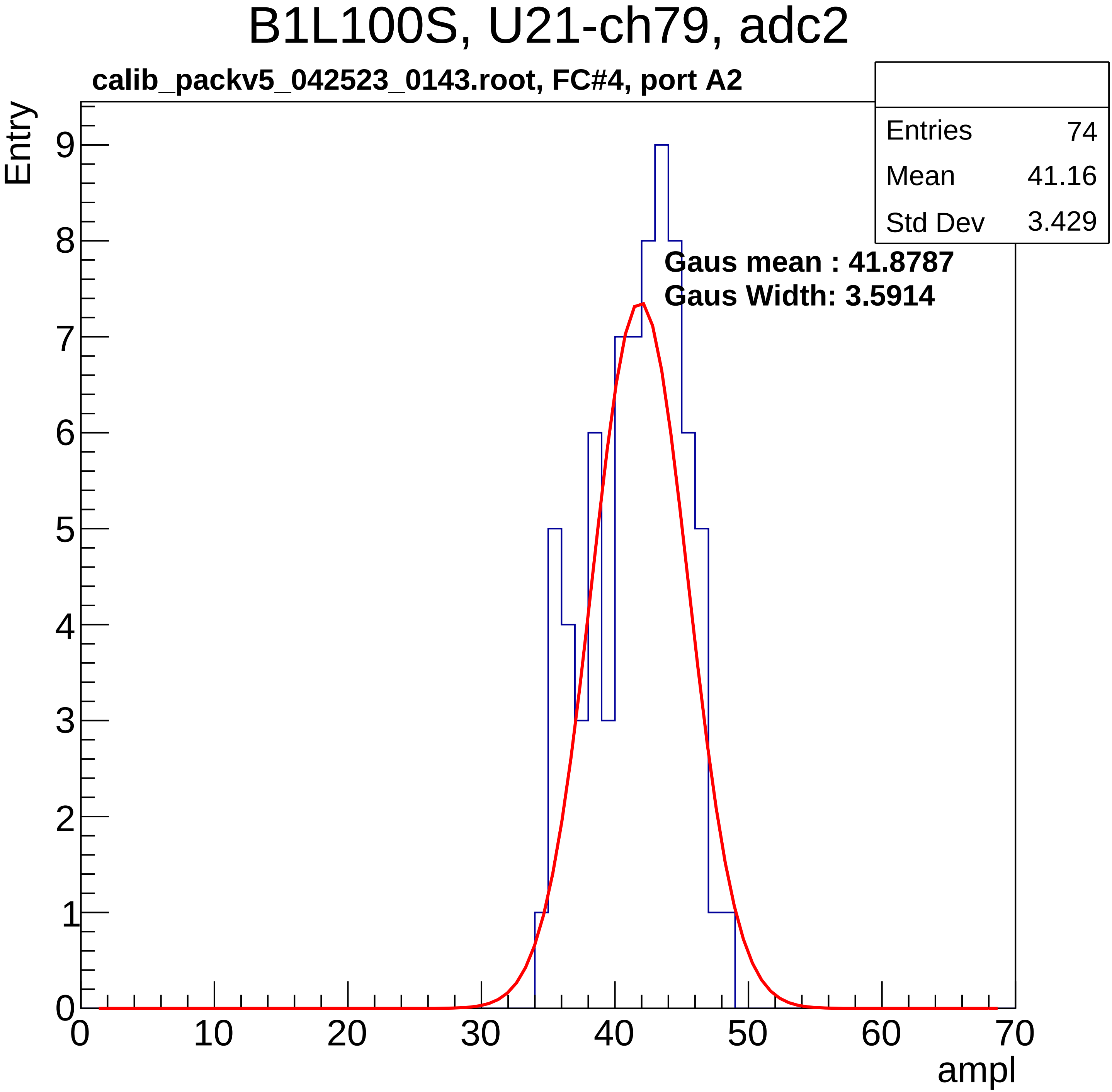

B1L100S, U21-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.77
Std Dev 2.568
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

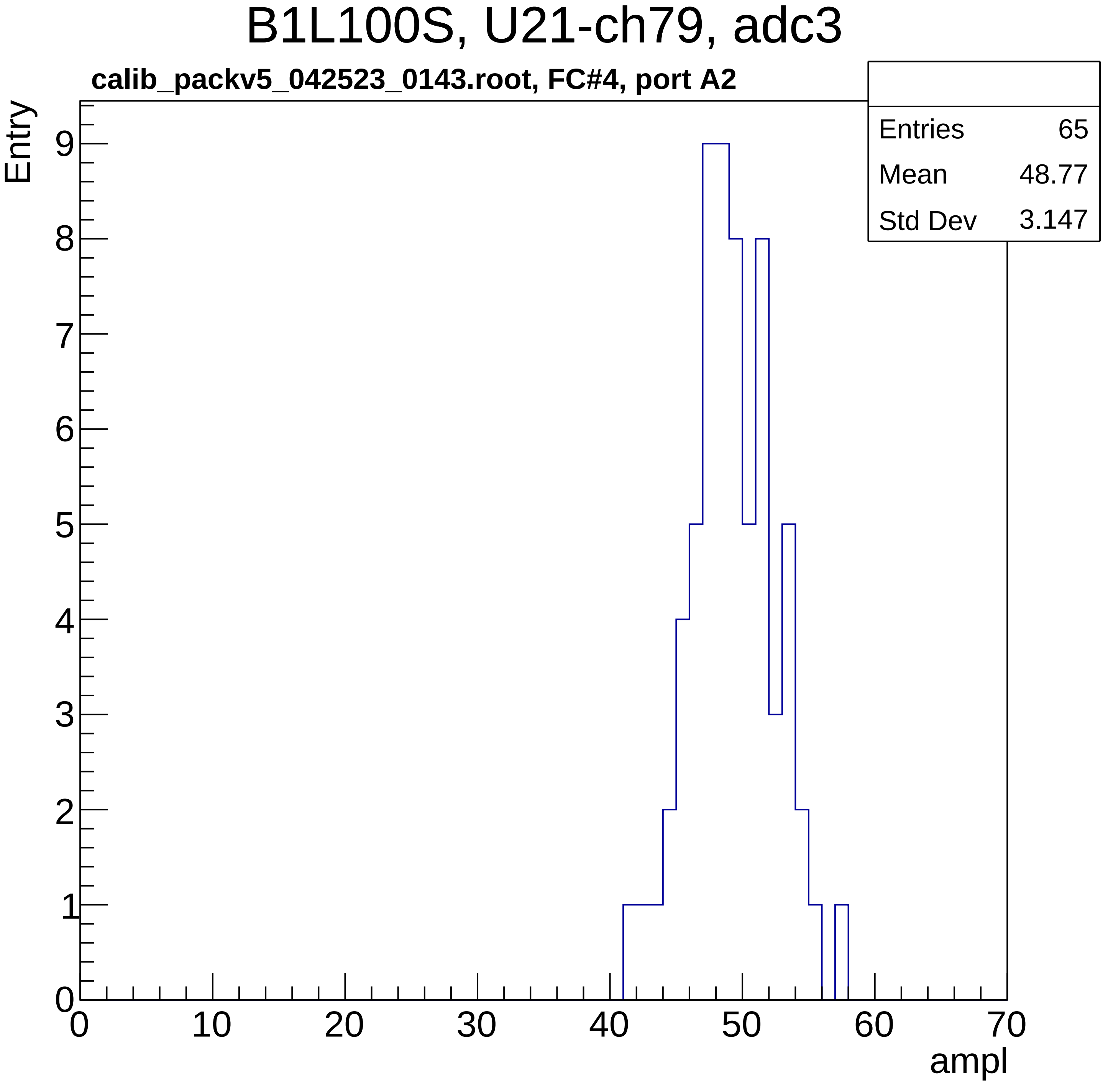

B1L100S, U21-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.38
Std Dev 1.409
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 27.65
Std Dev 3.145
Gaus mean : 28.3040
Gaus Width: 3.0075
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

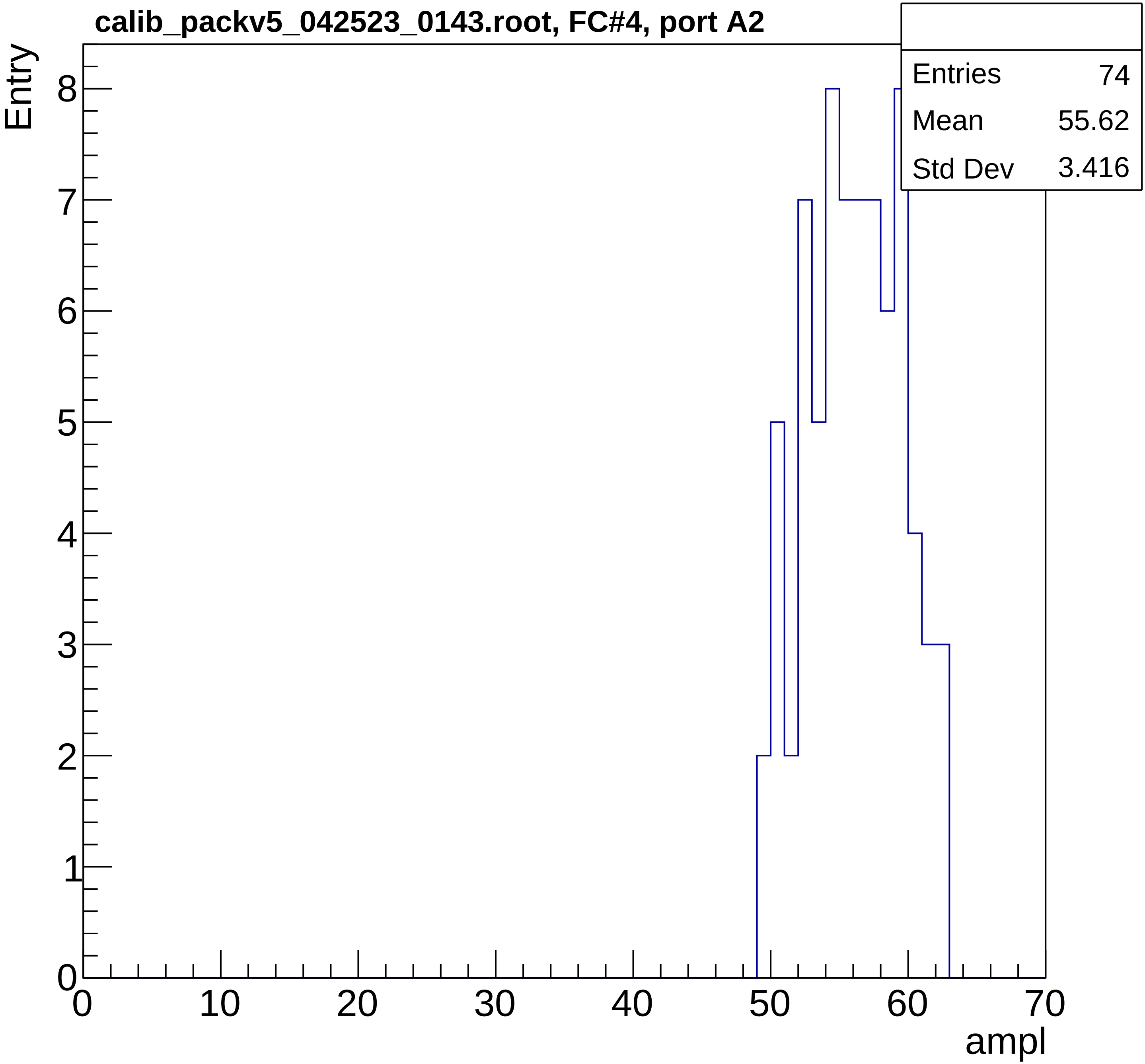

B1L100S, U21-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 34.72
Std Dev 3.625
Gaus mean : 35.6754
Gaus Width: 3.9485
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

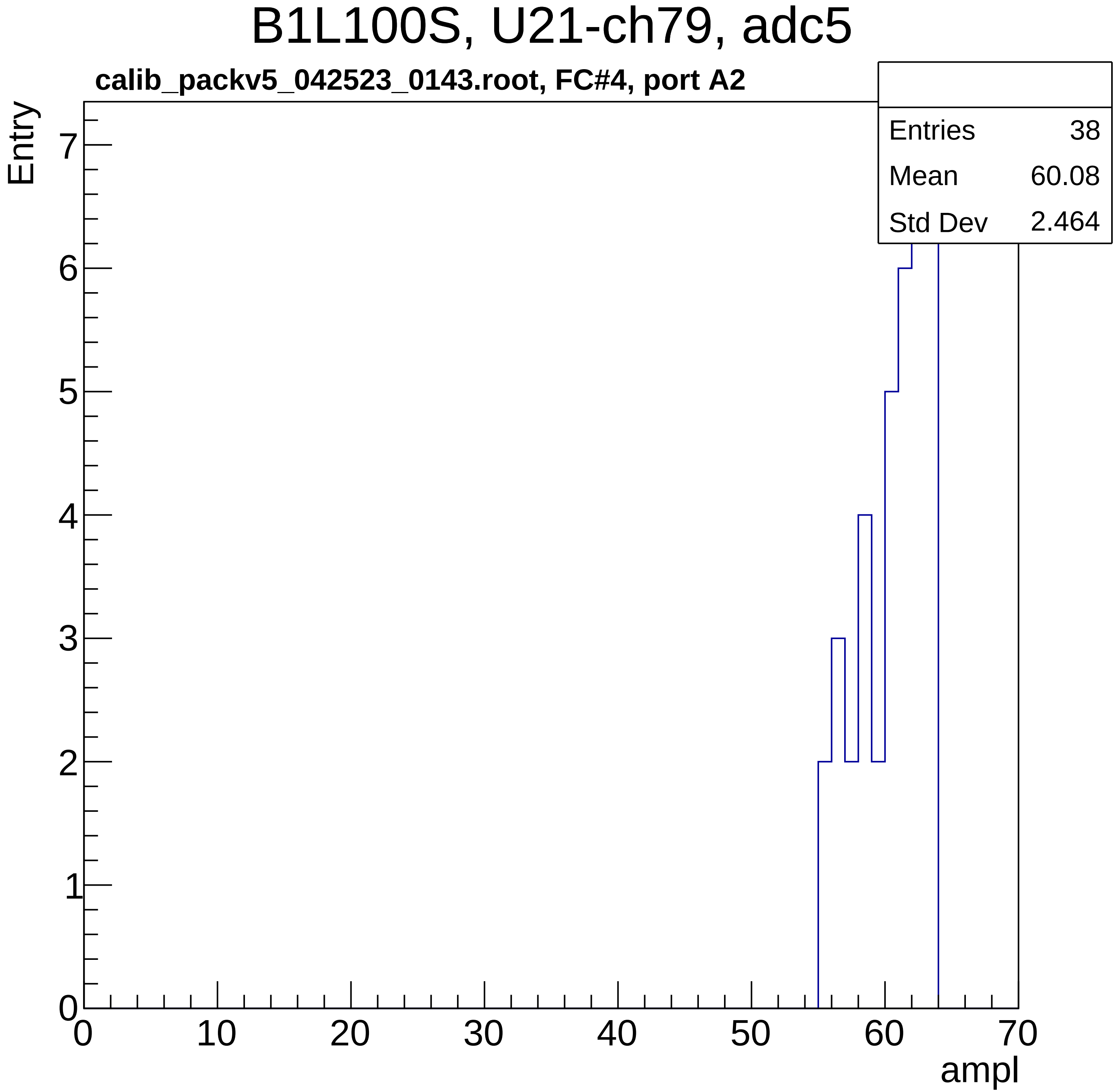

B1L100S, U21-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 41.16
Std Dev 3.429
Gaus mean : 41.8787
Gaus Width: 3.5914
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 48.77
Std Dev 3.147
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

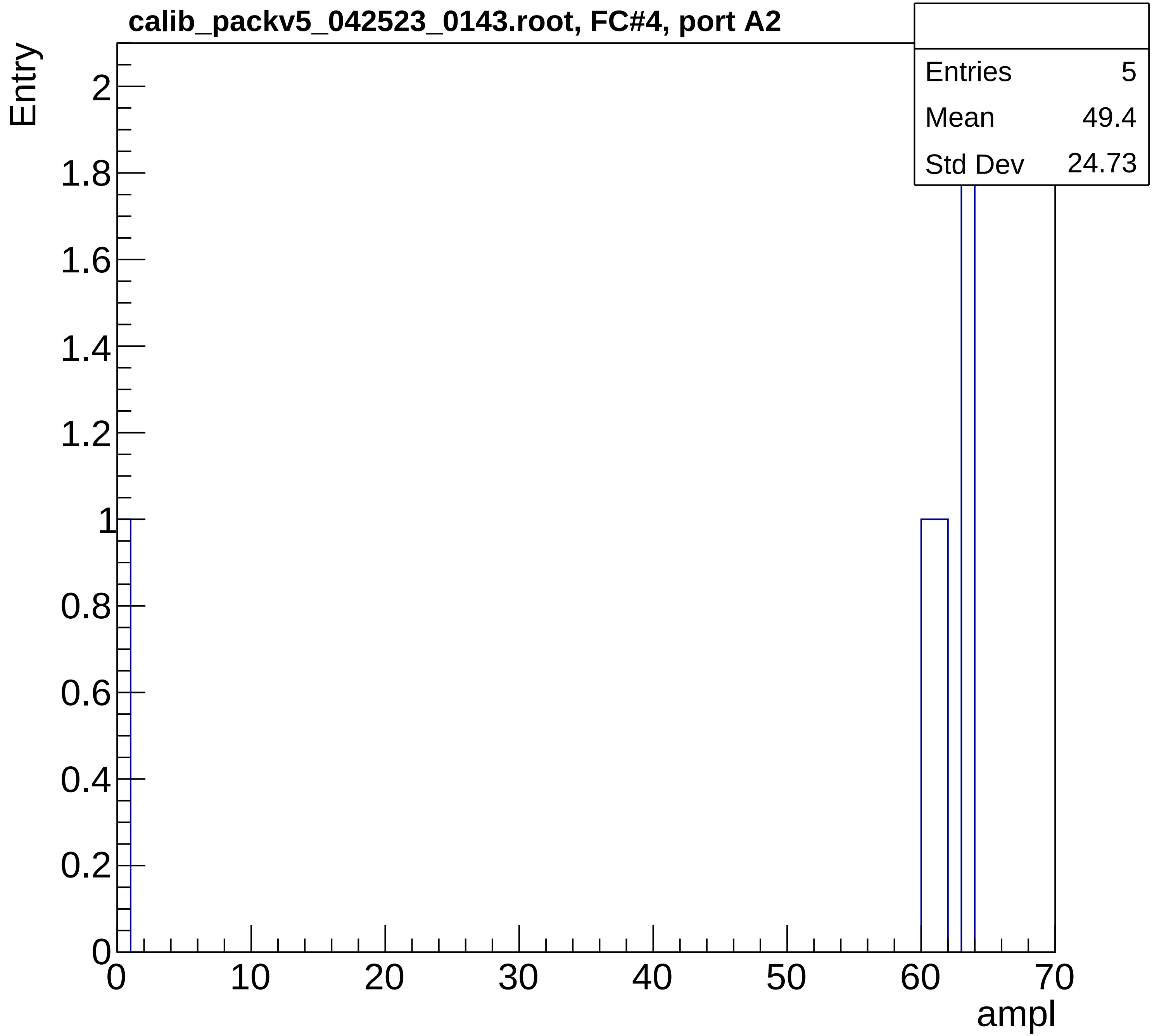
B1L100S, U21-ch79, adc4

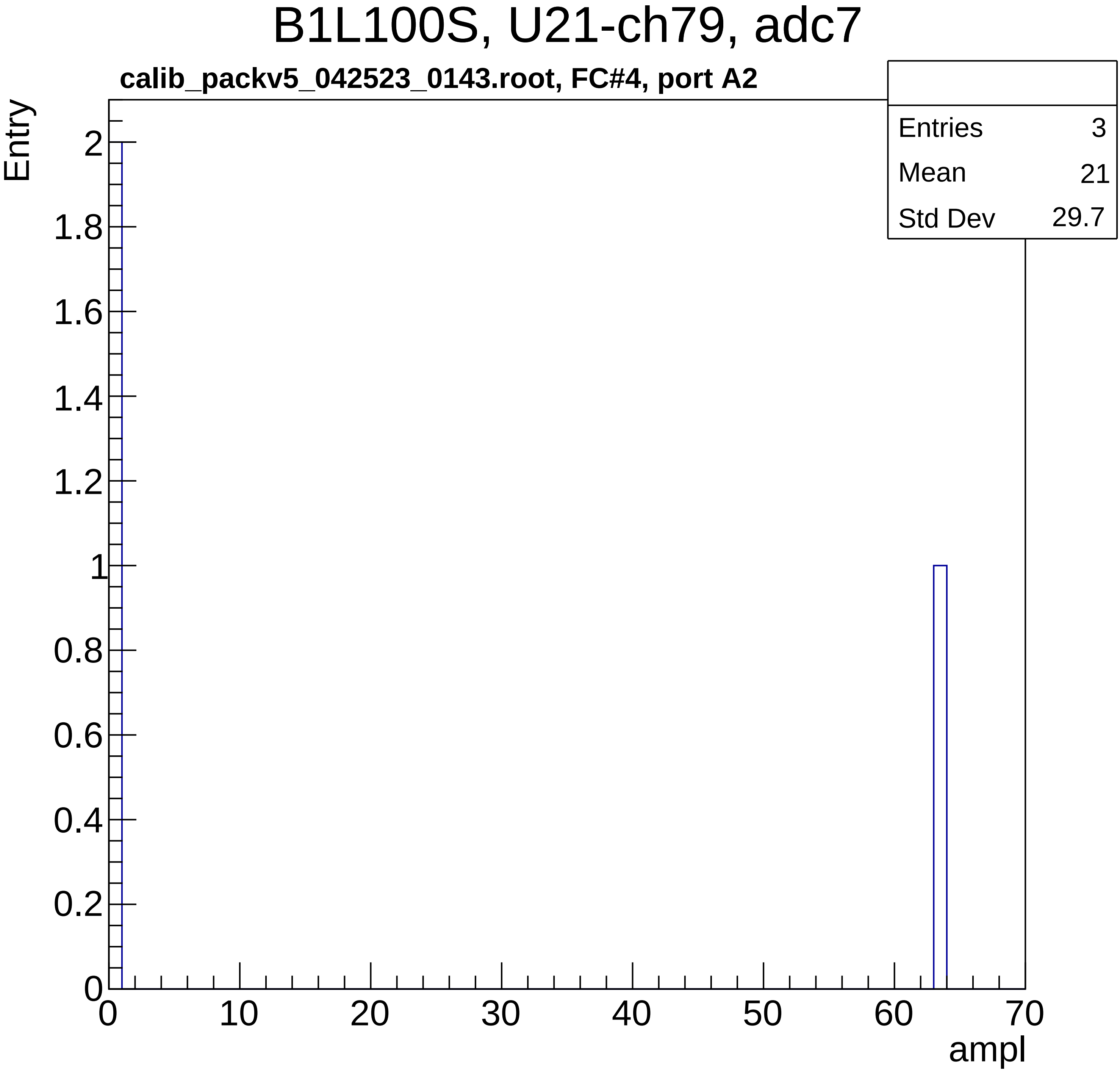
calib_packv5_042523_0143.root, FC#4, port A2

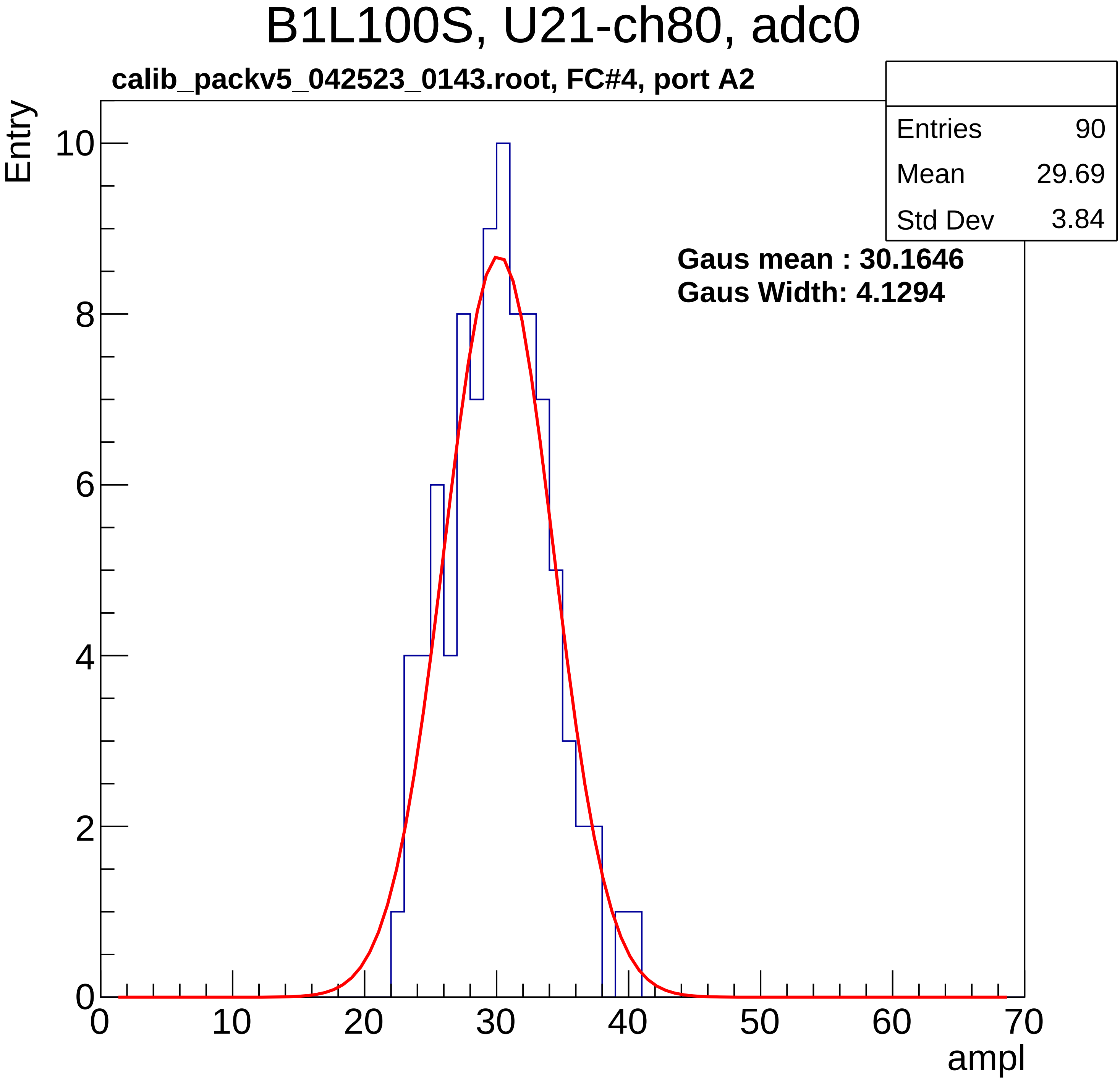


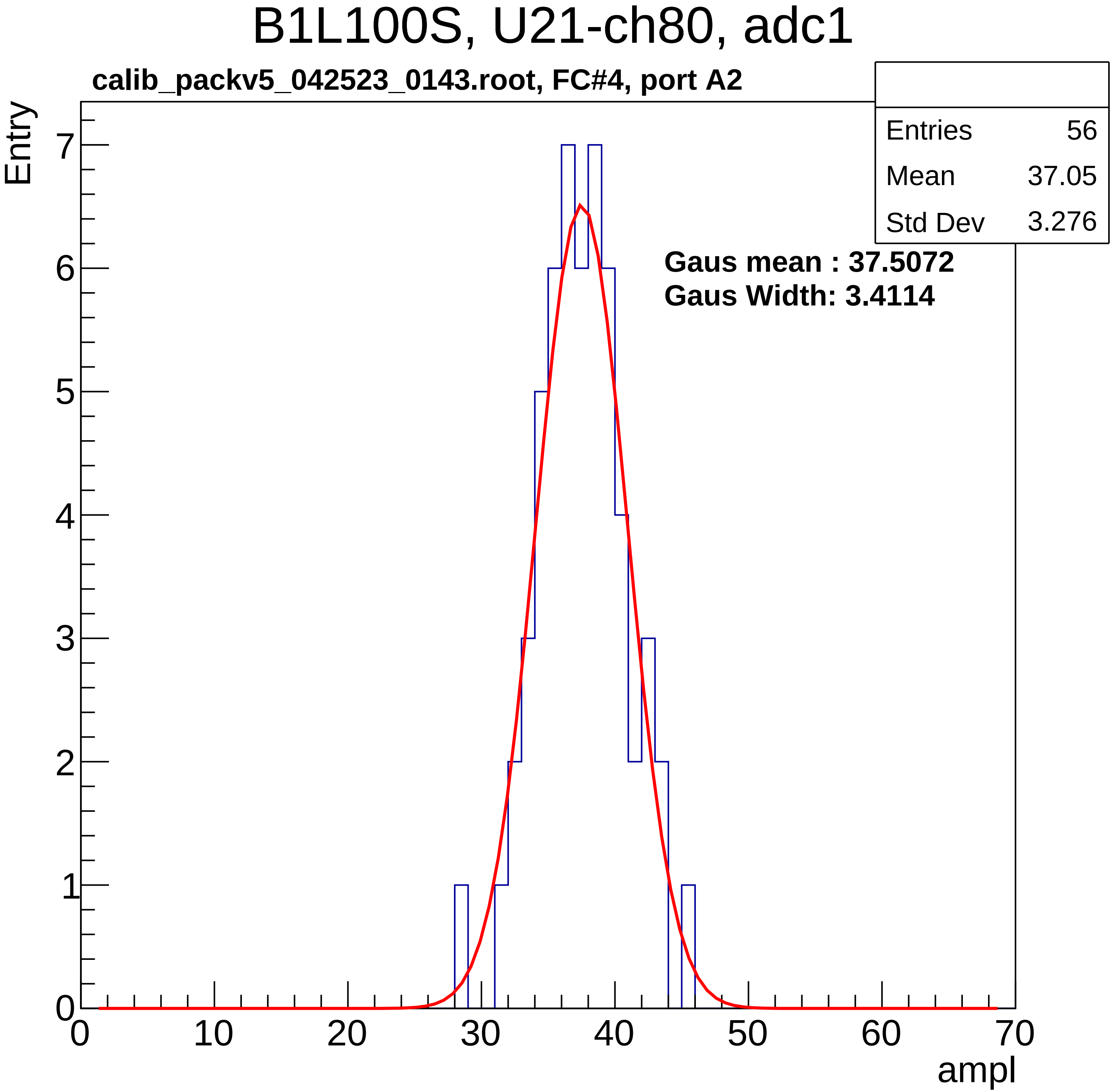

B1L100S, U21-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.08
Std Dev 2.464
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

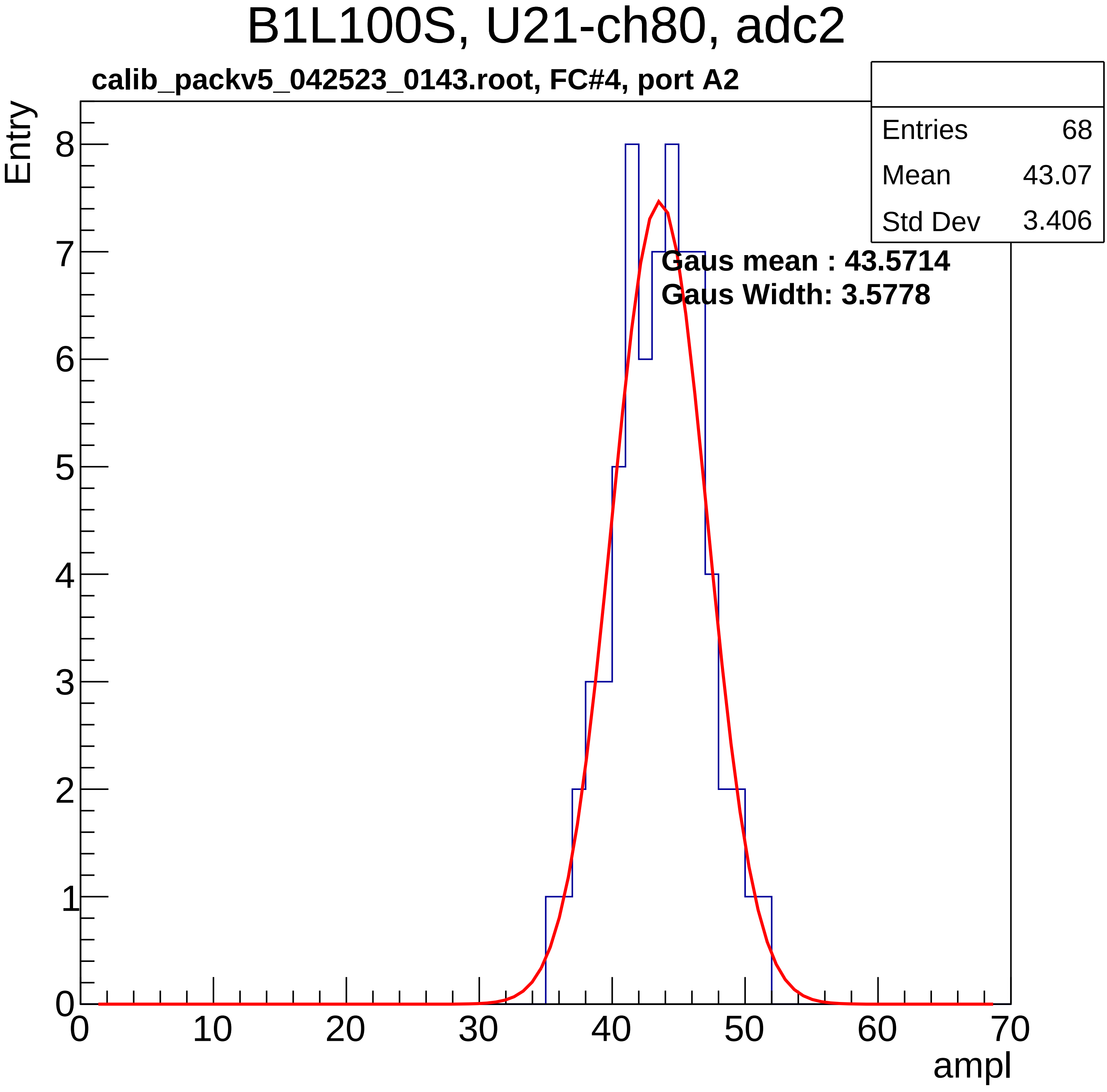
B1L100S, U21-ch79, adc6

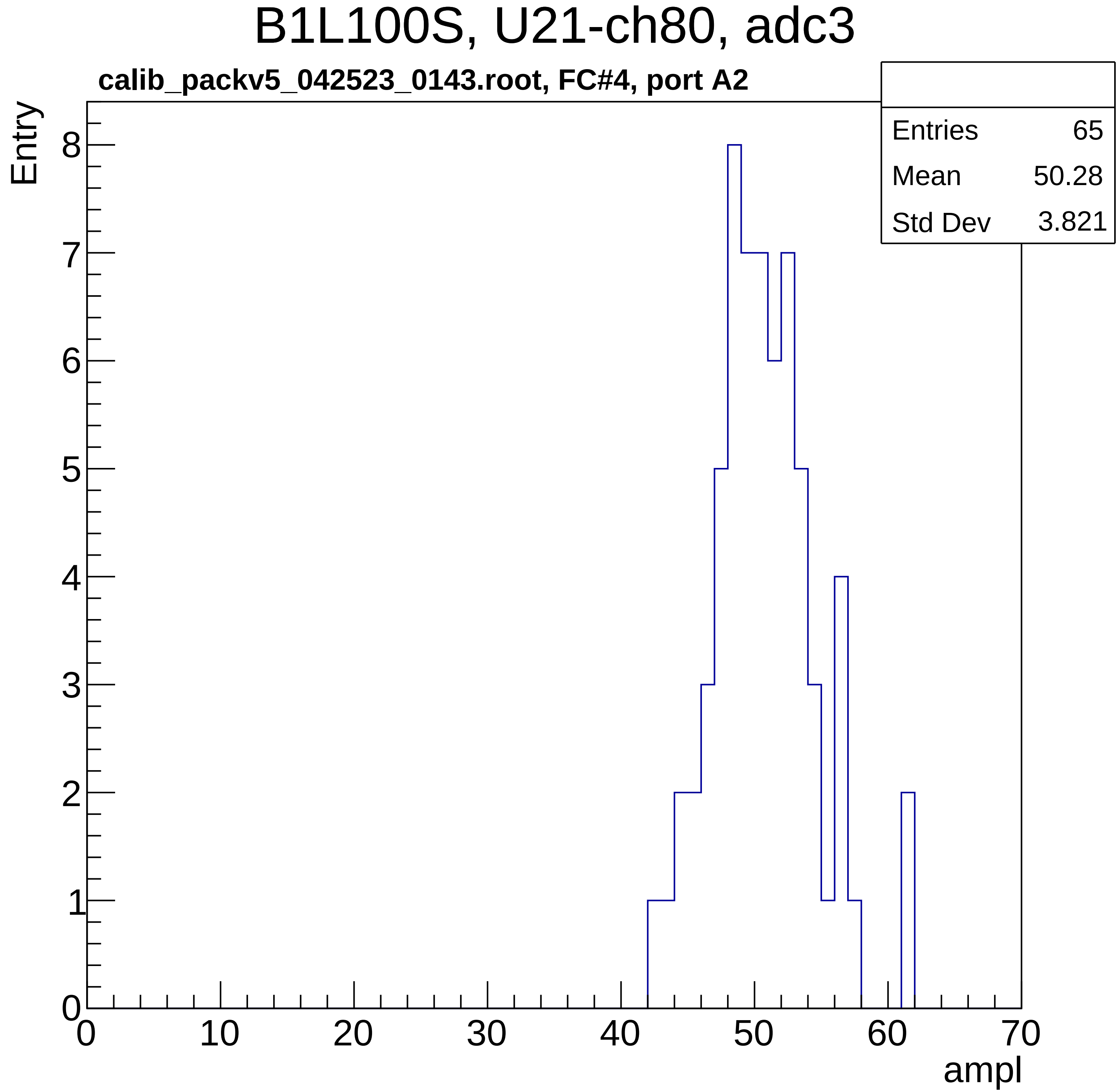


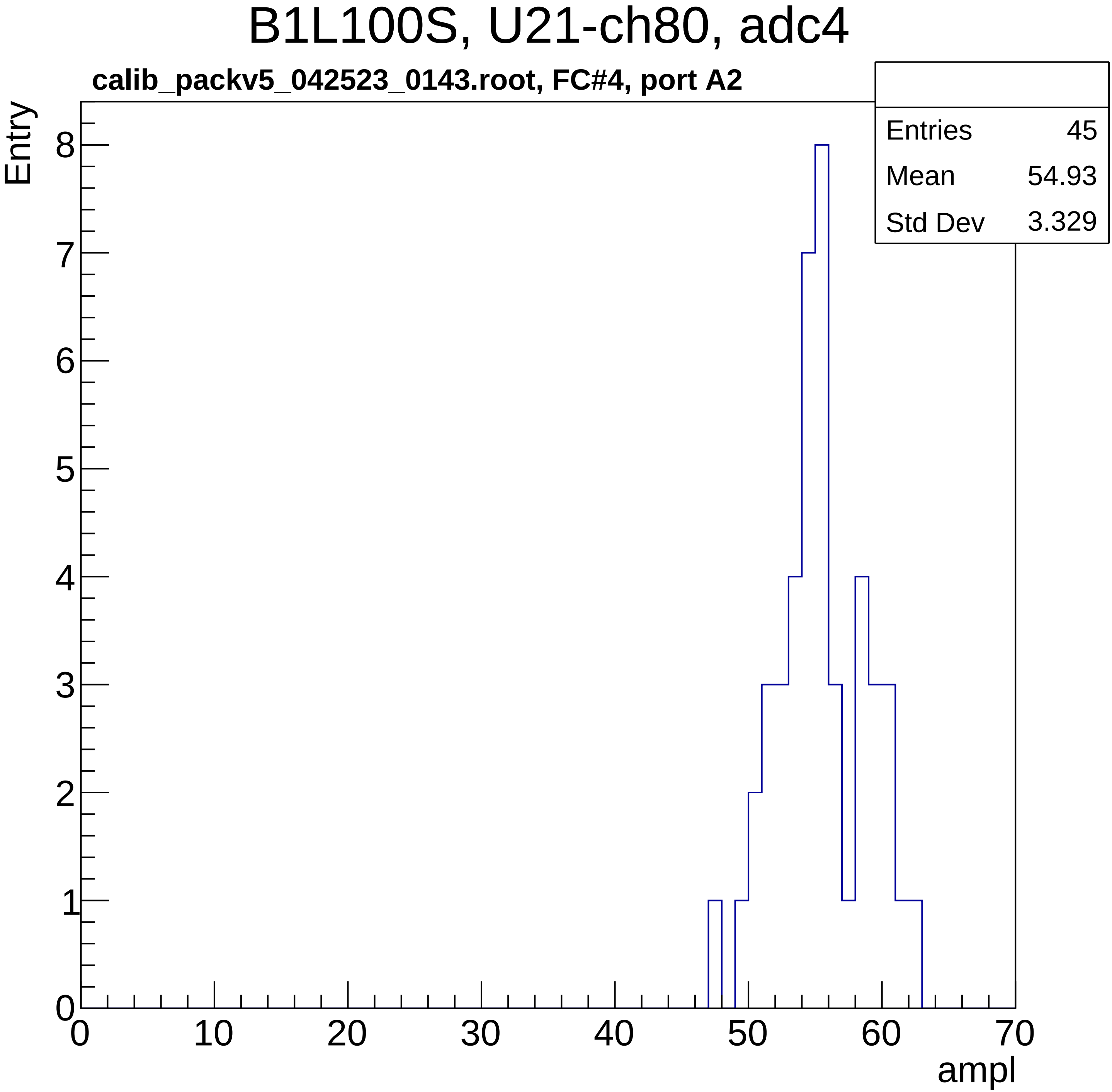

B1L100S, U21-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

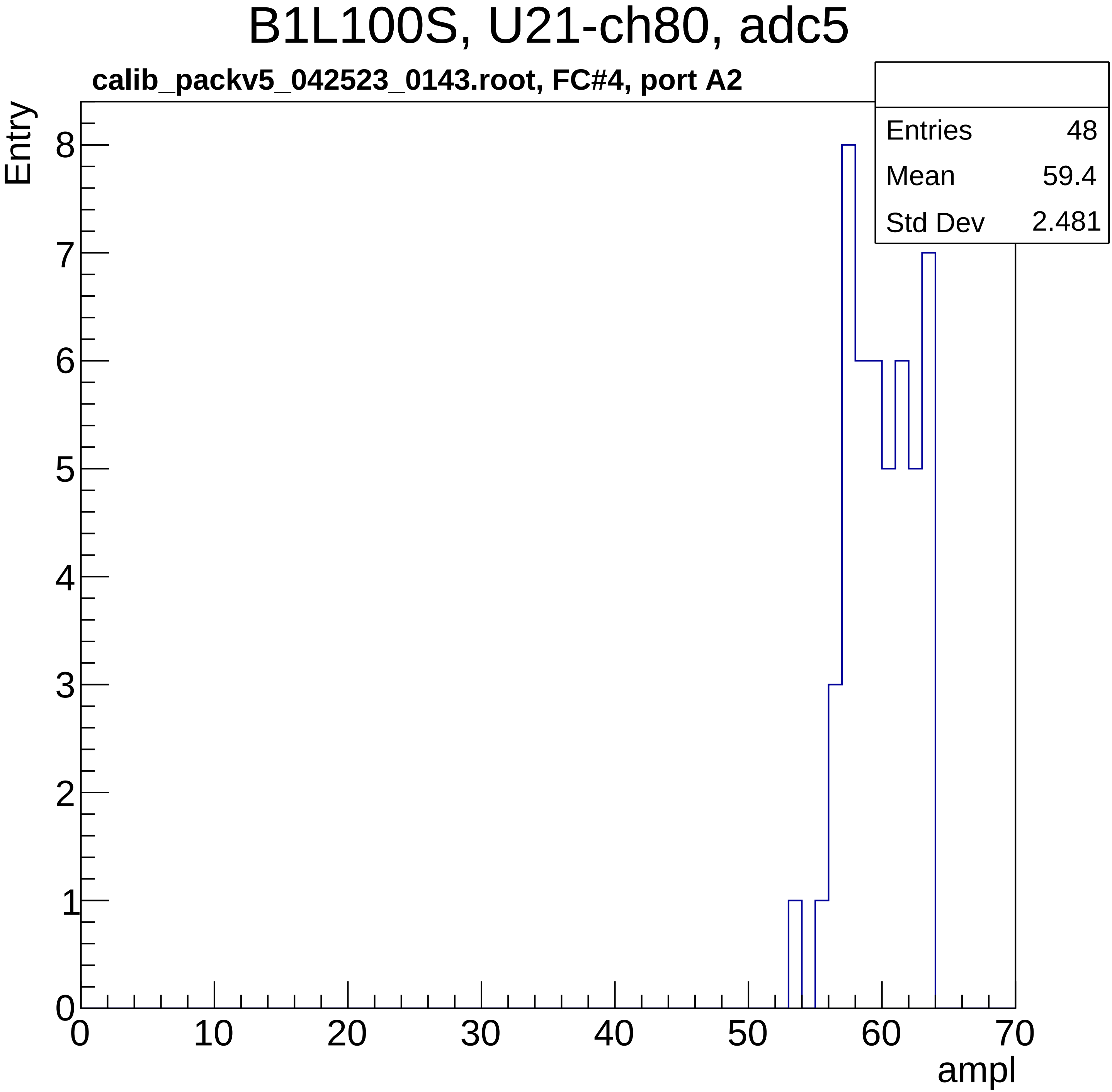

B1L100S, U21-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 29.69
Std Dev 3.84
Gaus mean : 30.1646
Gaus Width: 4.1294
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

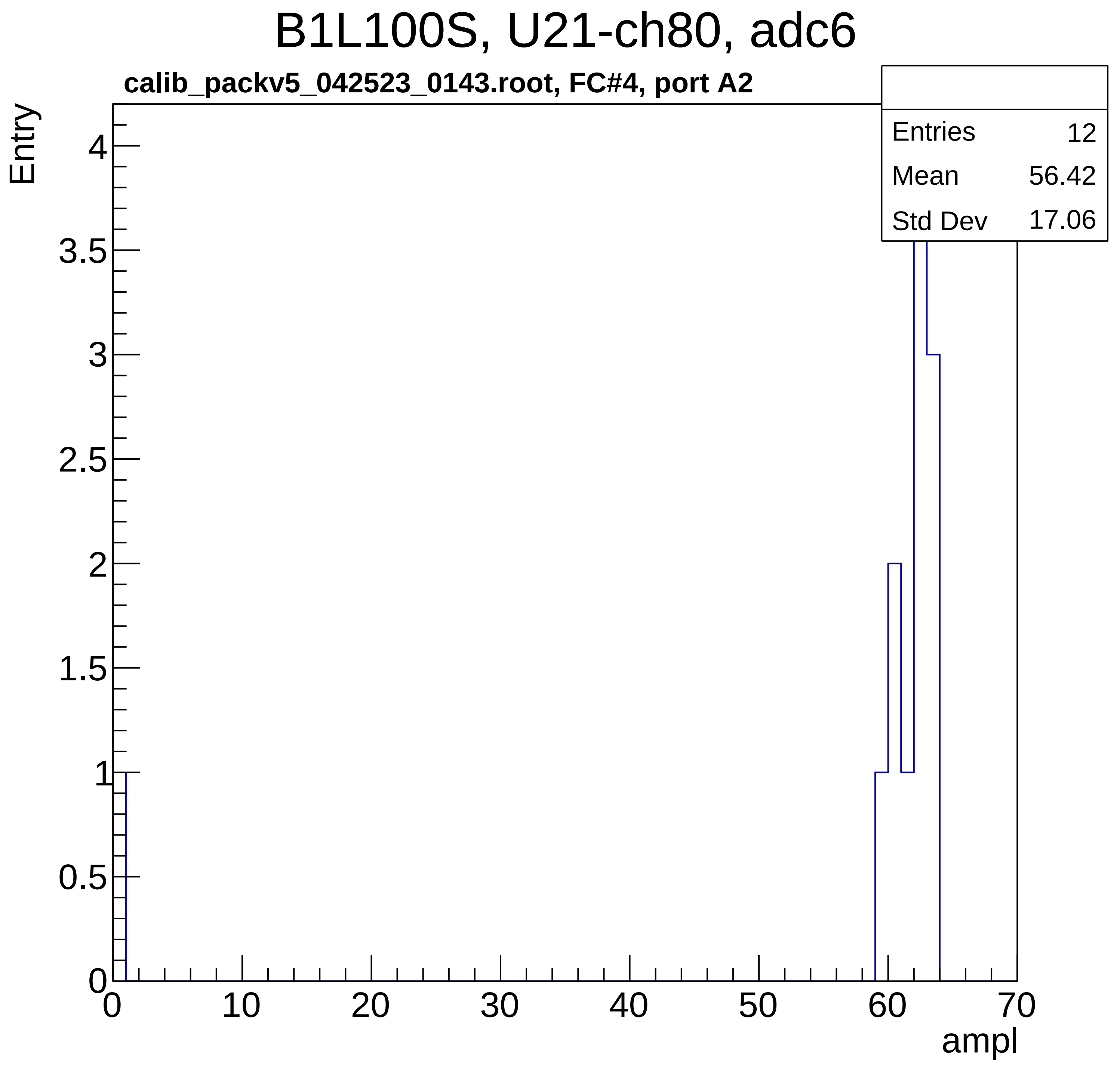

B1L100S, U21-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 37.05
Std Dev 3.276
Gaus mean : 37.5072
Gaus Width: 3.4114
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 68
Mean 43.07
Std Dev 3.406
Gaus mean : 43.5714
Gaus Width: 3.5778
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

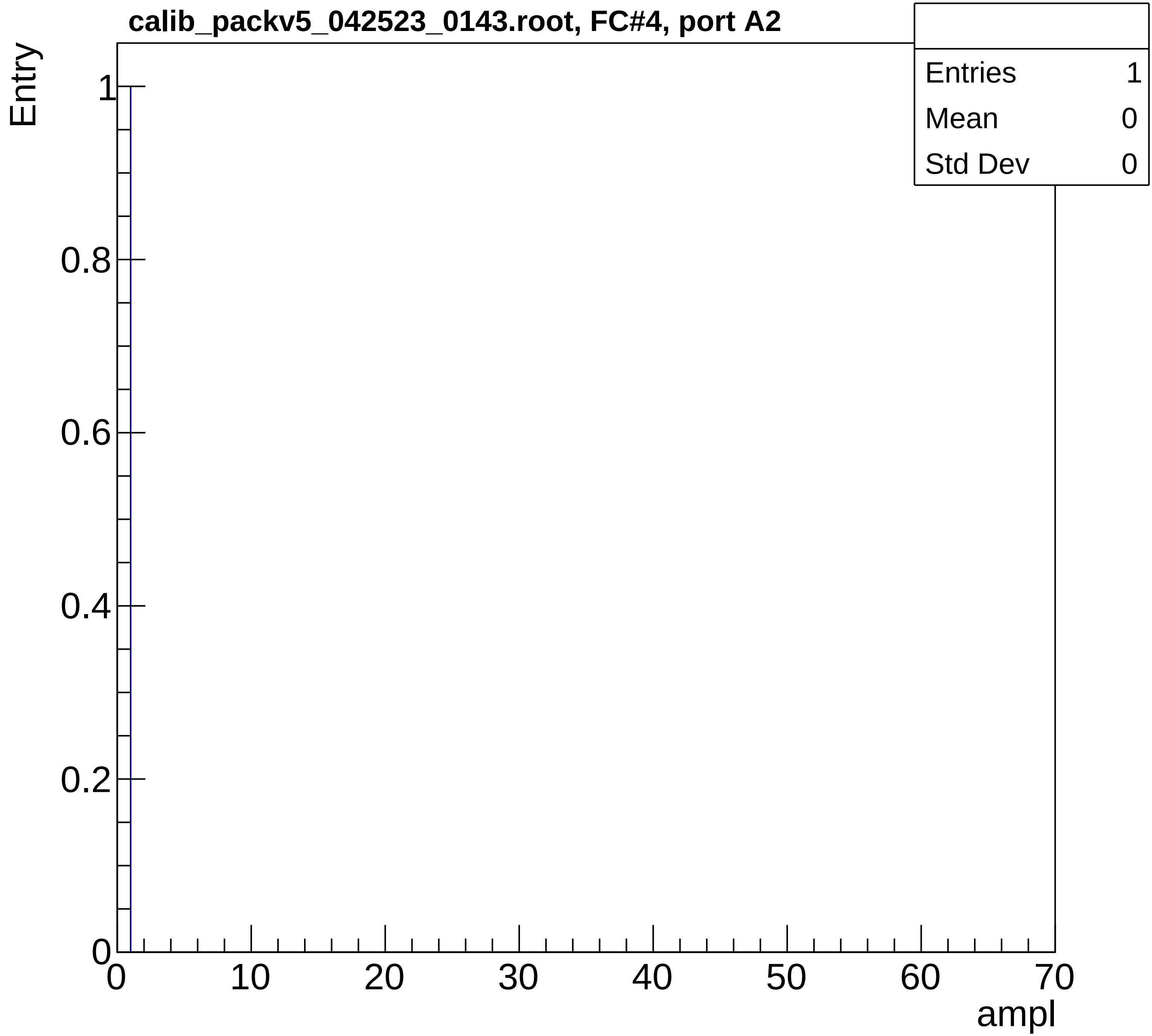

B1L100S, U21-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.28
Std Dev 3.821
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

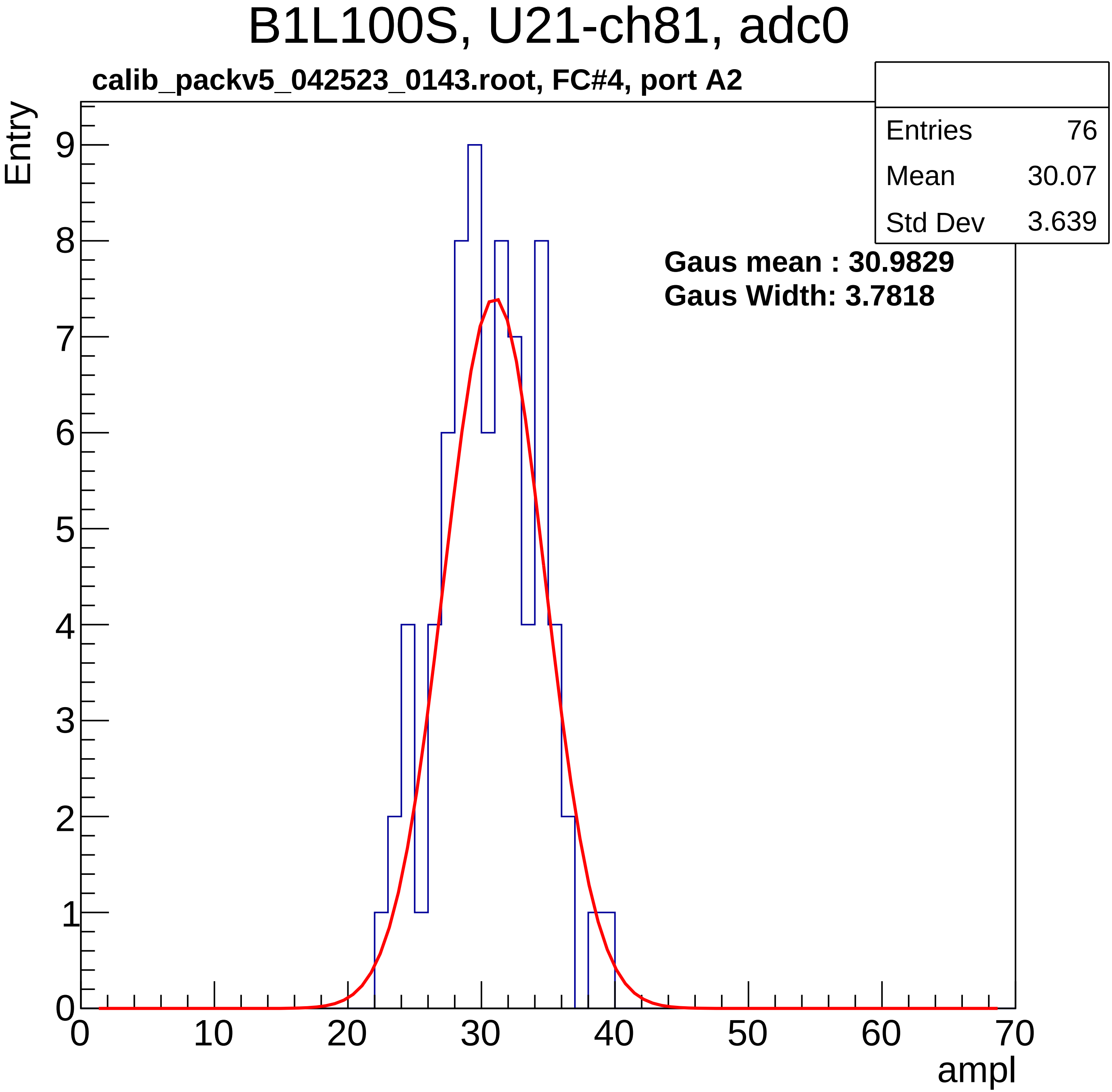

B1L100S, U21-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 54.93
Std Dev 3.329
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

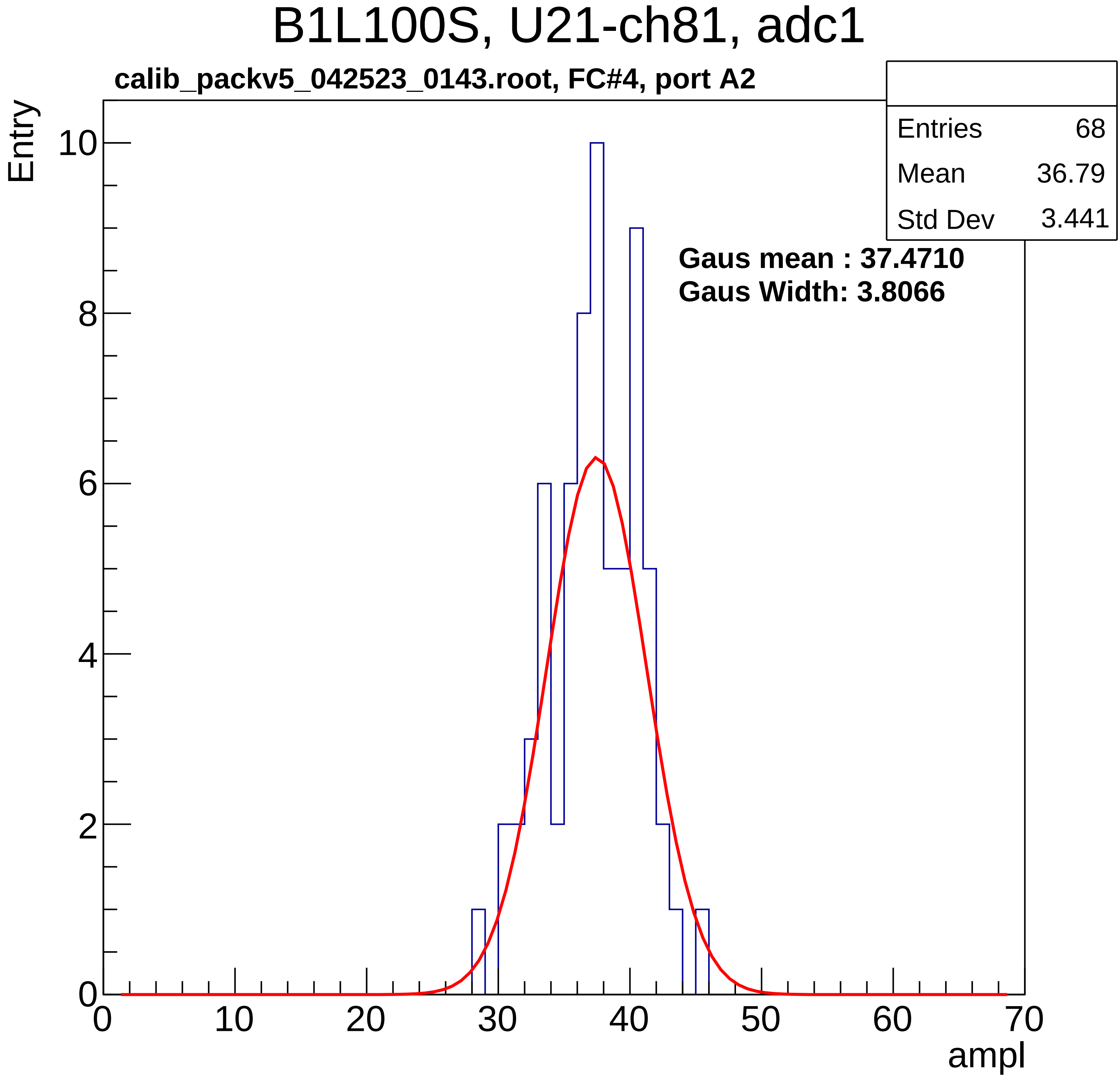

B1L100S, U21-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.4
Std Dev 2.481
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

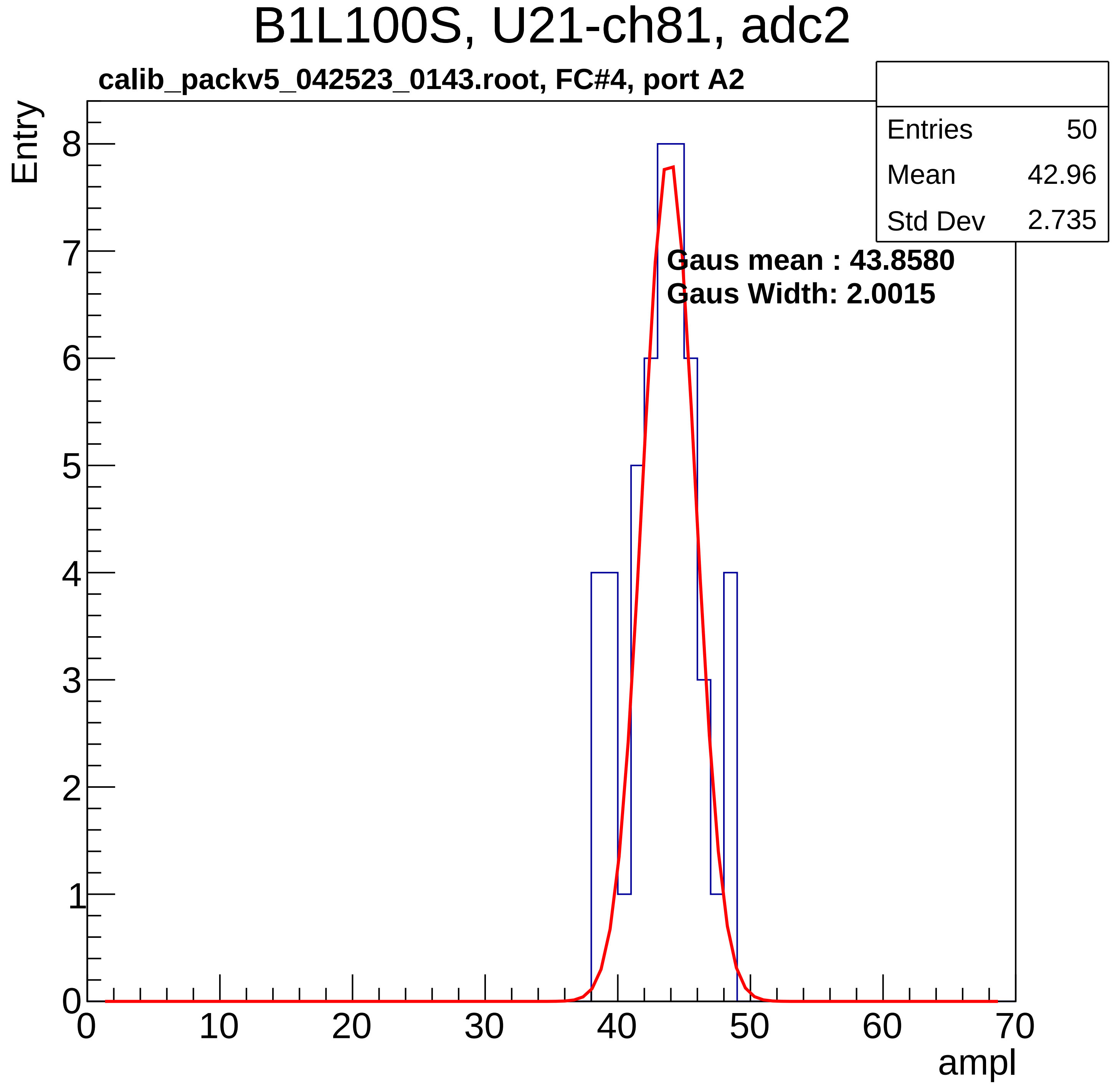

B1L100S, U21-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 12
Mean 56.42
Std Dev 17.06
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch80, adc7



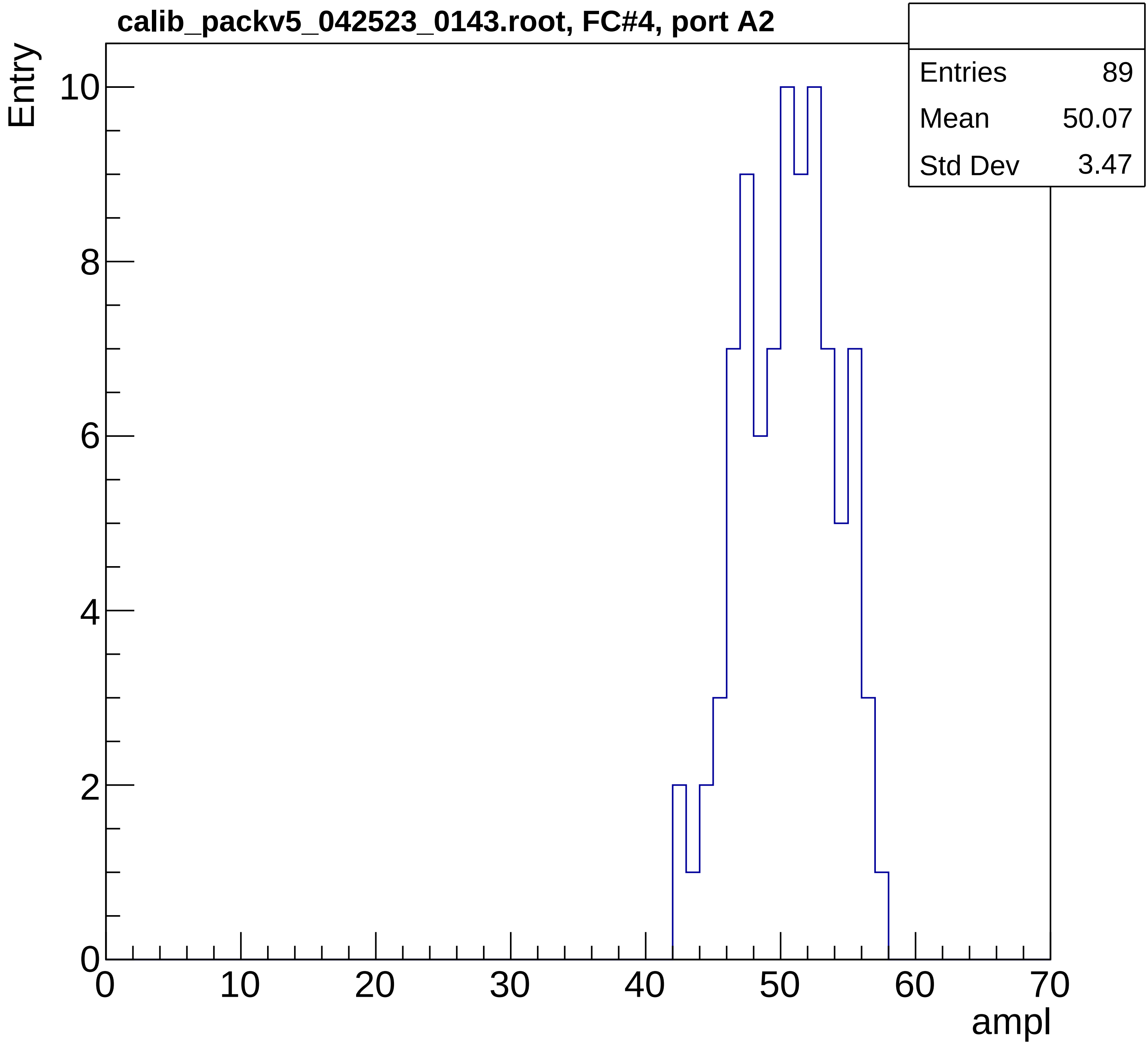

B1L100S, U21-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.07
Std Dev 3.639
Gaus mean : 30.9829
Gaus Width: 3.7818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.79
Std Dev 3.441
Gaus mean : 37.4710
Gaus Width: 3.8066
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

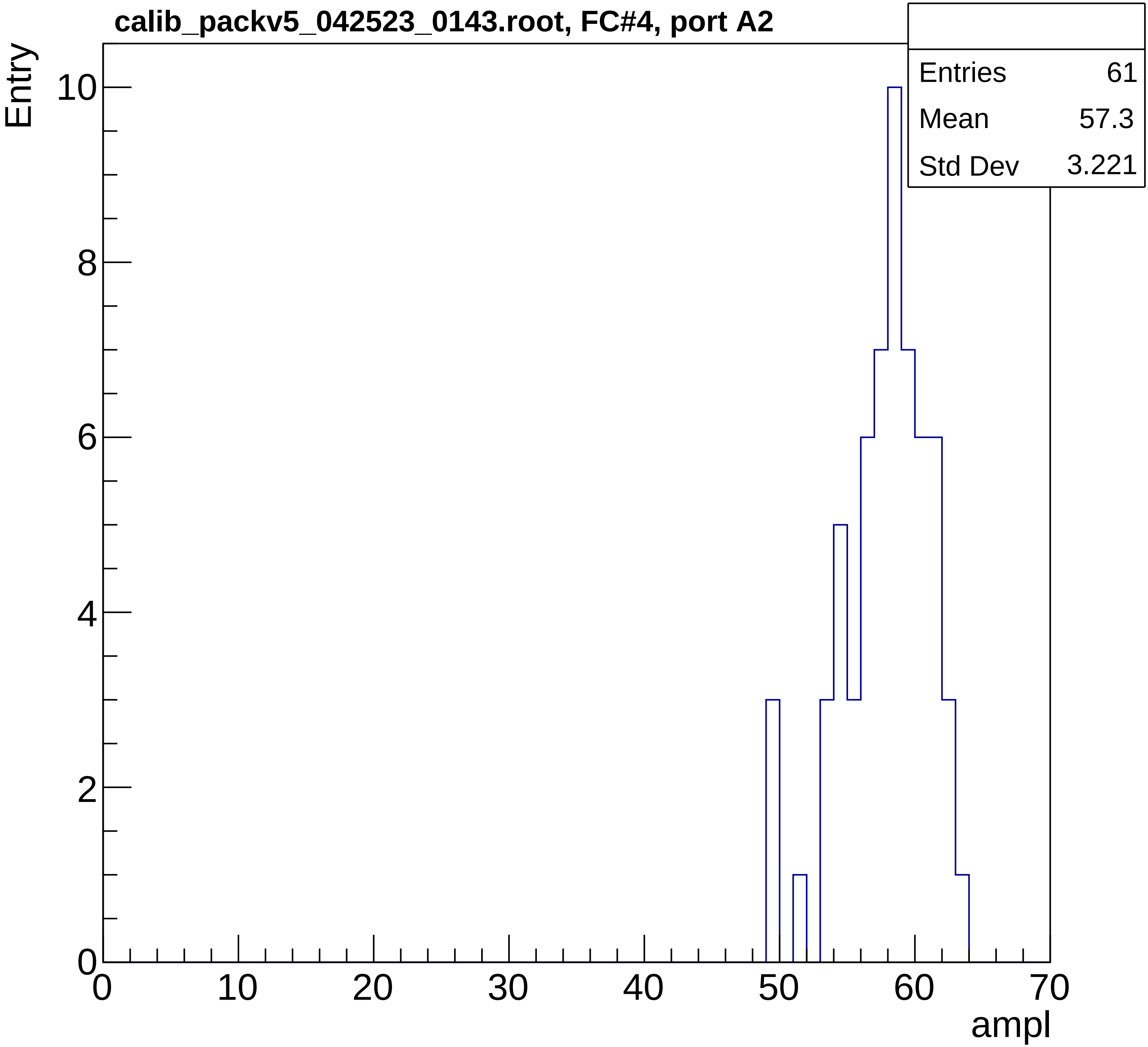

B1L100S, U21-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 42.96
Std Dev 2.735
Gaus mean : 43.8580
Gaus Width: 2.0015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

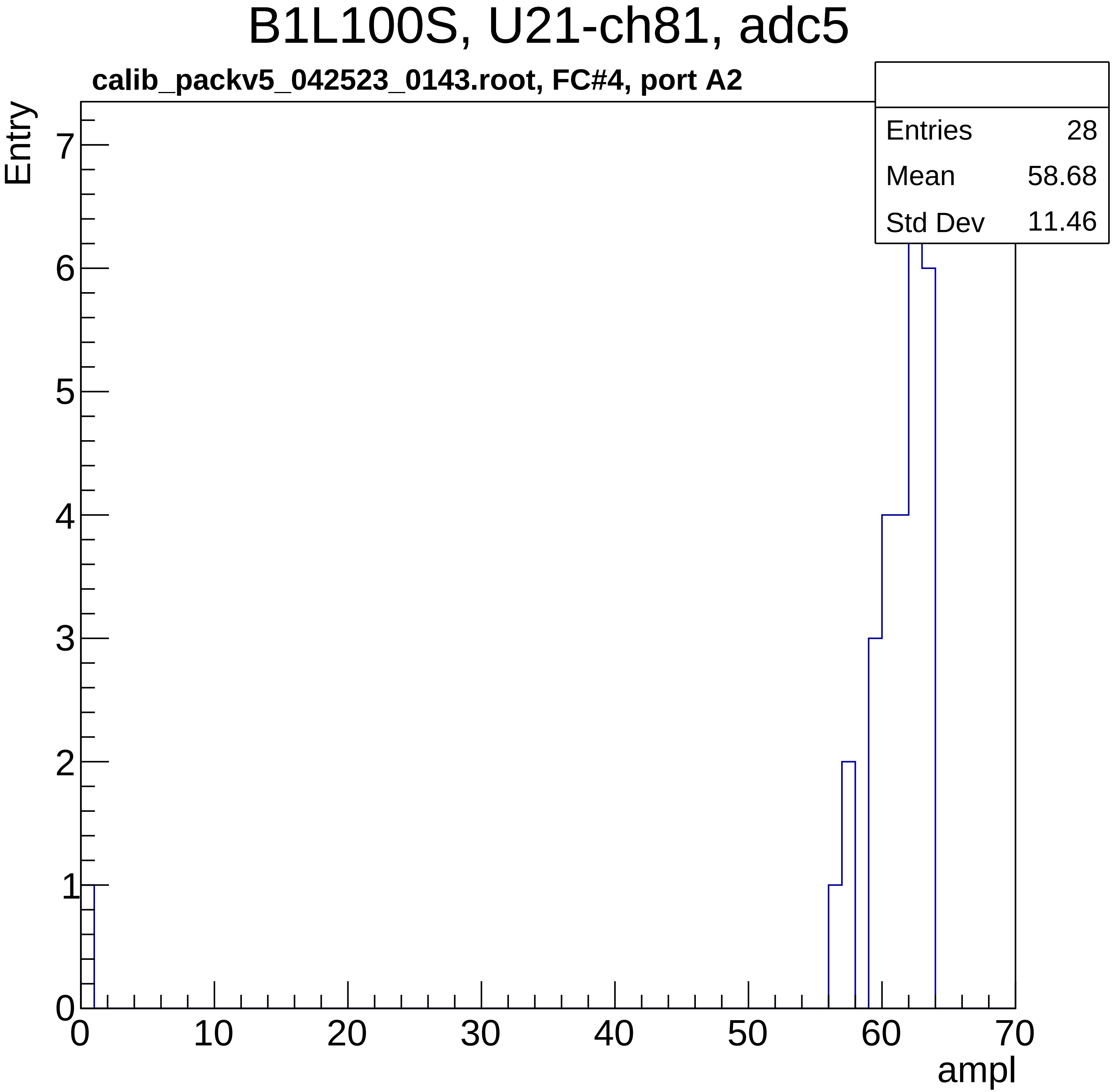
B1L100S, U21-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

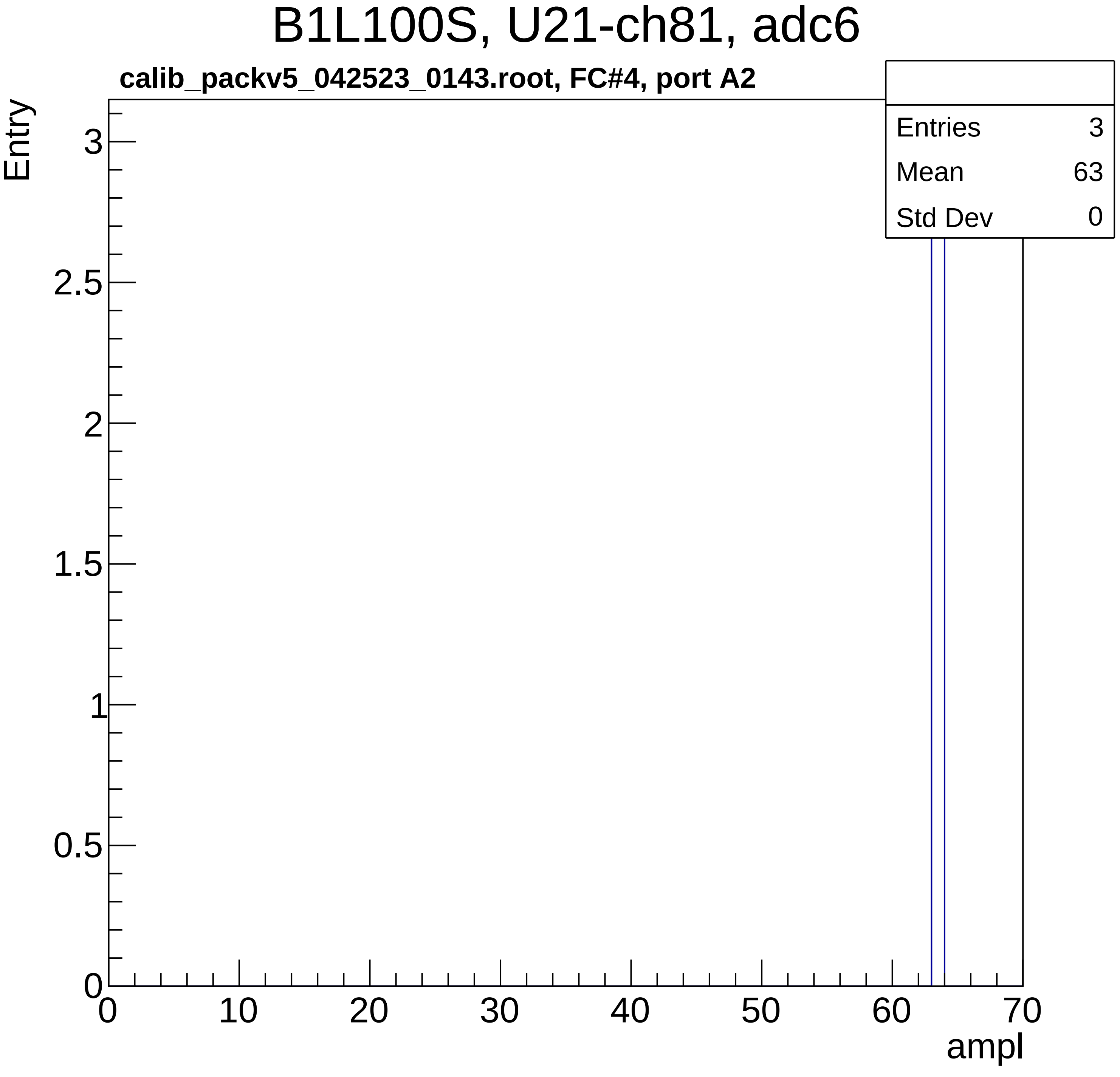


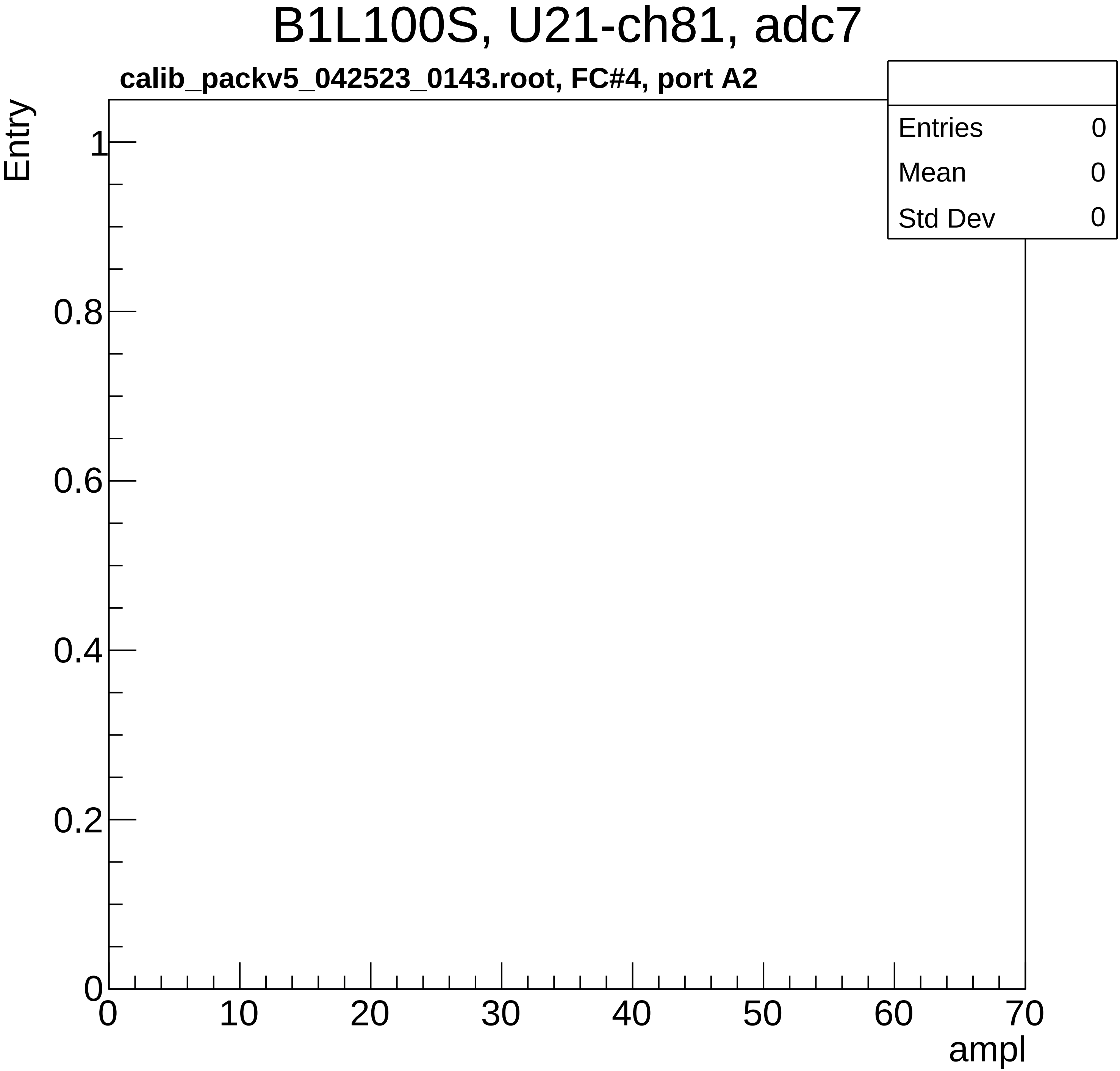
B1L100S, U21-ch81, adc4

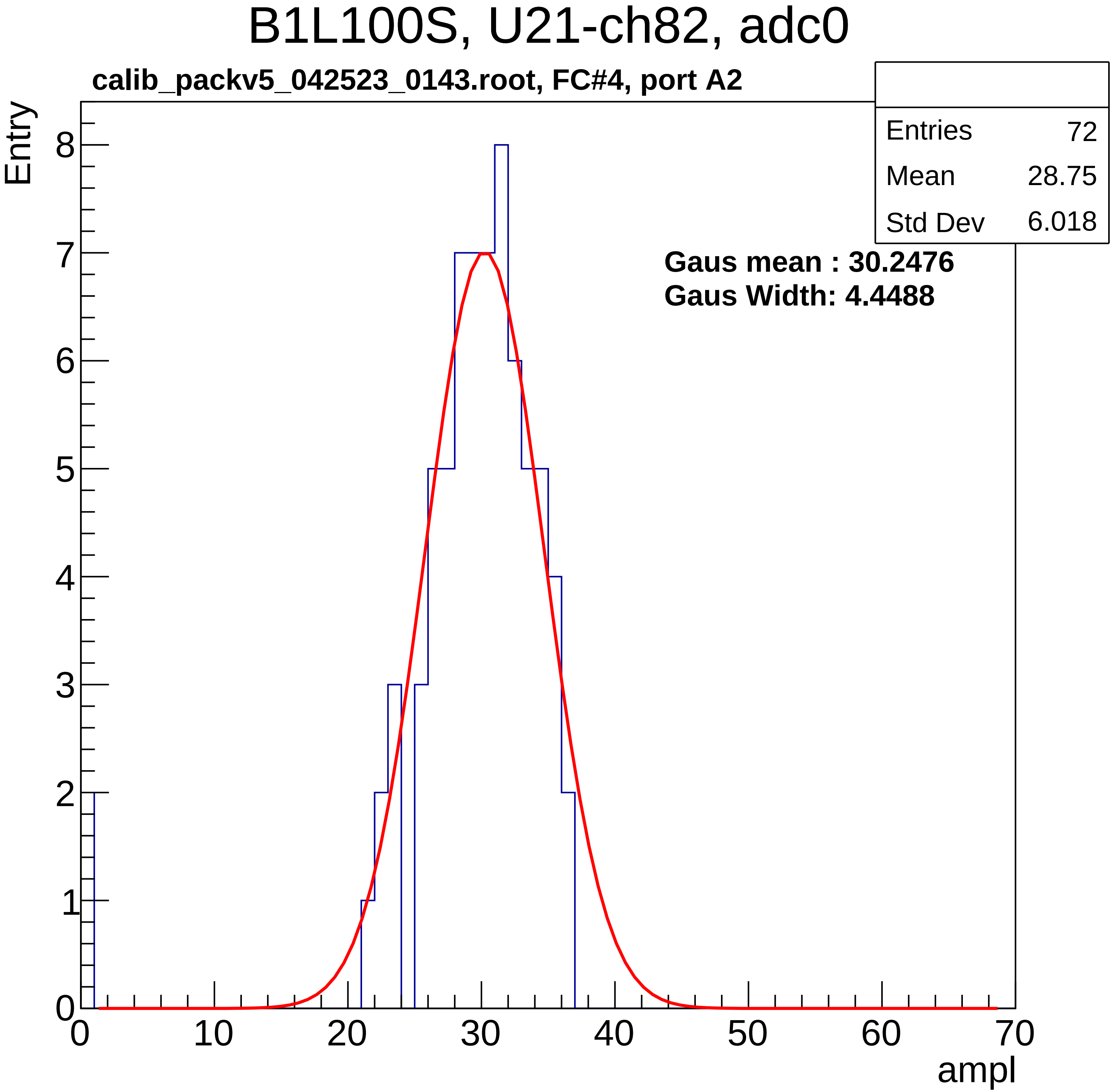


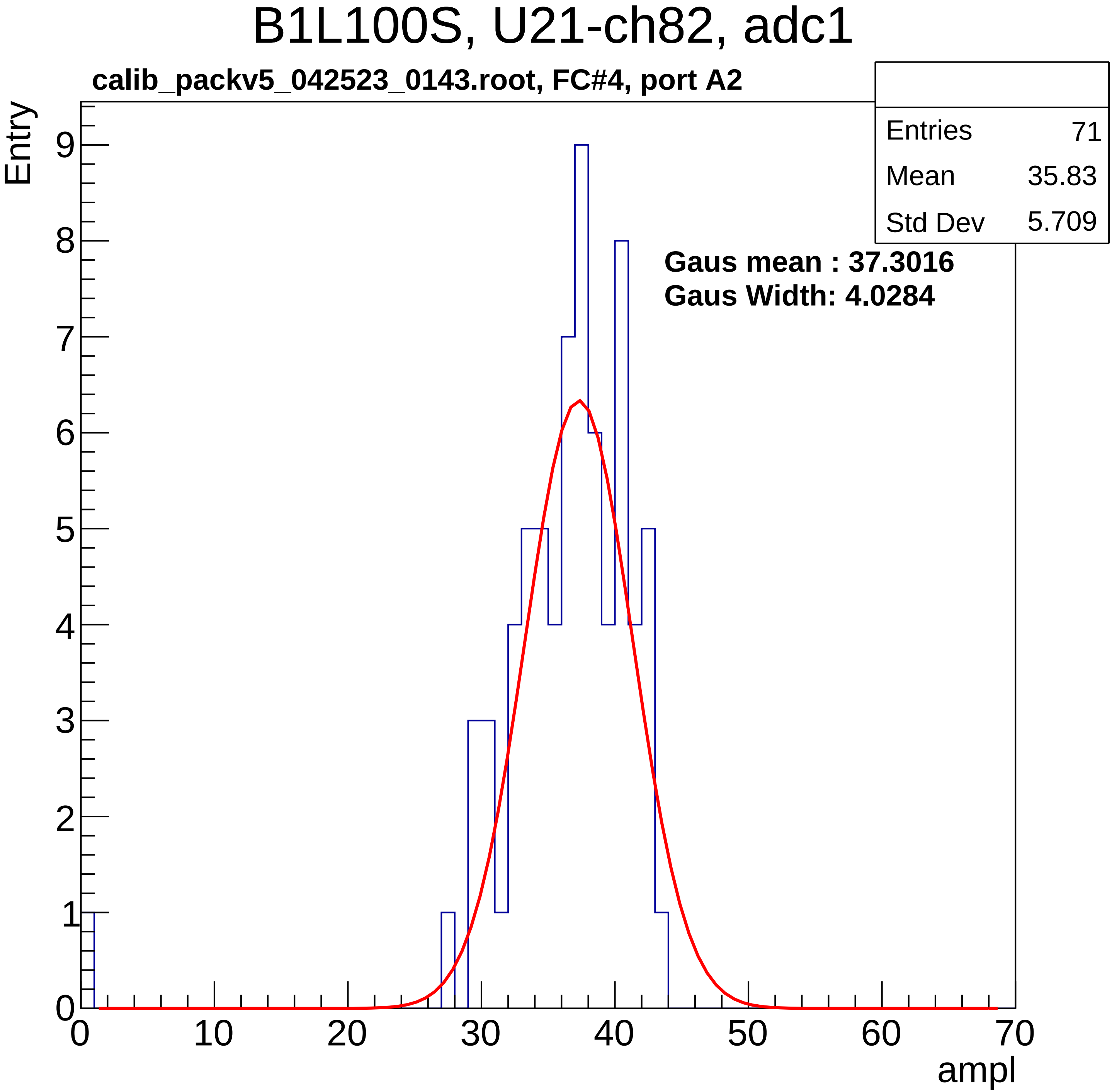


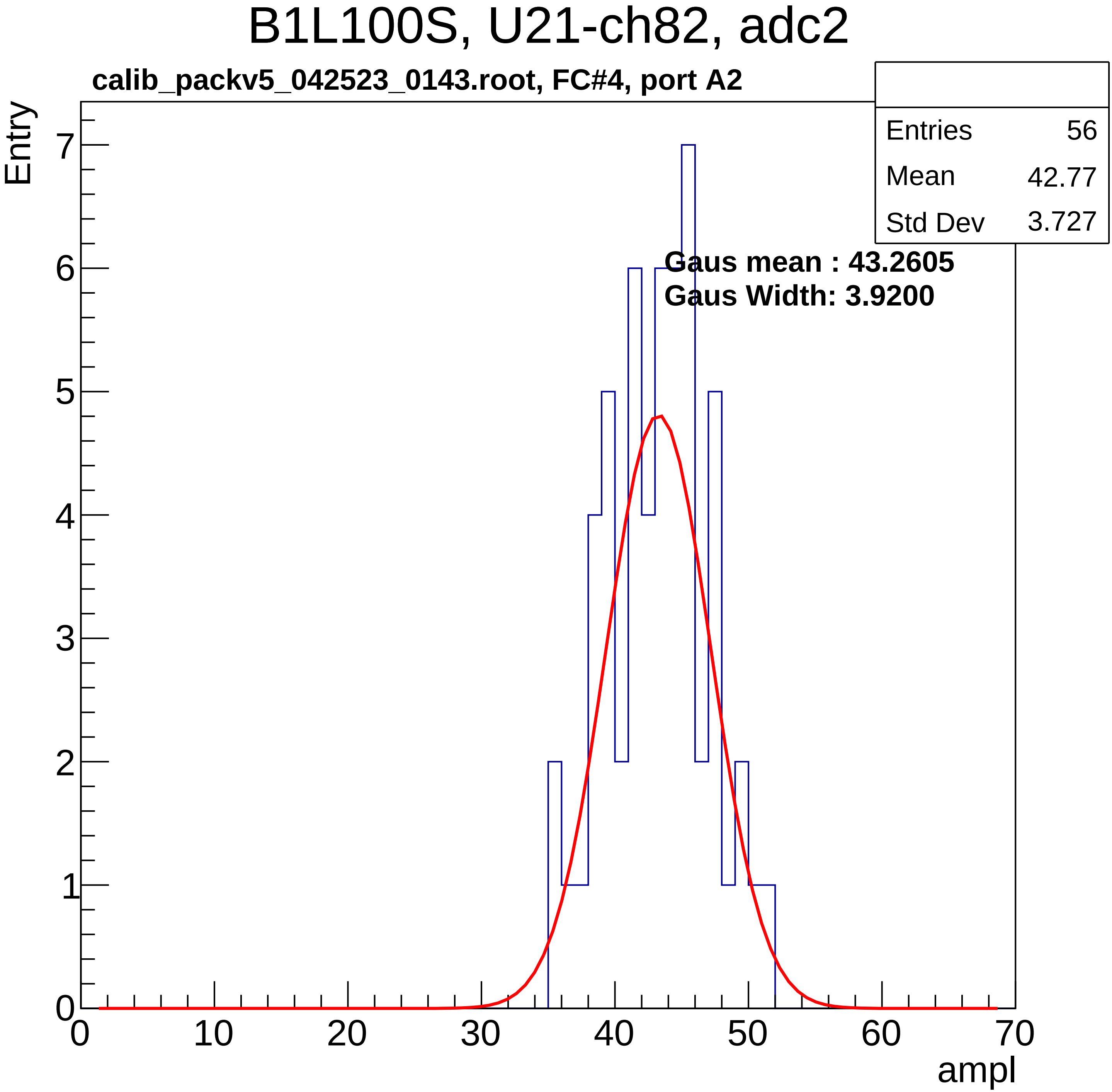
B1L100S, U21-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 28
Mean 58.68
Std Dev 11.46
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

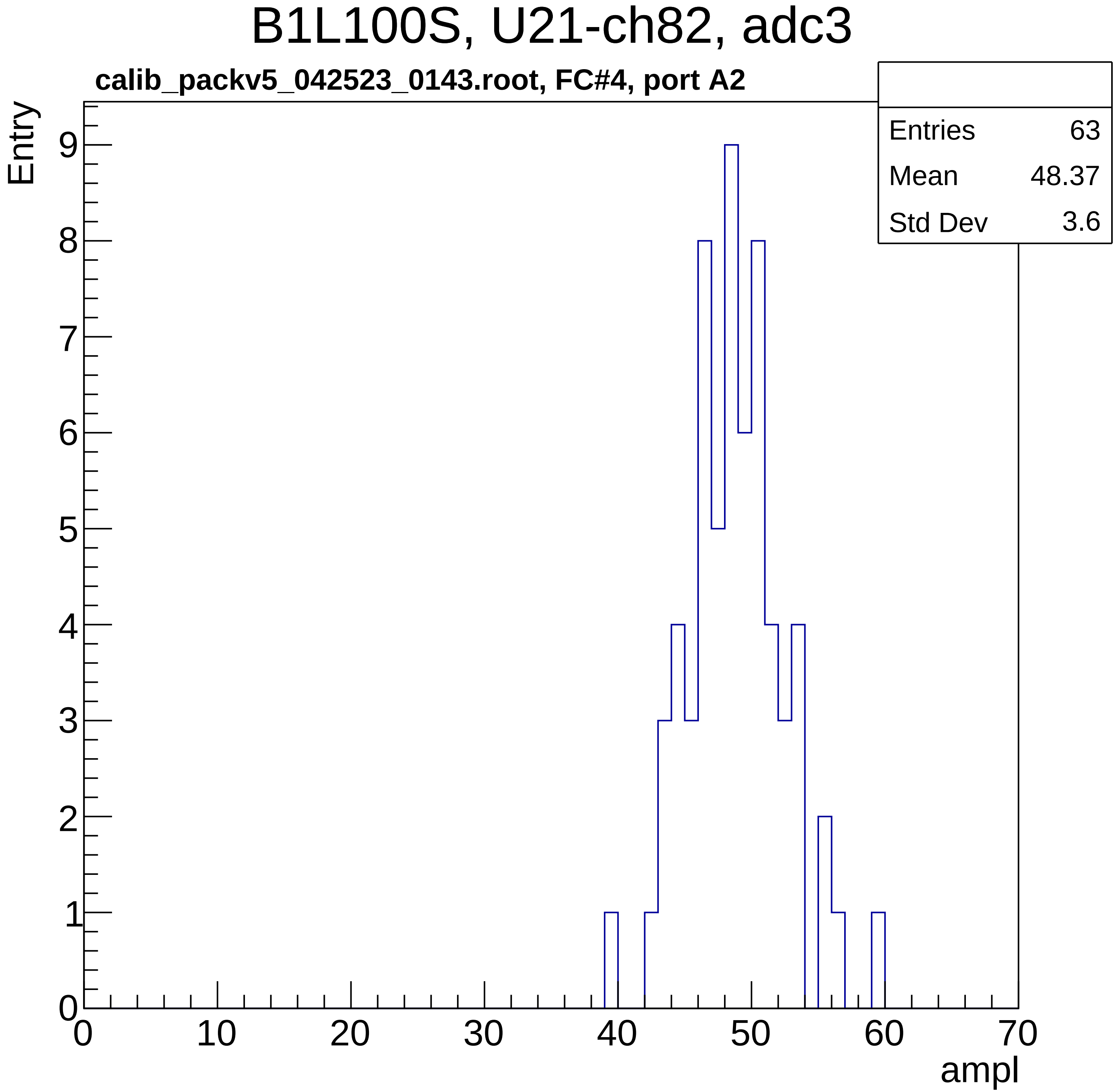

B1L100S, U21-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U21-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.75
Std Dev 6.018
Gaus mean : 30.2476
Gaus Width: 4.4488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

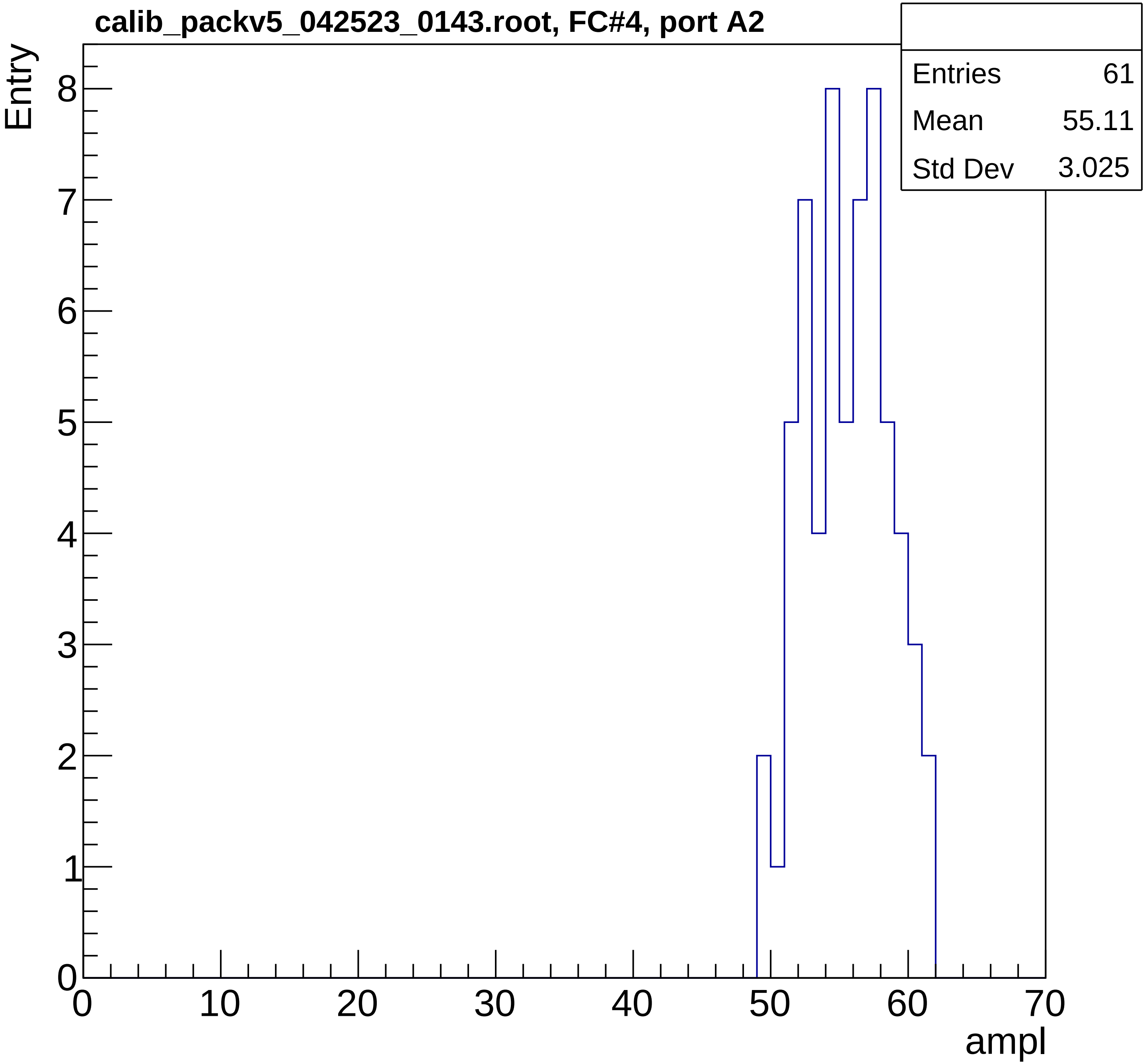

B1L100S, U21-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 35.83
Std Dev 5.709
Gaus mean : 37.3016
Gaus Width: 4.0284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

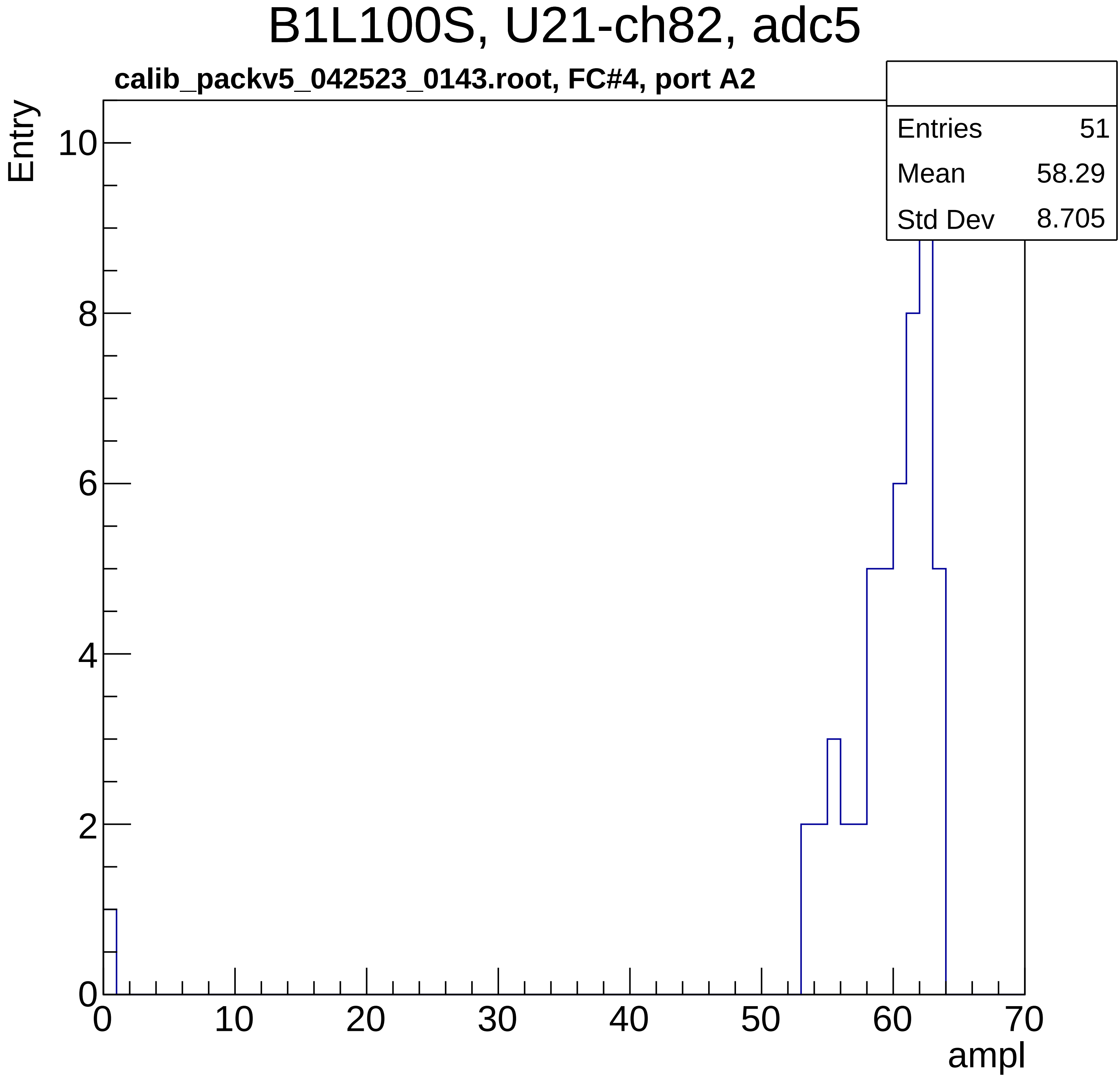

B1L100S, U21-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 56
Mean 42.77
Std Dev 3.727
Gaus mean : 43.2605
Gaus Width: 3.9200
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

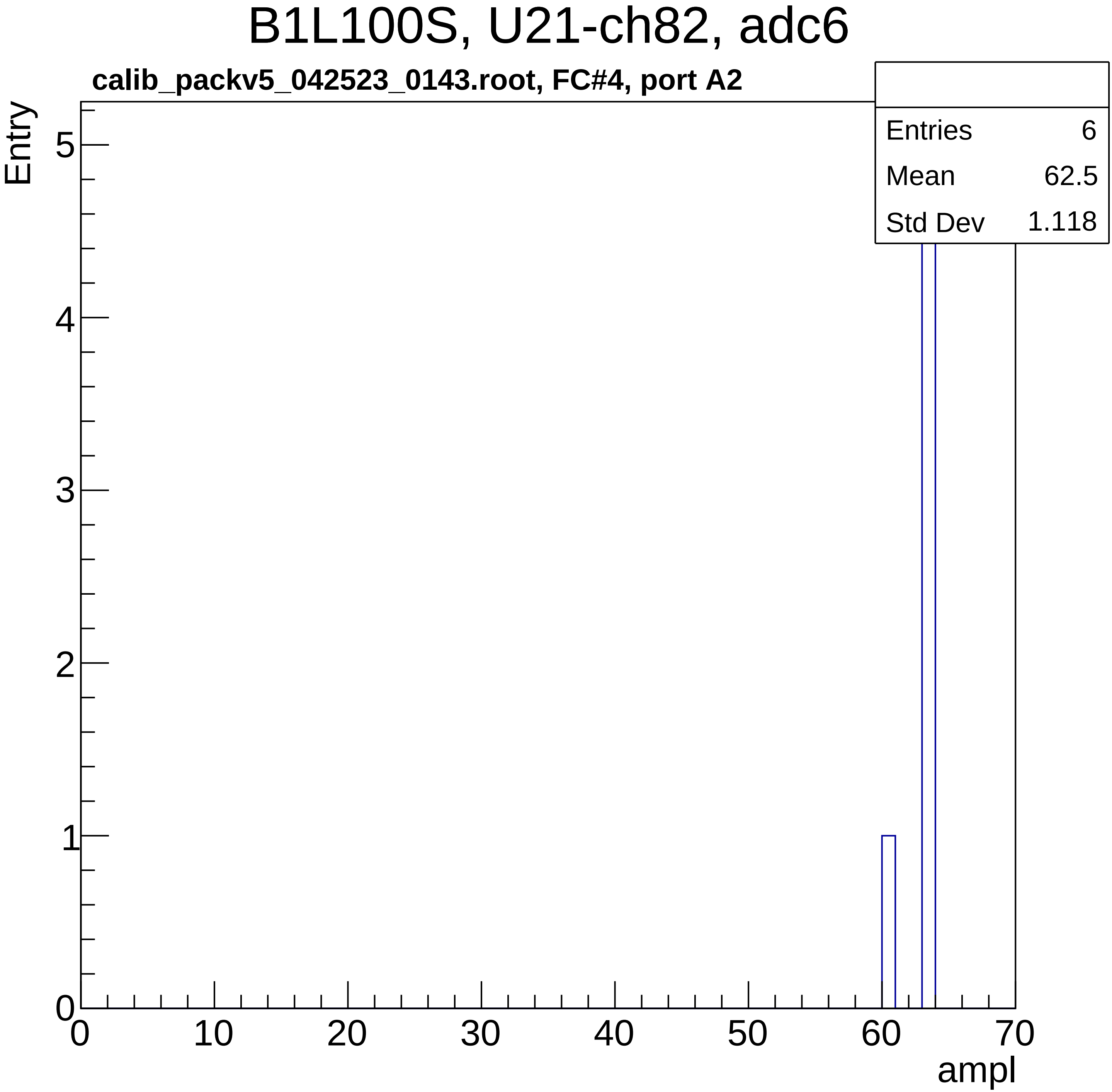

B1L100S, U21-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.37
Std Dev 3.6
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch82, adc4

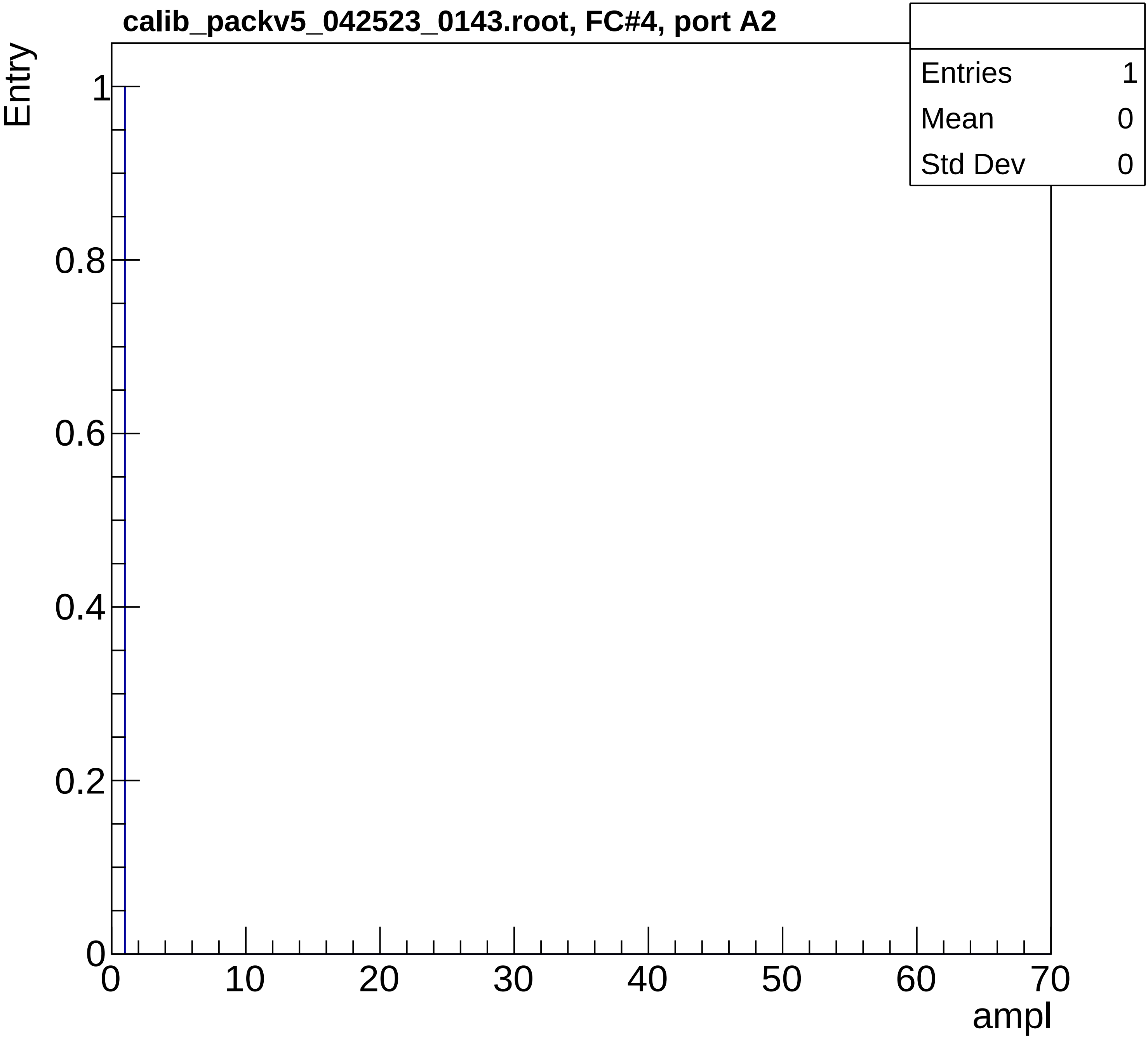
calib_packv5_042523_0143.root, FC#4, port A2

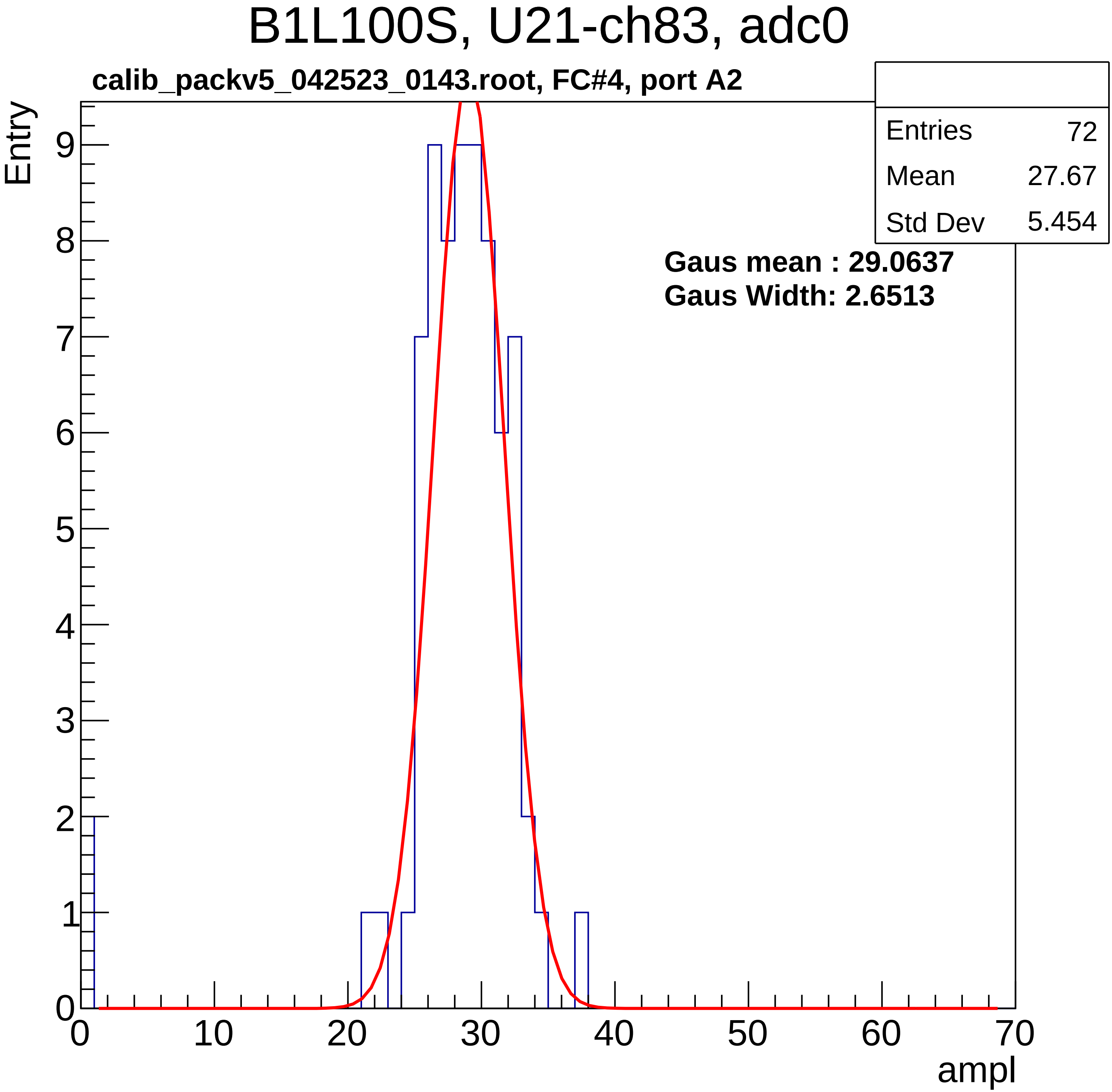



B1L100S, U21-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.29
Std Dev 8.705
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

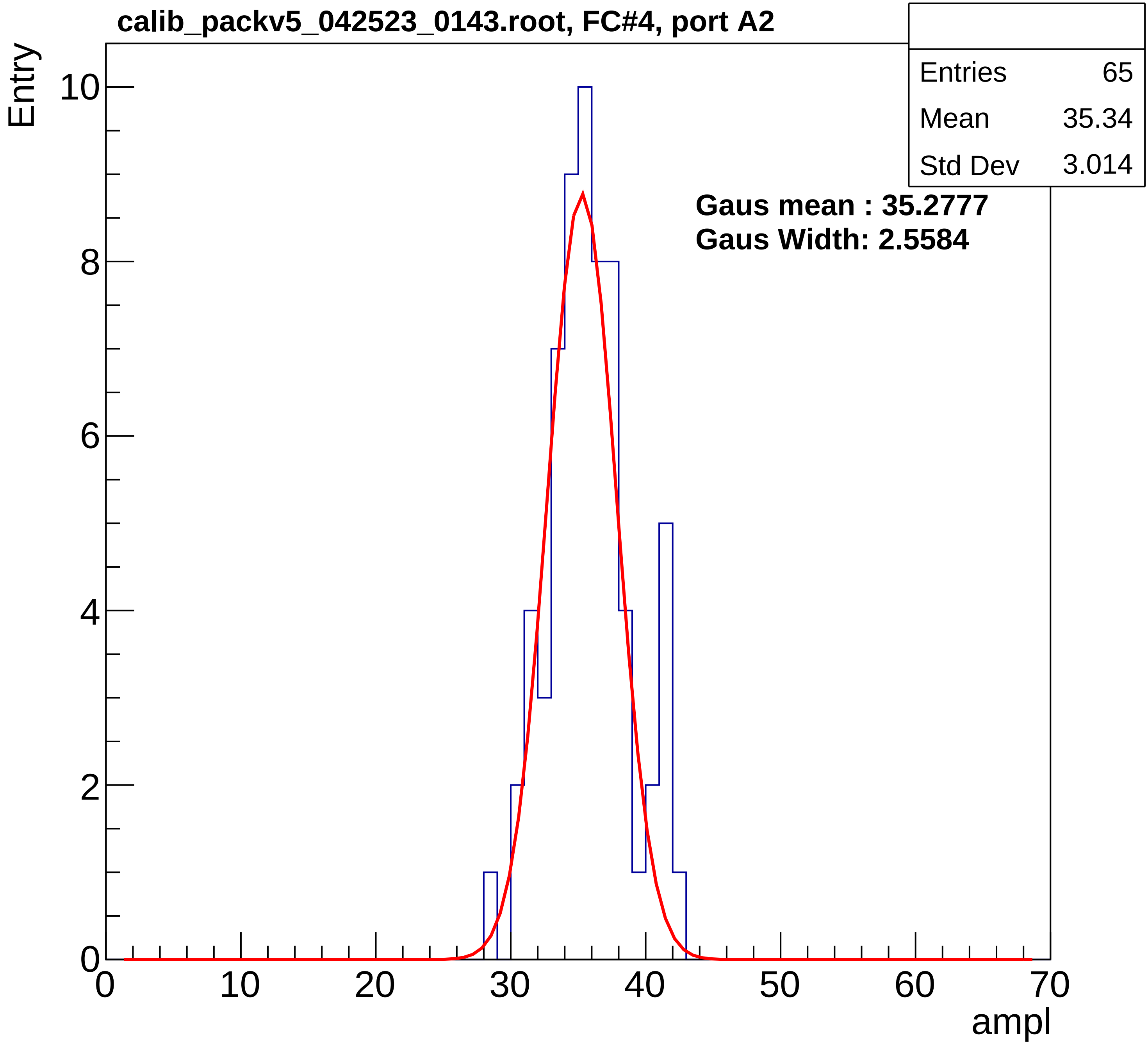

B1L100S, U21-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.5
Std Dev 1.118
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

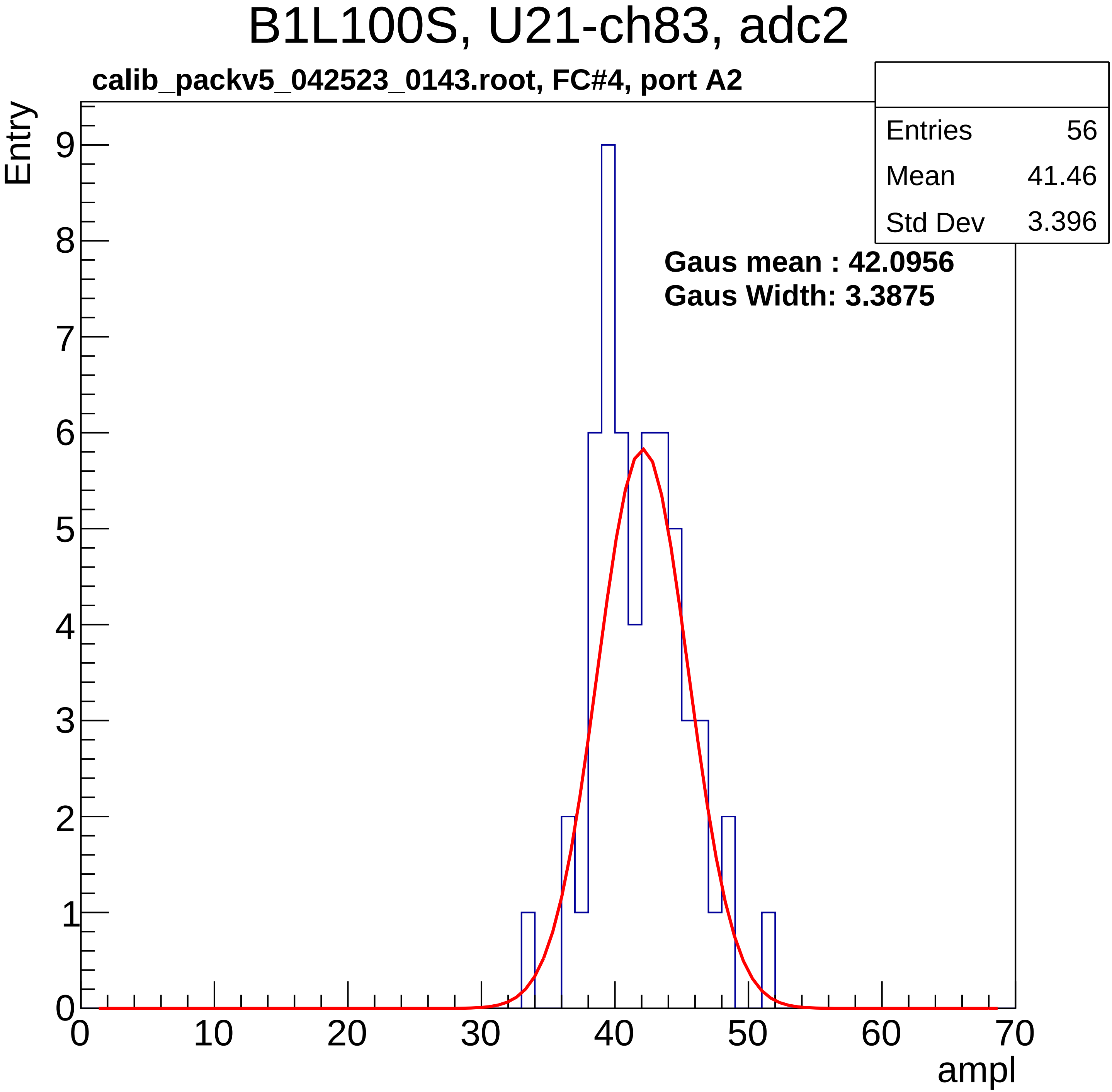
B1L100S, U21-ch82, adc7



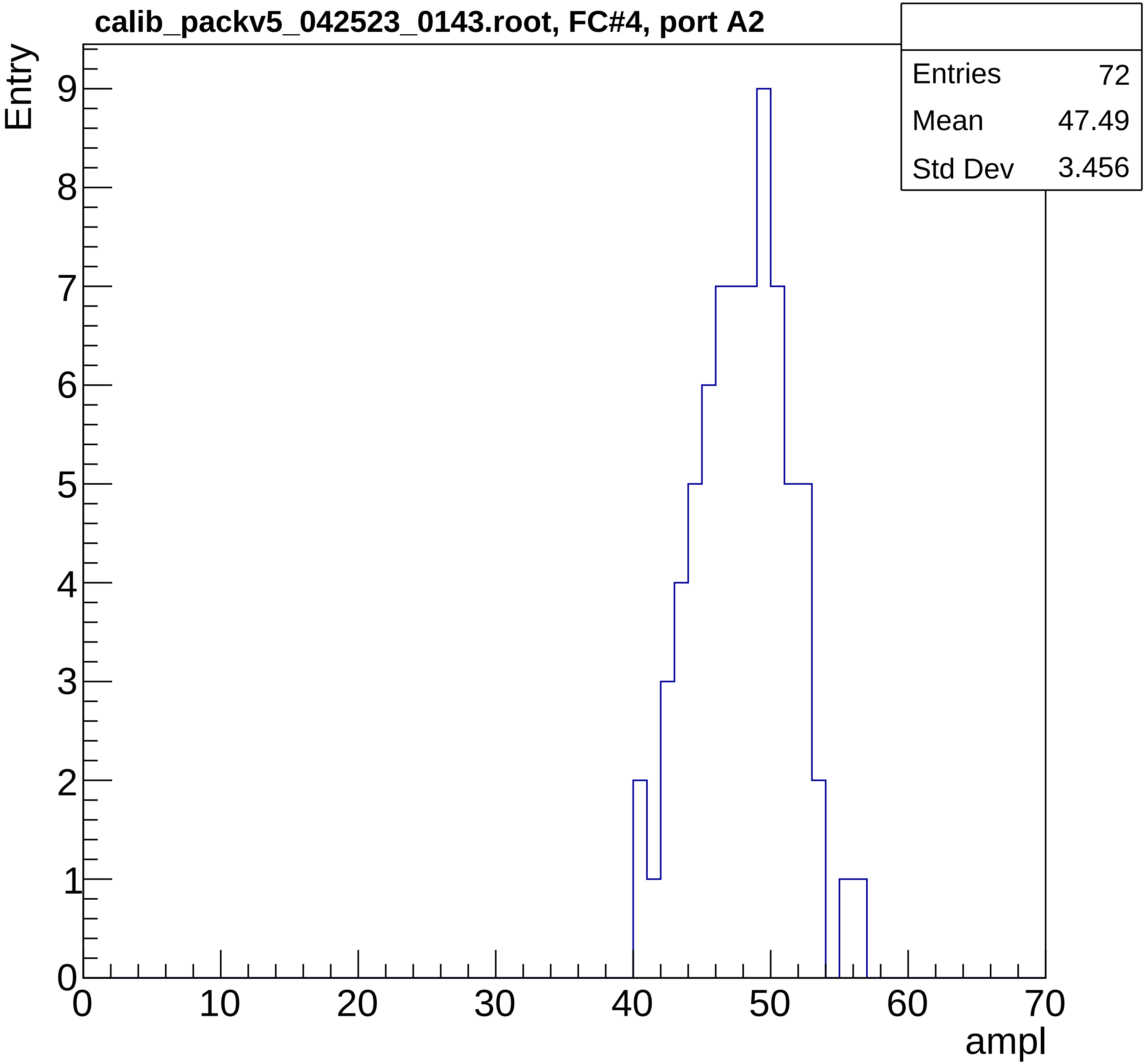

B1L100S, U21-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 27.67
Std Dev 5.454
Gaus mean : 29.0637
Gaus Width: 2.6513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

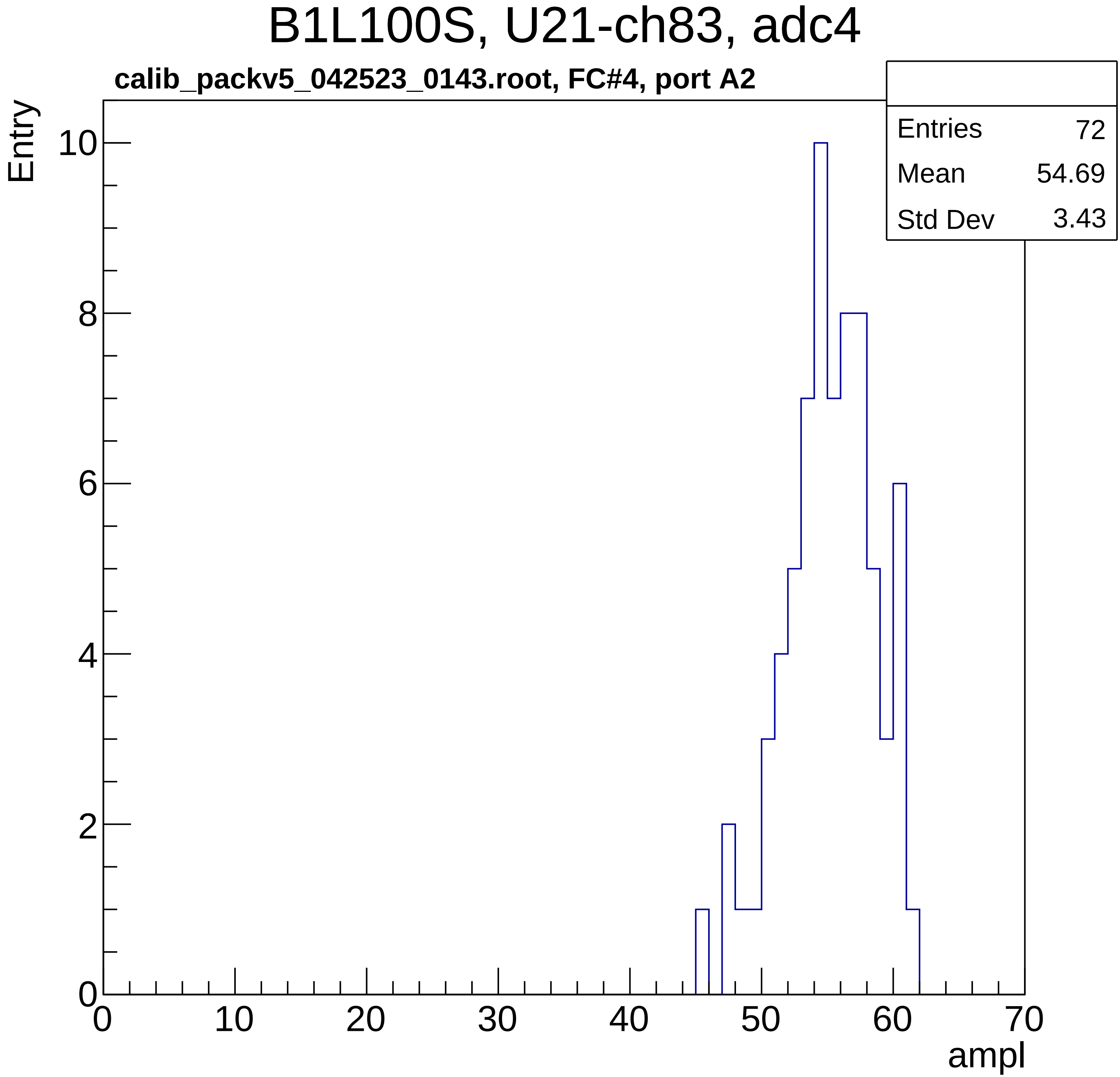
B1L100S, U21-ch83, adc1

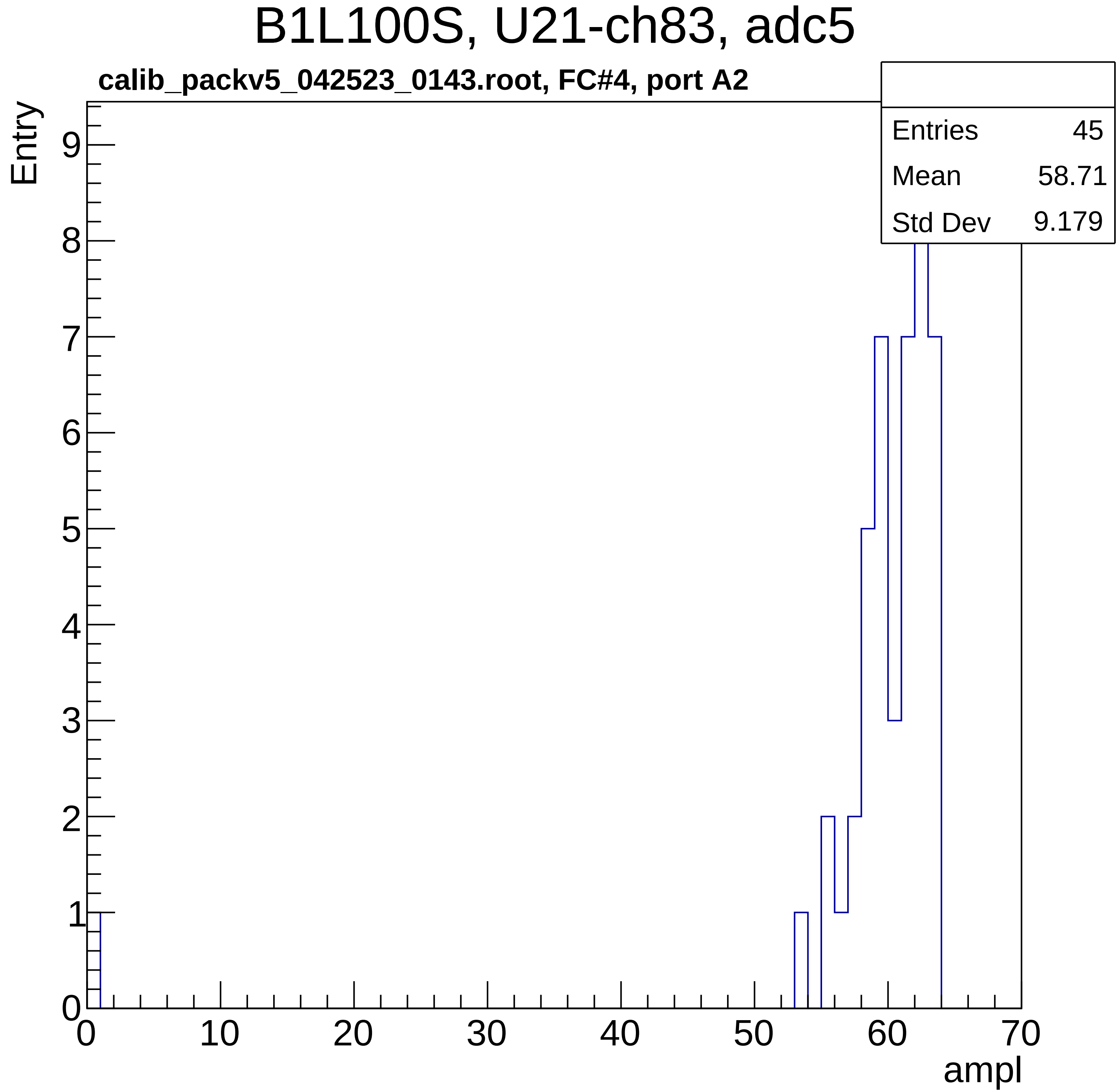


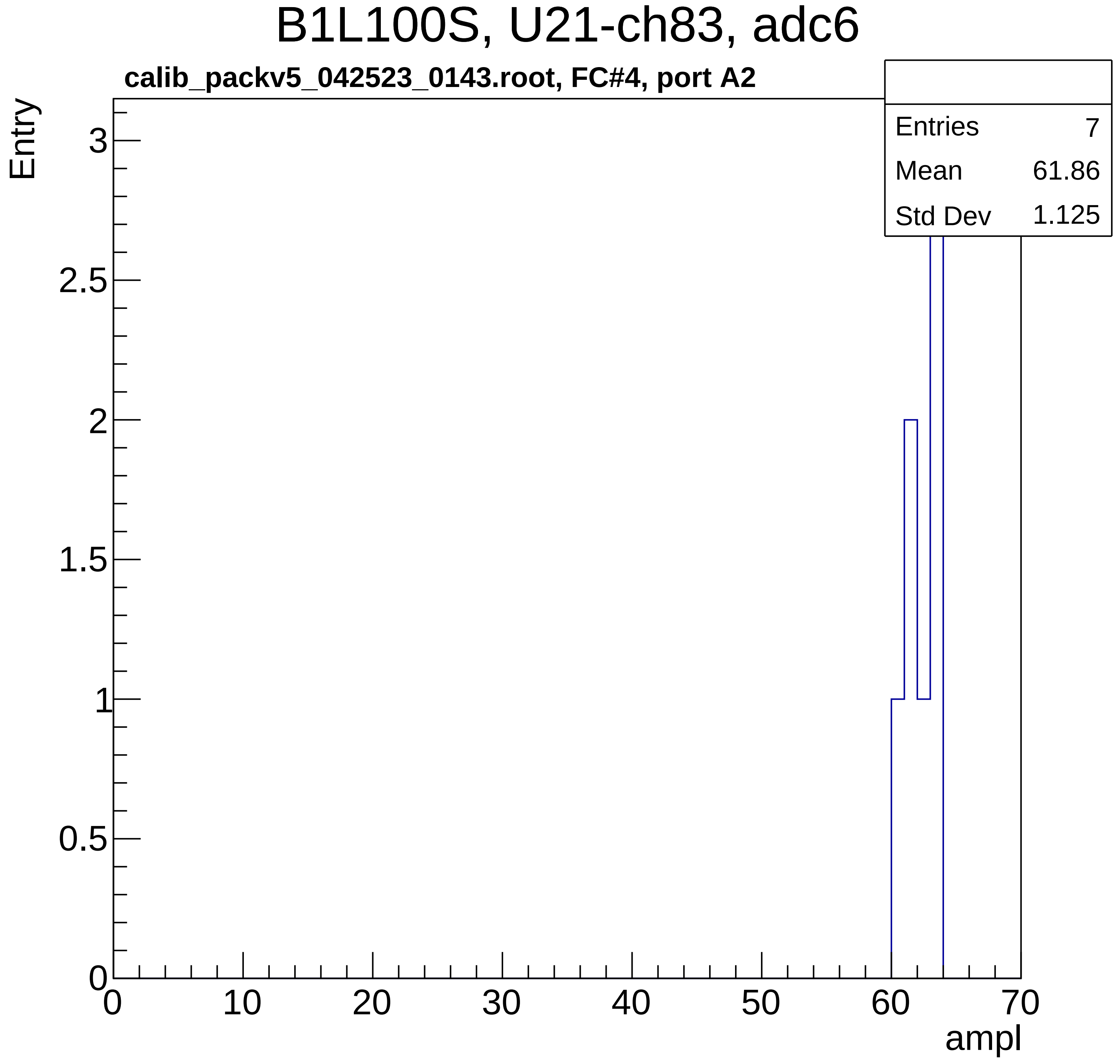

B1L100S, U21-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 41.46
Std Dev 3.396
Gaus mean : 42.0956
Gaus Width: 3.3875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch83, adc3



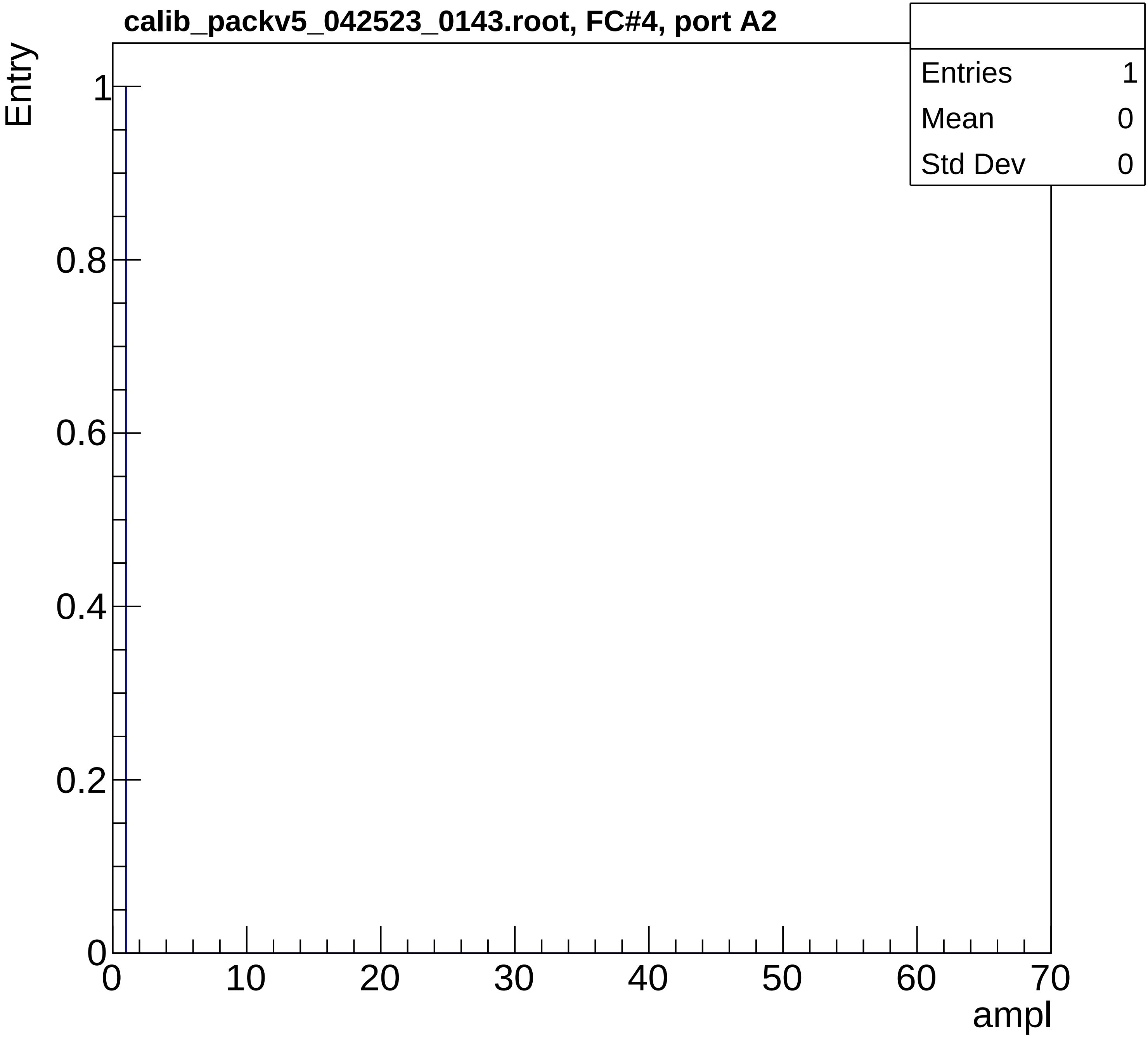

B1L100S, U21-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 54.69
Std Dev 3.43
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

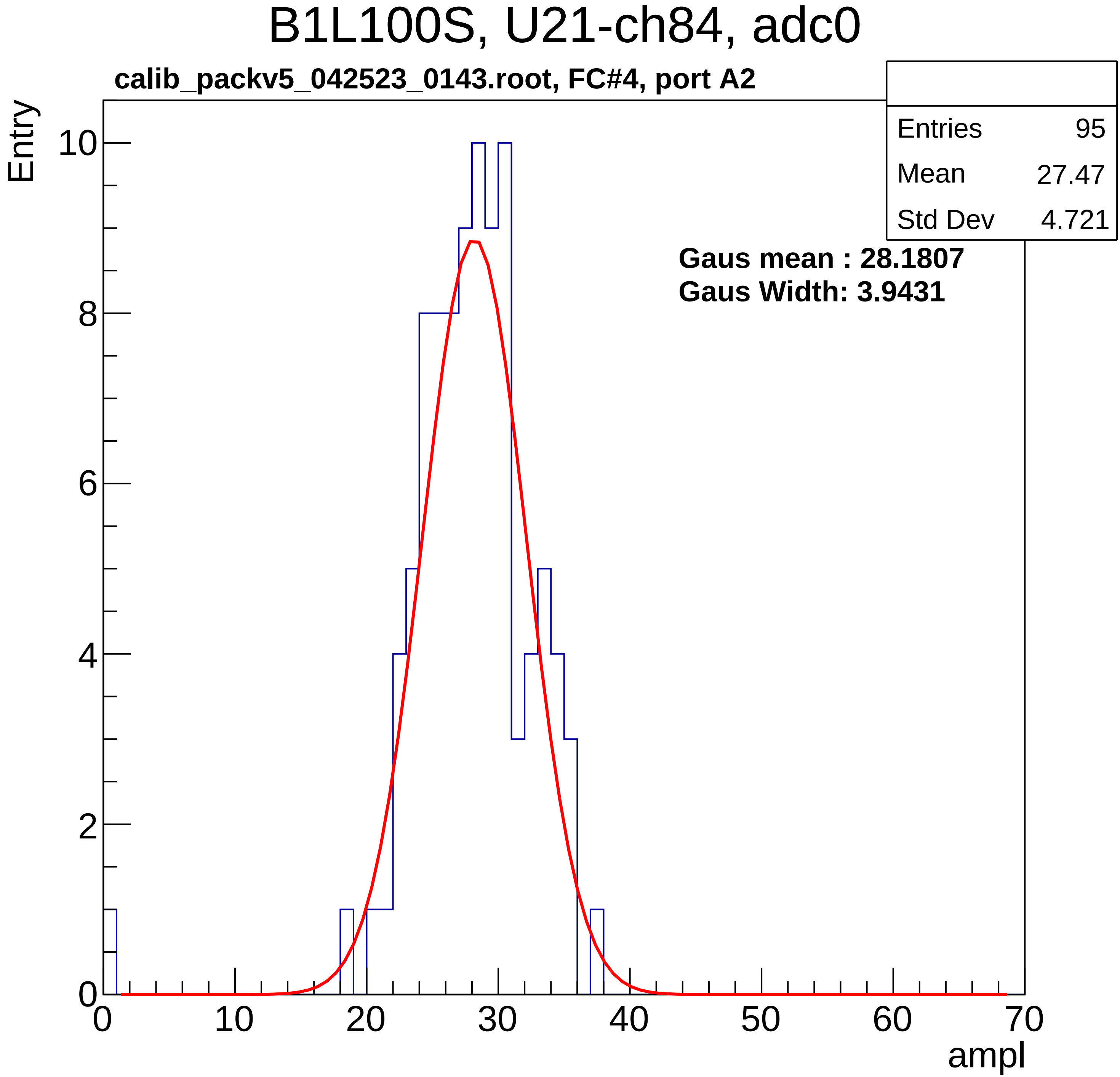

B1L100S, U21-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.71
Std Dev 9.179
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

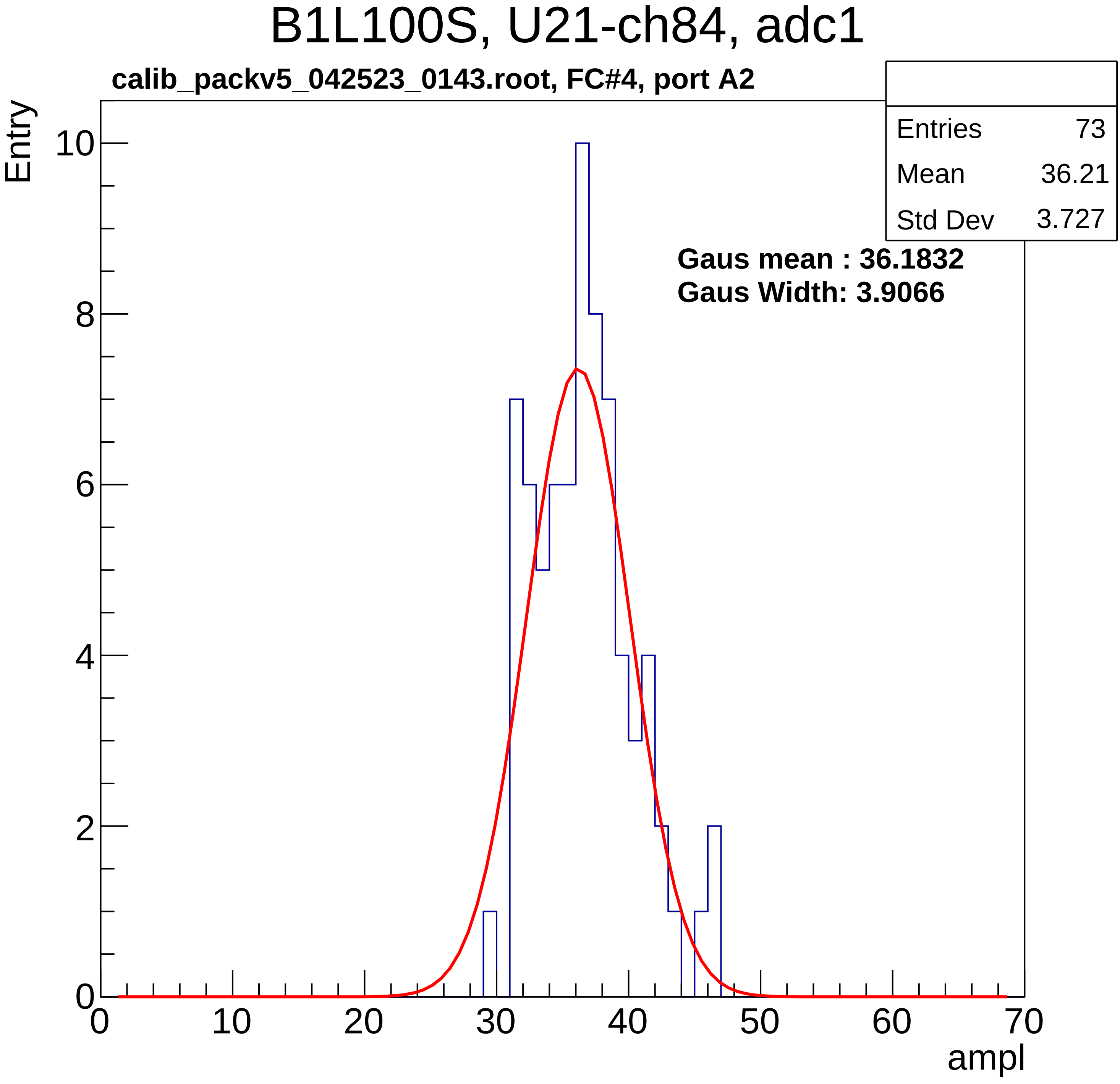

B1L100S, U21-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 1.125
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

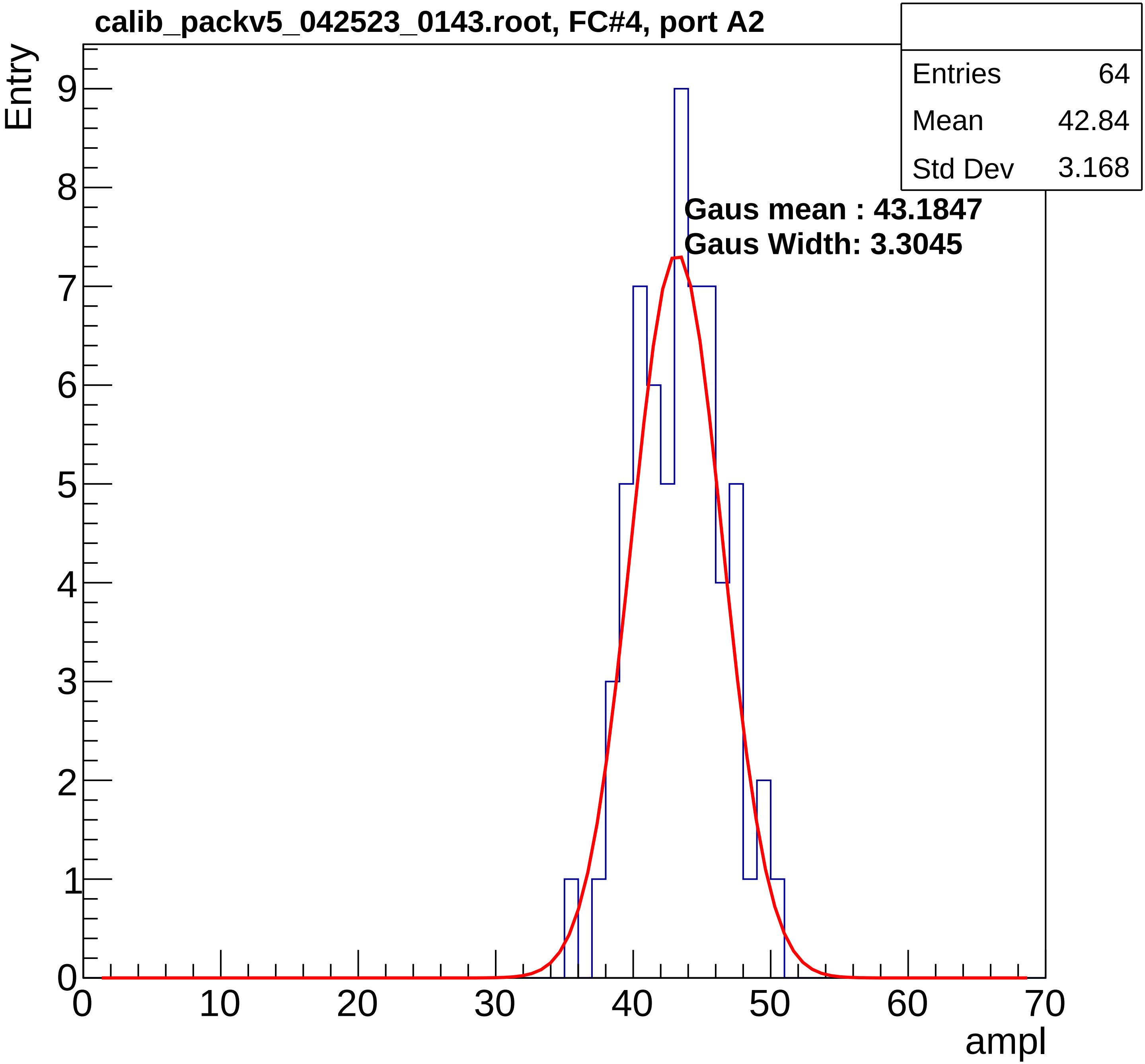



B1L100S, U21-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 27.47
Std Dev 4.721
Gaus mean : 28.1807
Gaus Width: 3.9431
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.21
Std Dev 3.727
Gaus mean : 36.1832
Gaus Width: 3.9066
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

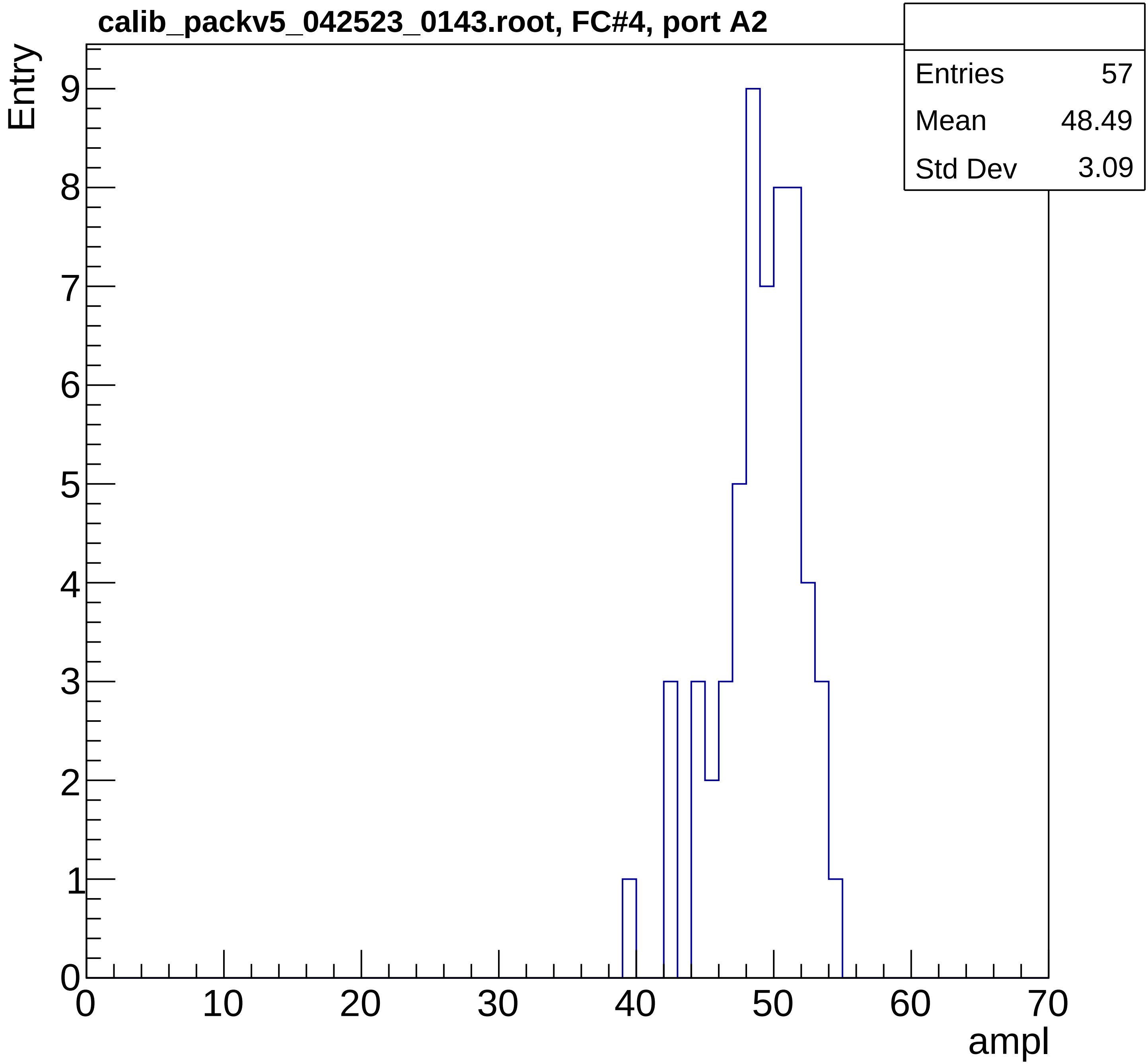
B1L100S, U21-ch84, adc2

calib_packv5_042523_0143.root, FC#4, port A2



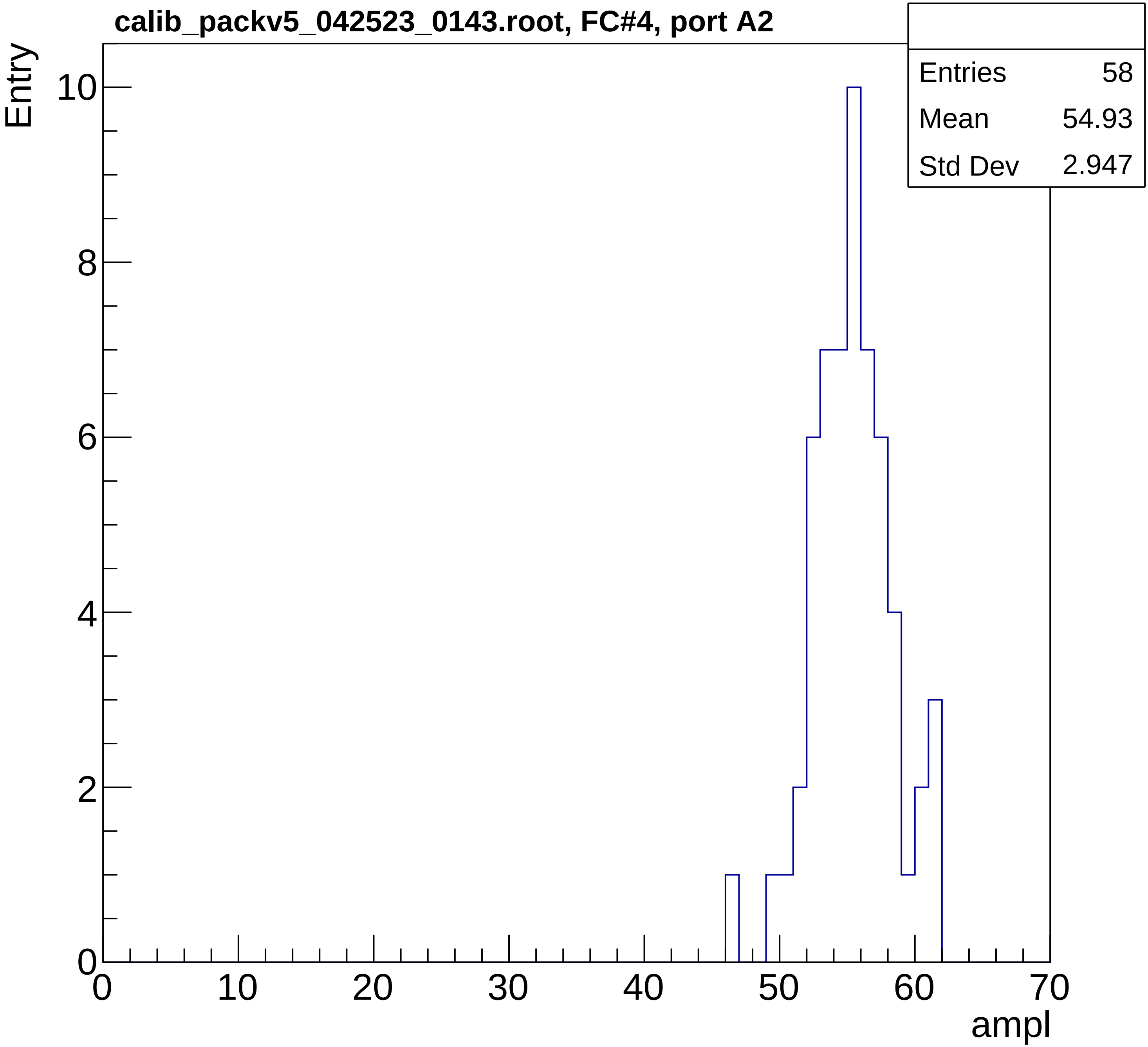
B1L100S, U21-ch84, adc3

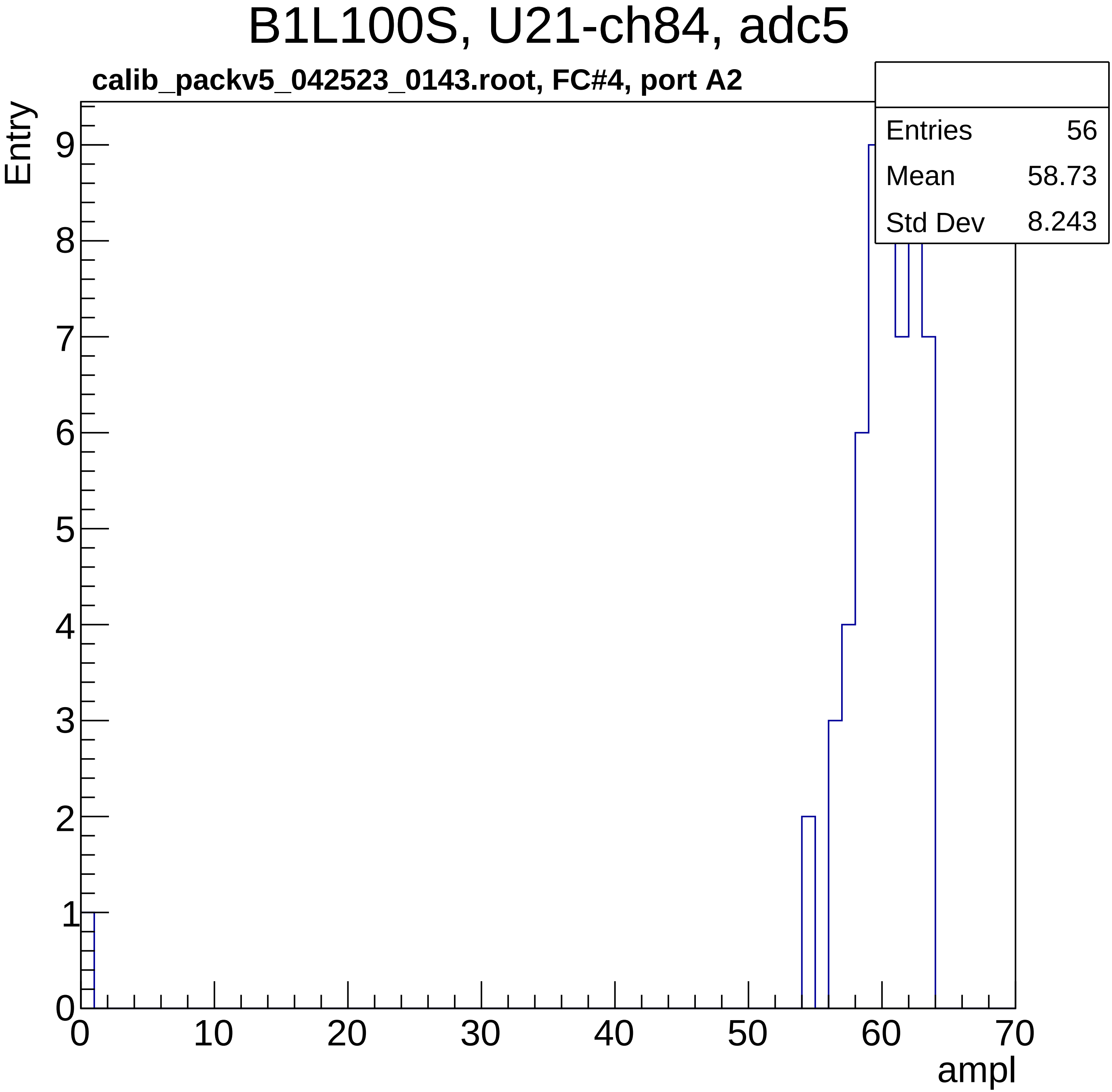
calib_packv5_042523_0143.root, FC#4, port A2

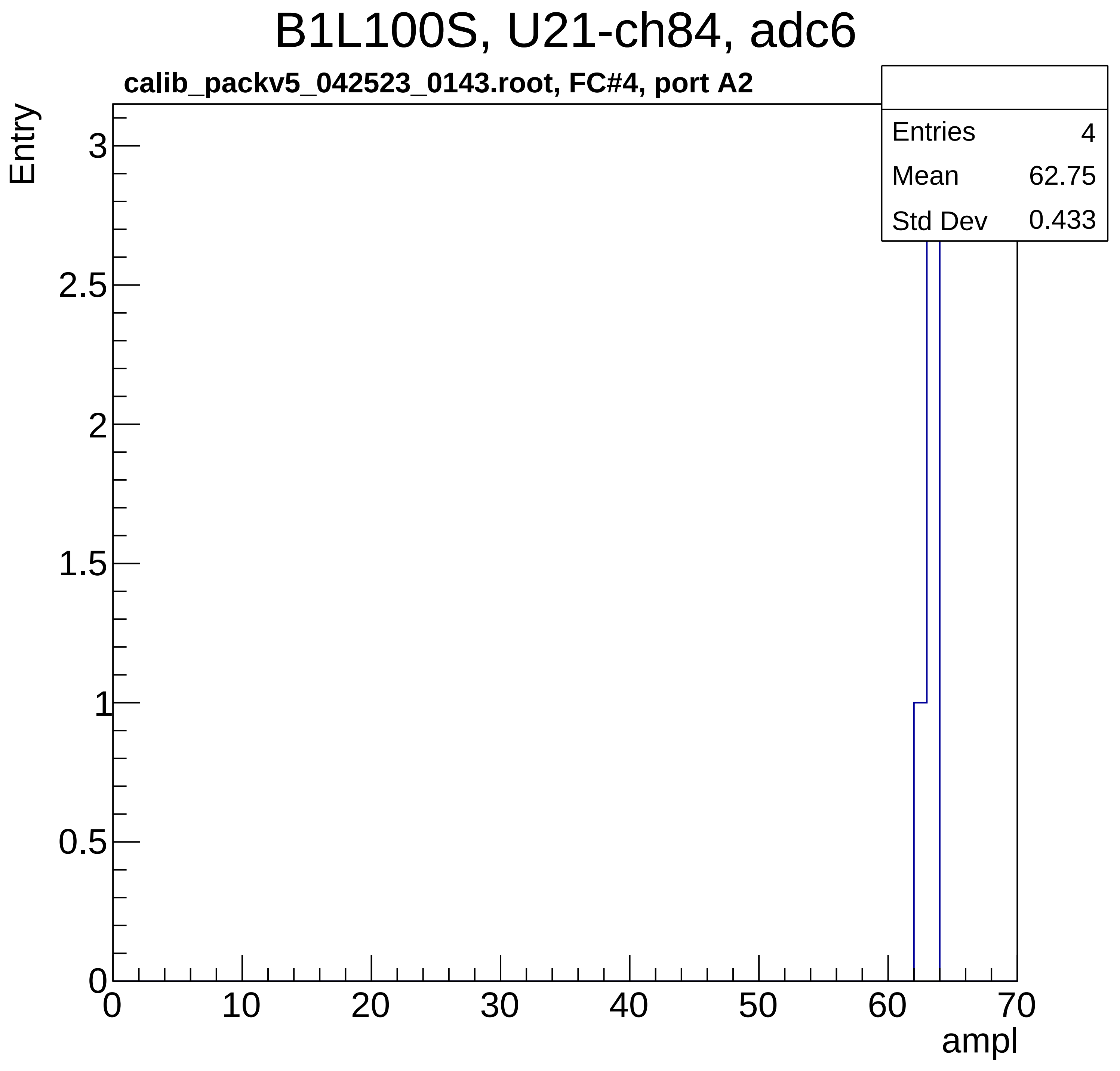


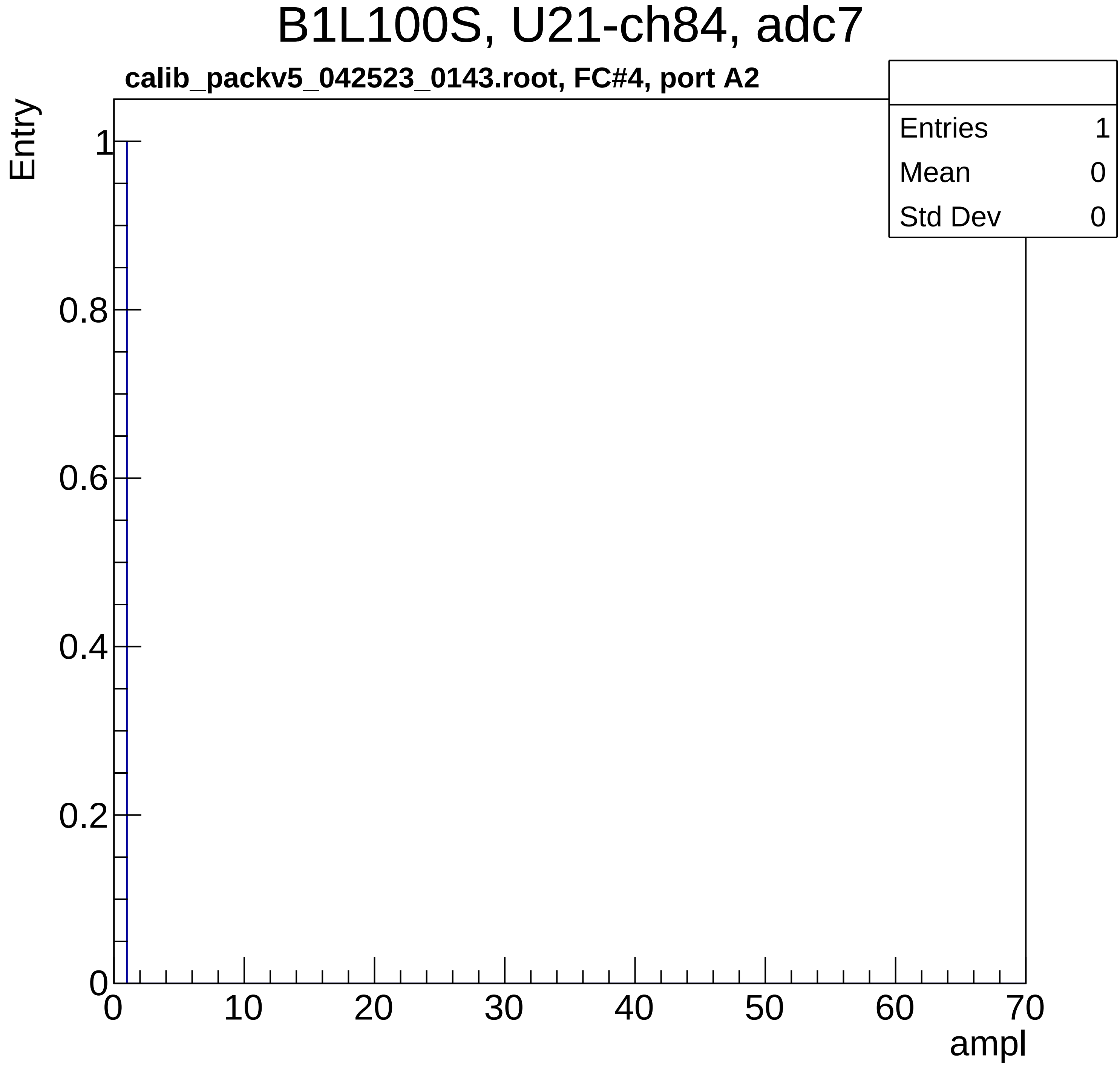
B1L100S, U21-ch84, adc4

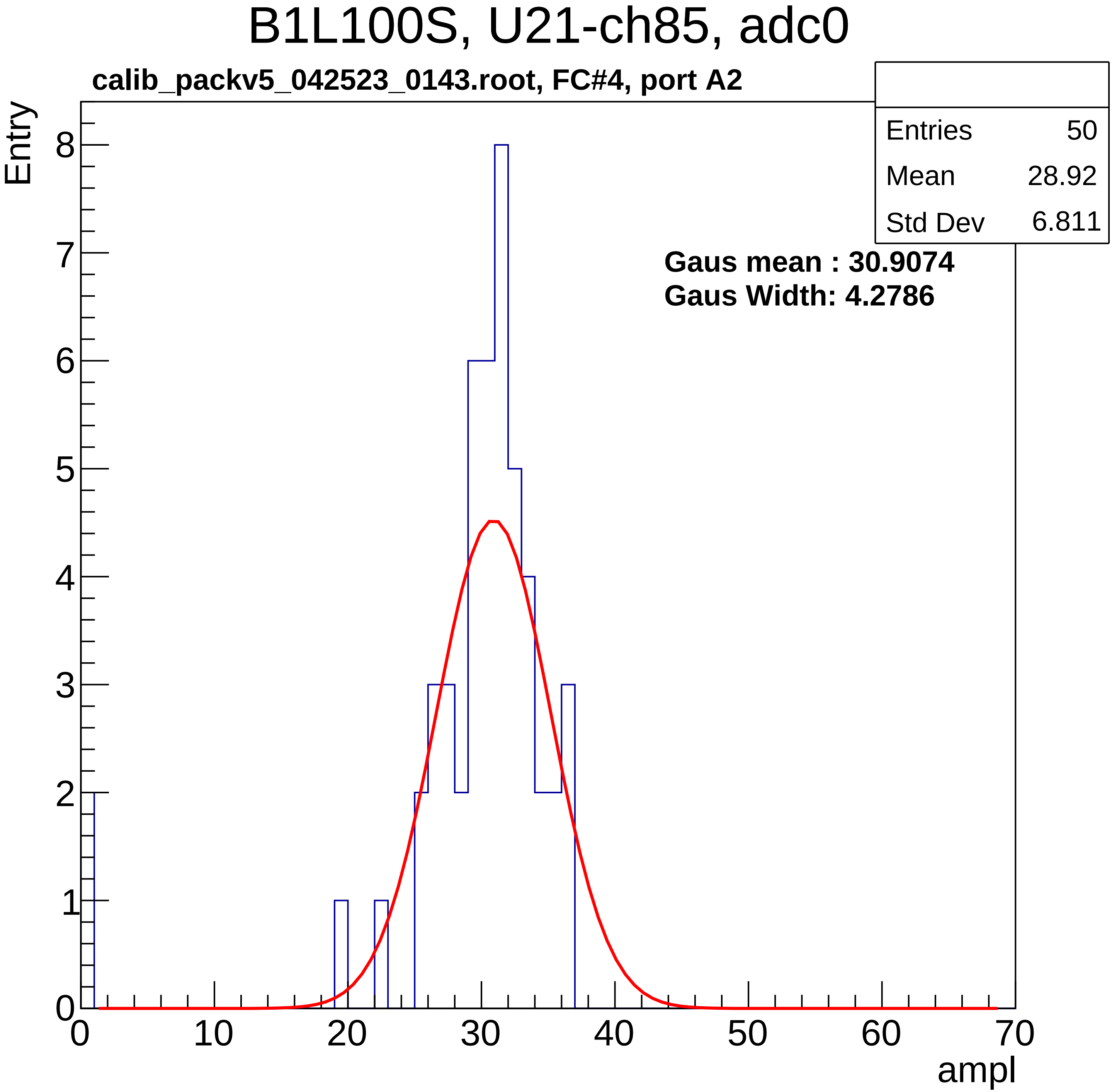
calib_packv5_042523_0143.root, FC#4, port A2

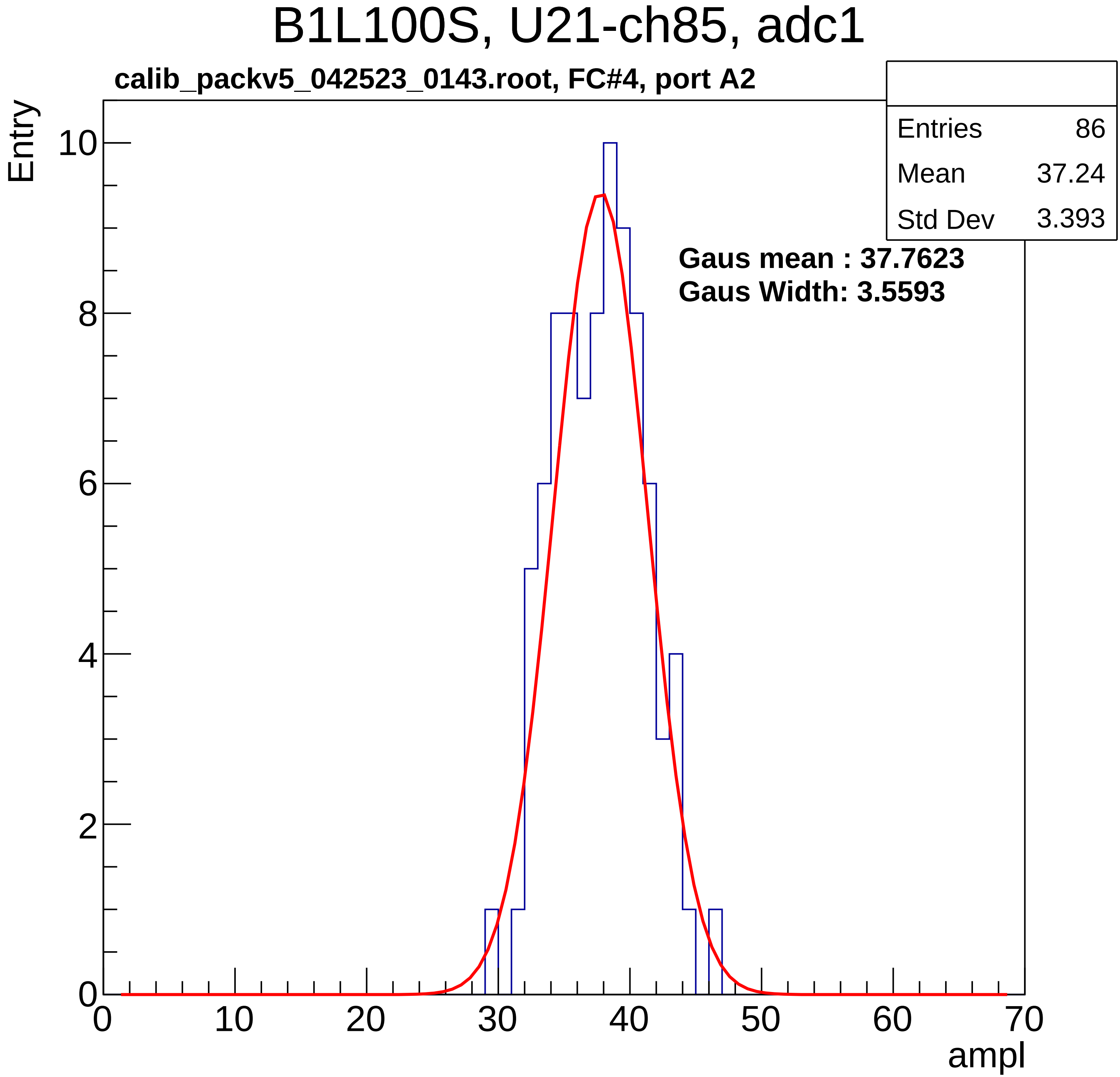


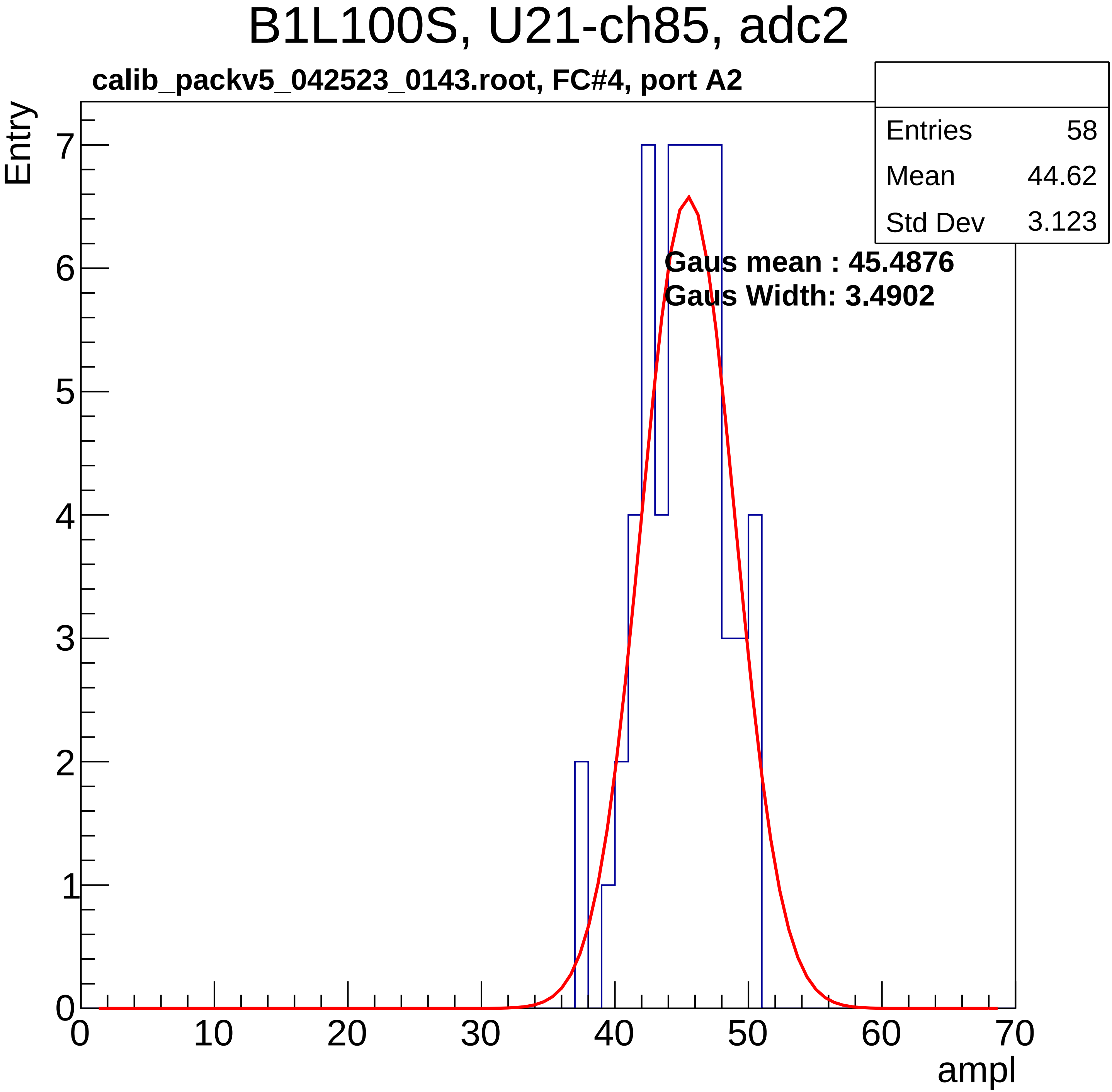

B1L100S, U21-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.73
Std Dev 8.243
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

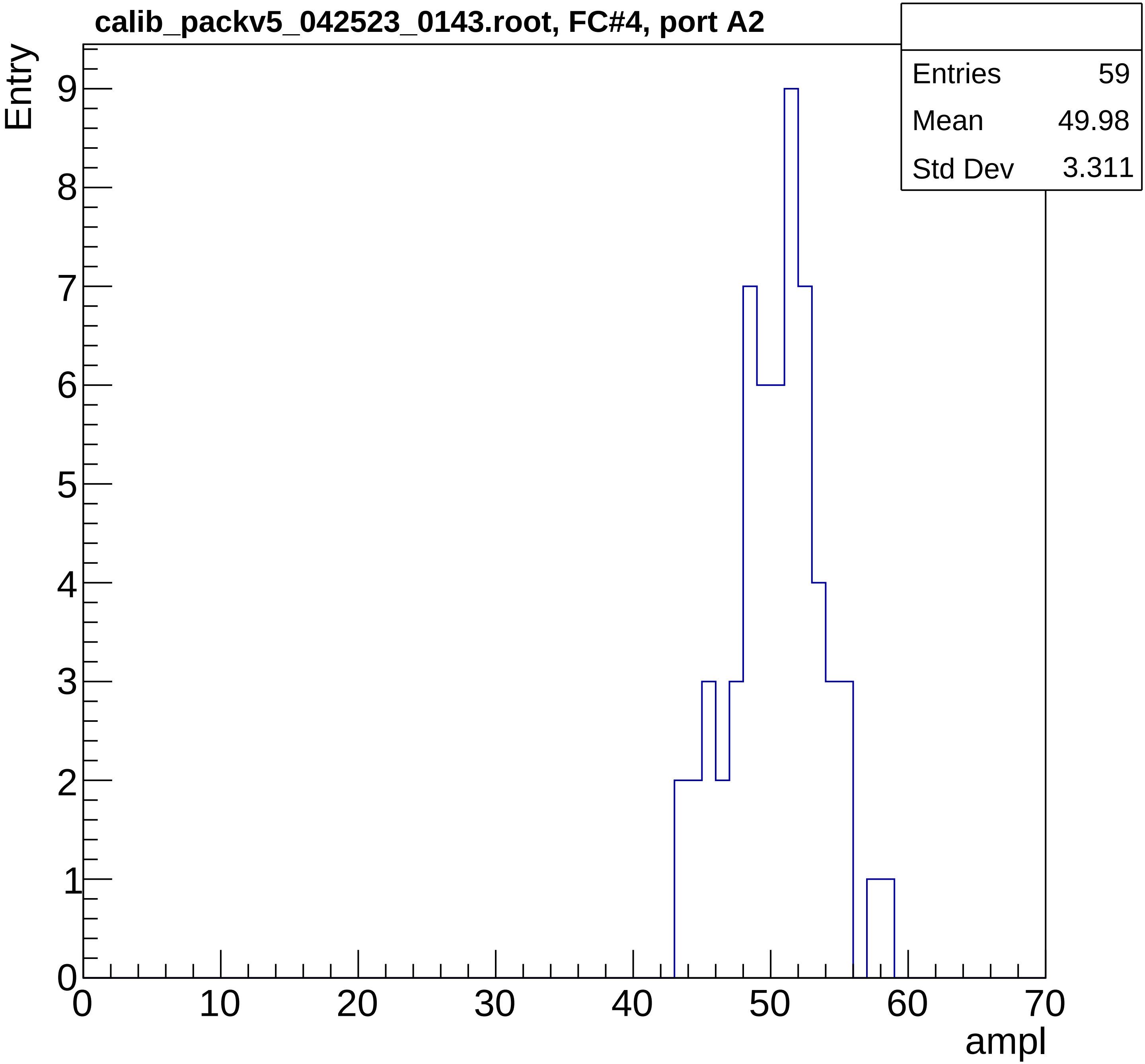

B1L100S, U21-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 28.92
Std Dev 6.811
Gaus mean : 30.9074
Gaus Width: 4.2786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 37.24
Std Dev 3.393
Gaus mean : 37.7623
Gaus Width: 3.5593
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 44.62
Std Dev 3.123
Gaus mean : 45.4876
Gaus Width: 3.4902
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

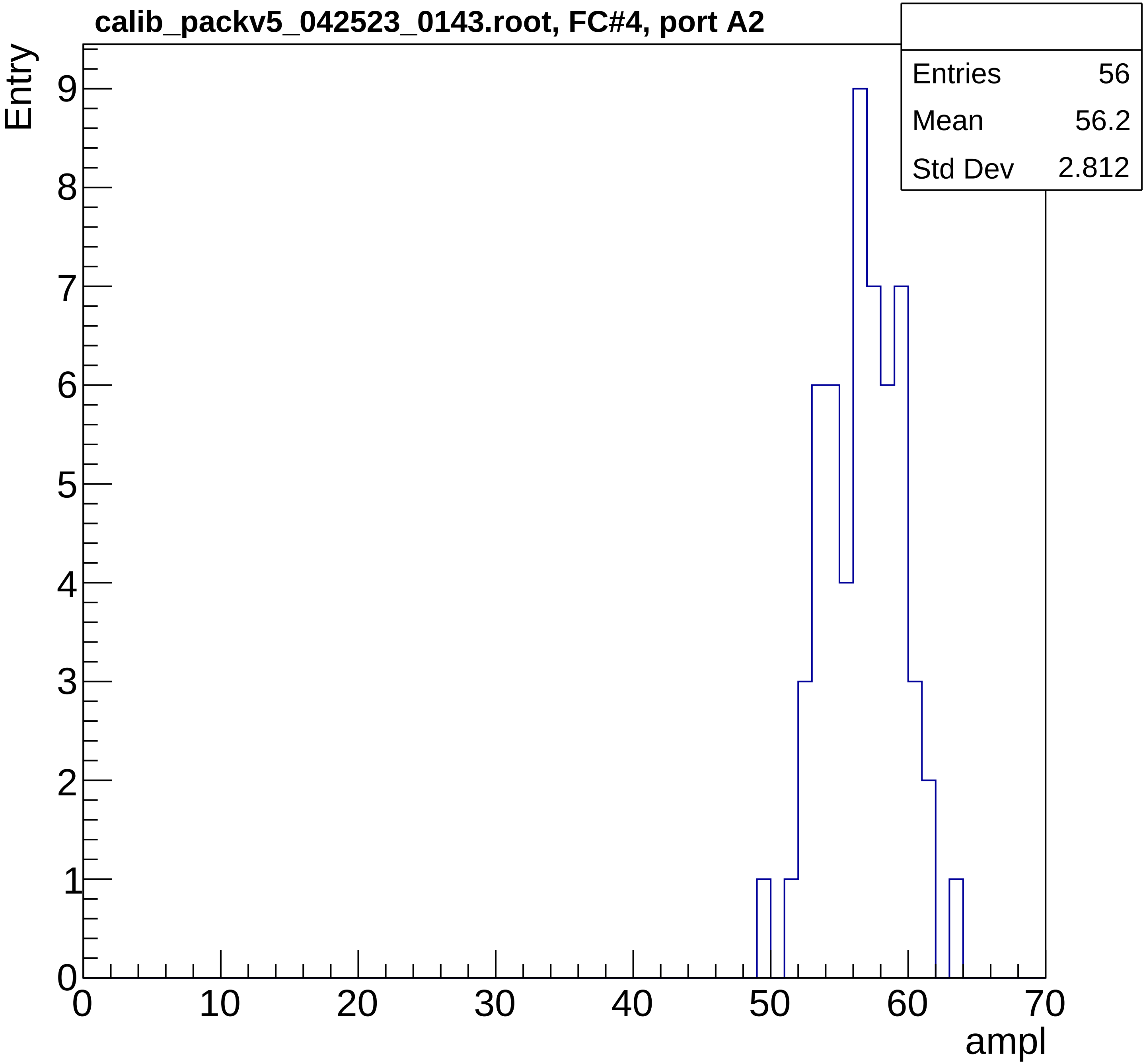
B1L100S, U21-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2



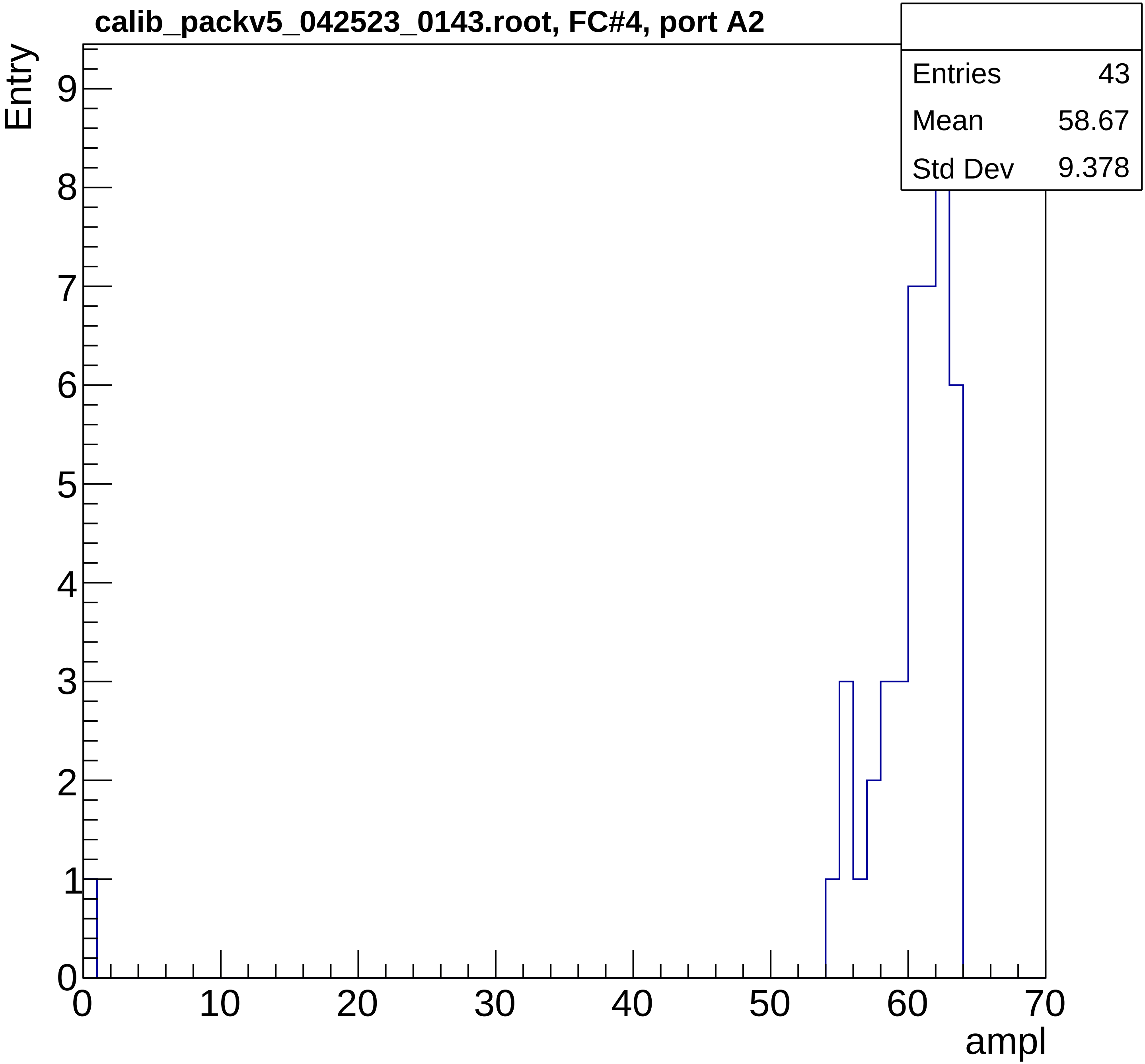
B1L100S, U21-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

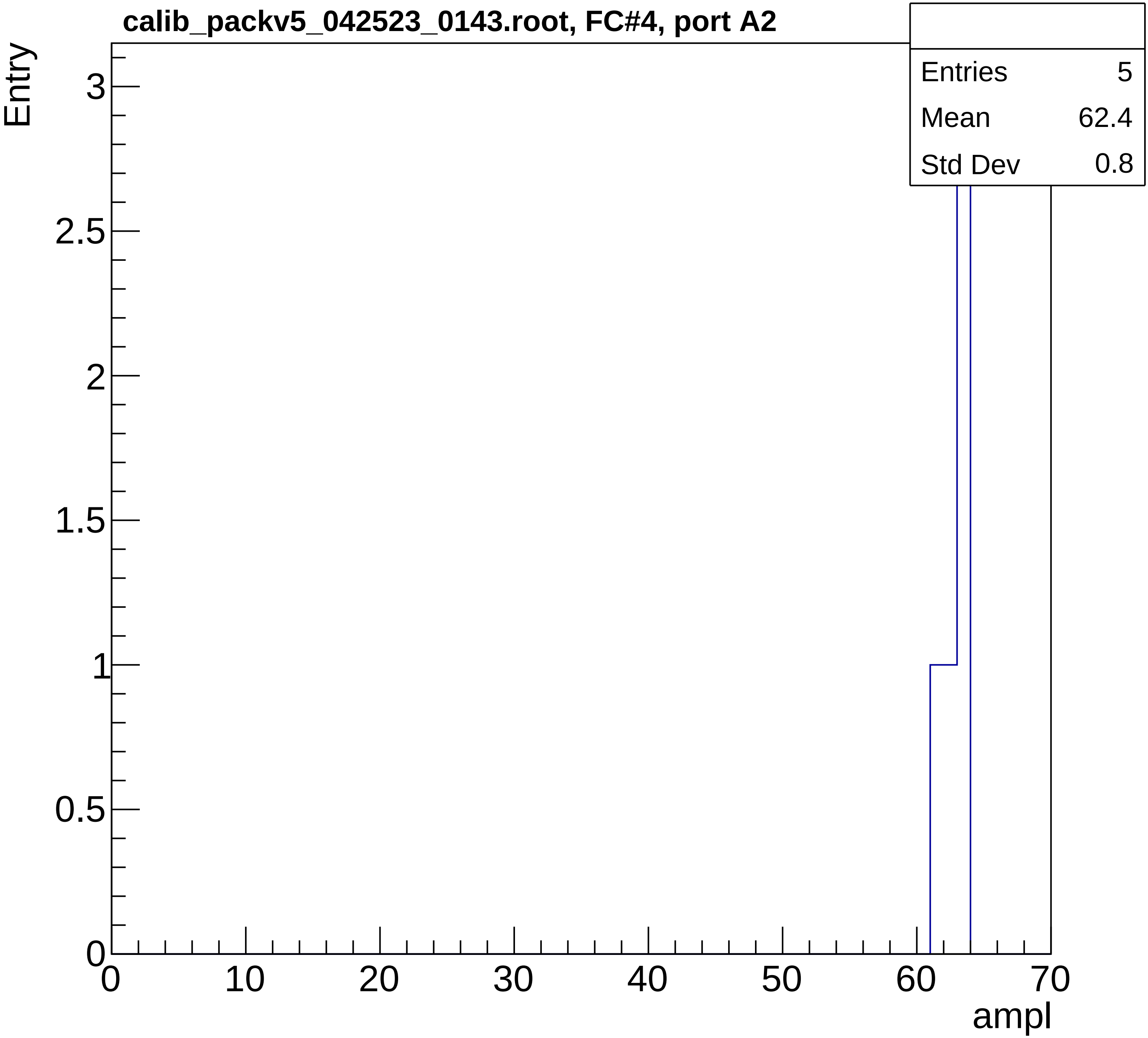


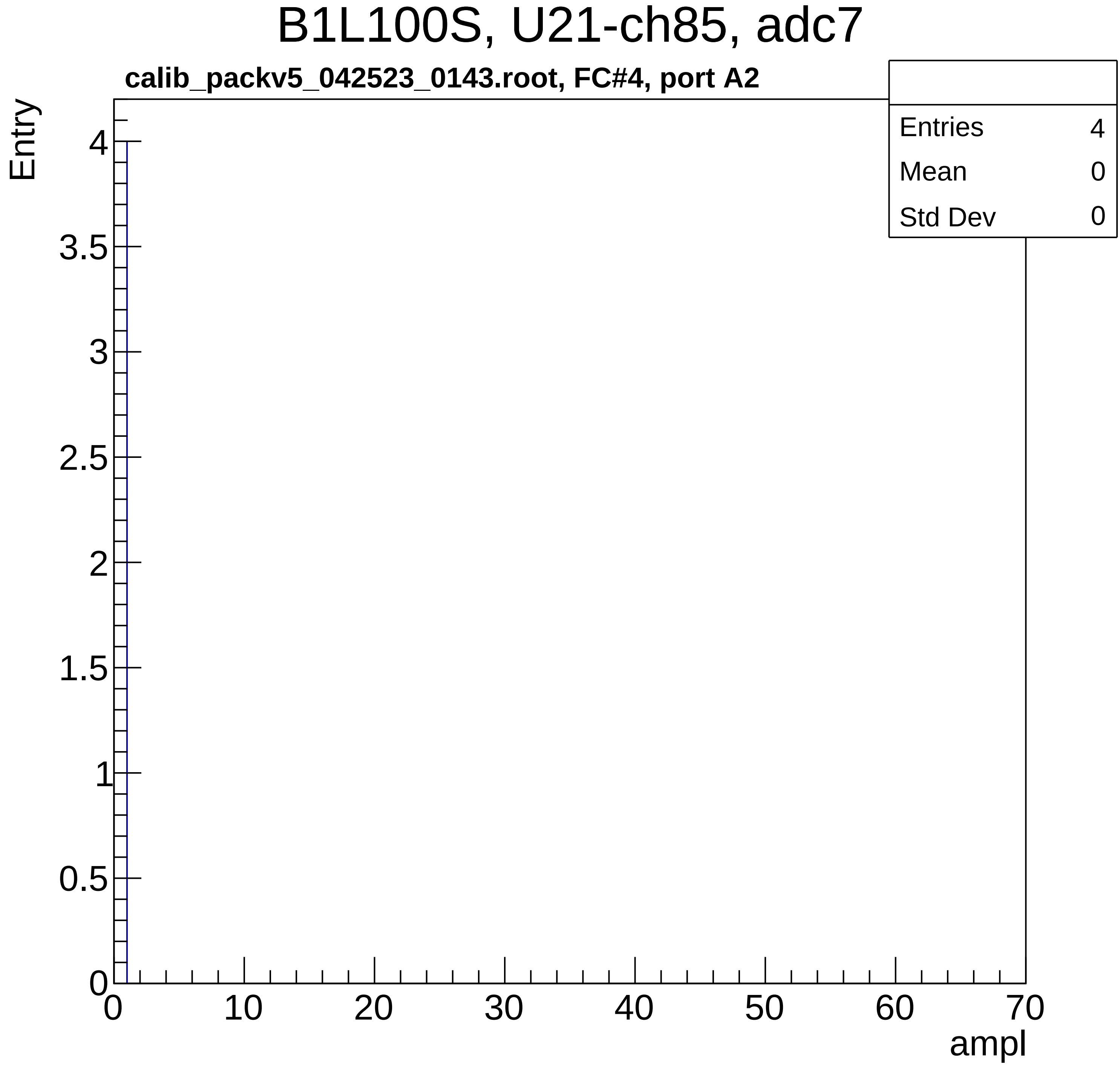
B1L100S, U21-ch85, adc5

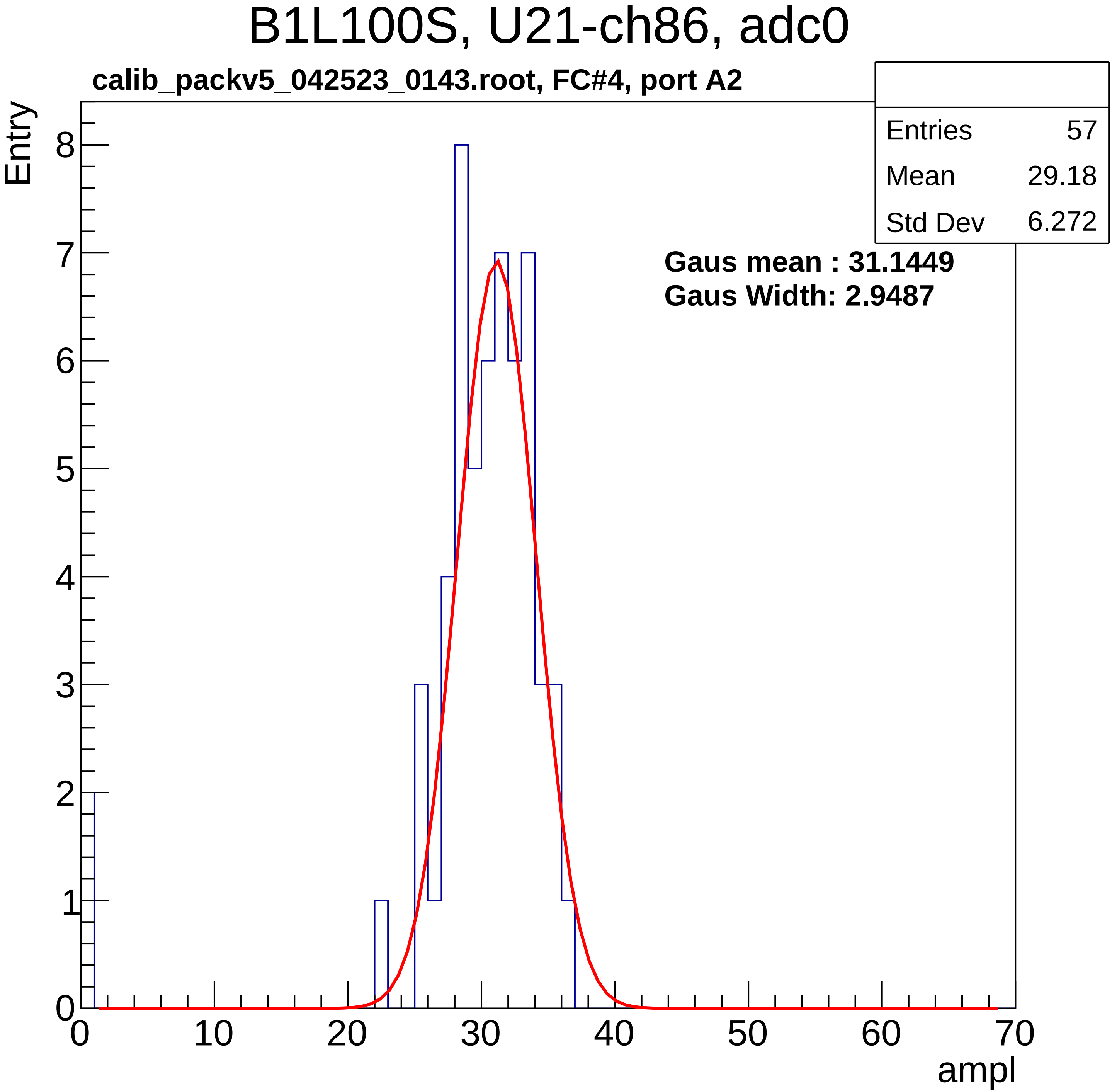
calib_packv5_042523_0143.root, FC#4, port A2

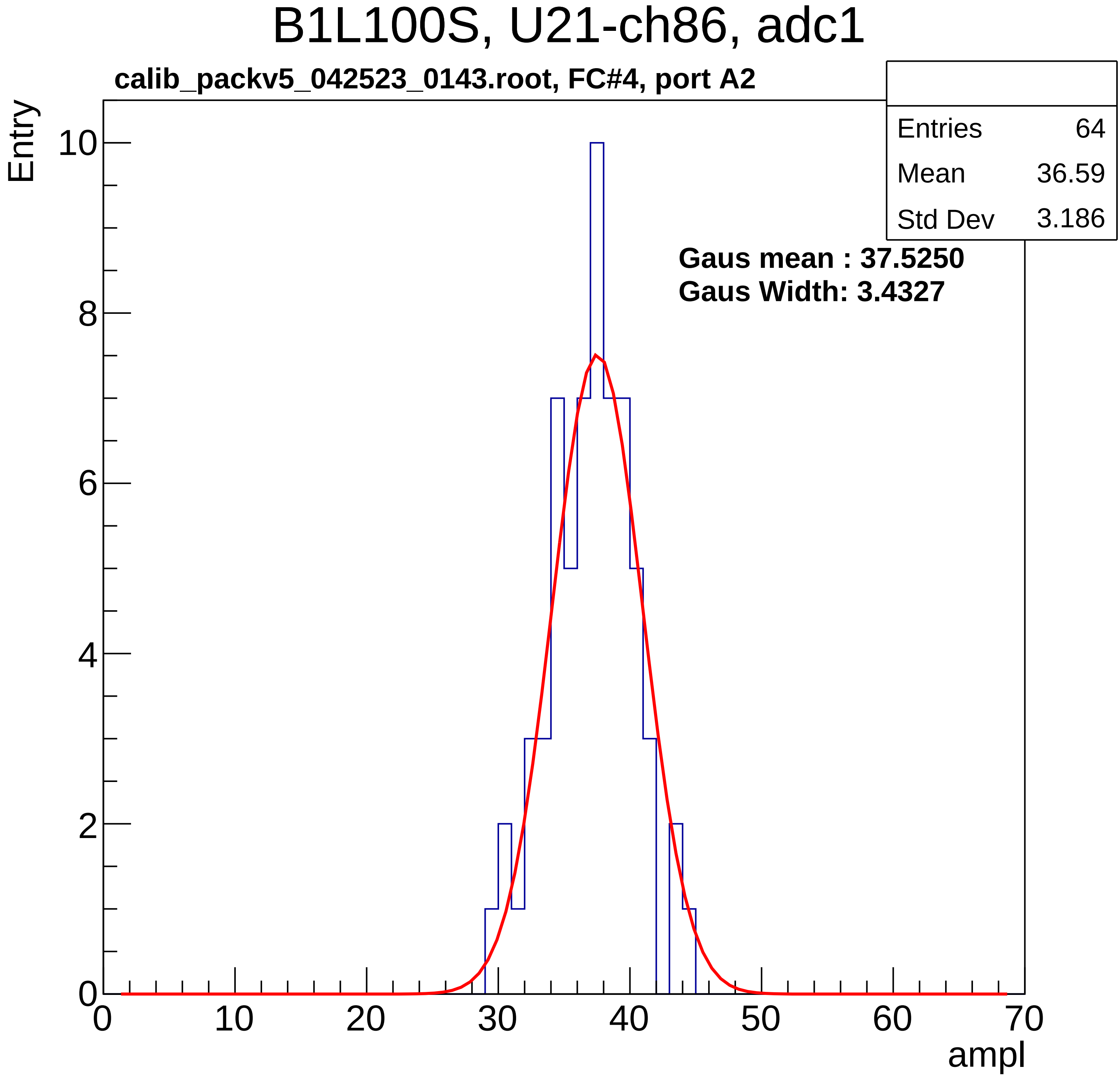


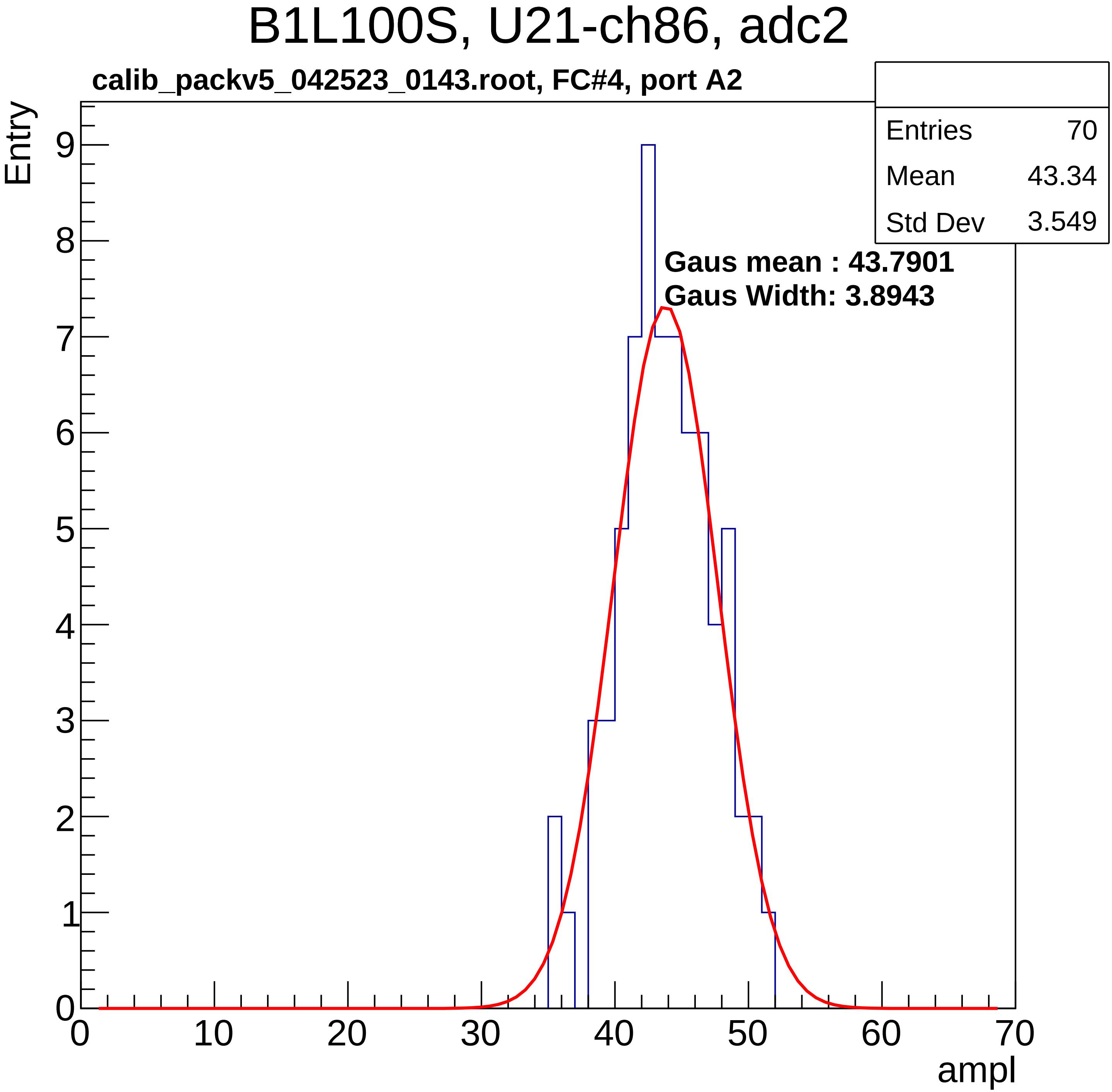
B1L100S, U21-ch85, adc6




B1L100S, U21-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 0
Std Dev 0
Entry
4
3.5
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

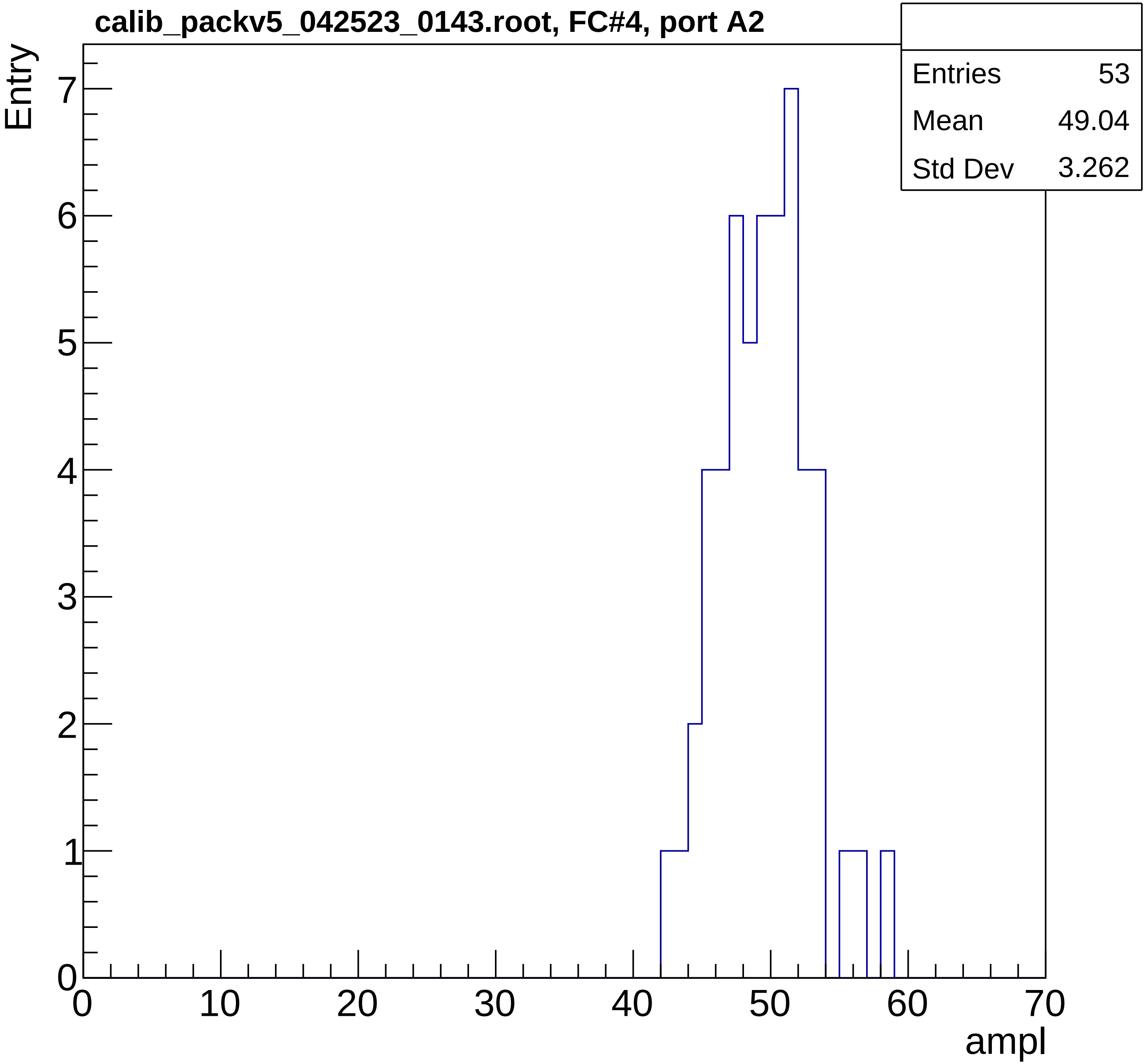

B1L100S, U21-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.18
Std Dev 6.272
Gaus mean : 31.1449
Gaus Width: 2.9487
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.59
Std Dev 3.186
Gaus mean : 37.5250
Gaus Width: 3.4327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

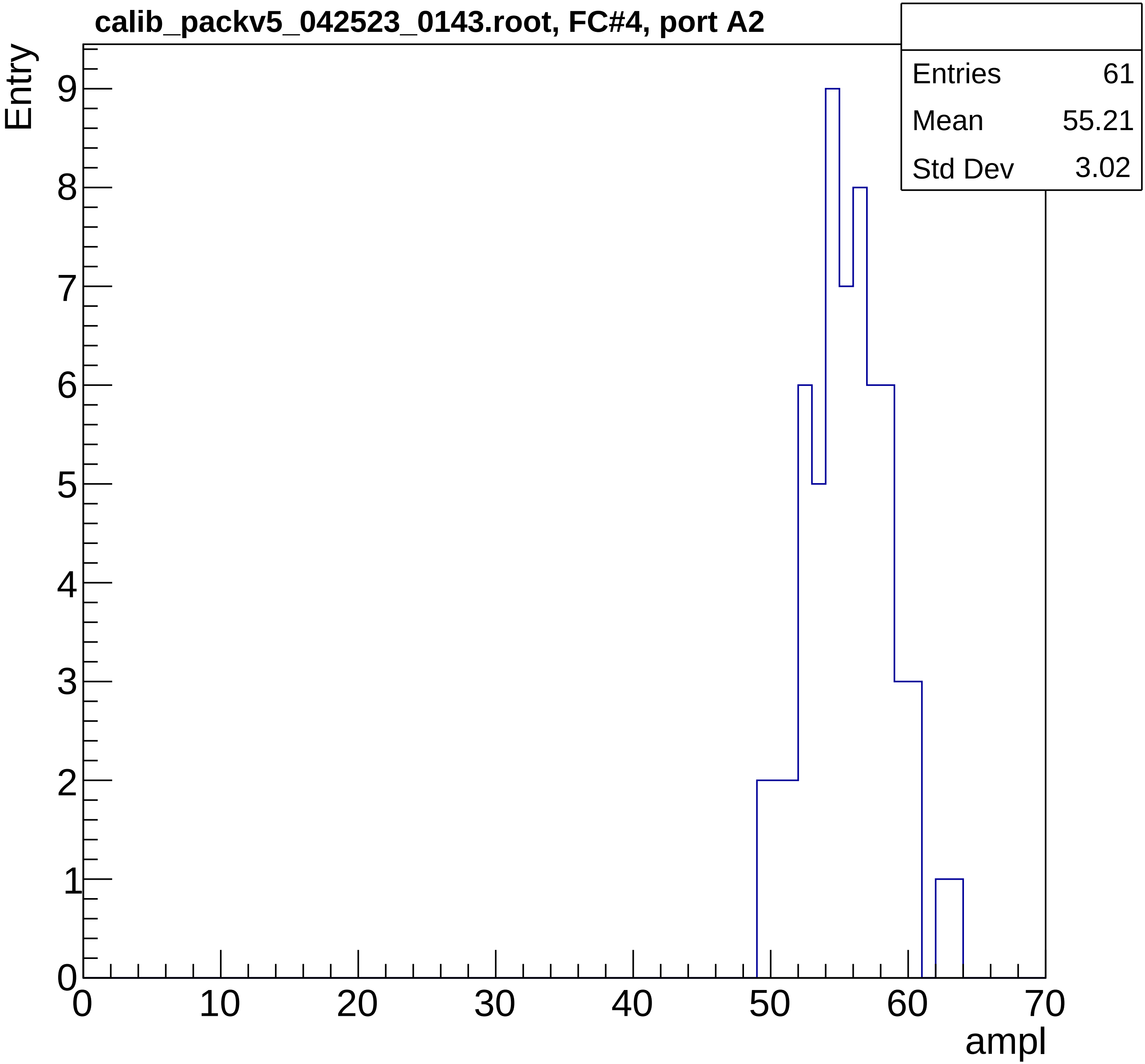

B1L100S, U21-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 70
Mean 43.34
Std Dev 3.549
Gaus mean : 43.7901
Gaus Width: 3.8943
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch86, adc3

calib_packv5_042523_0143.root, FC#4, port A2

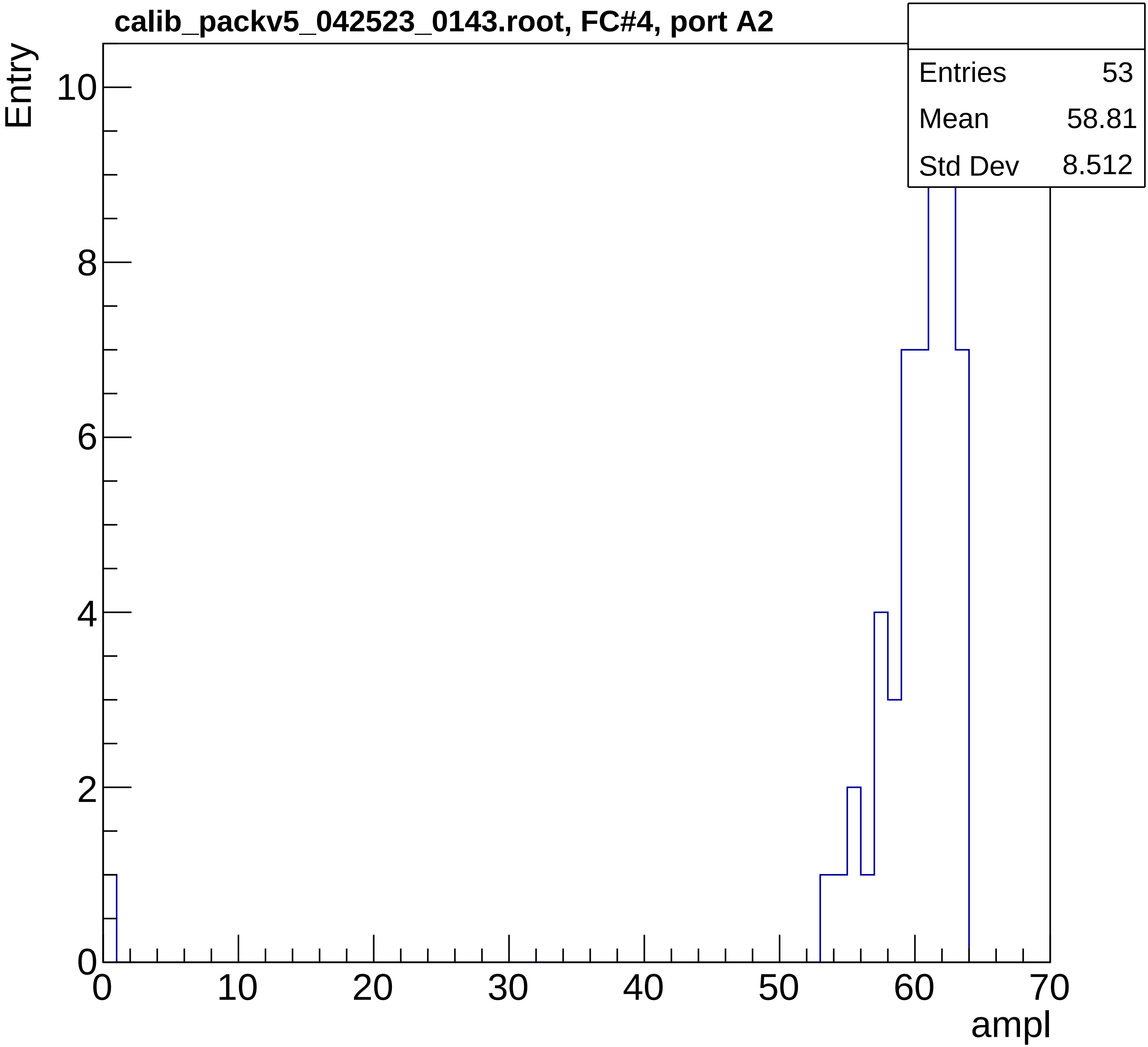


B1L100S, U21-ch86, adc4



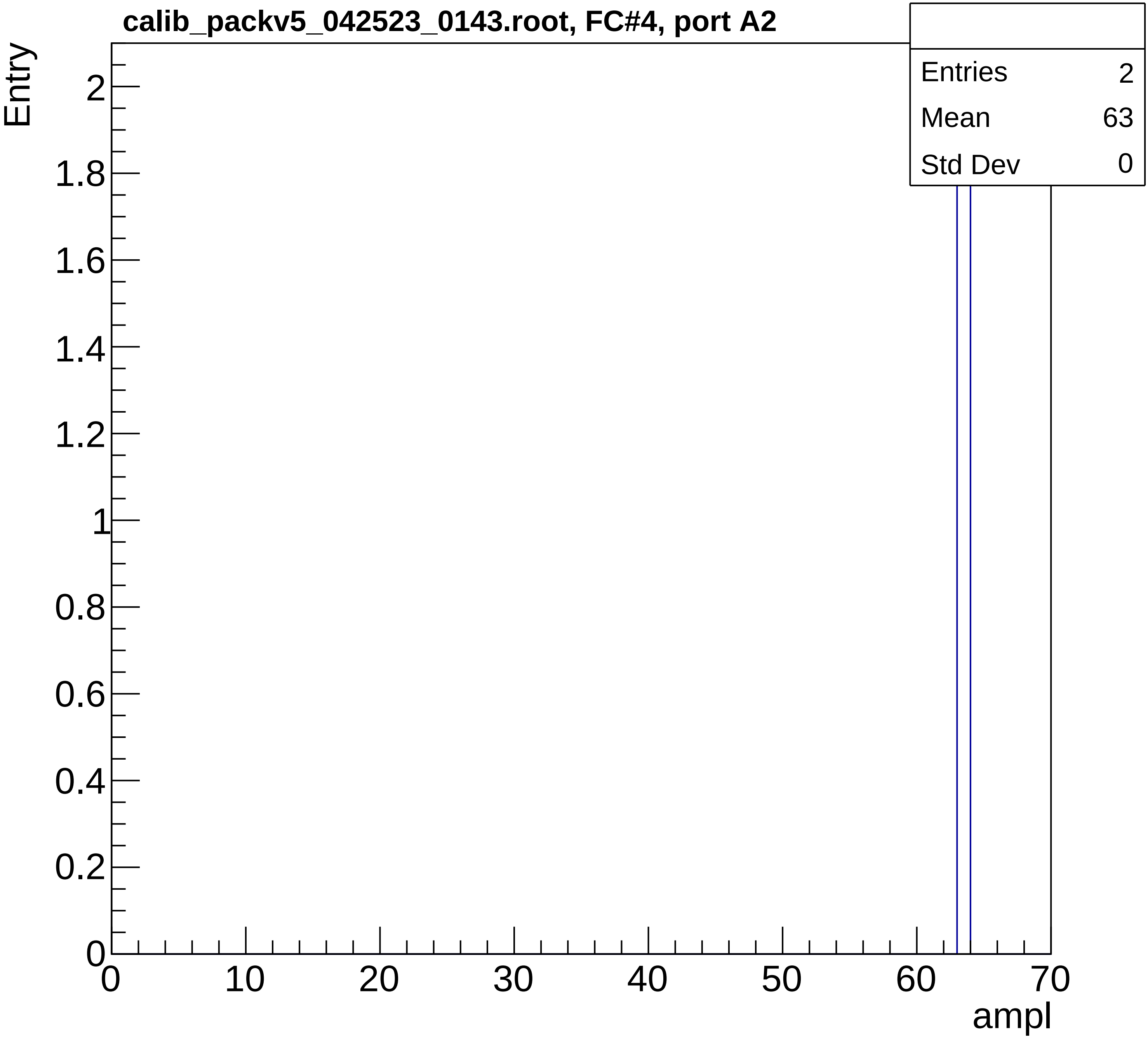
B1L100S, U21-ch86, adc5

calib_packv5_042523_0143.root, FC#4, port A2

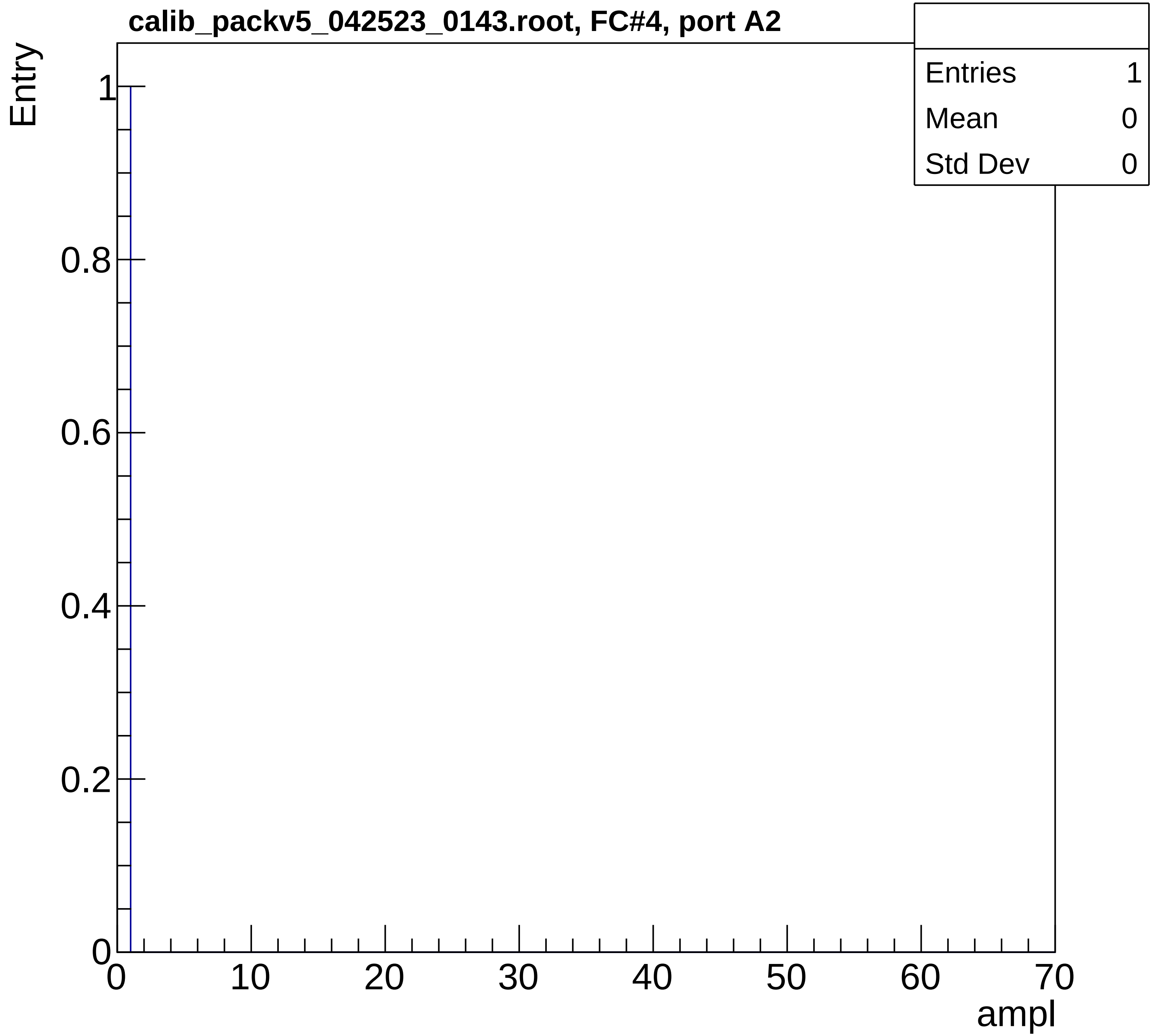


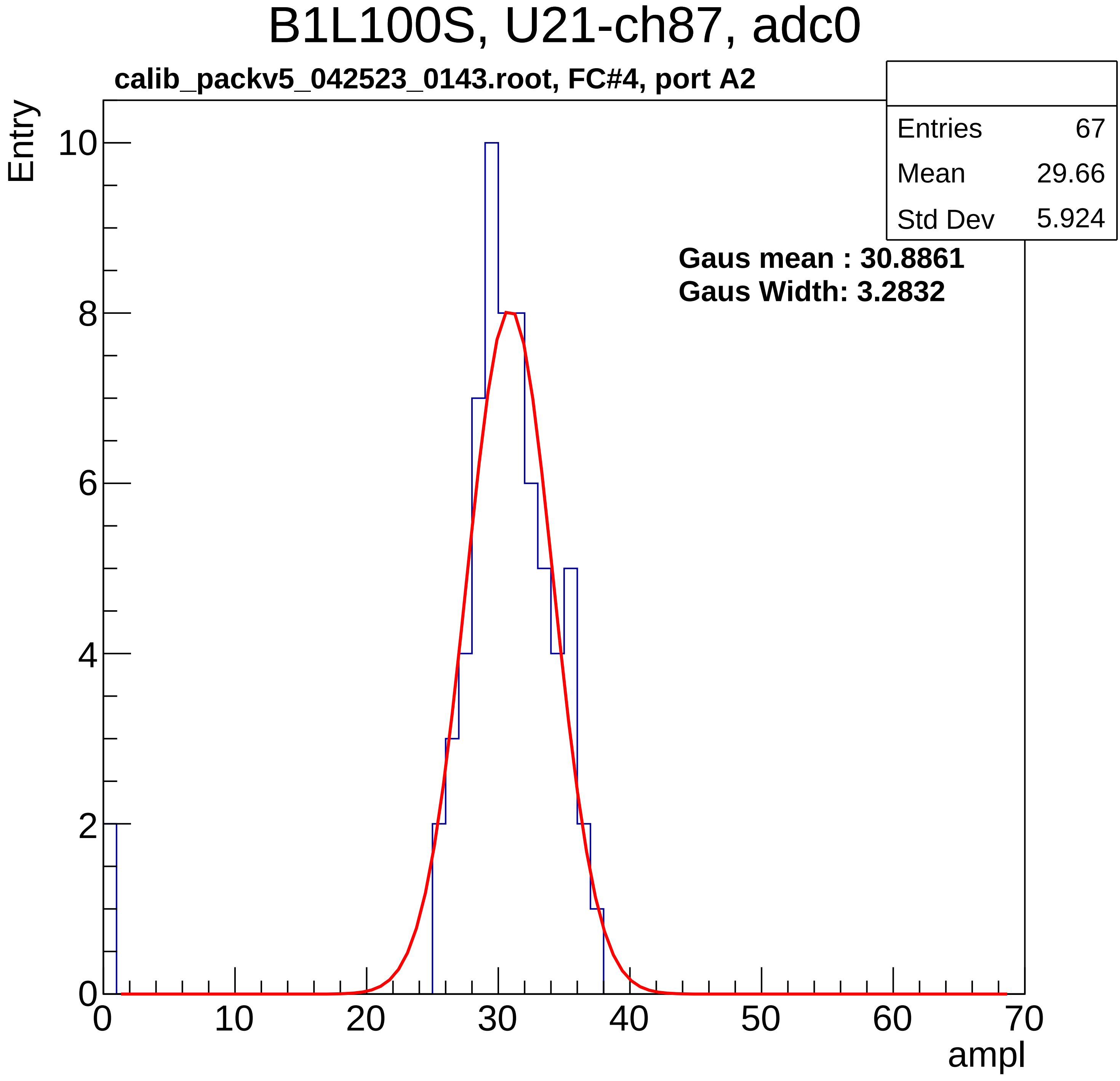
B1L100S, U21-ch86, adc6

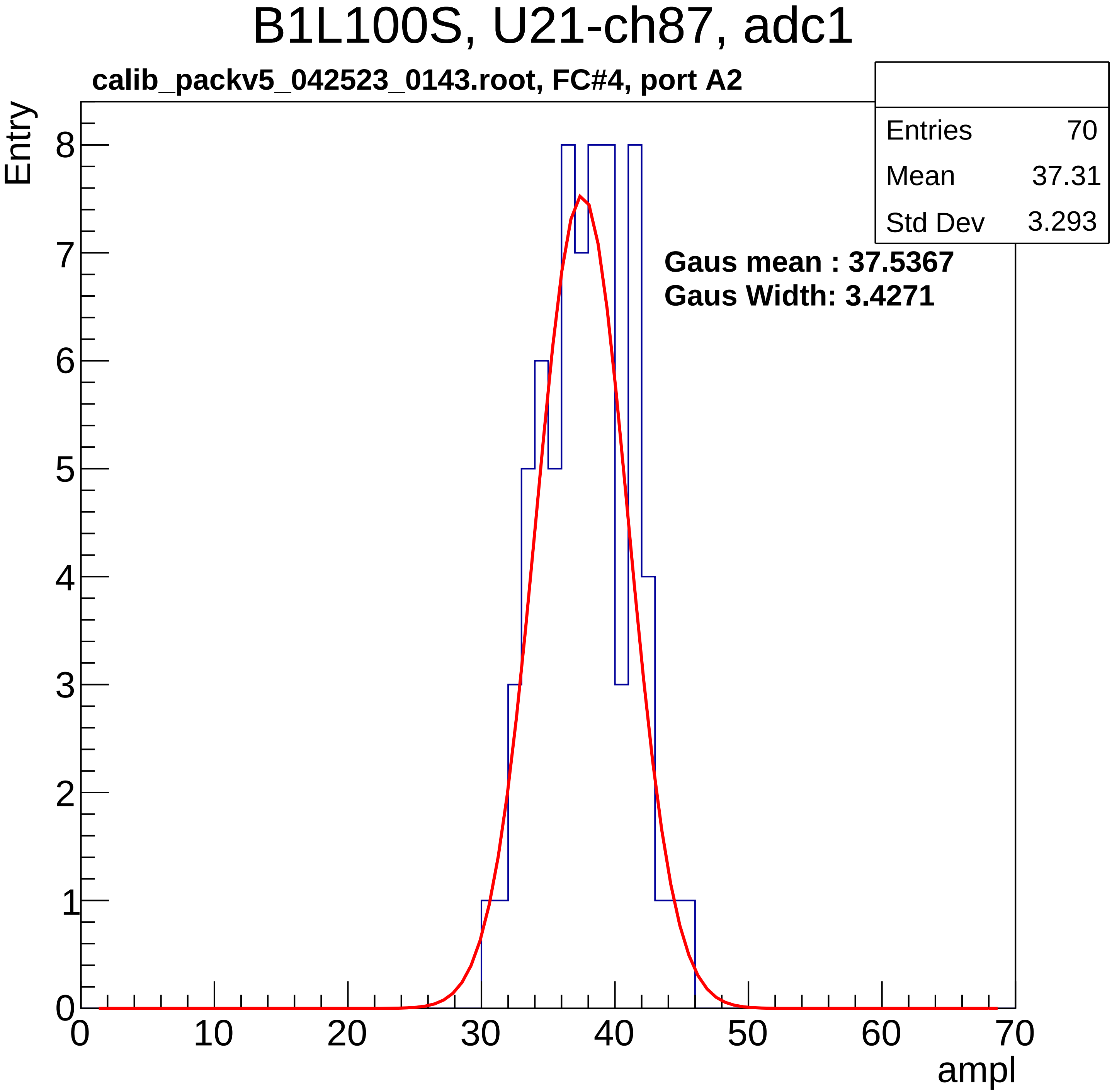
calib_packv5_042523_0143.root, FC#4, port A2

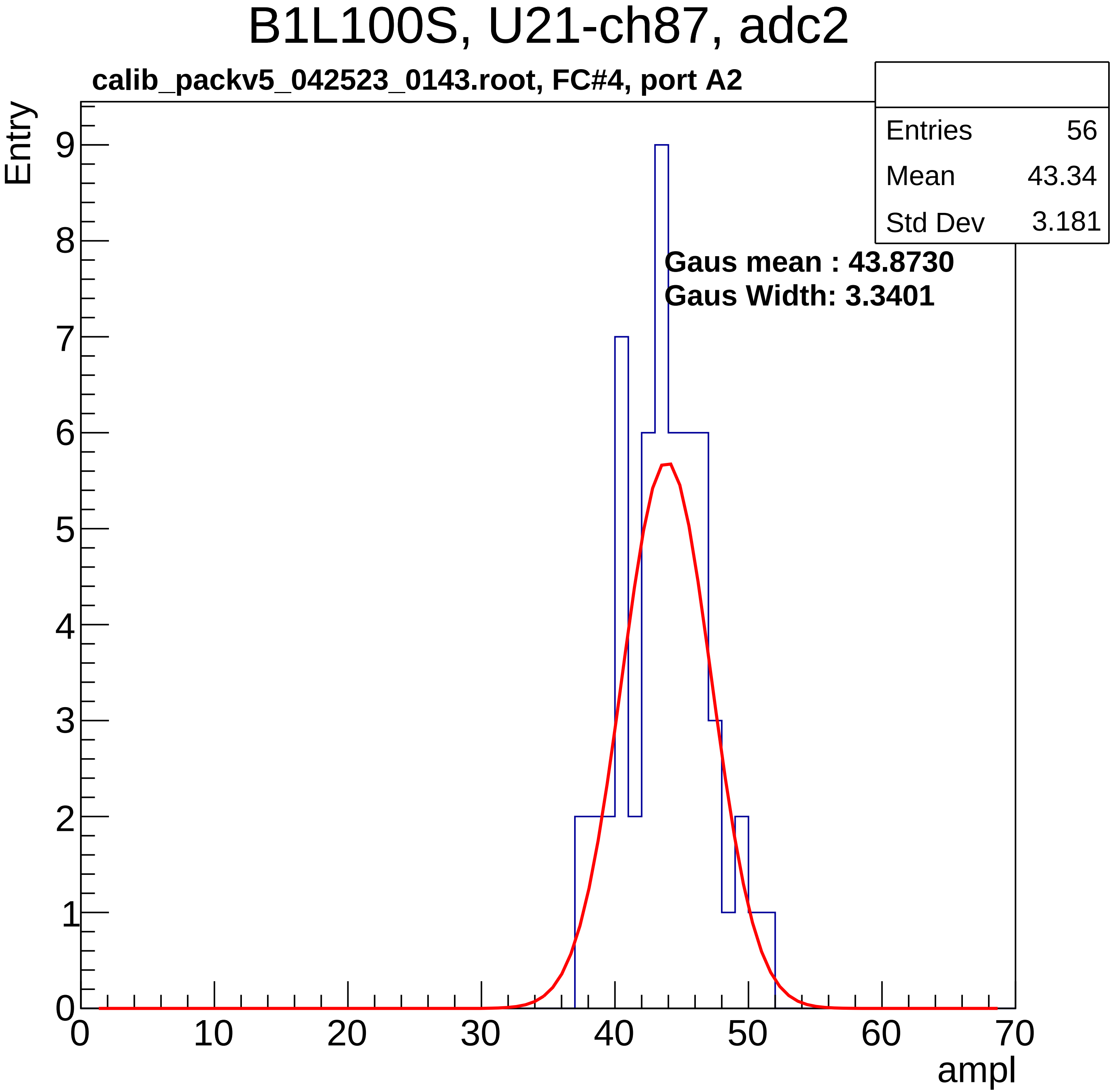


B1L100S, U21-ch86, adc7



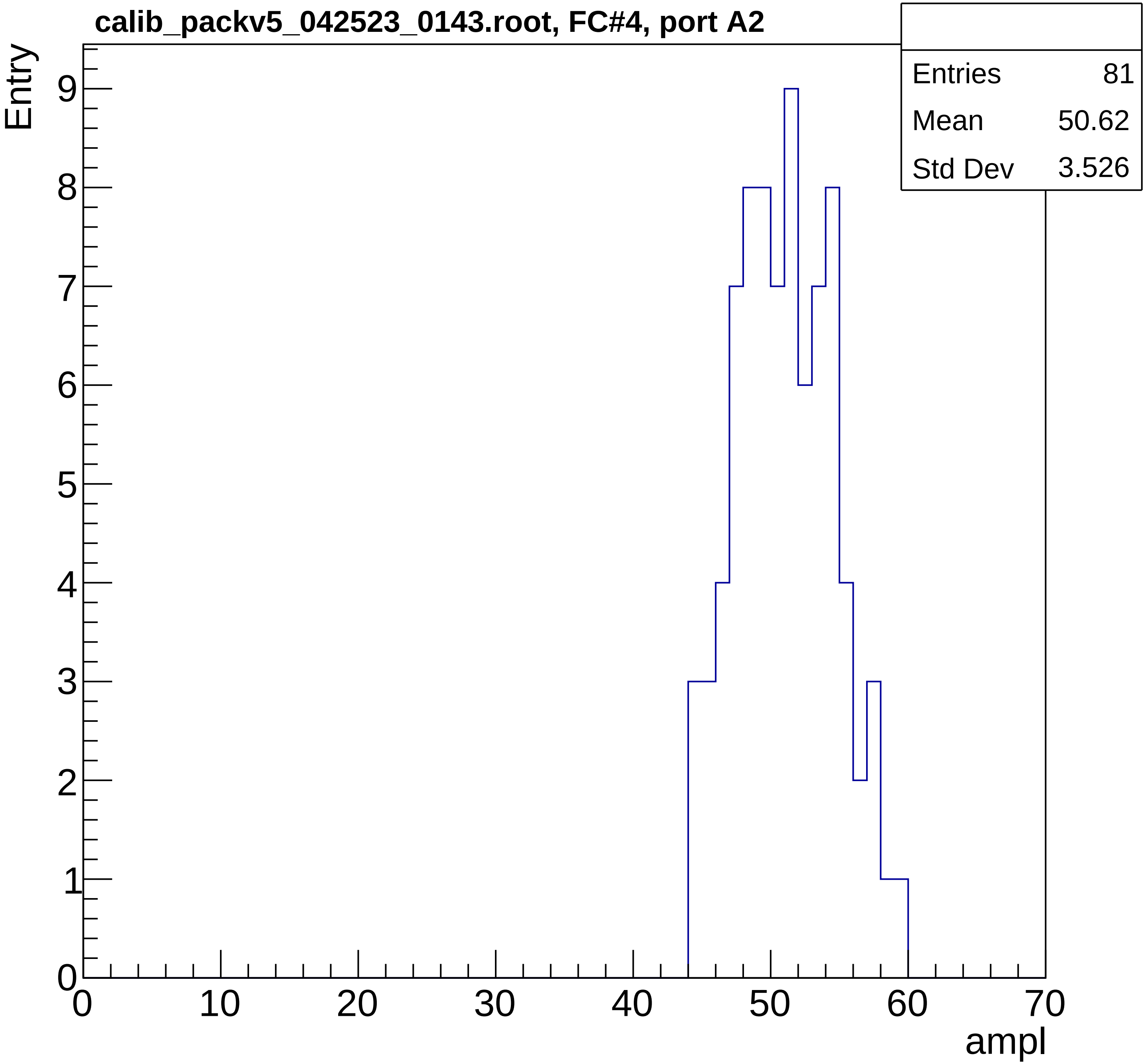

B1L100S, U21-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 67
Mean 29.66
Std Dev 5.924
Gaus mean : 30.8861
Gaus Width: 3.2832
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.31
Std Dev 3.293
Gaus mean : 37.5367
Gaus Width: 3.4271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.34
Std Dev 3.181
Gaus mean : 43.8730
Gaus Width: 3.3401
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

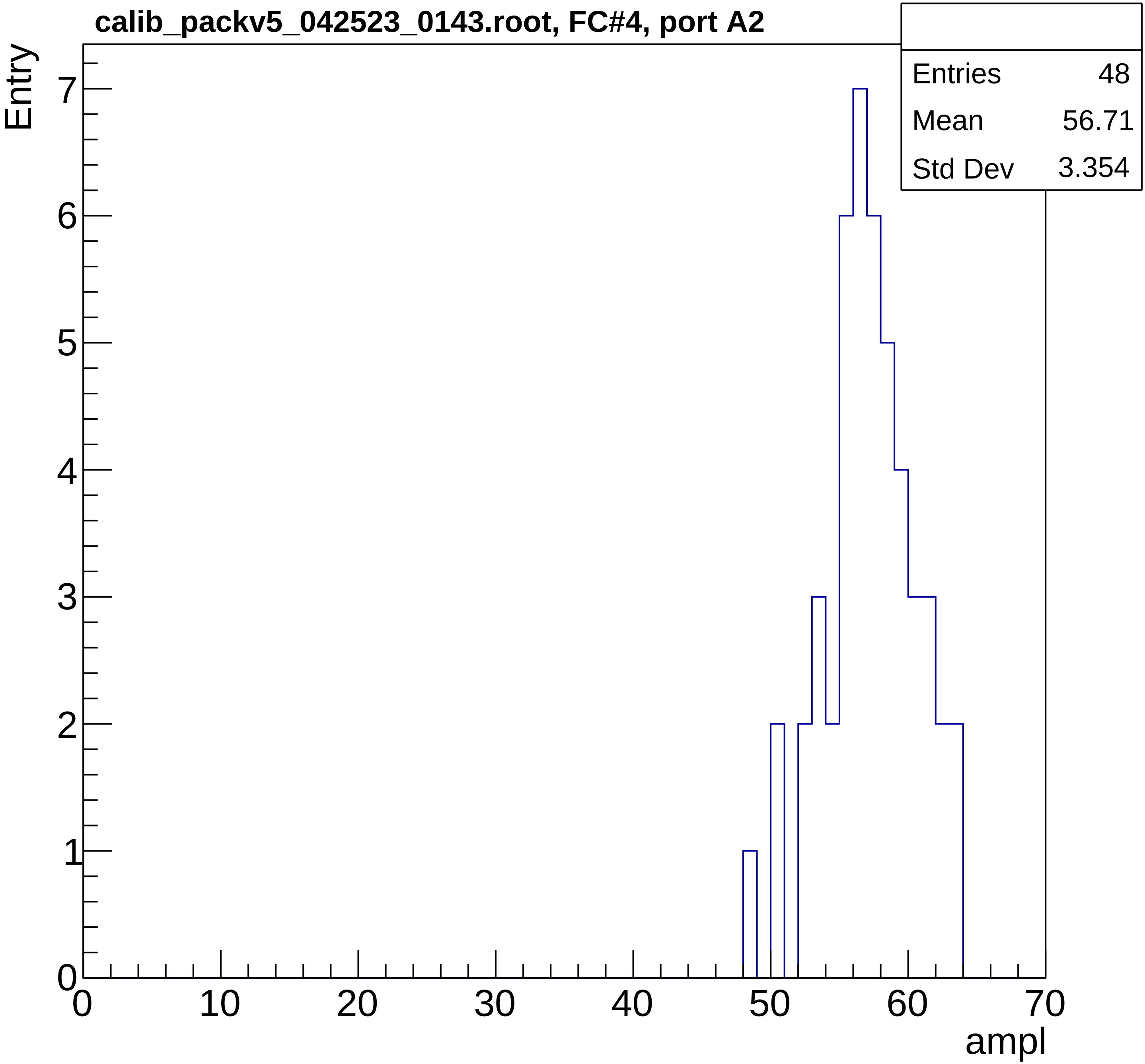
B1L100S, U21-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

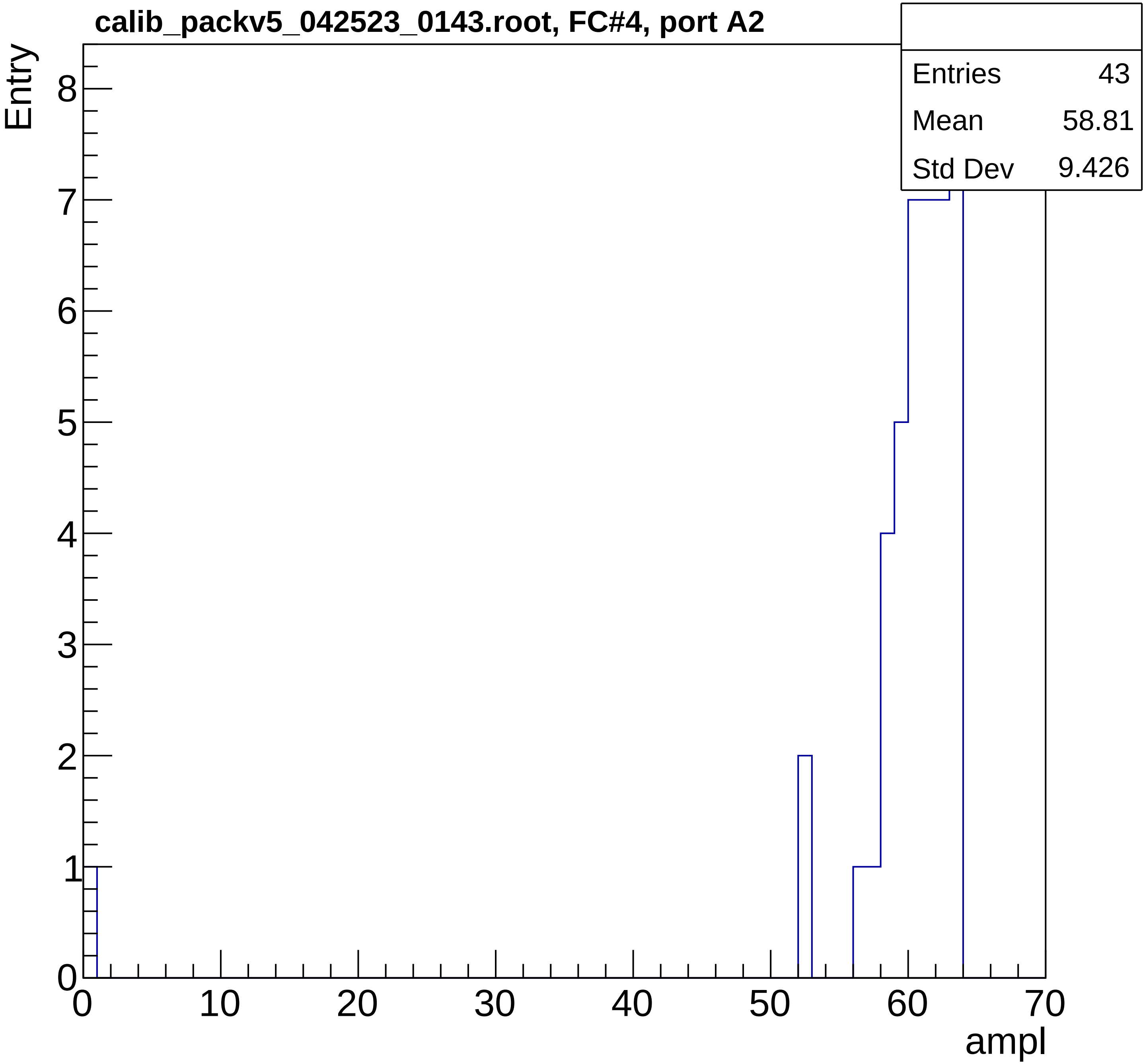


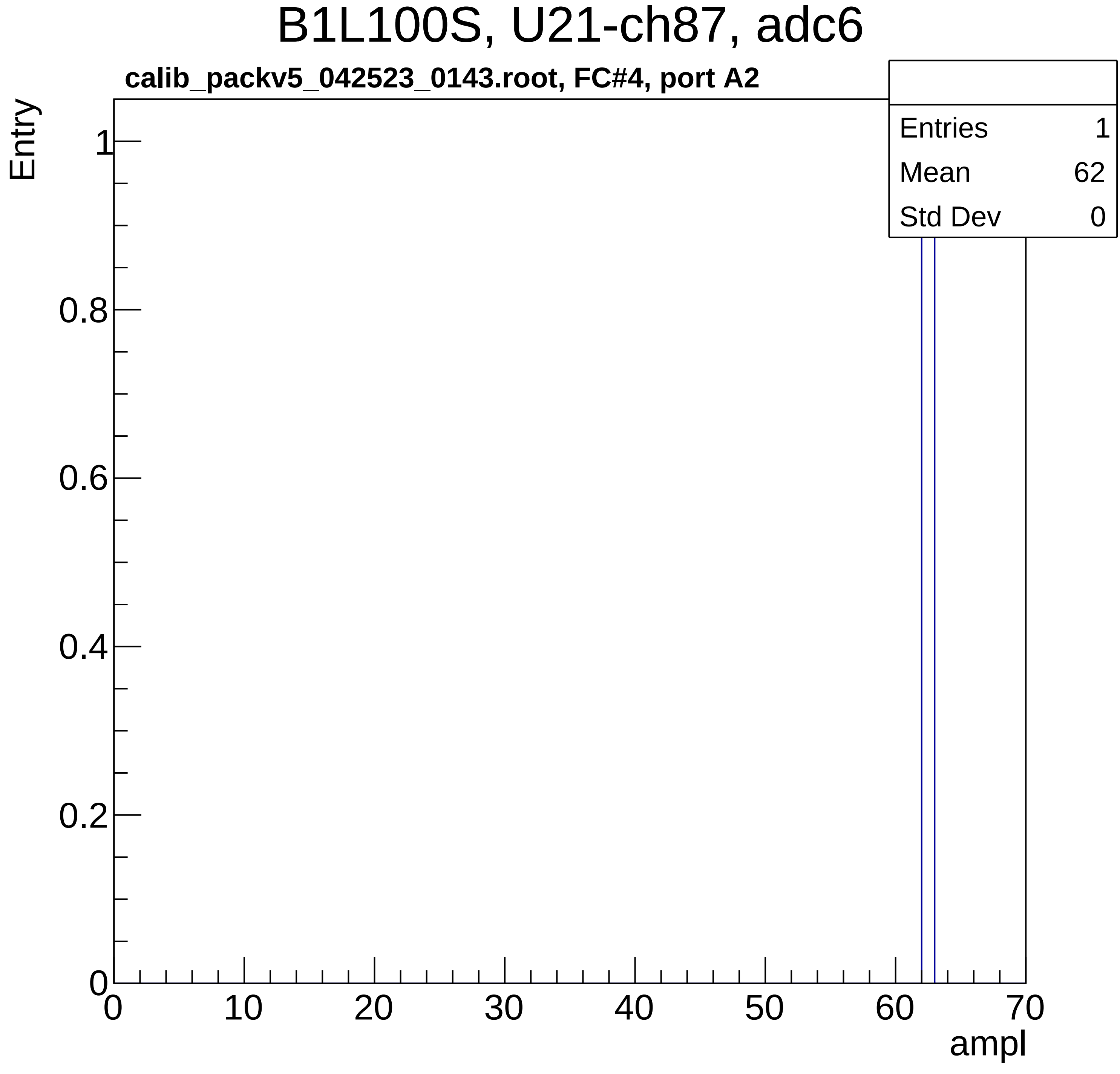
B1L100S, U21-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2



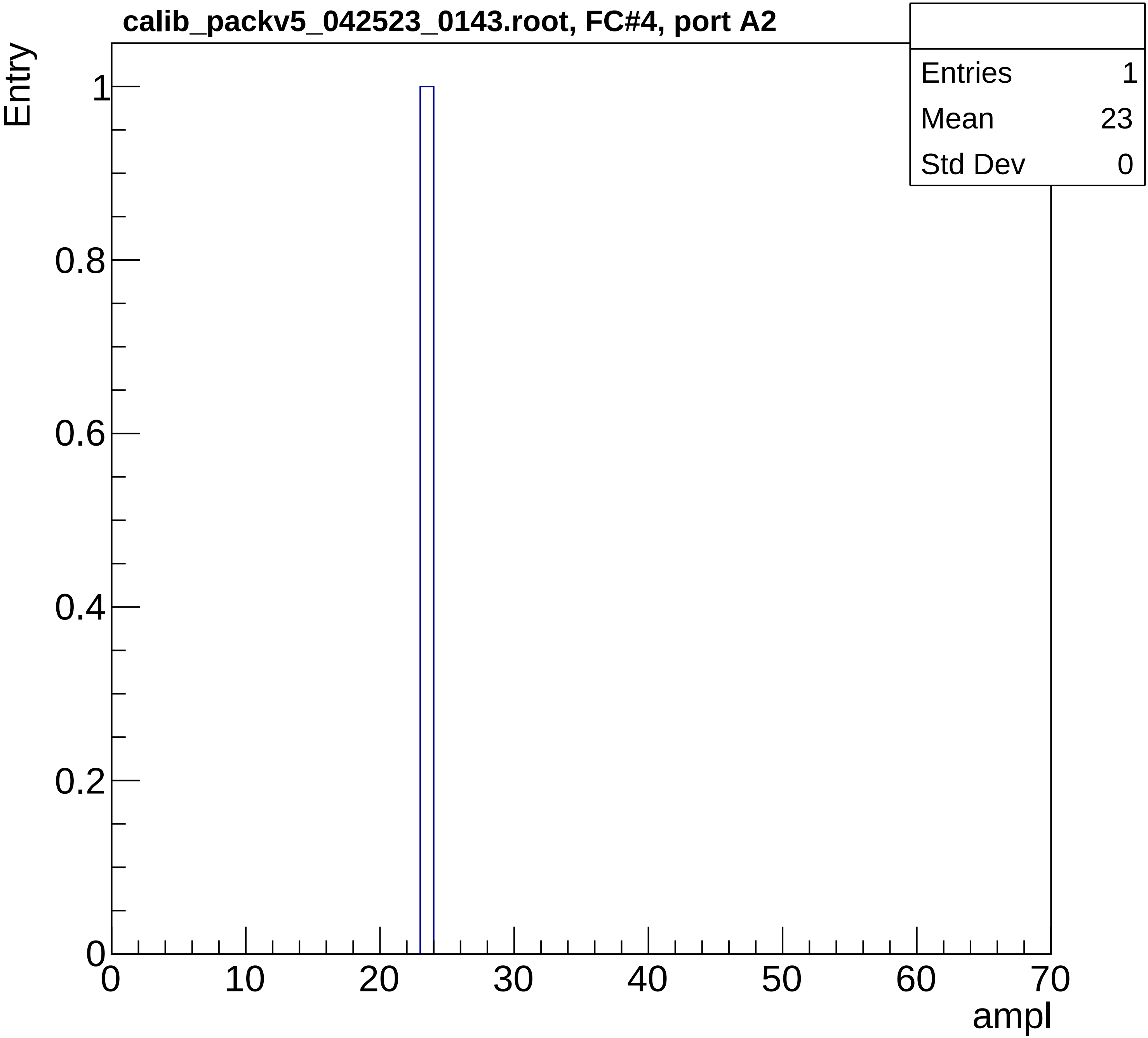
B1L100S, U21-ch87, adc5



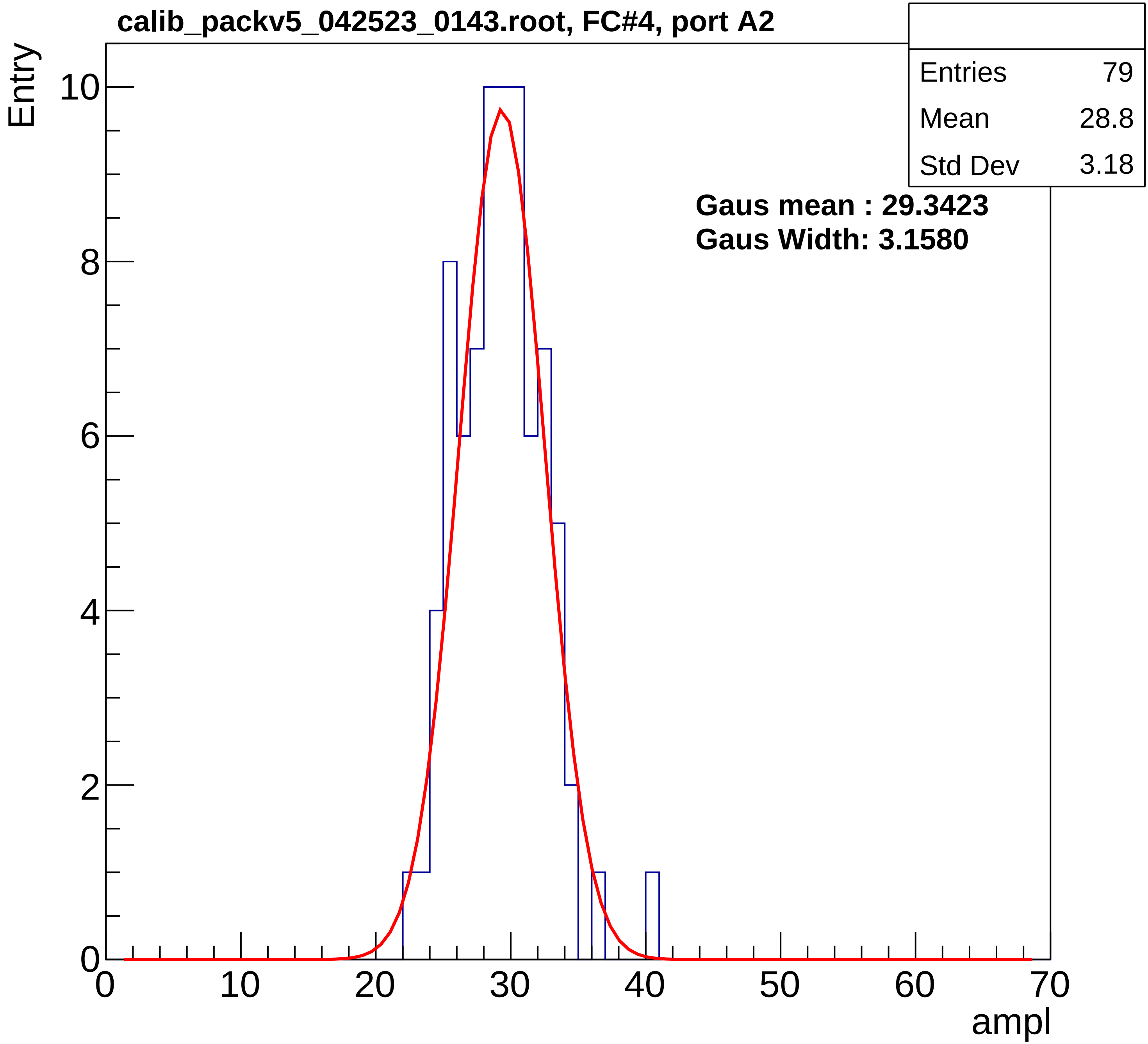

B1L100S, U21-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 1
Mean 62
Std Dev 0
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch87, adc7

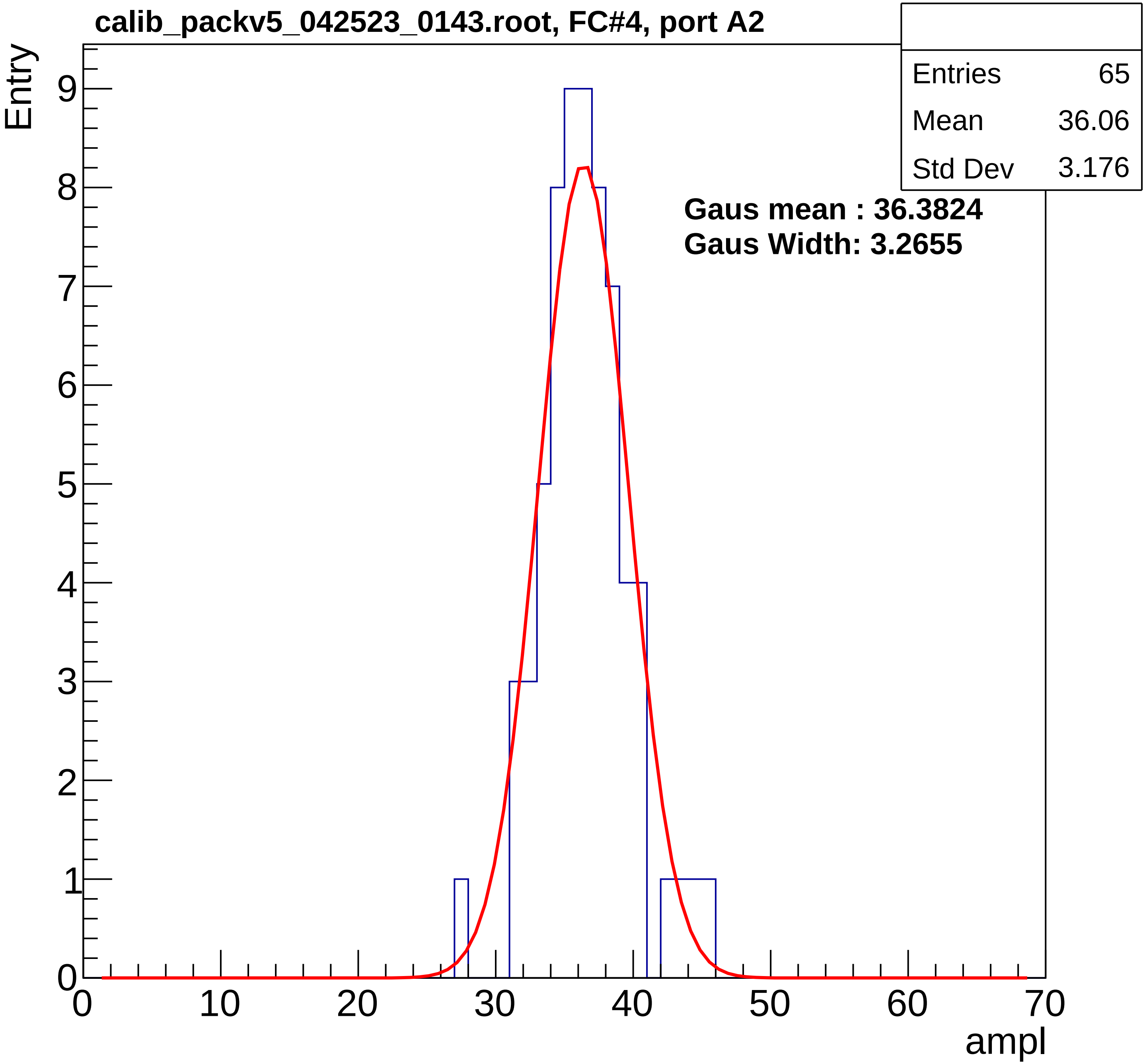
calib_packv5_042523_0143.root, FC#4, port A2

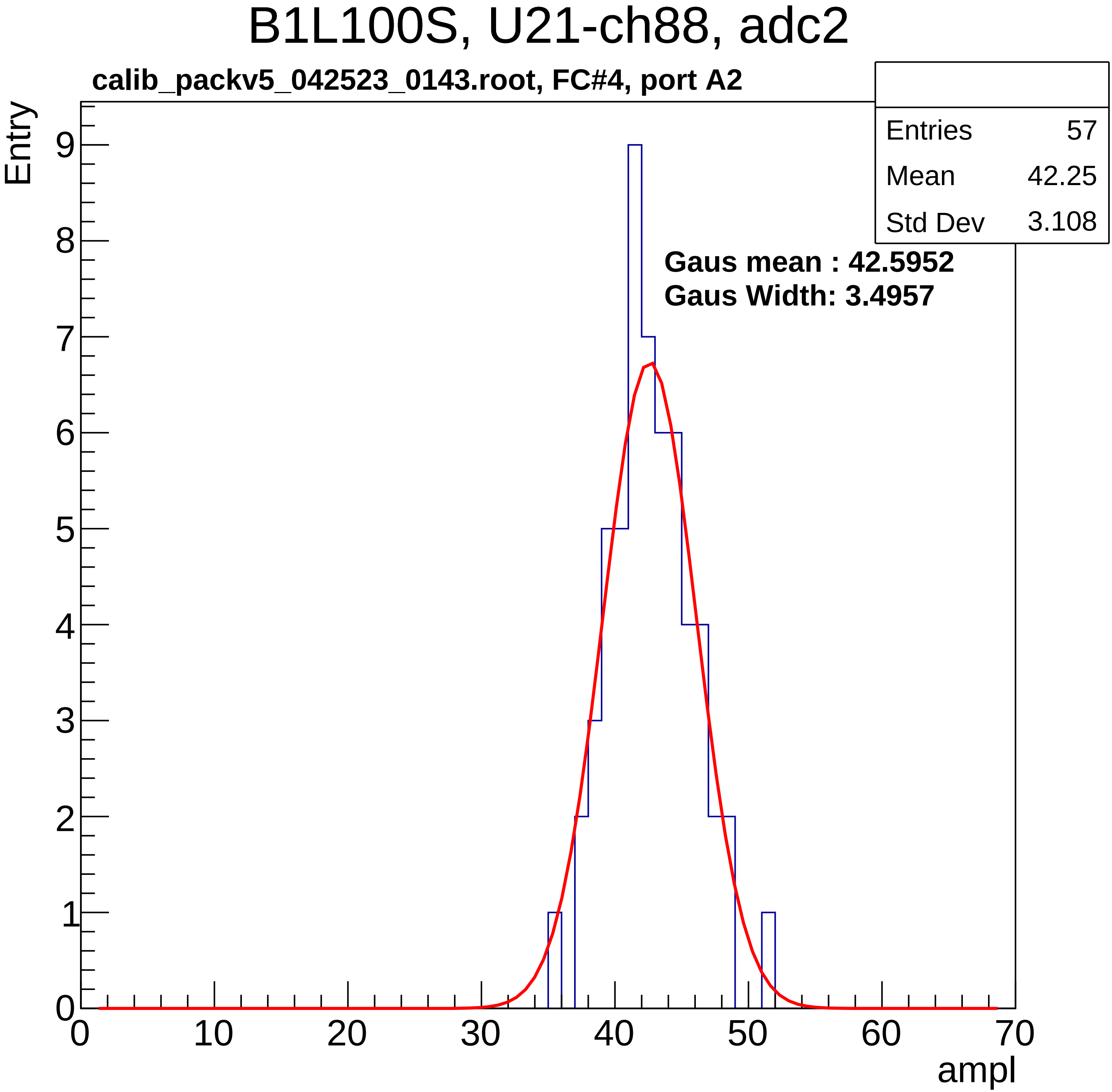


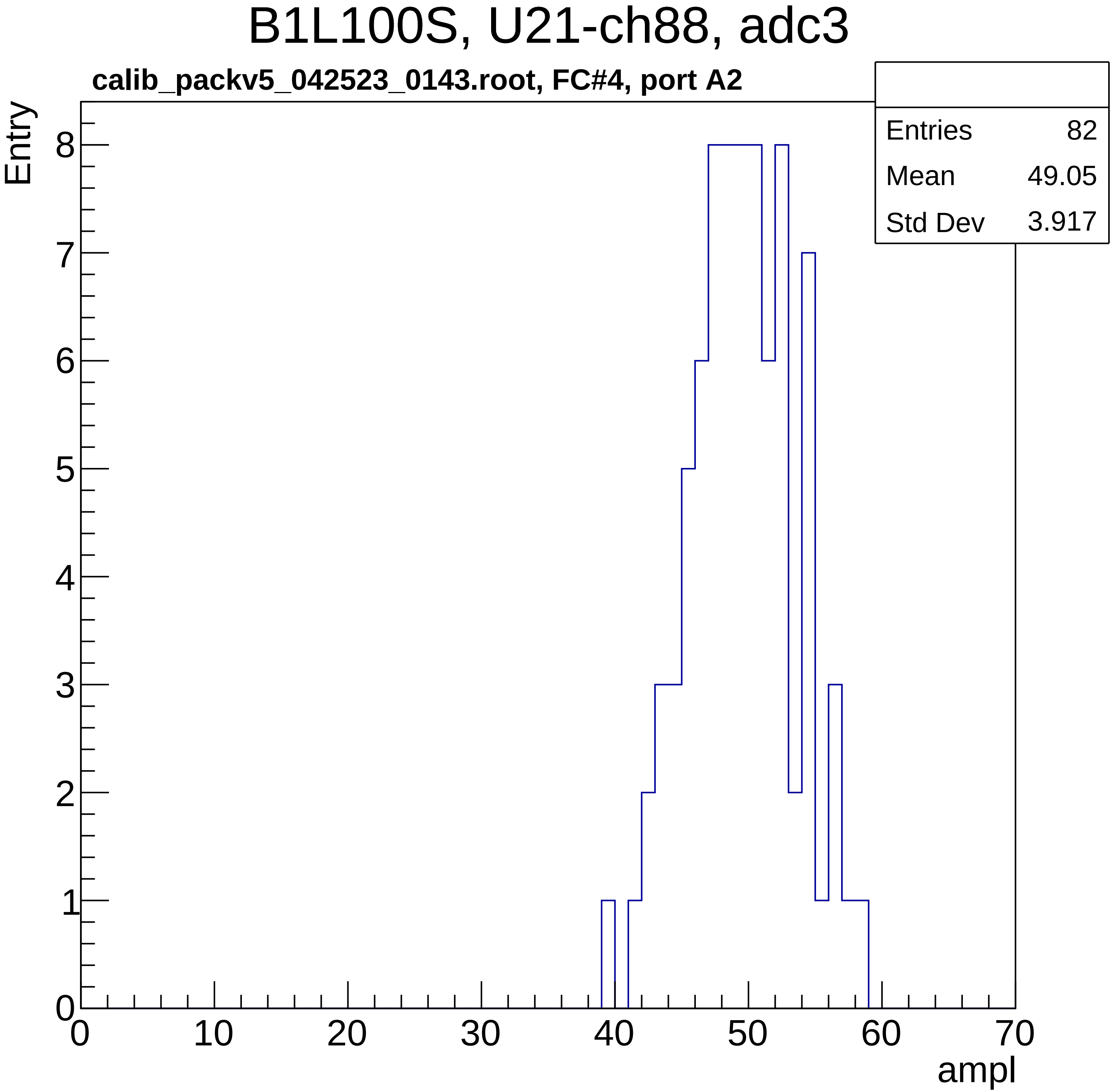
B1L100S, U21-ch88, adc0



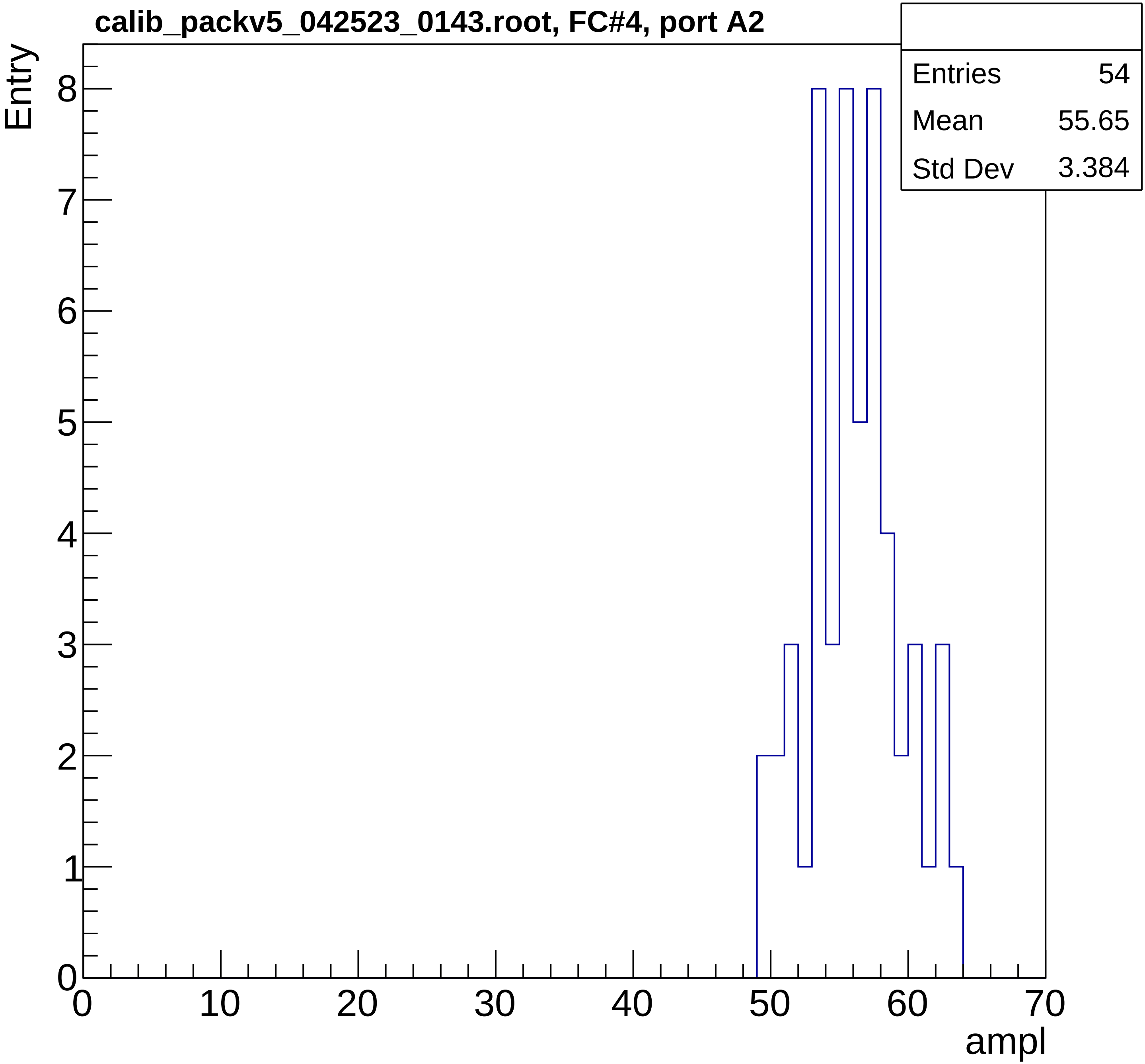
B1L100S, U21-ch88, adc1

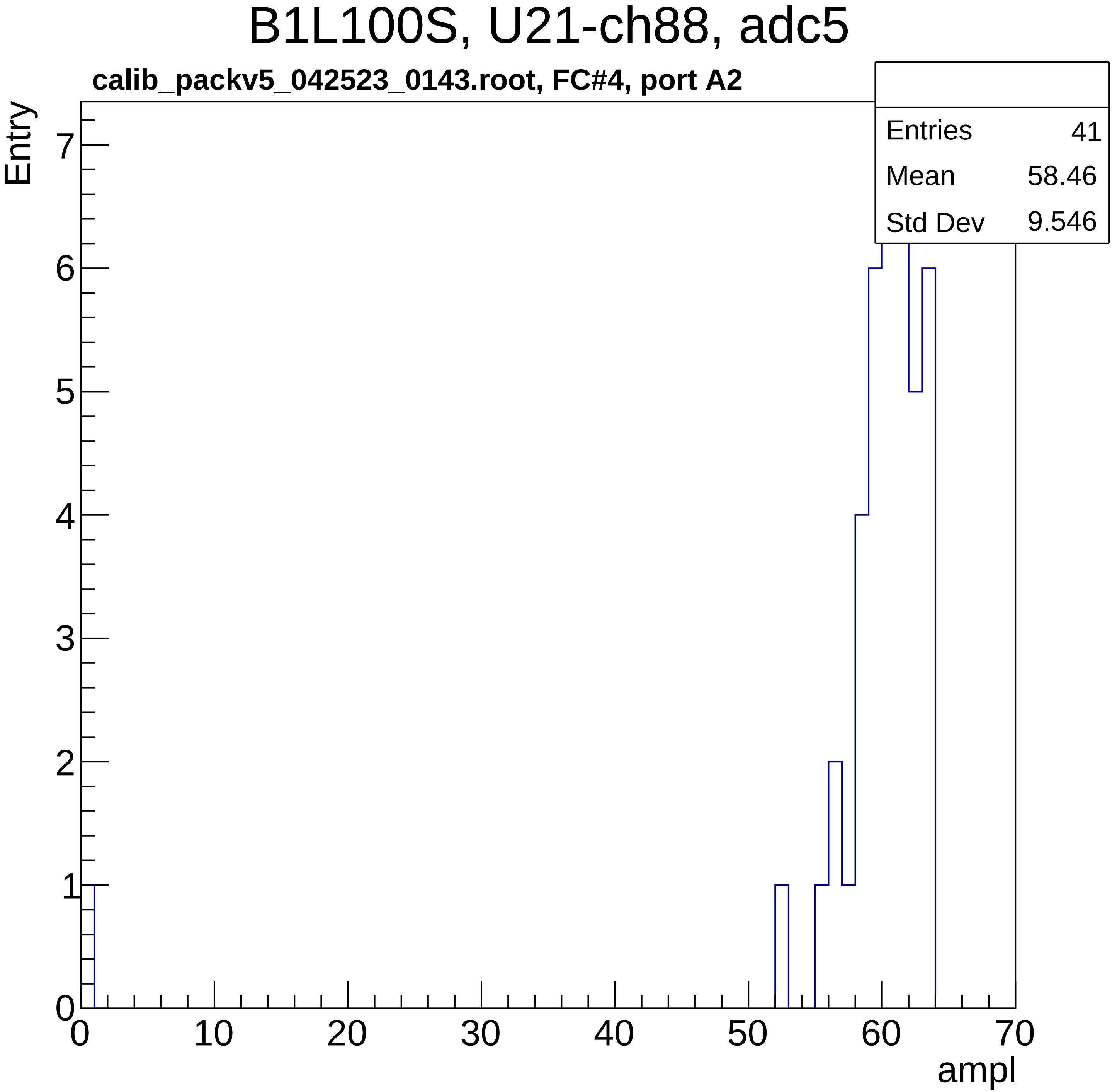



B1L100S, U21-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.25
Std Dev 3.108
Gaus mean : 42.5952
Gaus Width: 3.4957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 49.05
Std Dev 3.917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

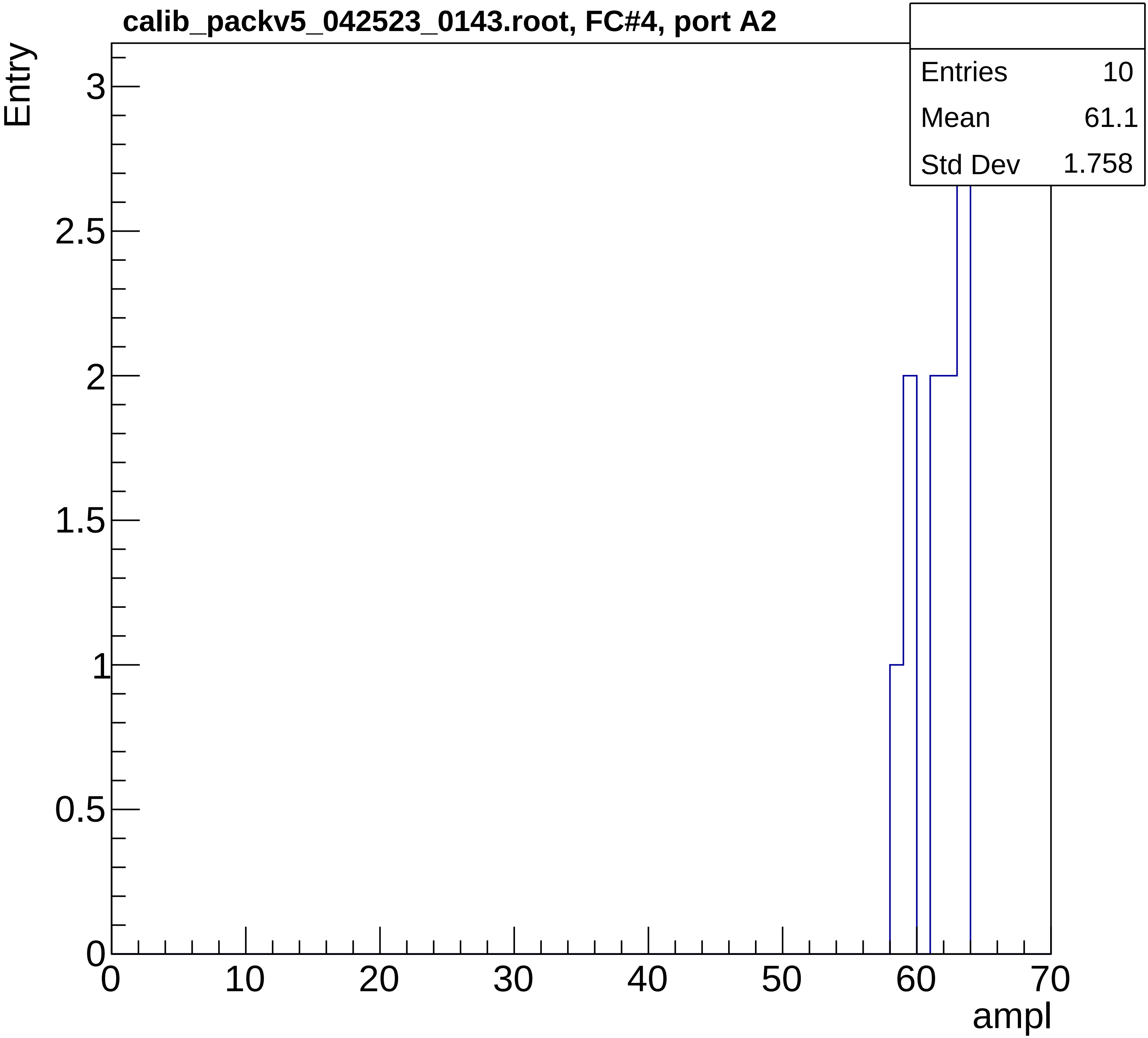
B1L100S, U21-ch88, adc4



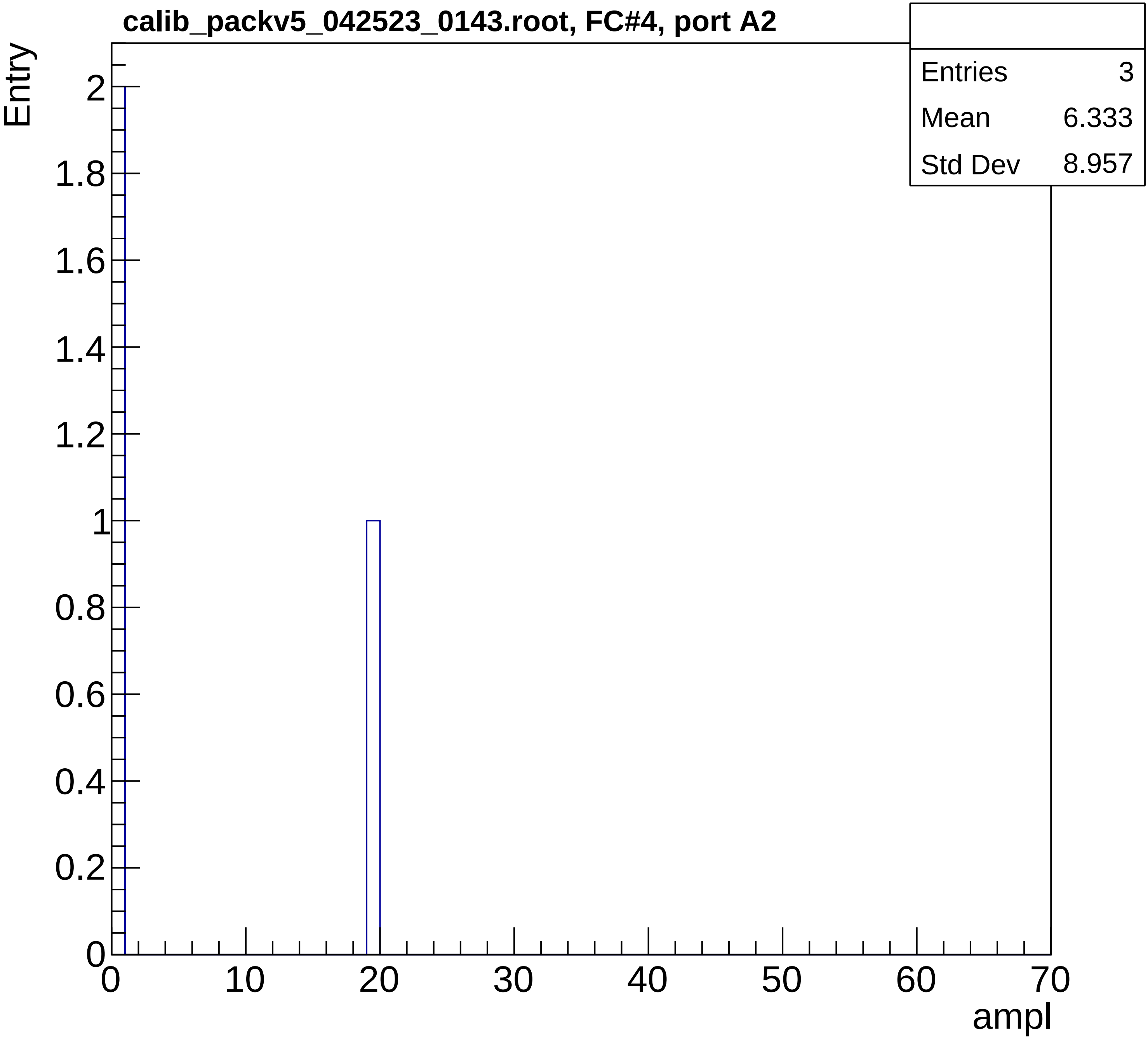

B1L100S, U21-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 41
Mean 58.46
Std Dev 9.546
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

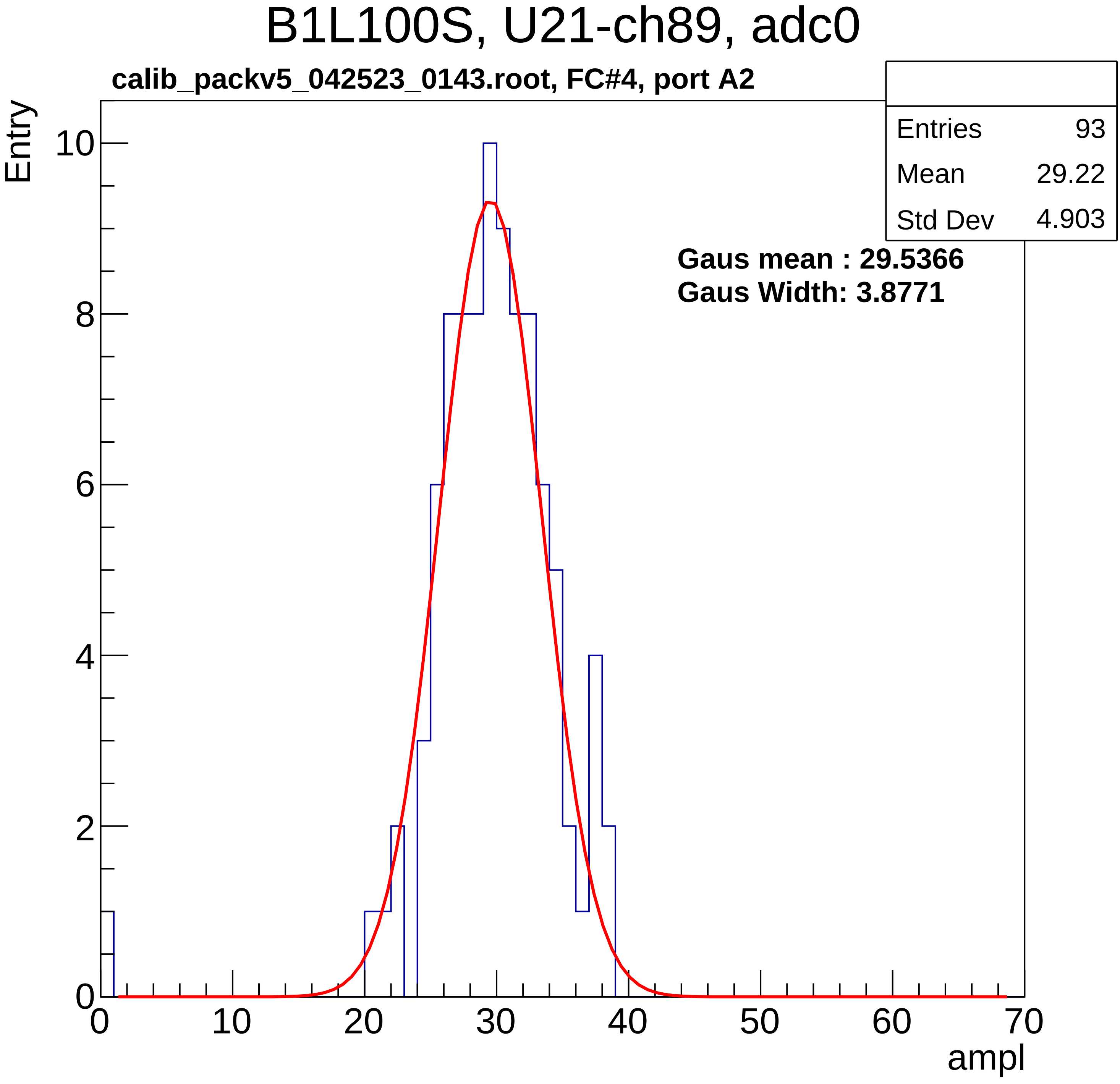
B1L100S, U21-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

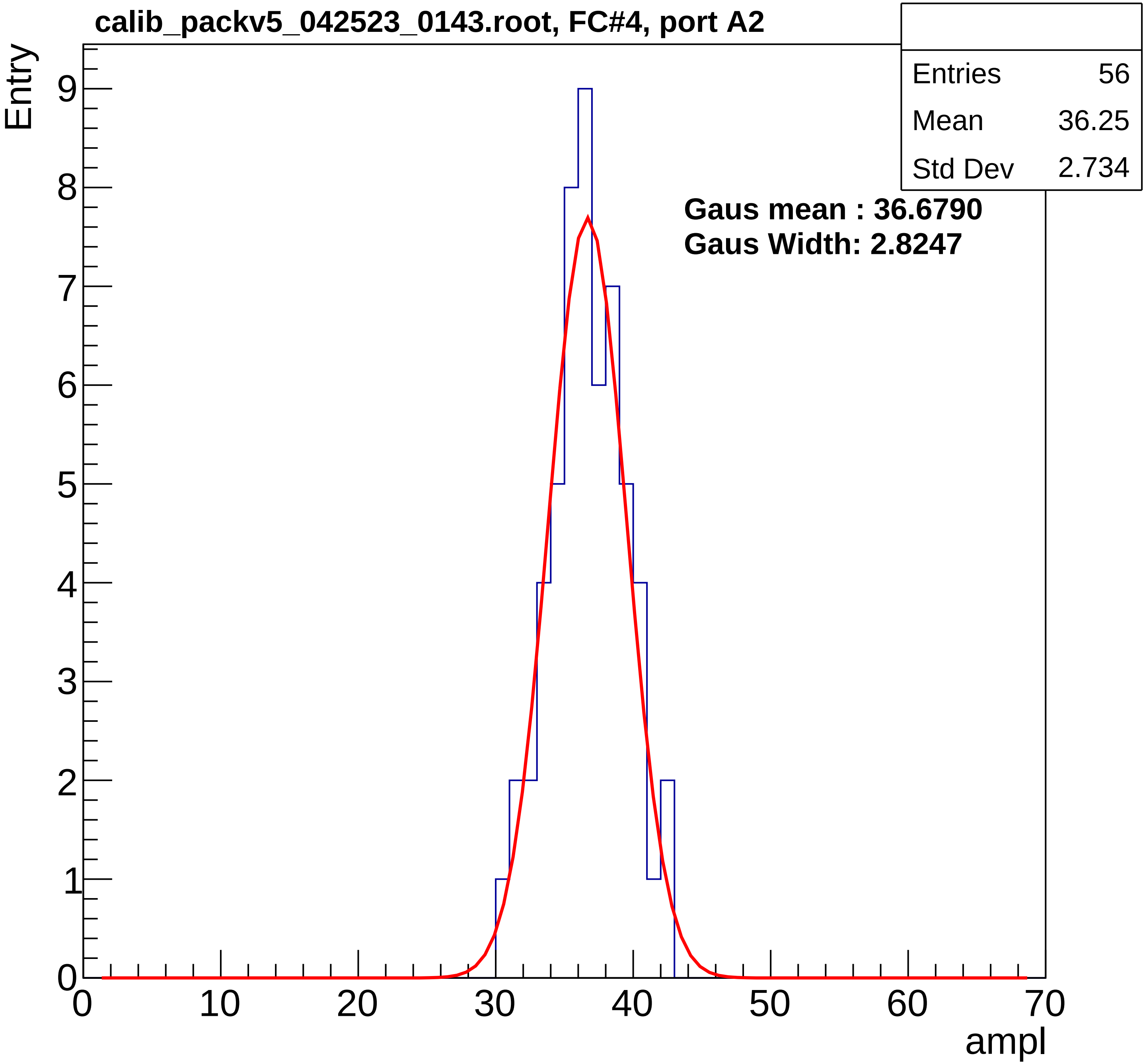


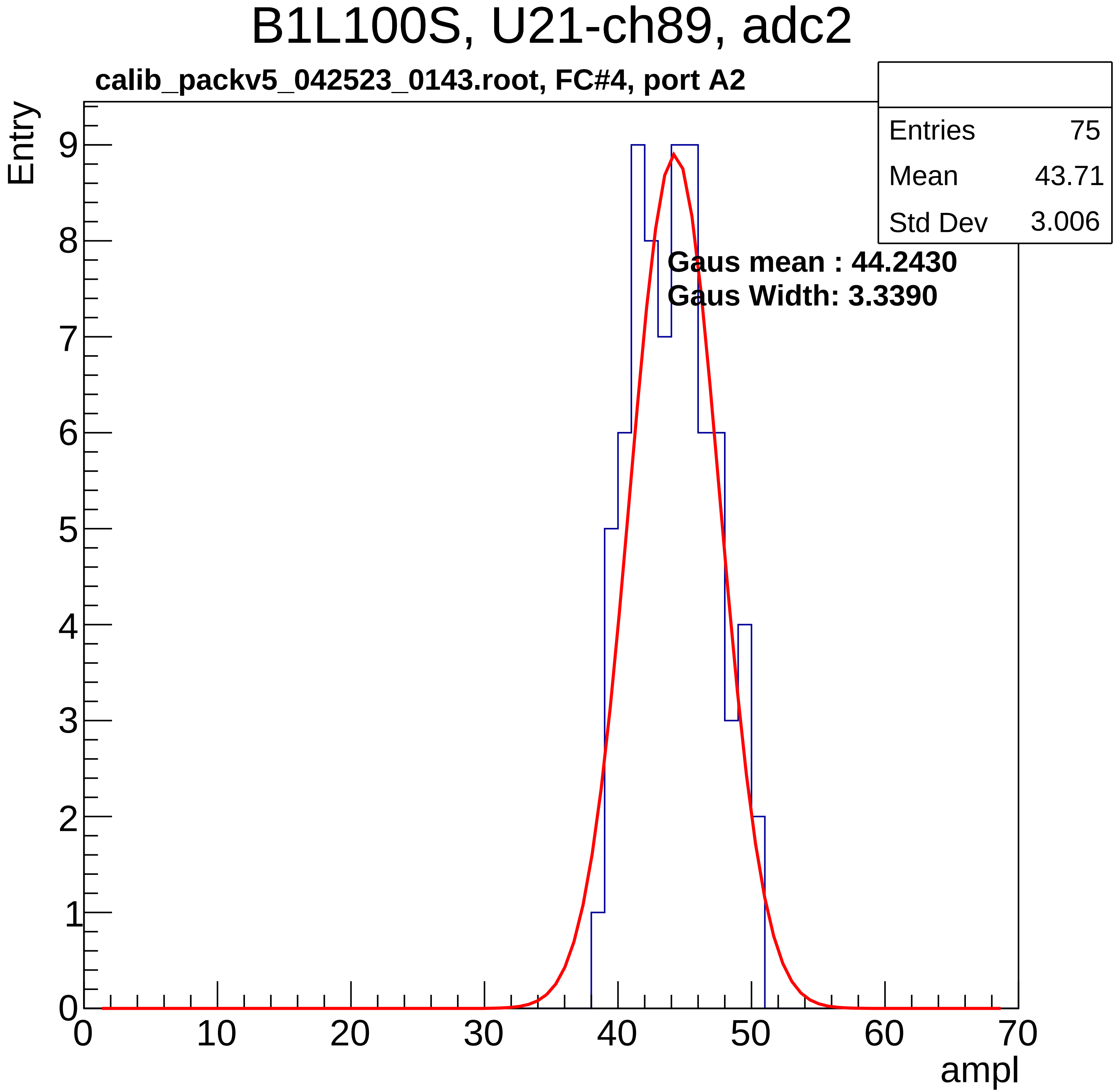
B1L100S, U21-ch88, adc7




B1L100S, U21-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 93
Mean 29.22
Std Dev 4.903
Gaus mean : 29.5366
Gaus Width: 3.8771
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

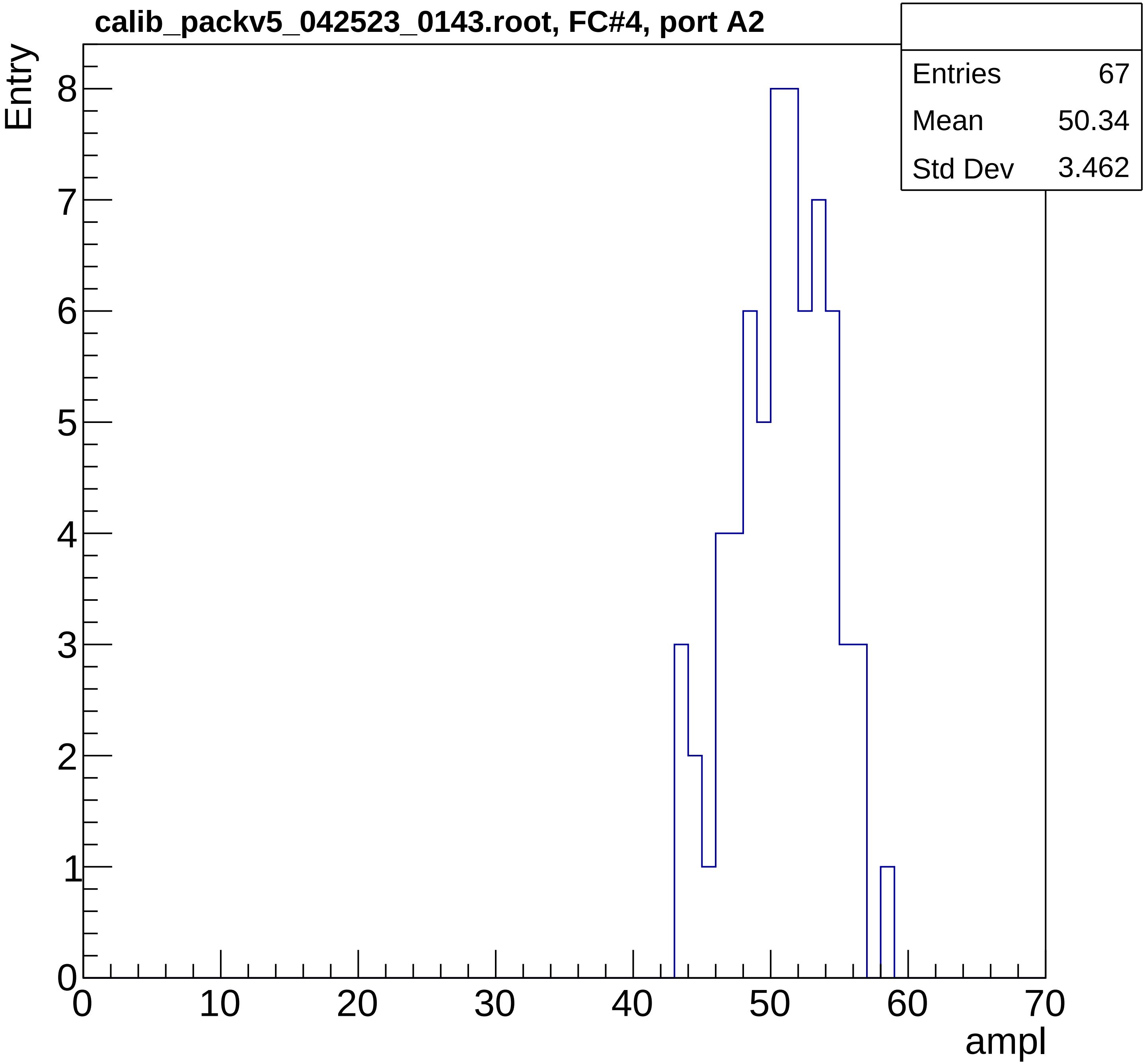
B1L100S, U21-ch89, adc1

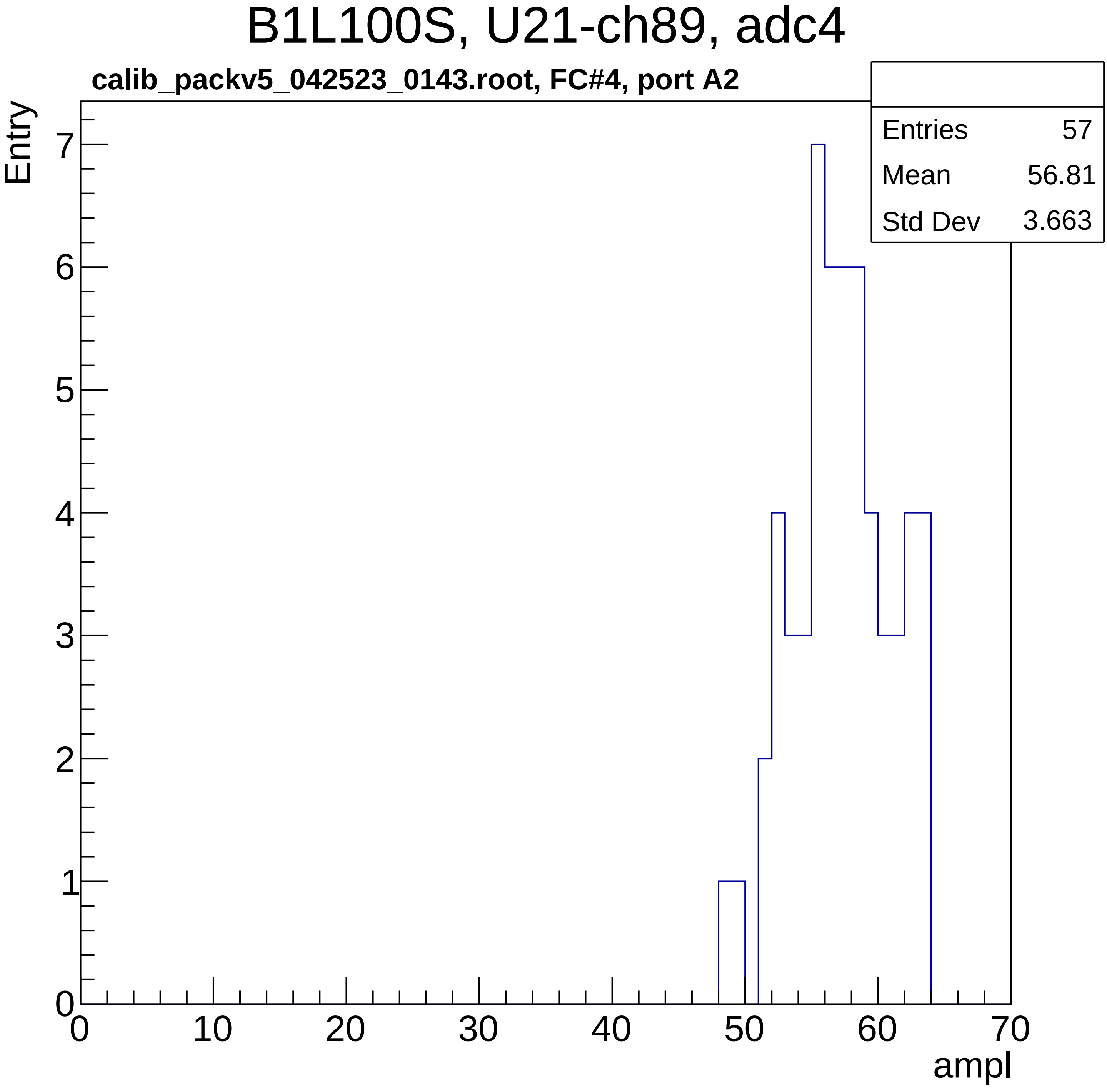


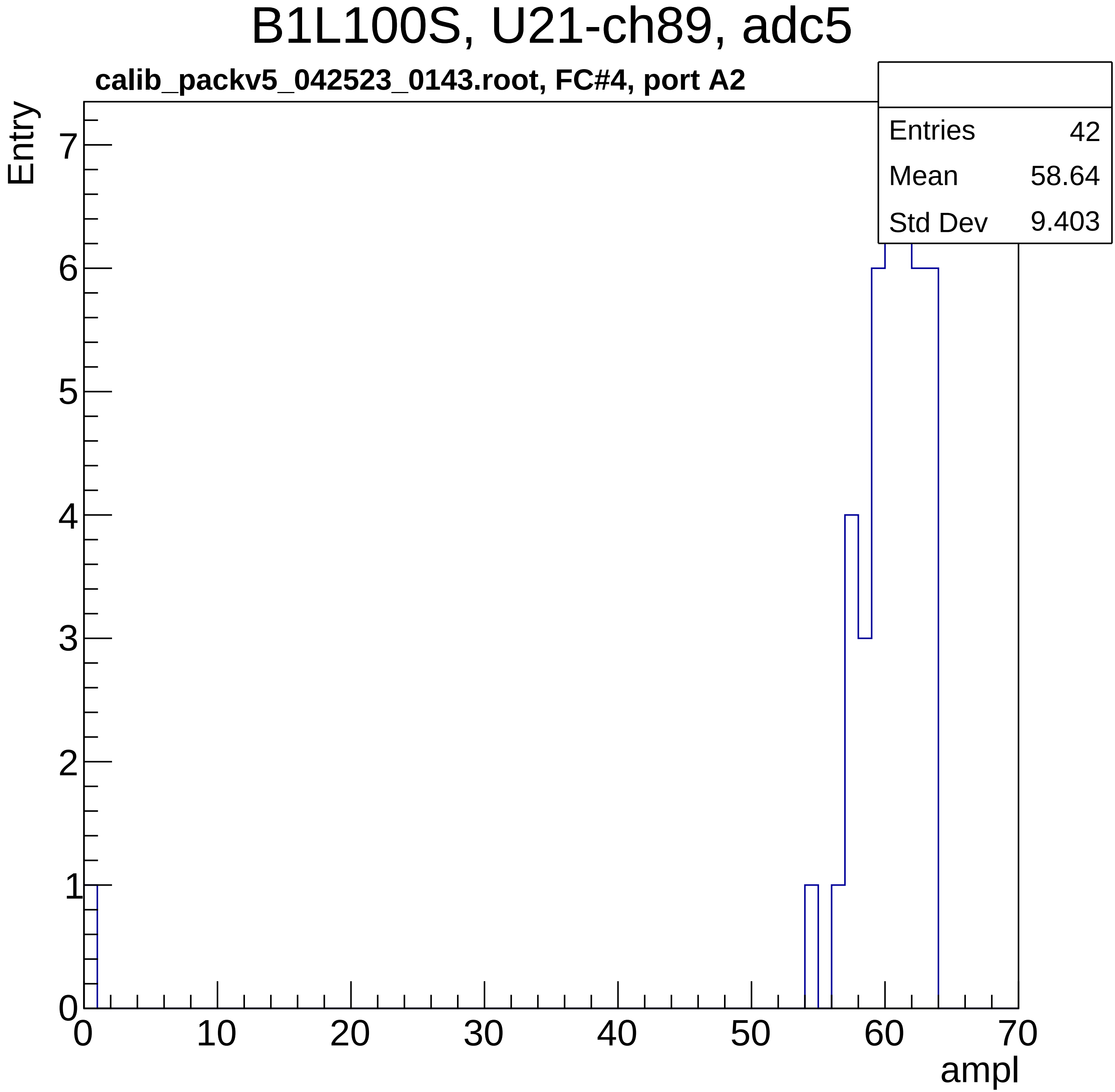

B1L100S, U21-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.71
Std Dev 3.006
Gaus mean : 44.2430
Gaus Width: 3.3390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

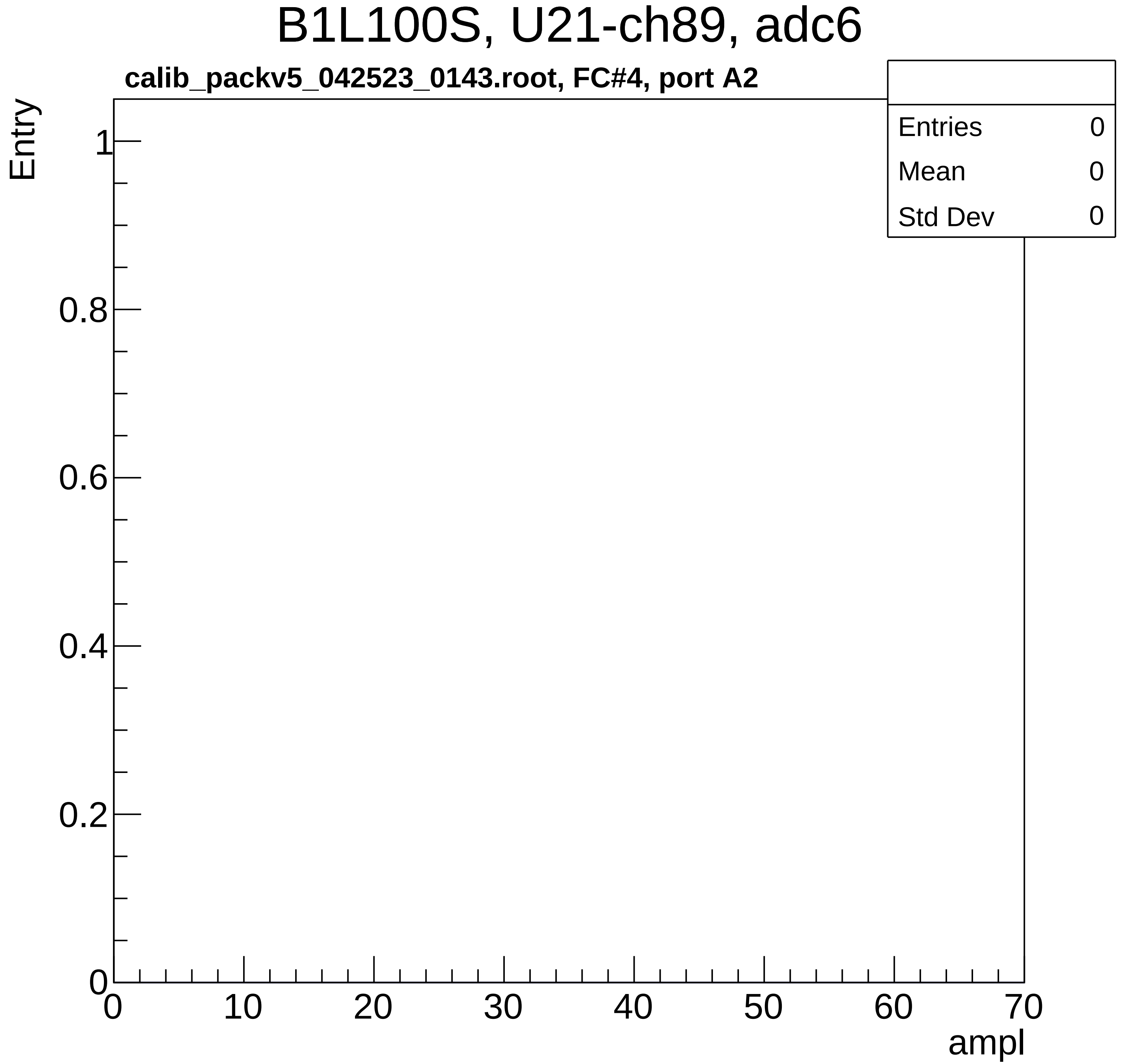
B1L100S, U21-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2



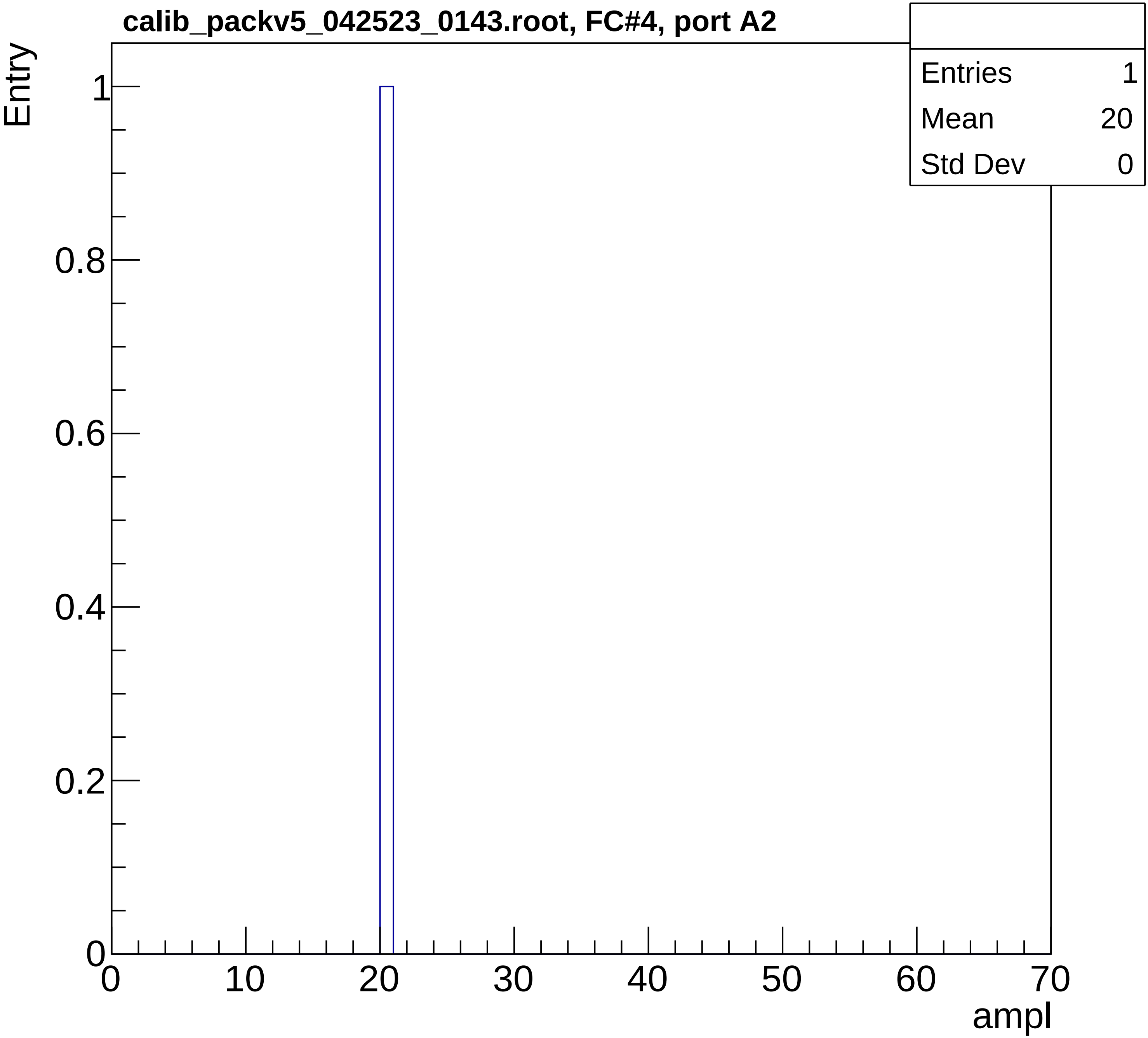

B1L100S, U21-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.81
Std Dev 3.663
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

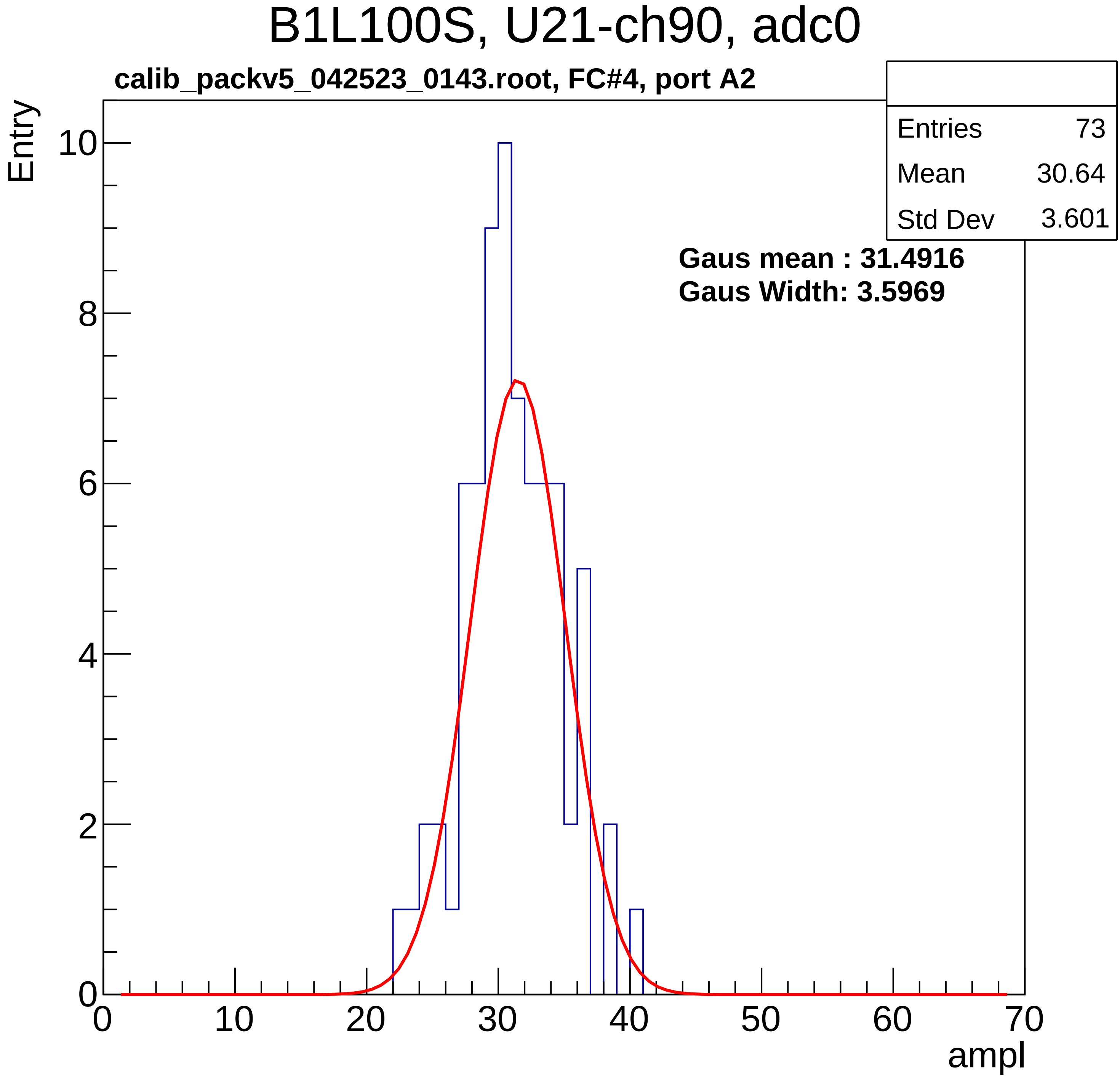

B1L100S, U21-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.64
Std Dev 9.403
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

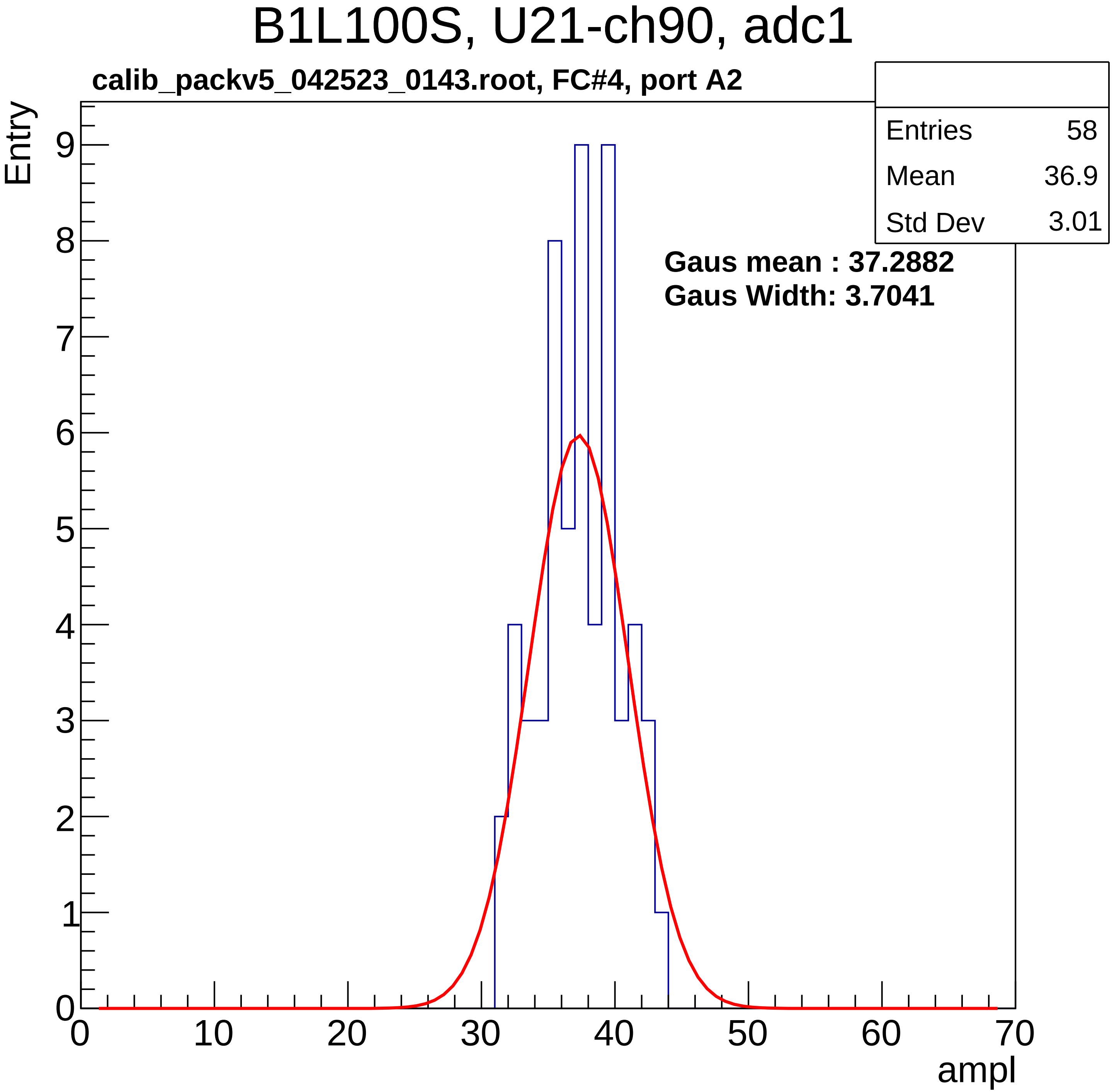

B1L100S, U21-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 0
Mean 0
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

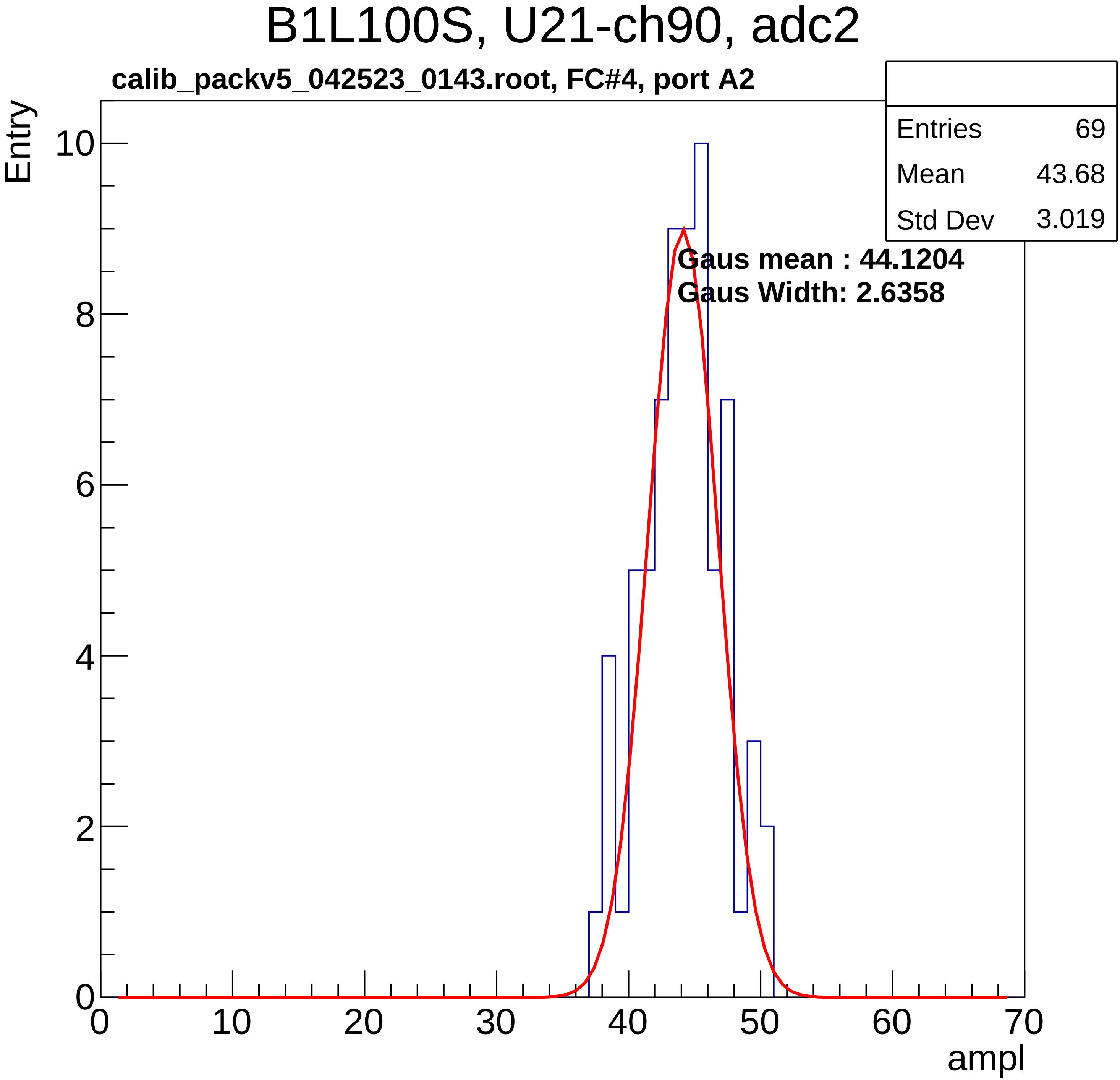
B1L100S, U21-ch89, adc7

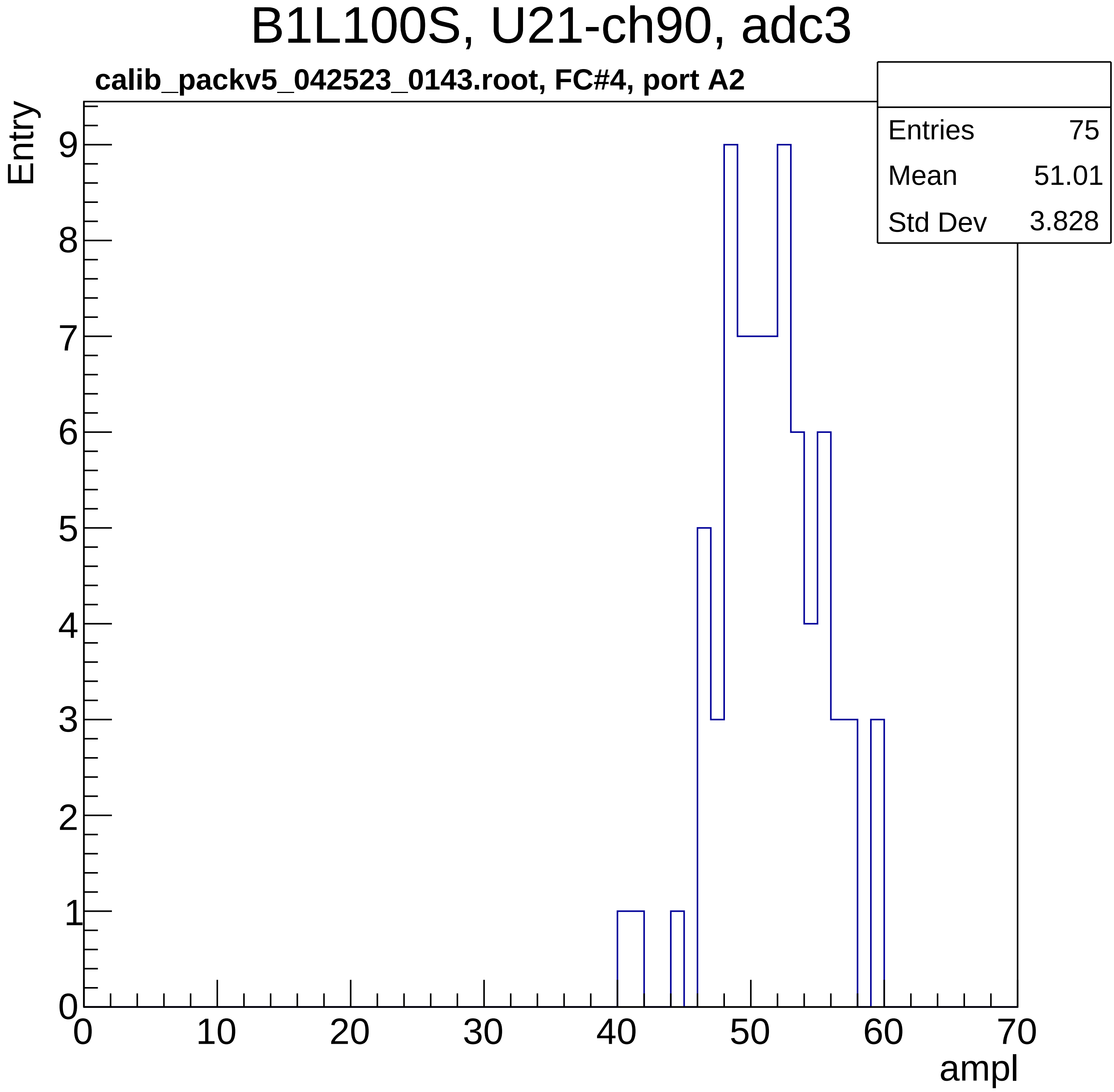
calib_packv5_042523_0143.root, FC#4, port A2

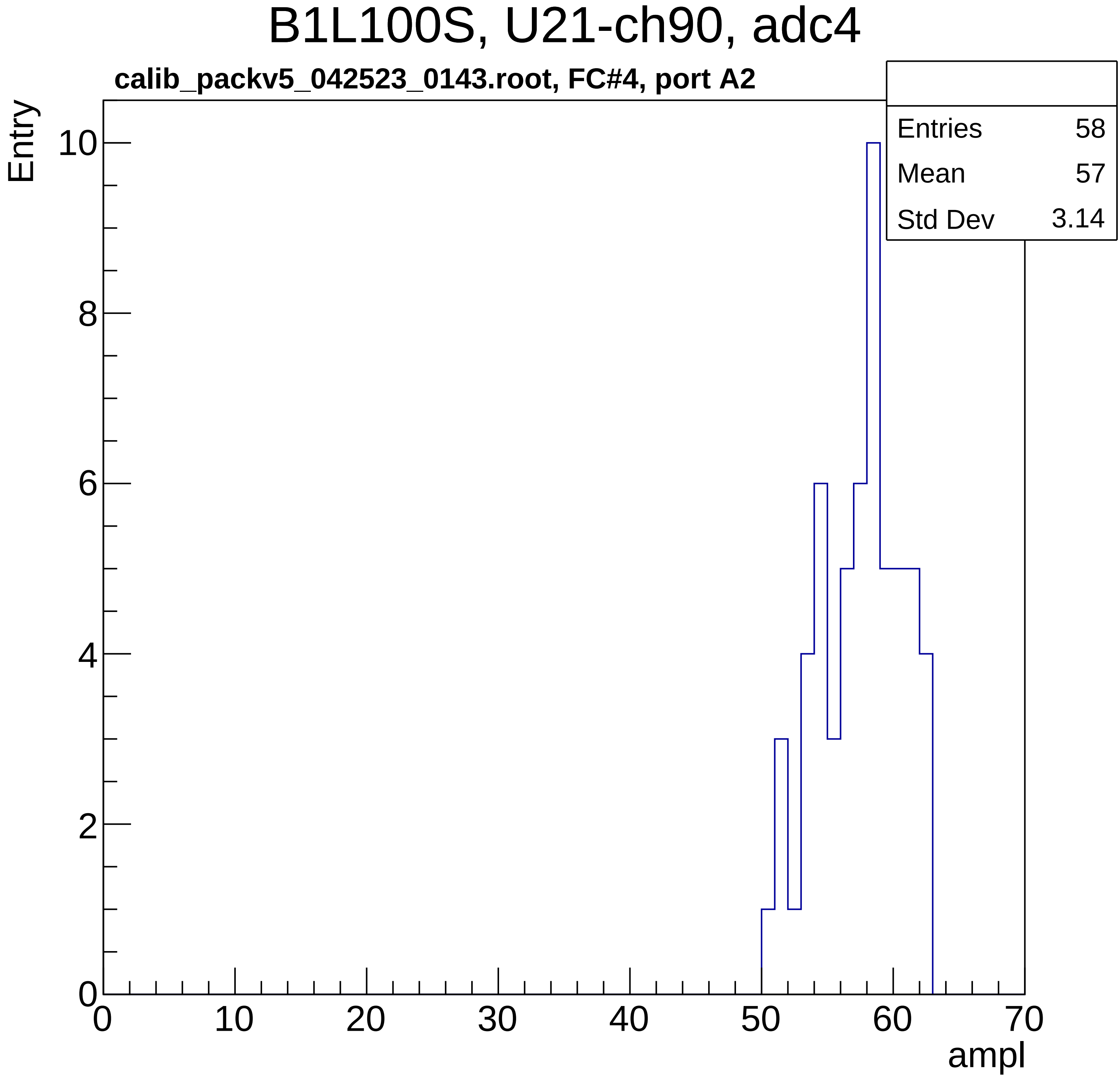


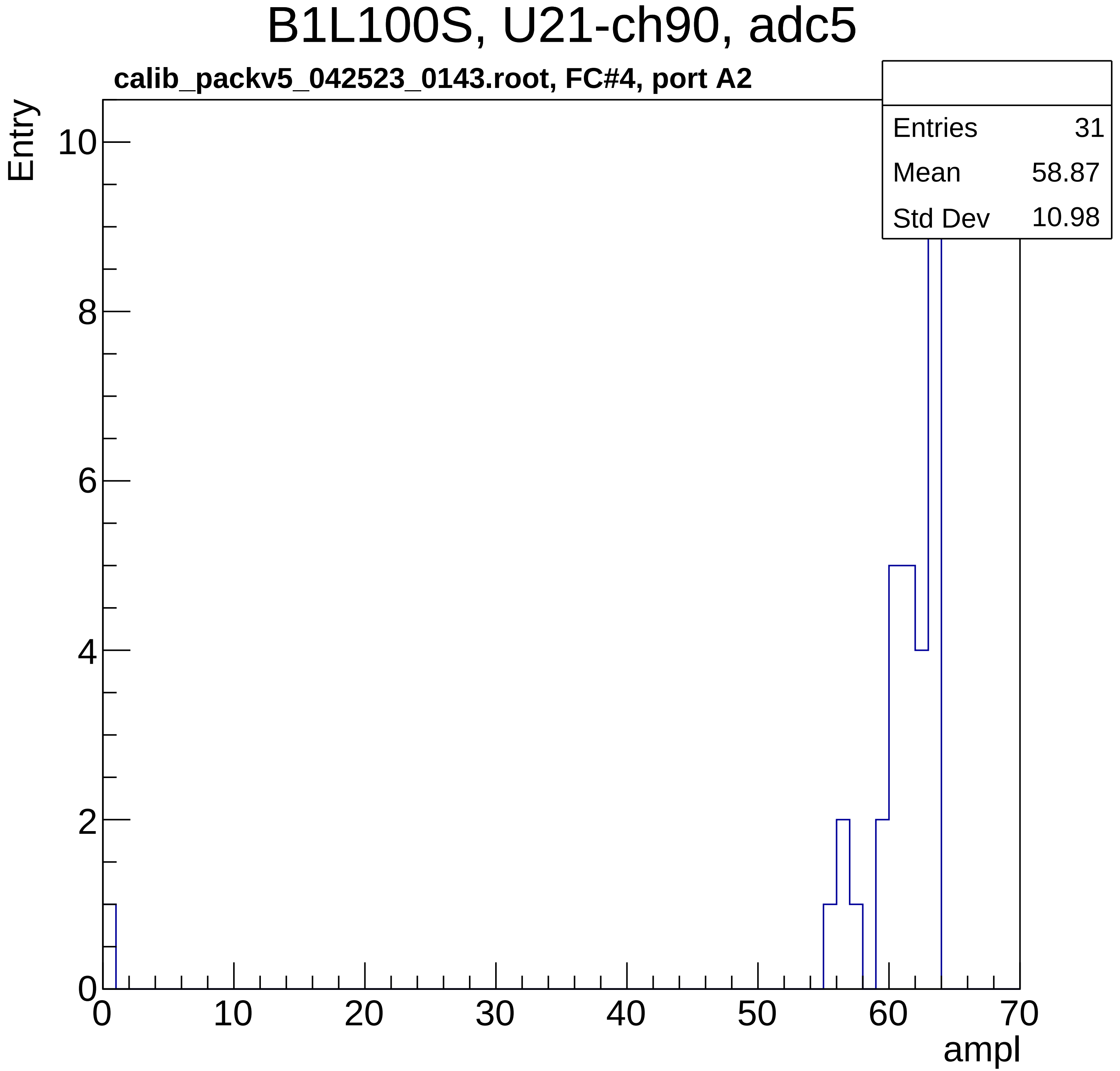

B1L100S, U21-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 73
Mean 30.64
Std Dev 3.601
Gaus mean : 31.4916
Gaus Width: 3.5969
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 36.9
Std Dev 3.01
Gaus mean : 37.2882
Gaus Width: 3.7041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.68
Std Dev 3.019
Gaus mean : 44.1204
Gaus Width: 2.6358
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

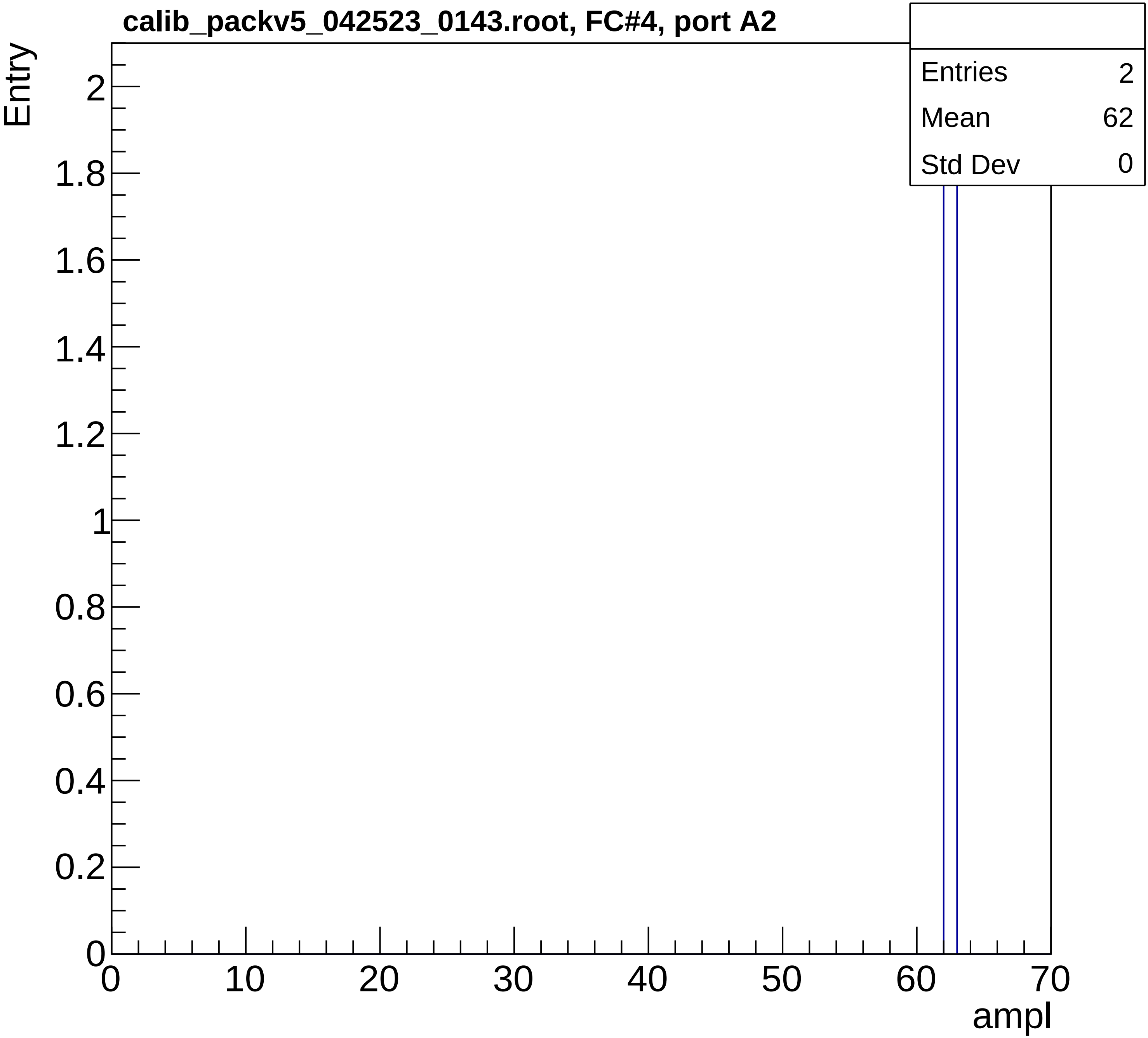

B1L100S, U21-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 75
Mean 51.01
Std Dev 3.828
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

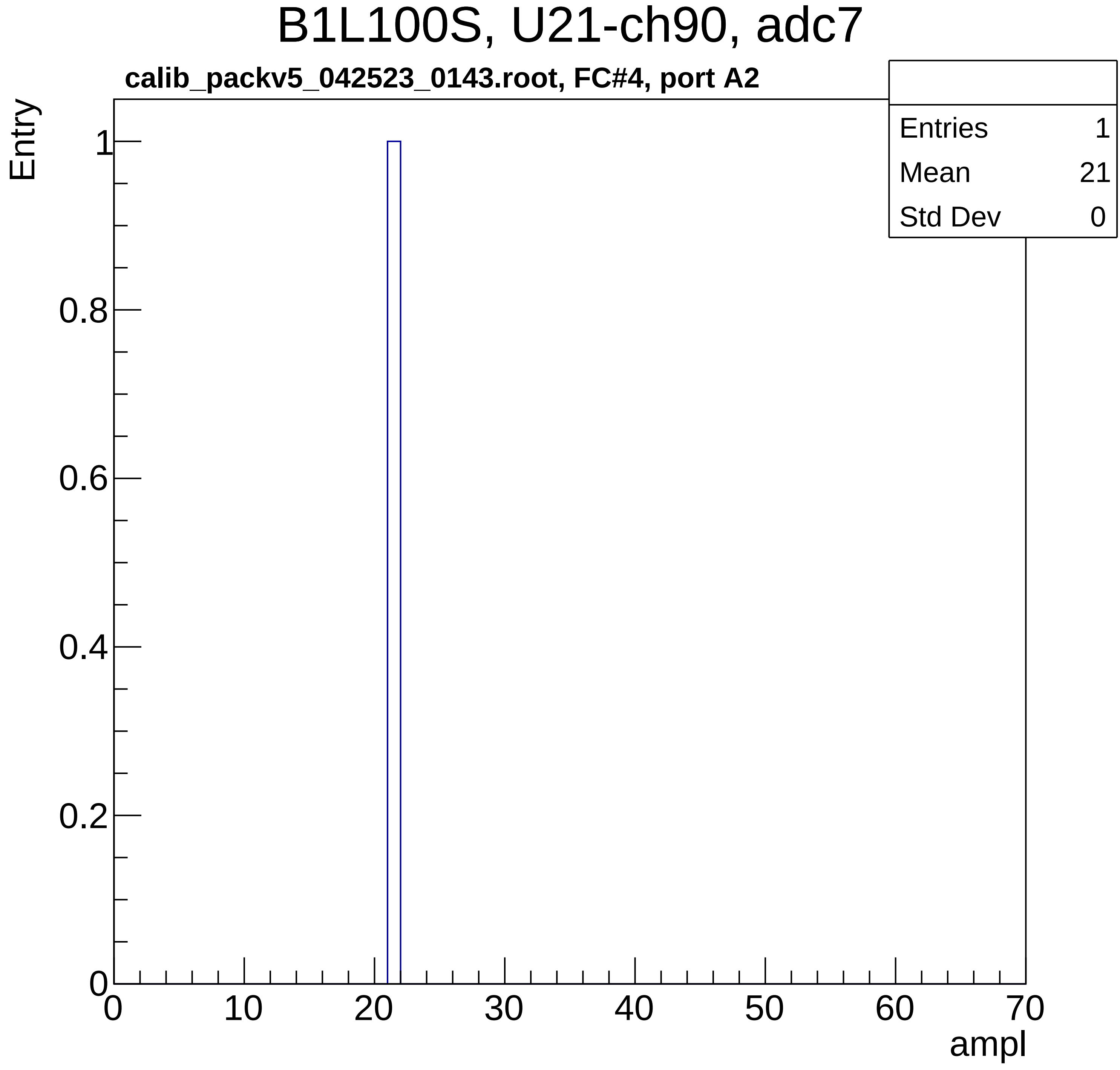

B1L100S, U21-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57
Std Dev 3.14
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

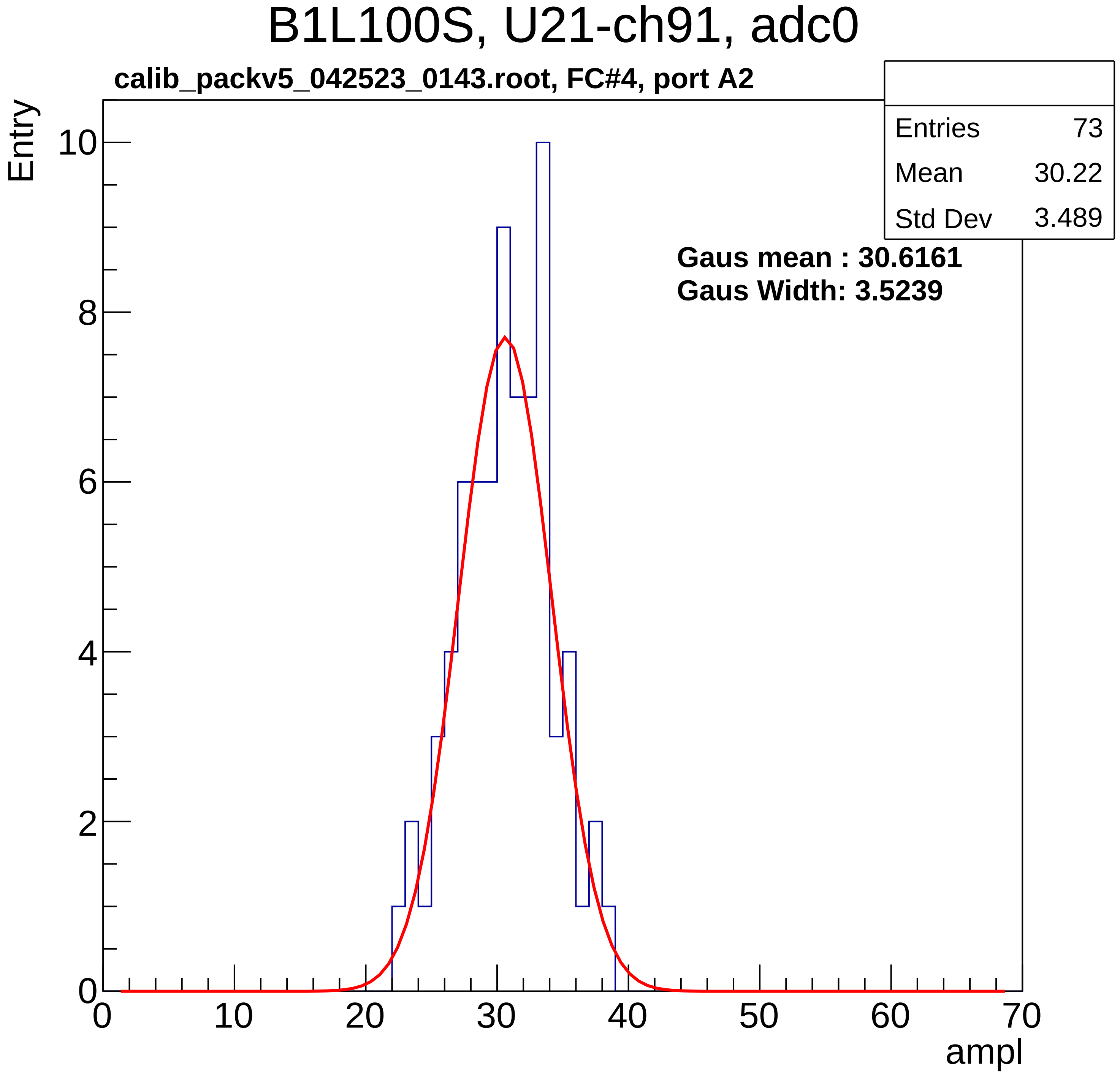

B1L100S, U21-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70
Entries 31
Mean 58.87
Std Dev 10.98

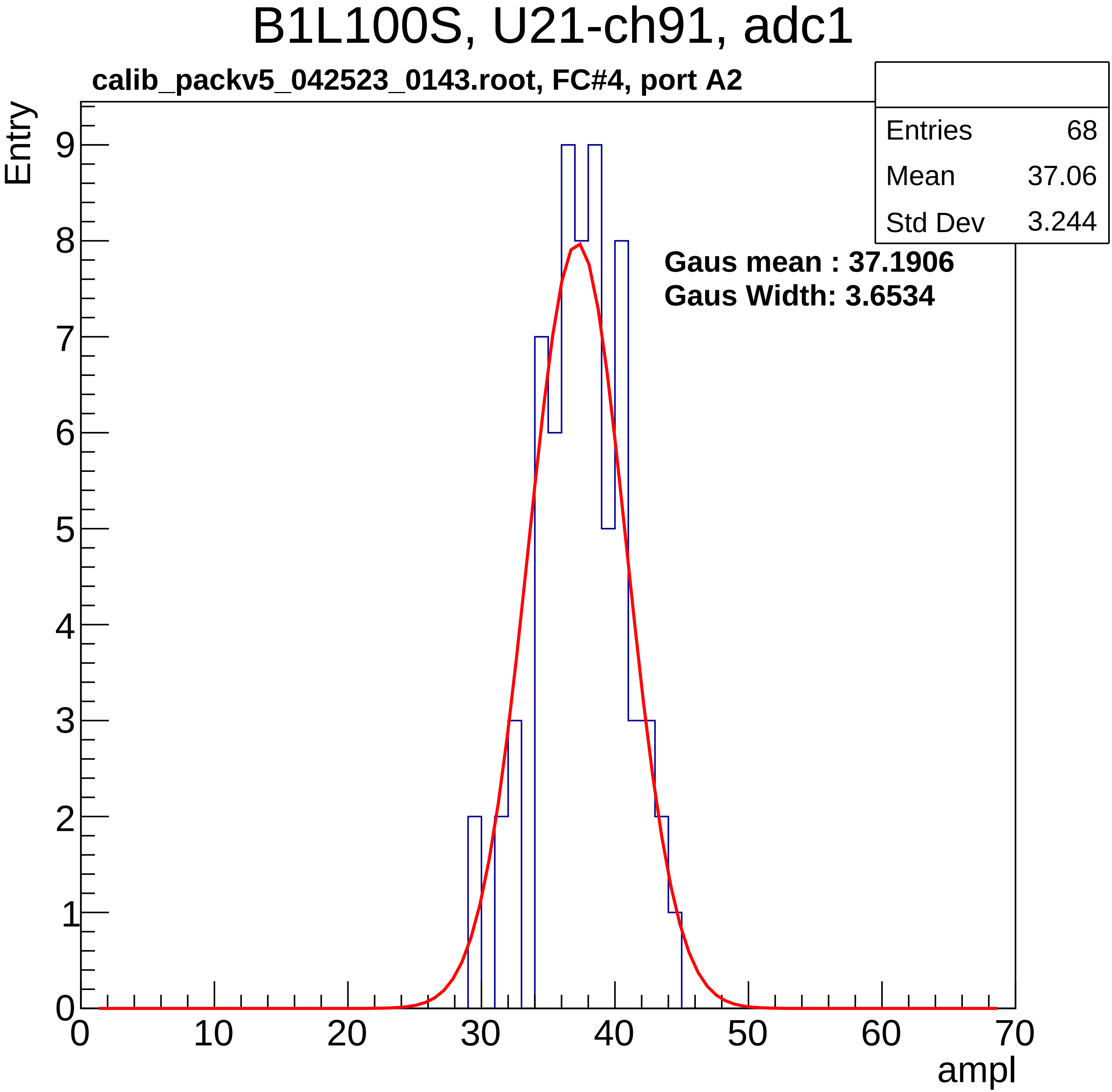
B1L100S, U21-ch90, adc6

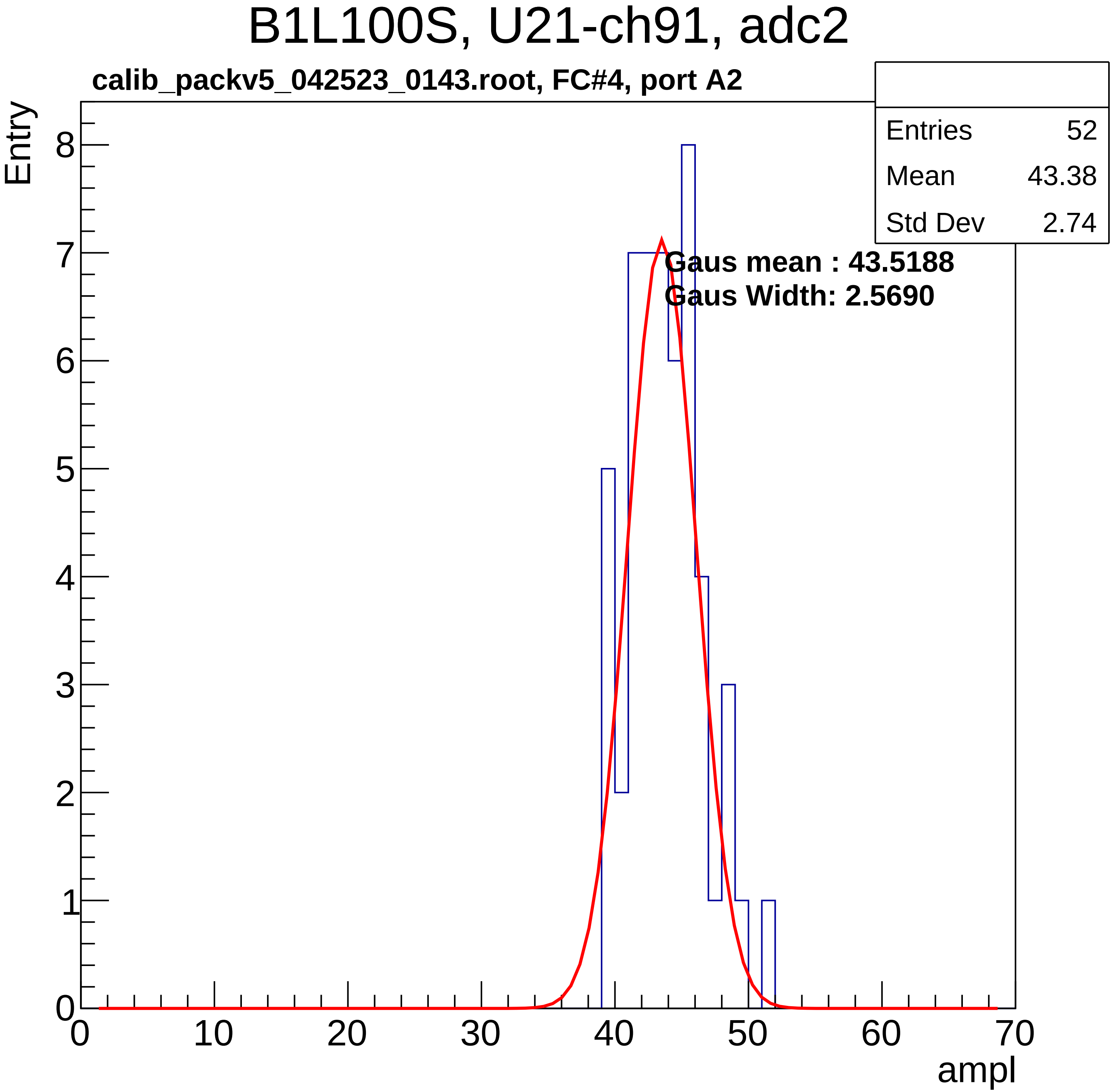
calib_packv5_042523_0143.root, FC#4, port A2

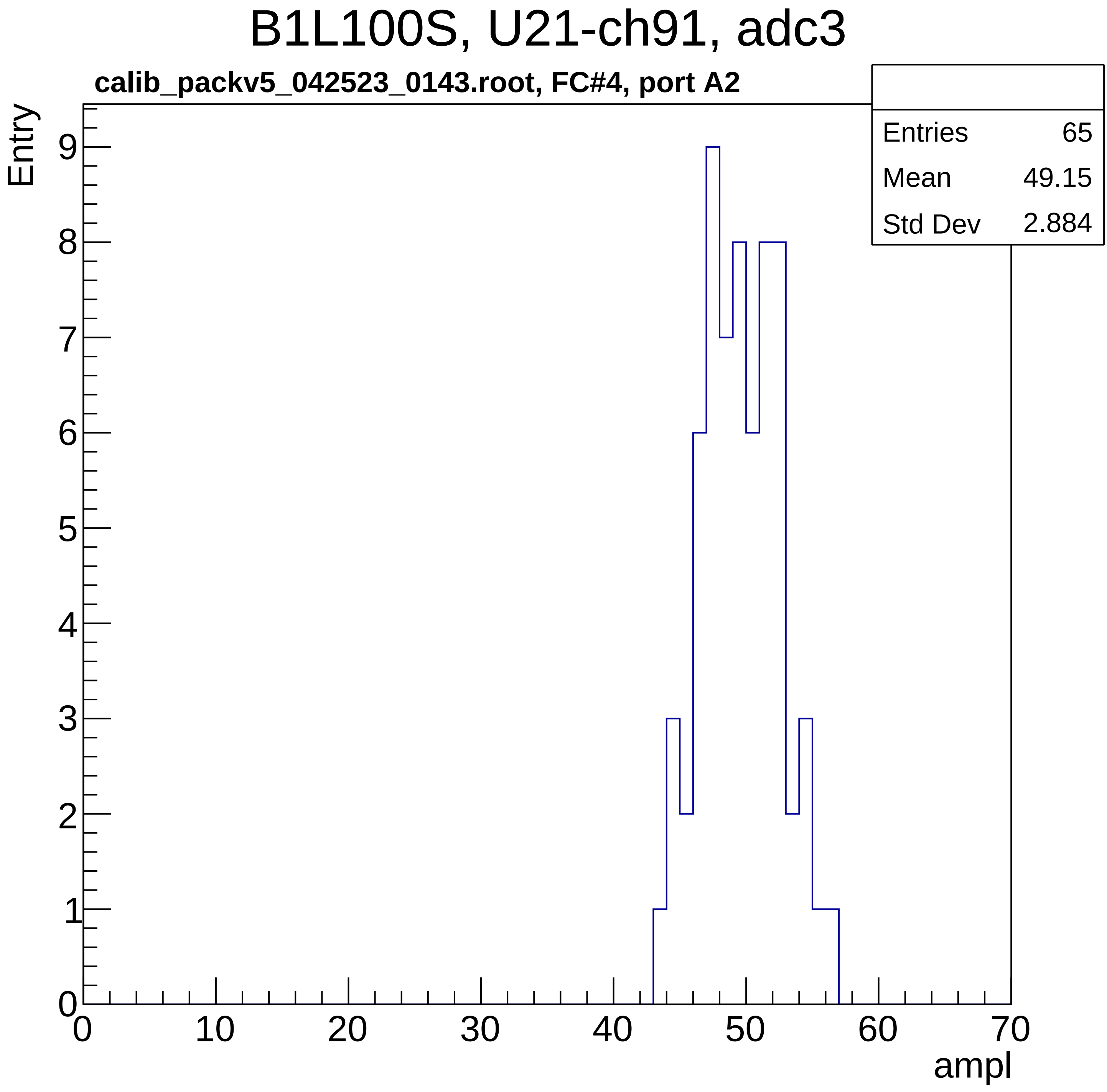


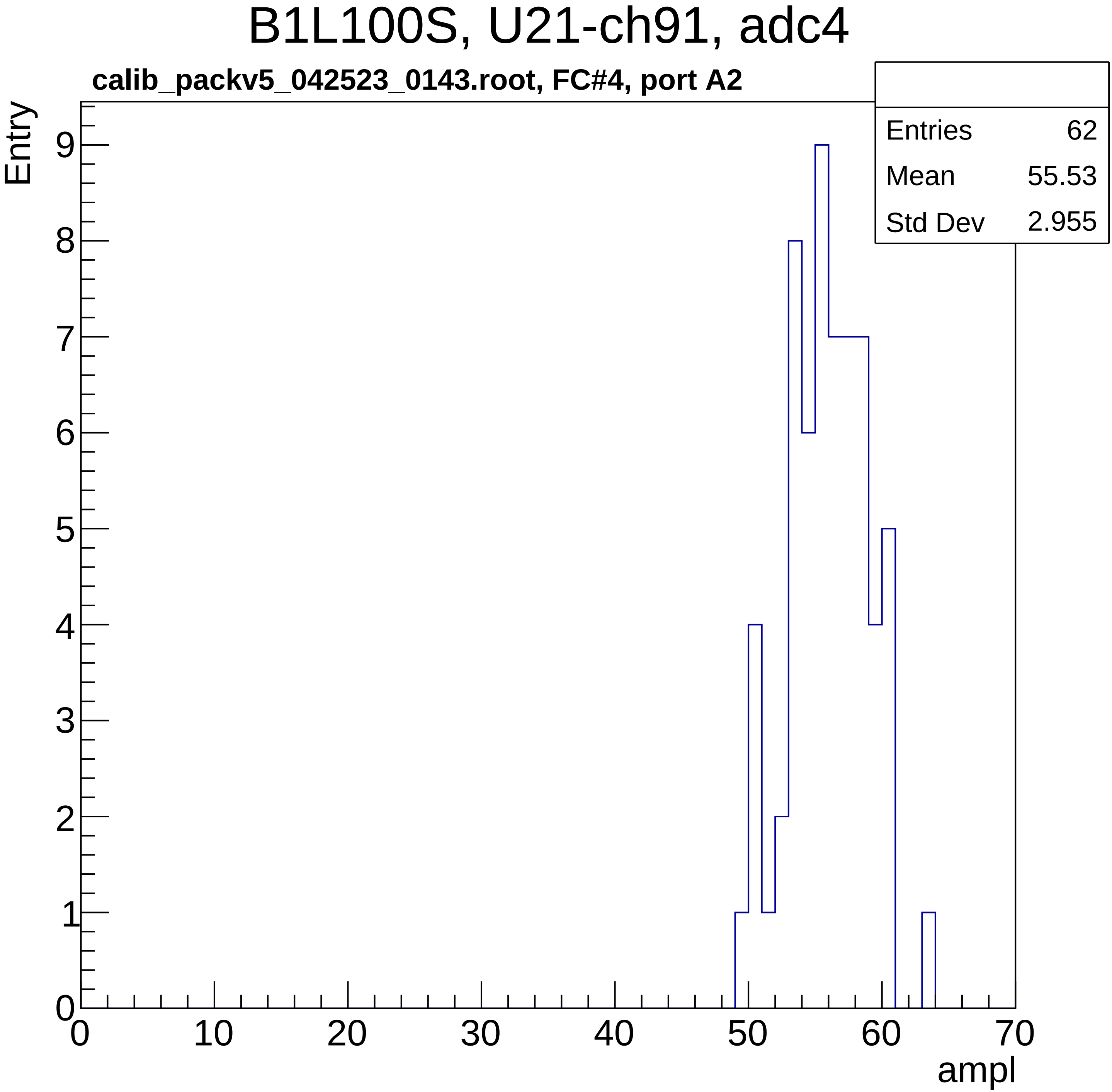

B1L100S, U21-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

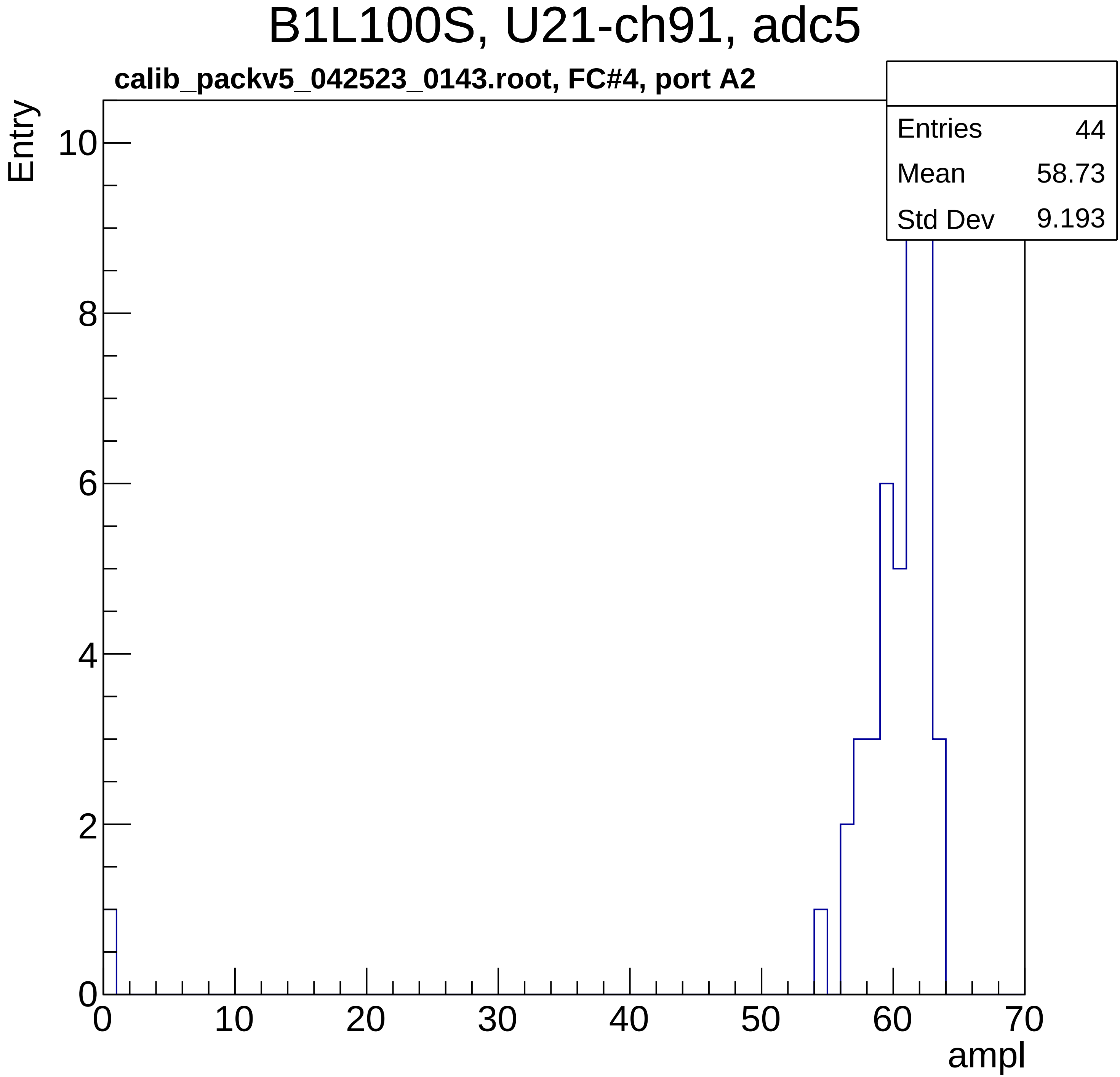

B1L100S, U21-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.22
Std Dev 3.489
Gaus mean : 30.6161
Gaus Width: 3.5239
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

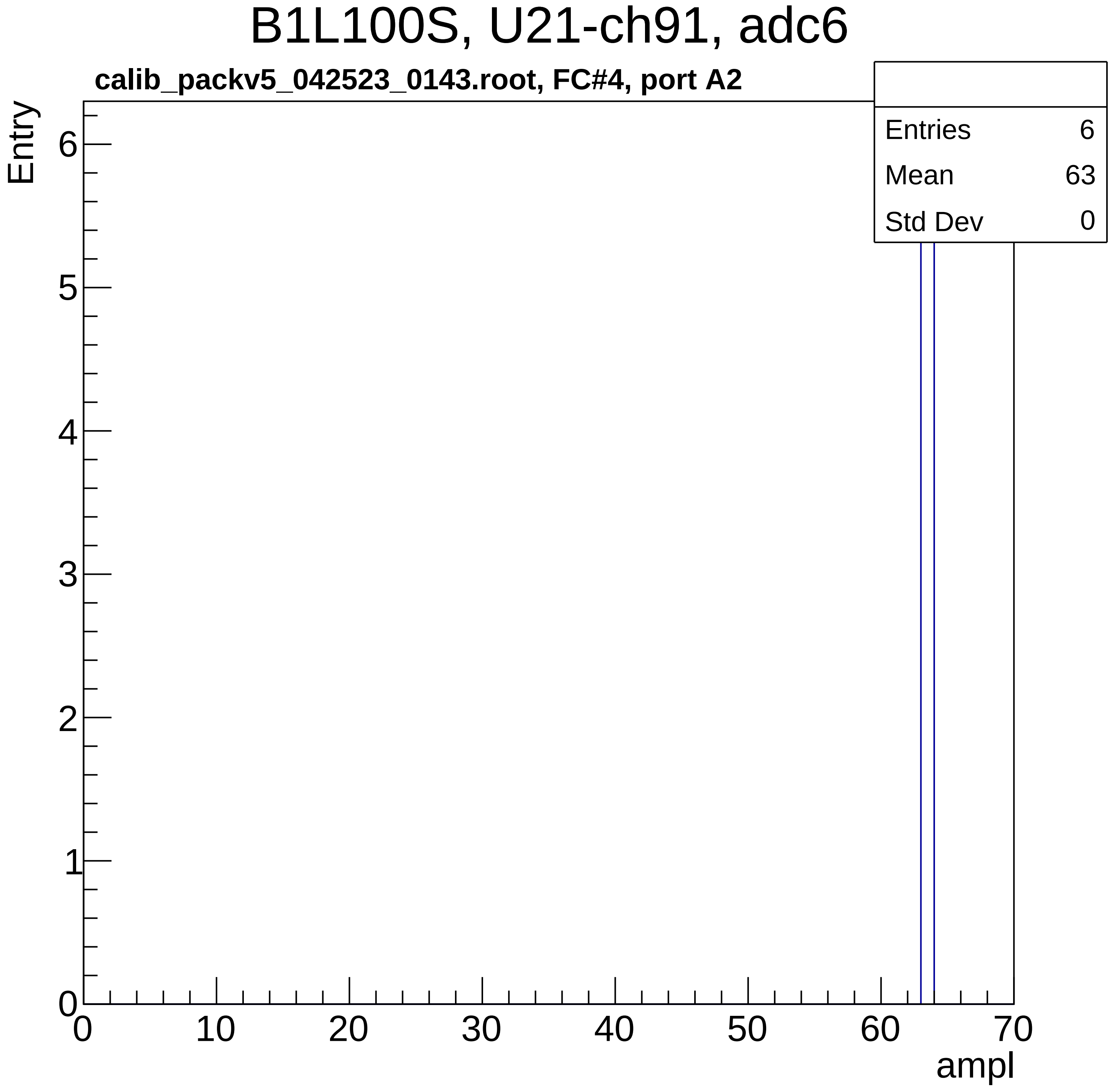

B1L100S, U21-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 68
Mean 37.06
Std Dev 3.244
Gaus mean : 37.1906
Gaus Width: 3.6534
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

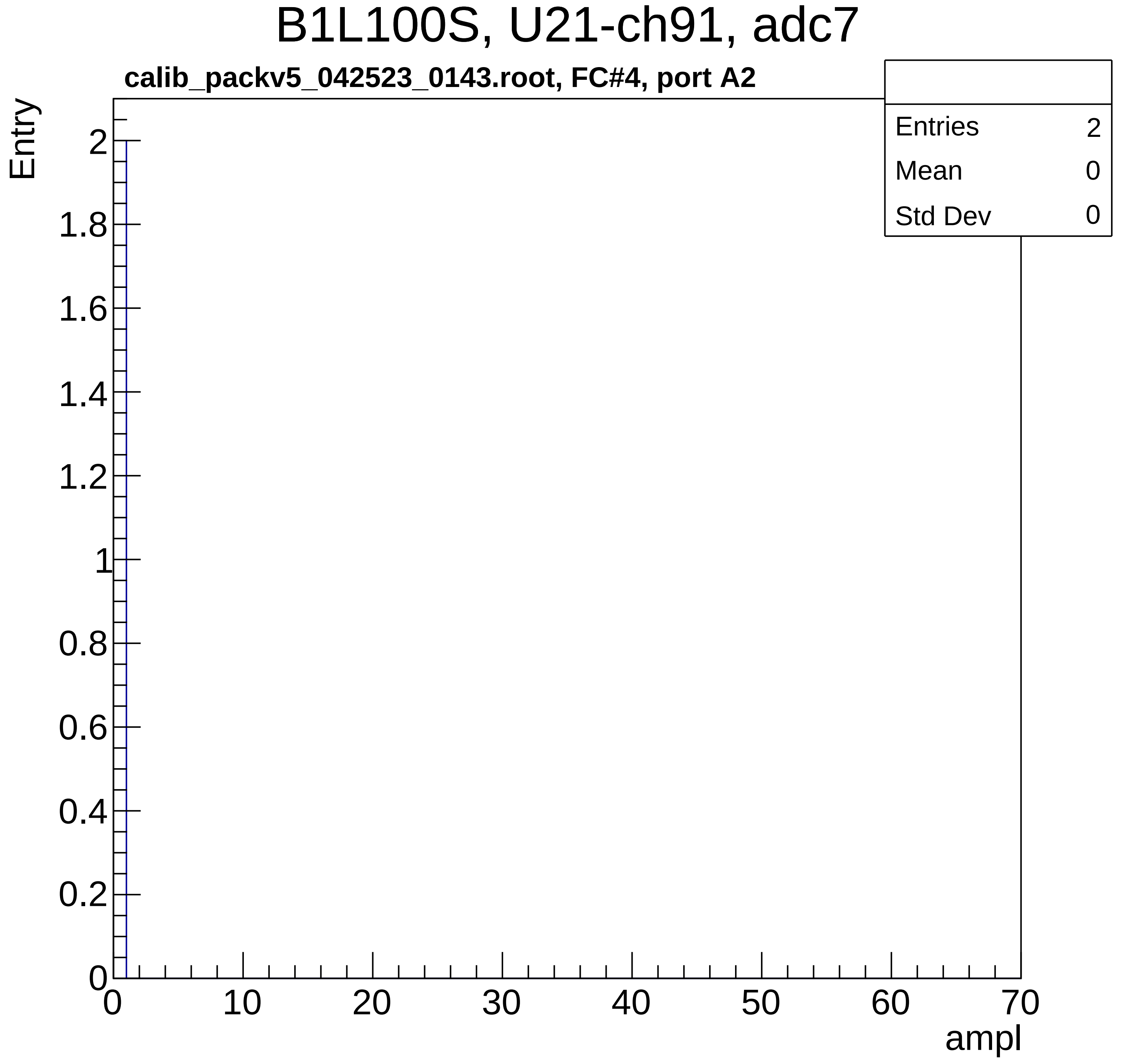

B1L100S, U21-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.38
Std Dev 2.74
Gaus mean : 43.5188
Gaus Width: 2.5690
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

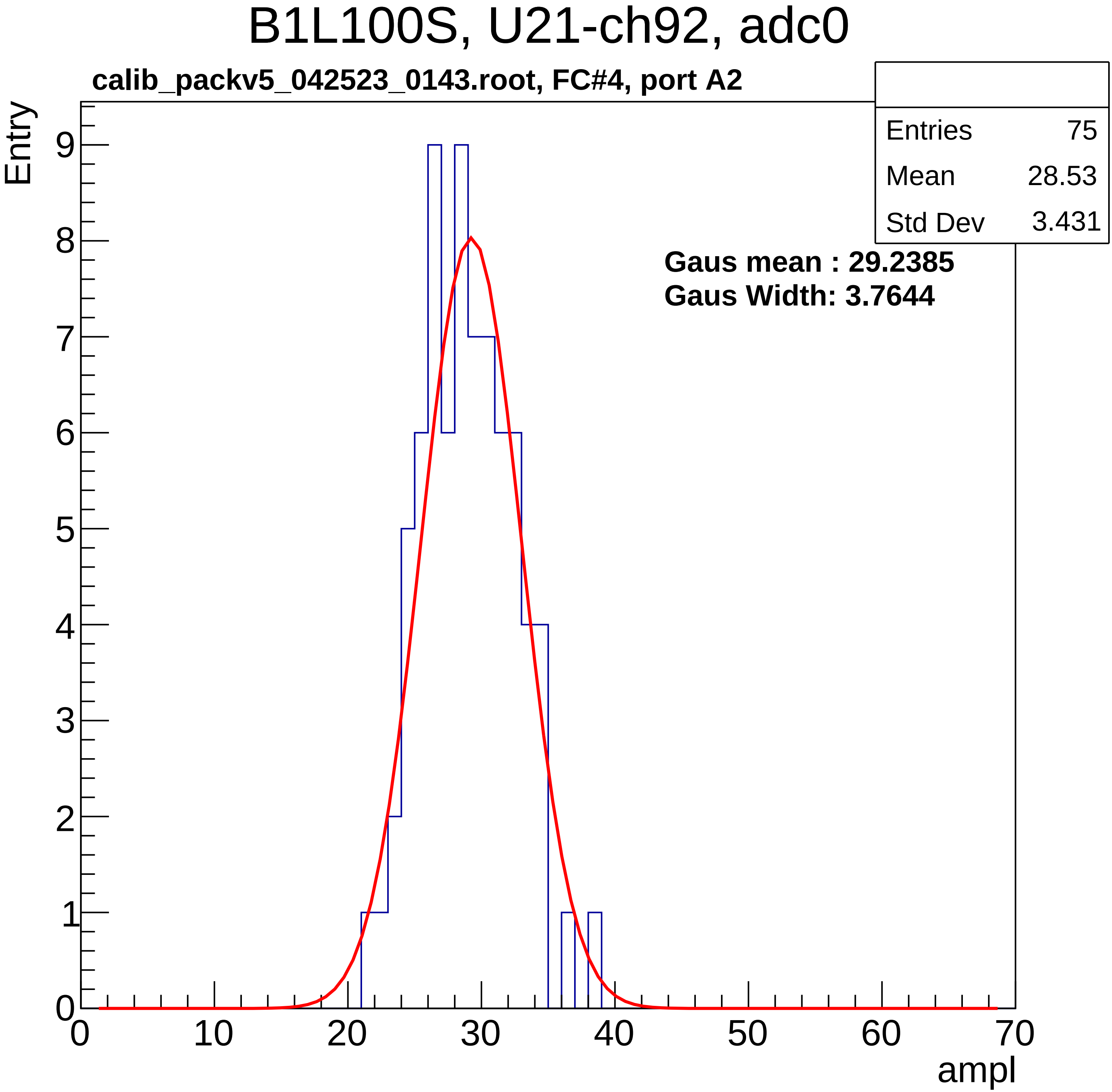

B1L100S, U21-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.15
Std Dev 2.884
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

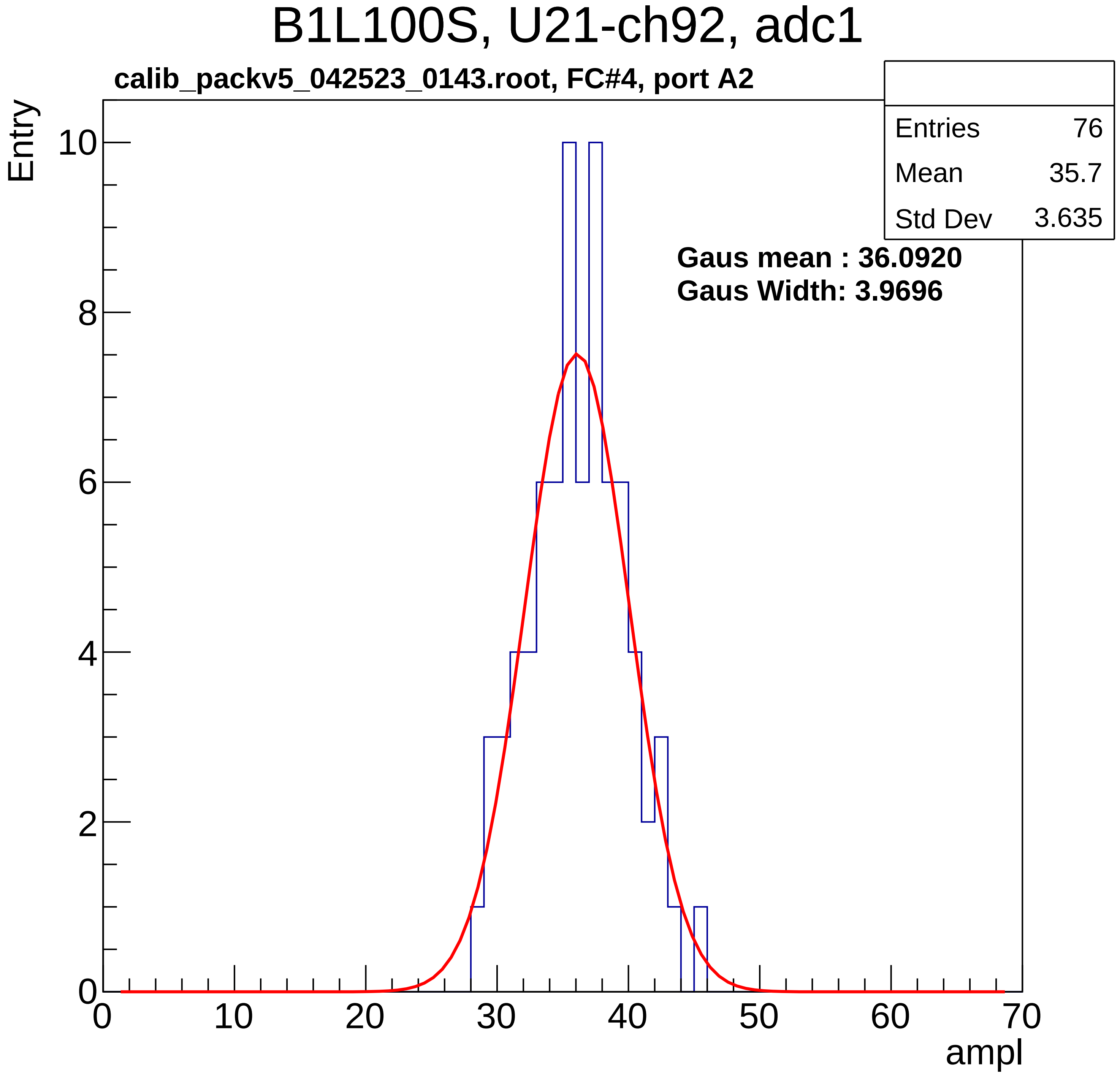

B1L100S, U21-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.53
Std Dev 2.955
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

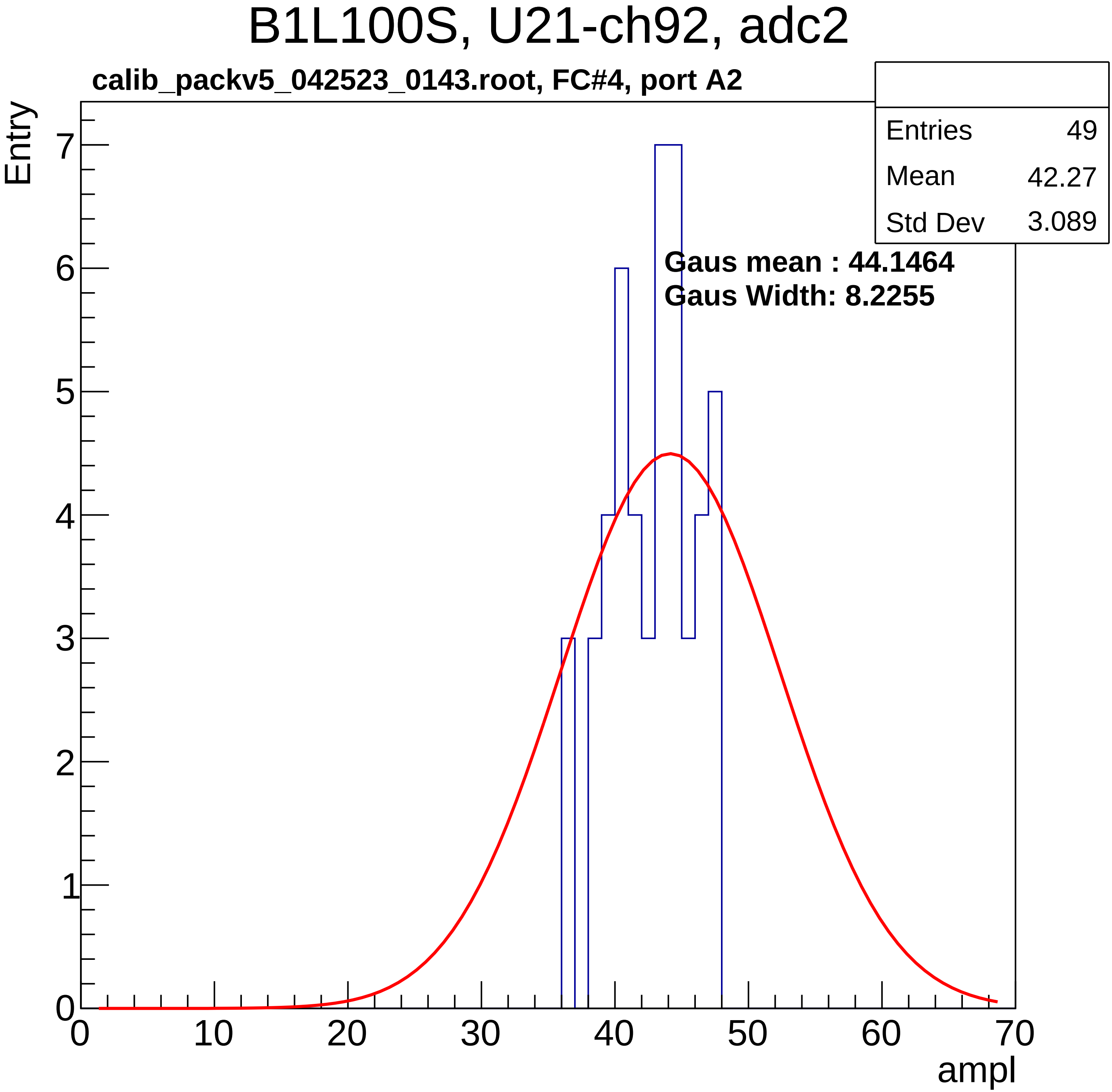

B1L100S, U21-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.73
Std Dev 9.193
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 63
Std Dev 0
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

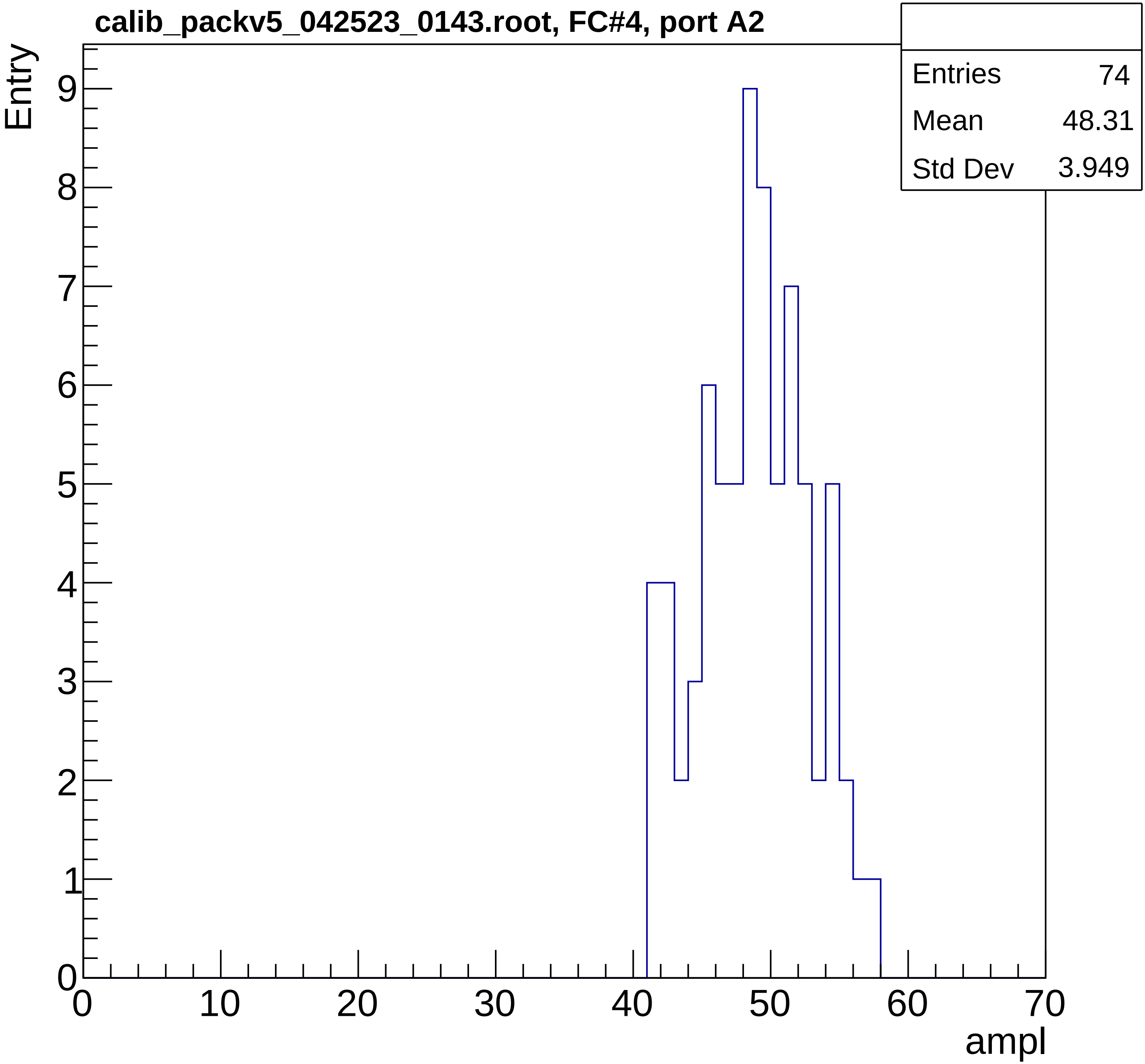

B1L100S, U21-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.53
Std Dev 3.431
Gaus mean : 29.2385
Gaus Width: 3.7644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

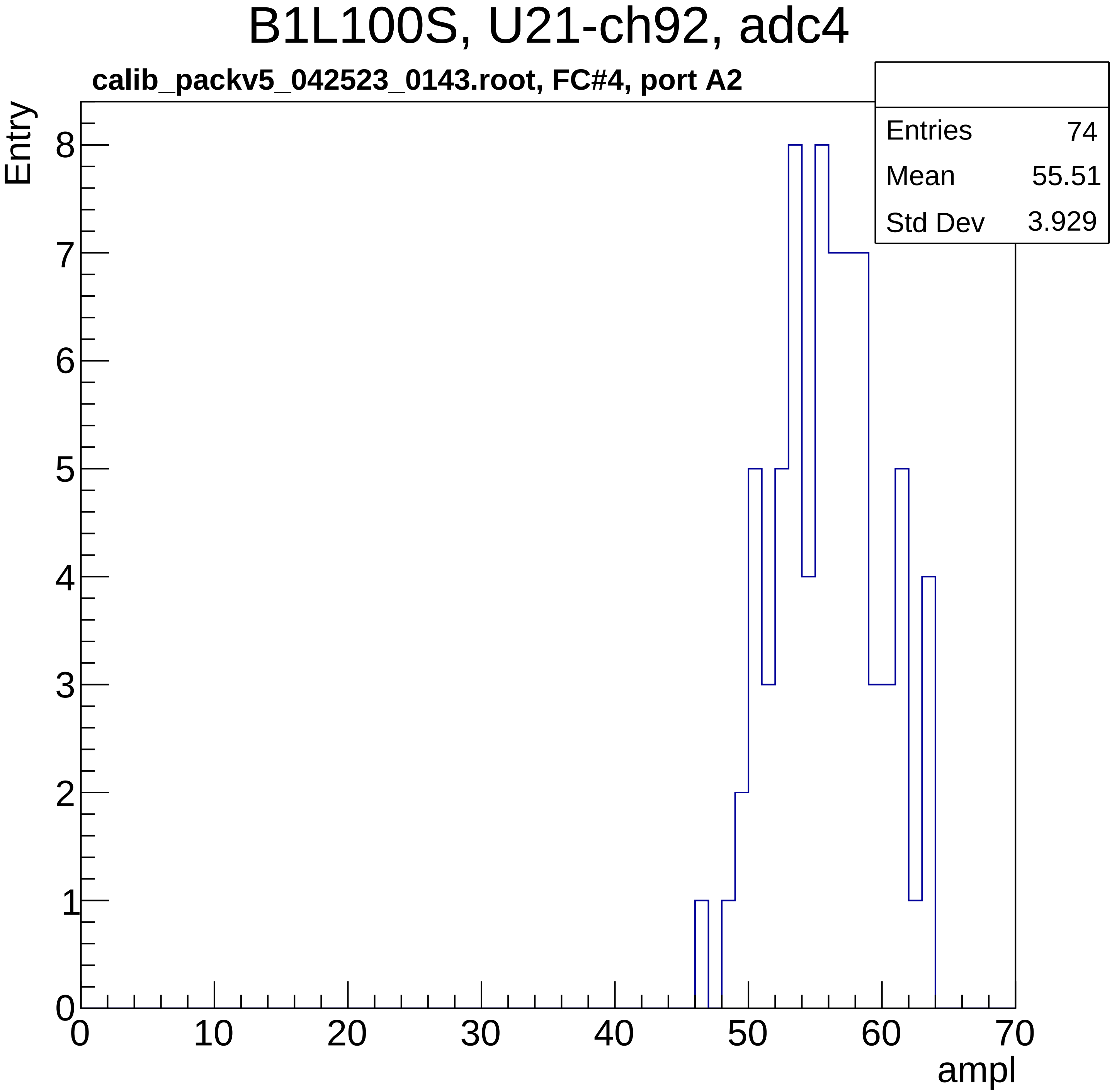

B1L100S, U21-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.7
Std Dev 3.635
Gaus mean : 36.0920
Gaus Width: 3.9696
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

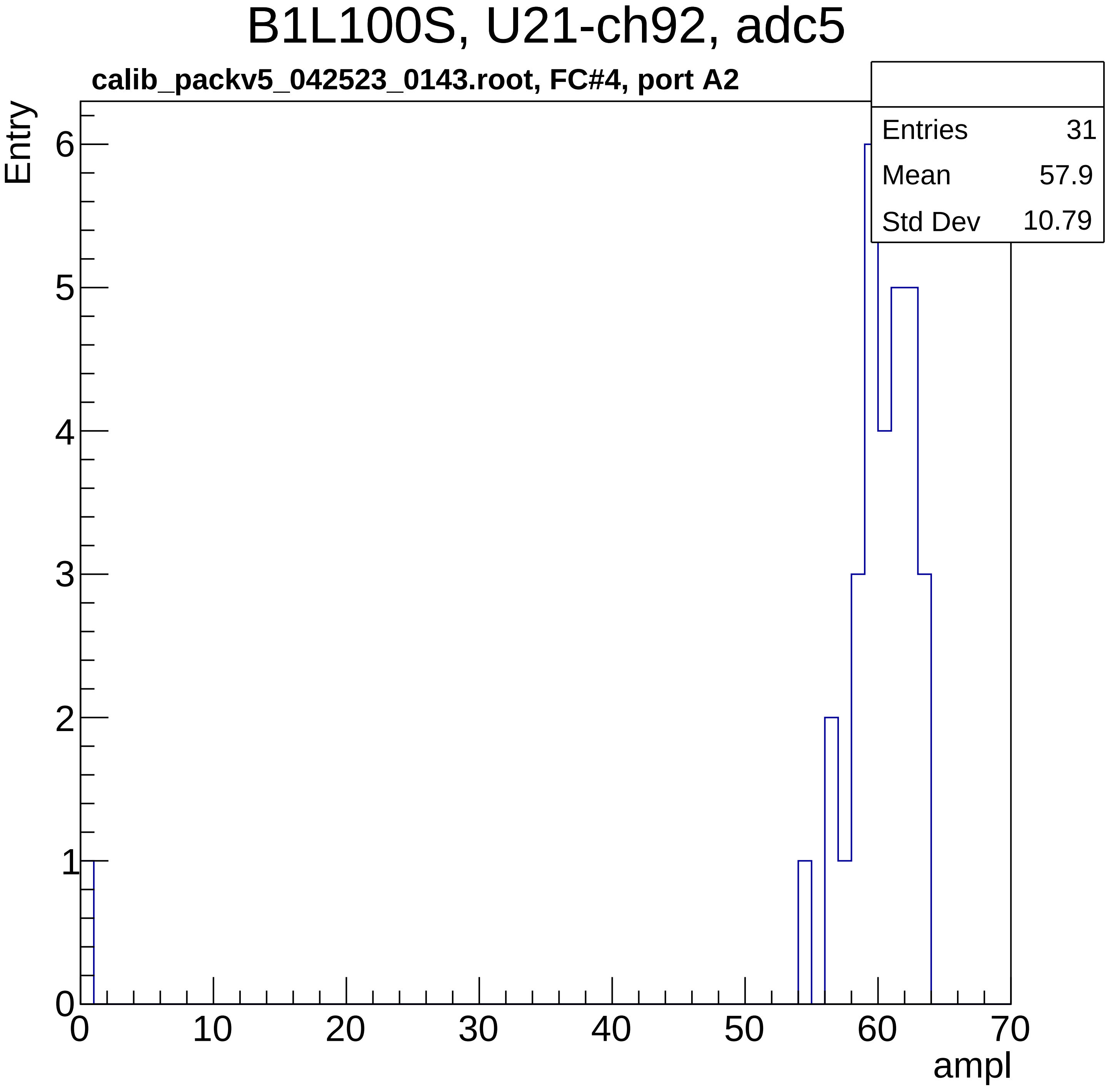

B1L100S, U21-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 49
Mean 42.27
Std Dev 3.089
Gaus mean : 44.1464
Gaus Width: 8.2255
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2

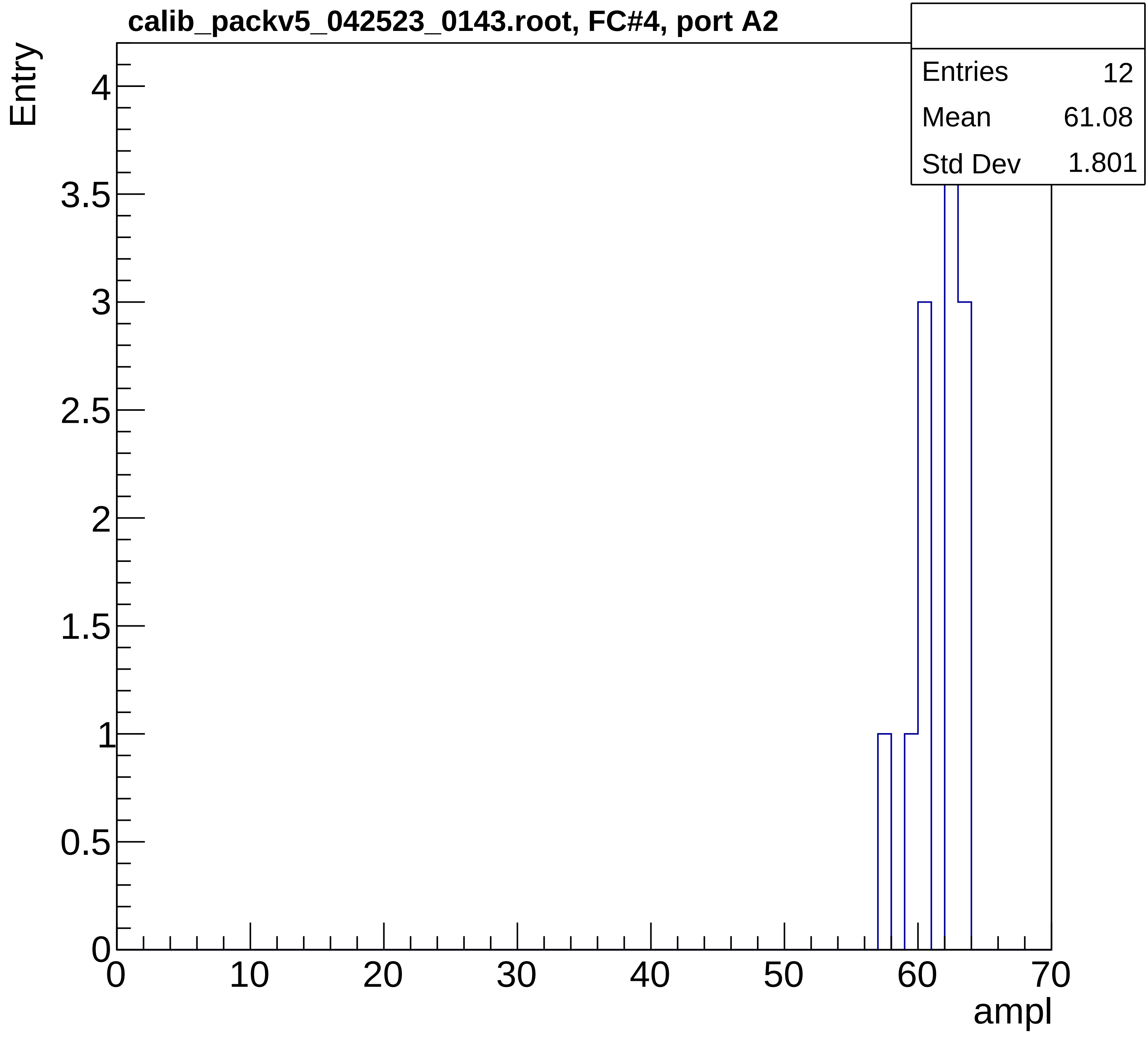


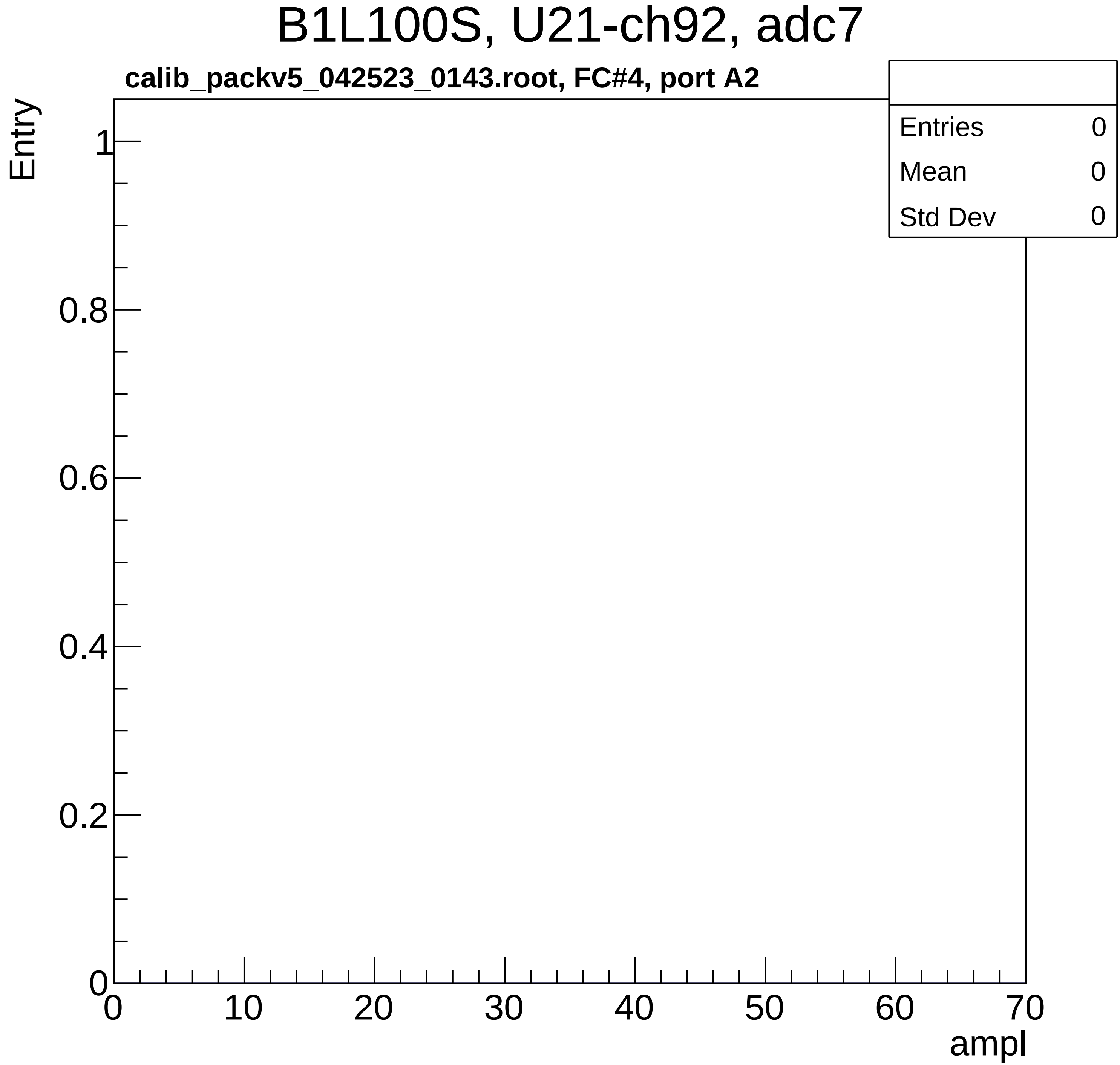

B1L100S, U21-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 55.51
Std Dev 3.929
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 57.9
Std Dev 10.79
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch92, adc6

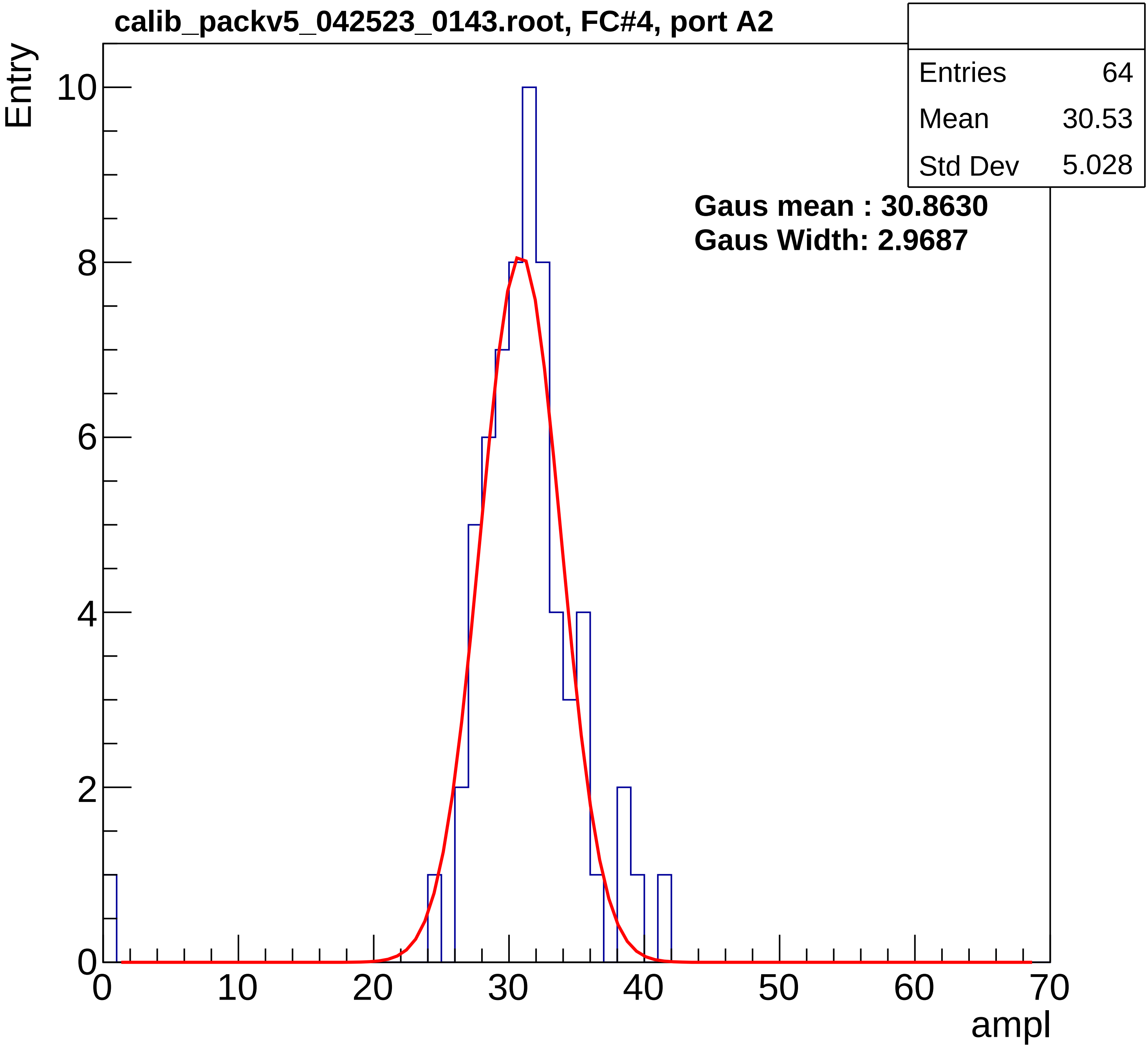
calib_packv5_042523_0143.root, FC#4, port A2

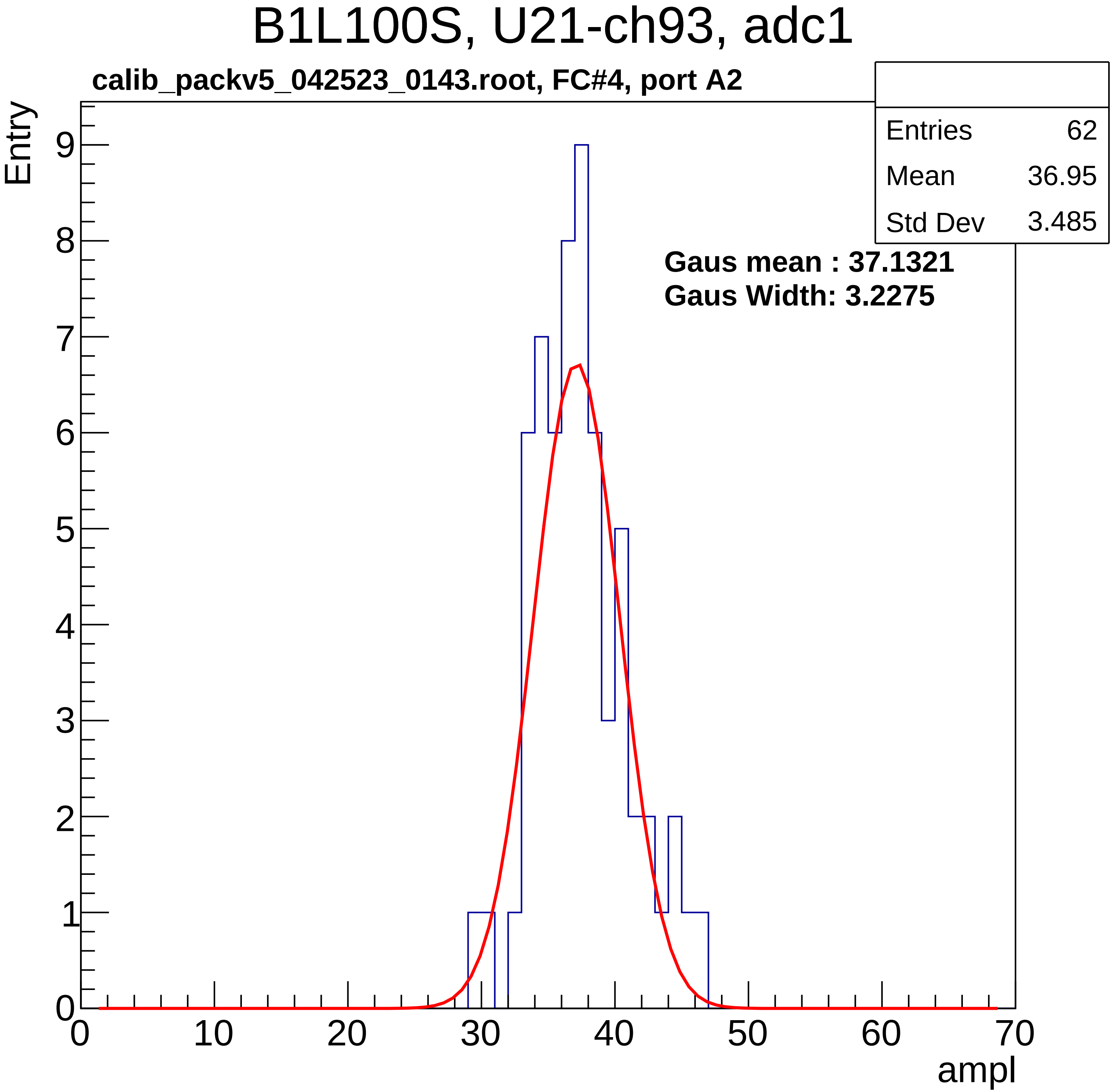


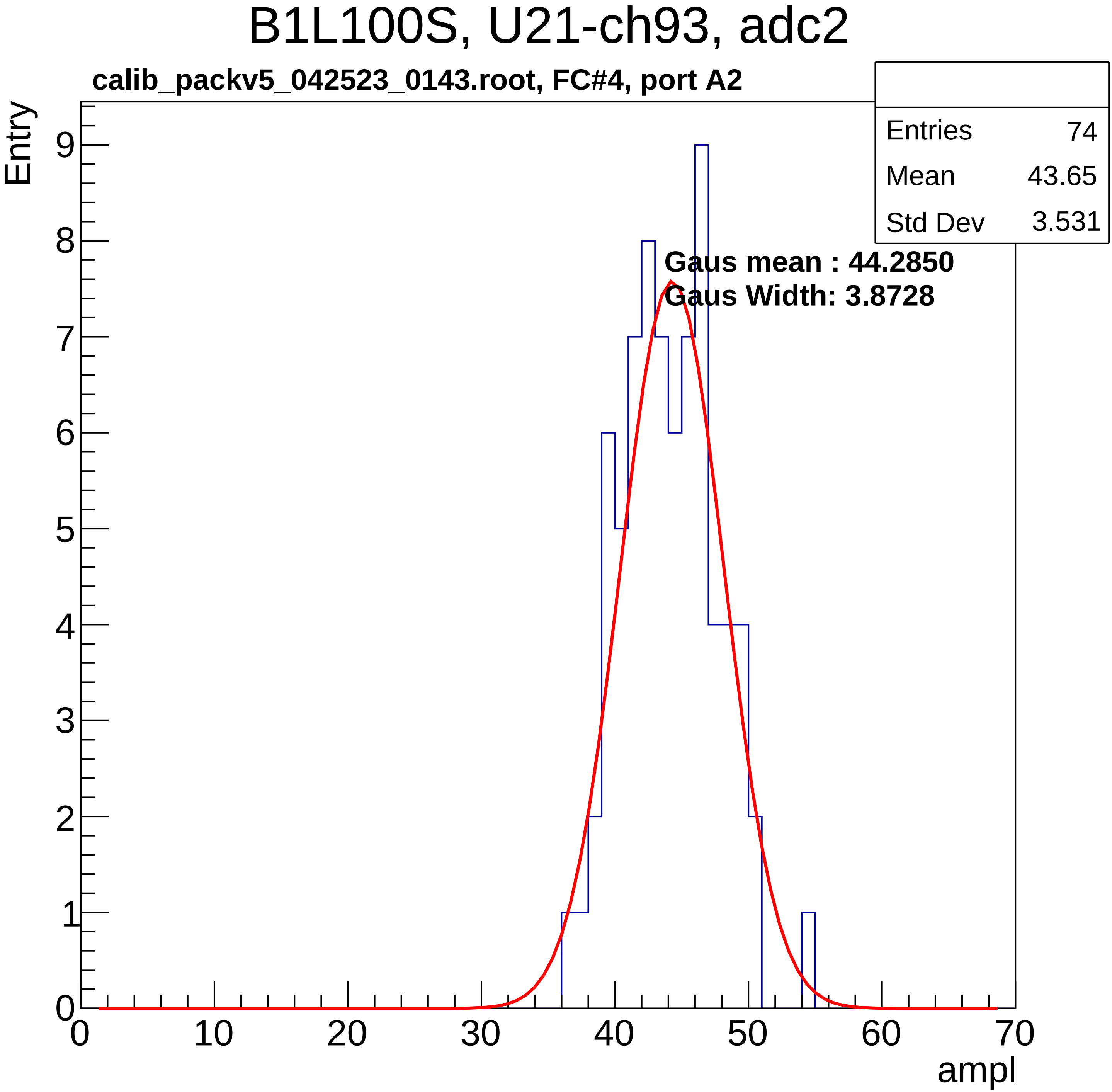

B1L100S, U21-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch93, adc0

calib_packv5_042523_0143.root, FC#4, port A2



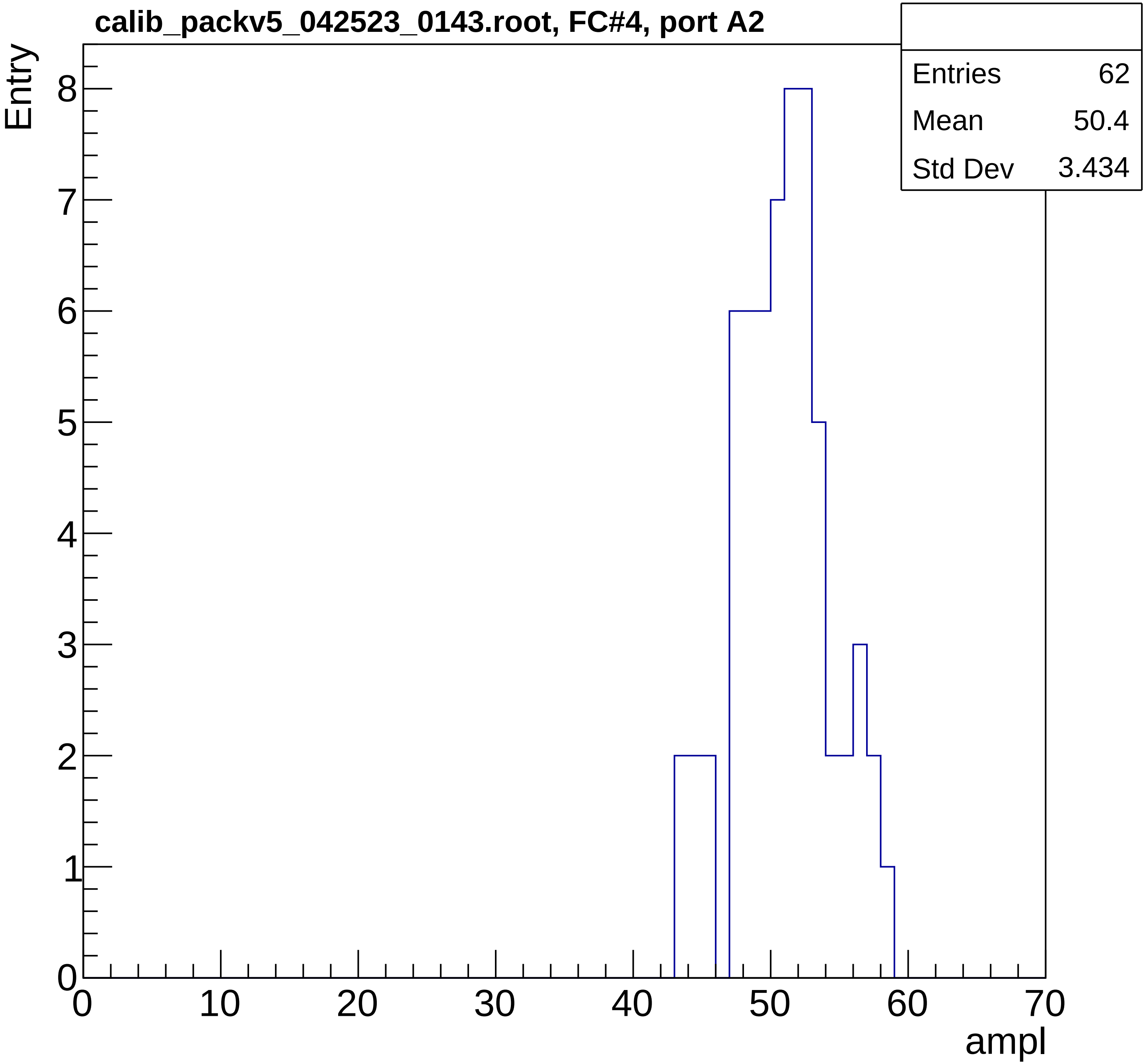

B1L100S, U21-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.95
Std Dev 3.485
Gaus mean : 37.1321
Gaus Width: 3.2275
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

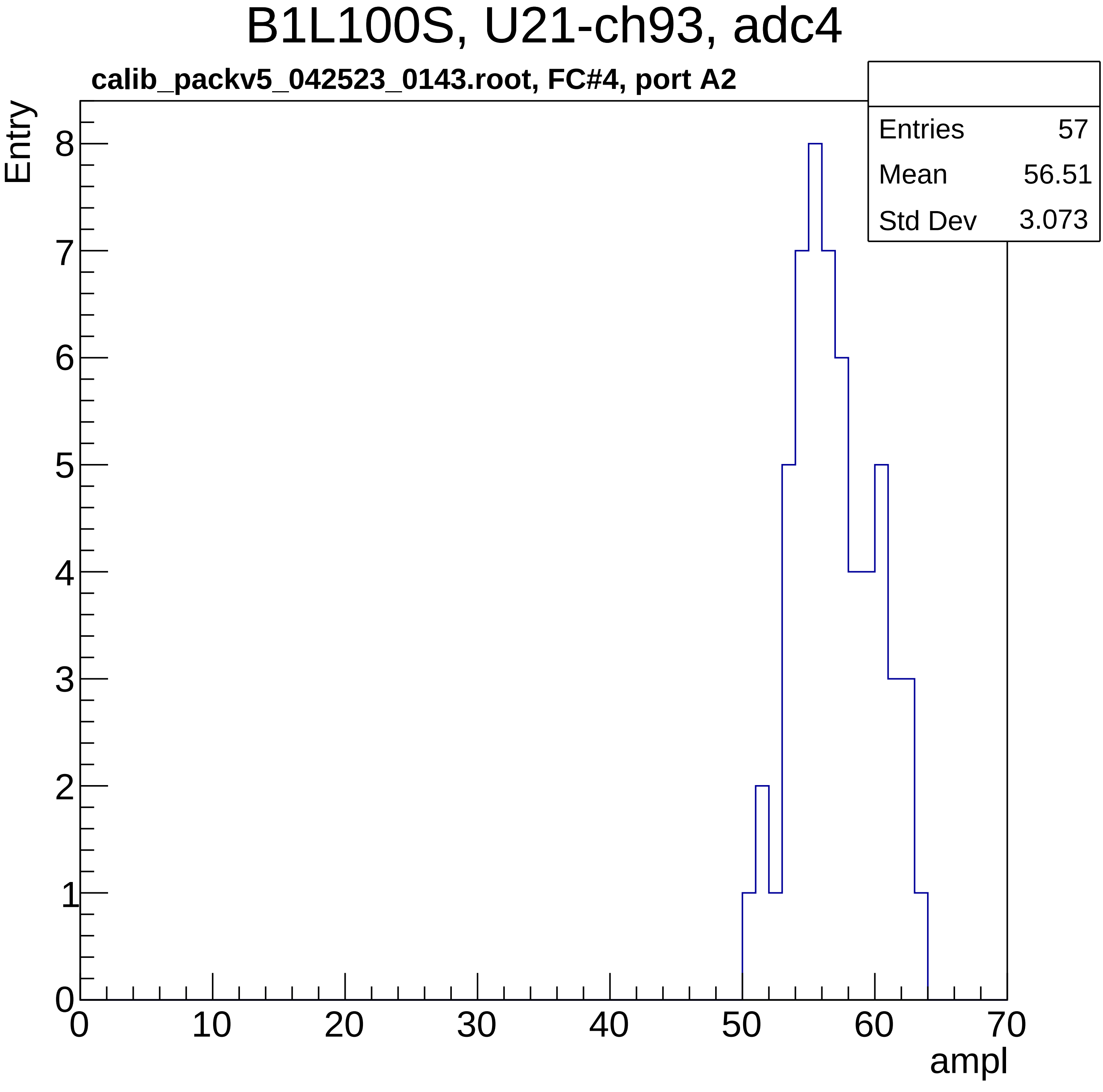


B1L100S, U21-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.65
Std Dev 3.531
Gaus mean : 44.2850
Gaus Width: 3.8728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

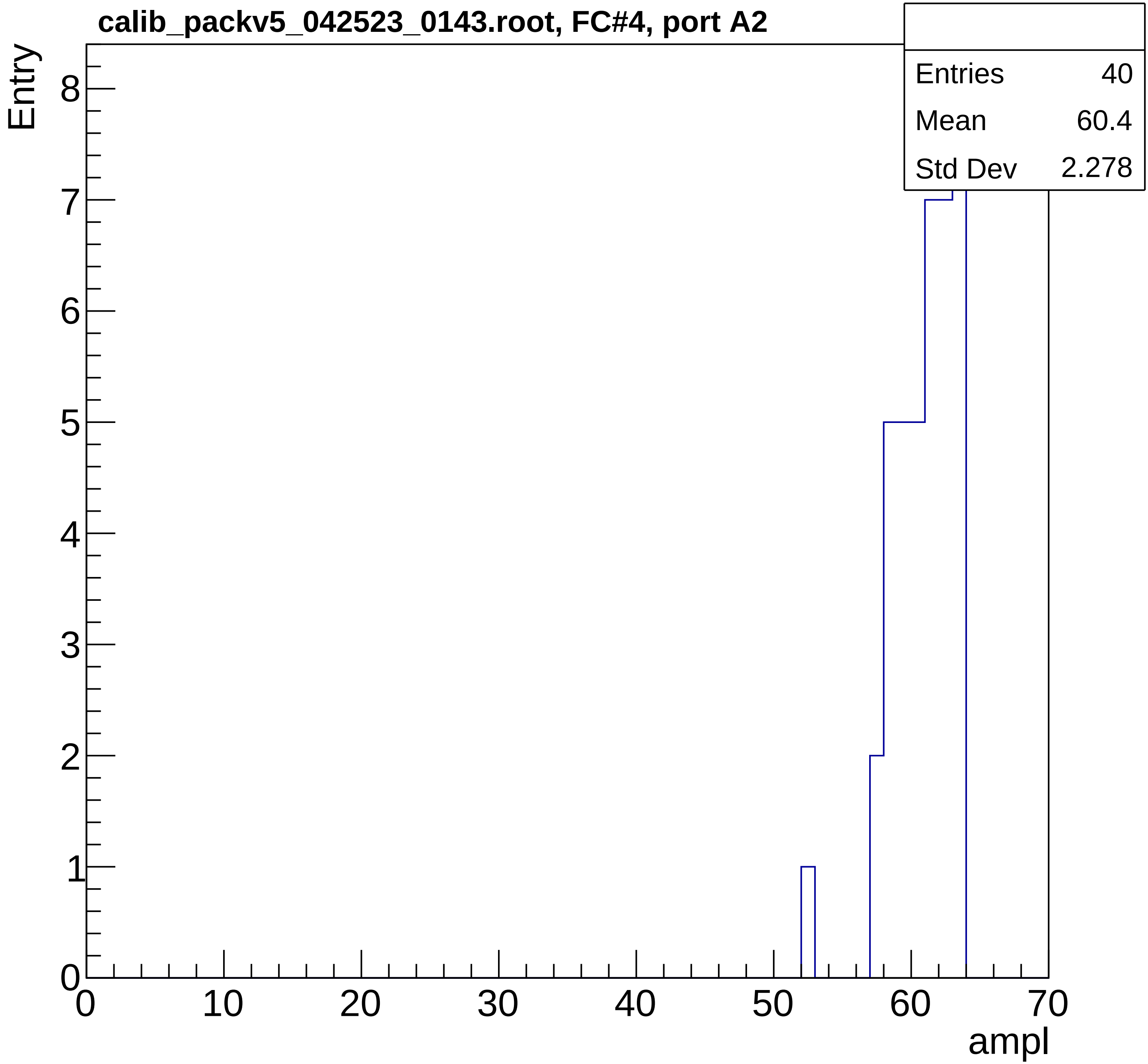
B1L100S, U21-ch93, adc3

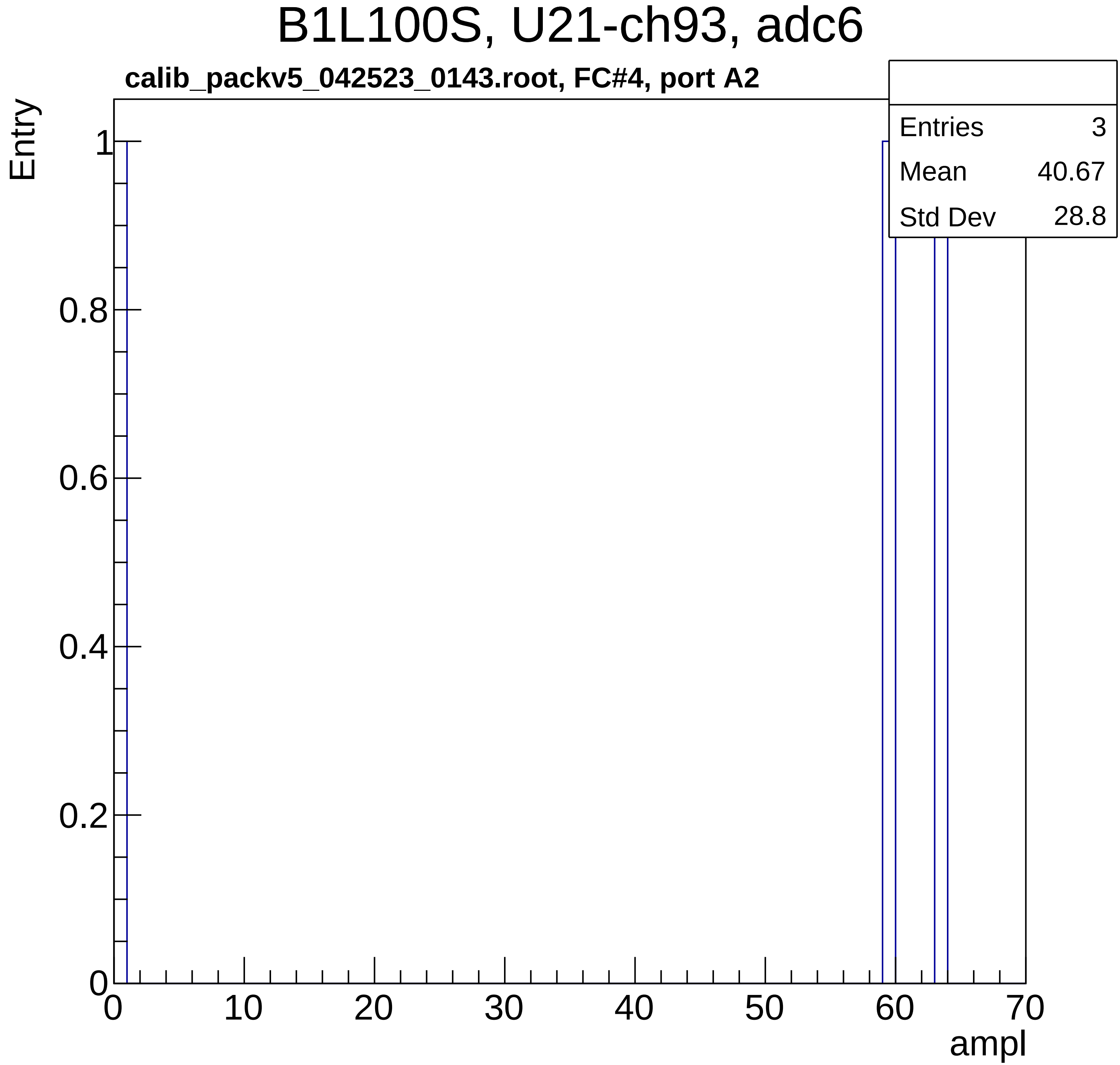
calib_packv5_042523_0143.root, FC#4, port A2



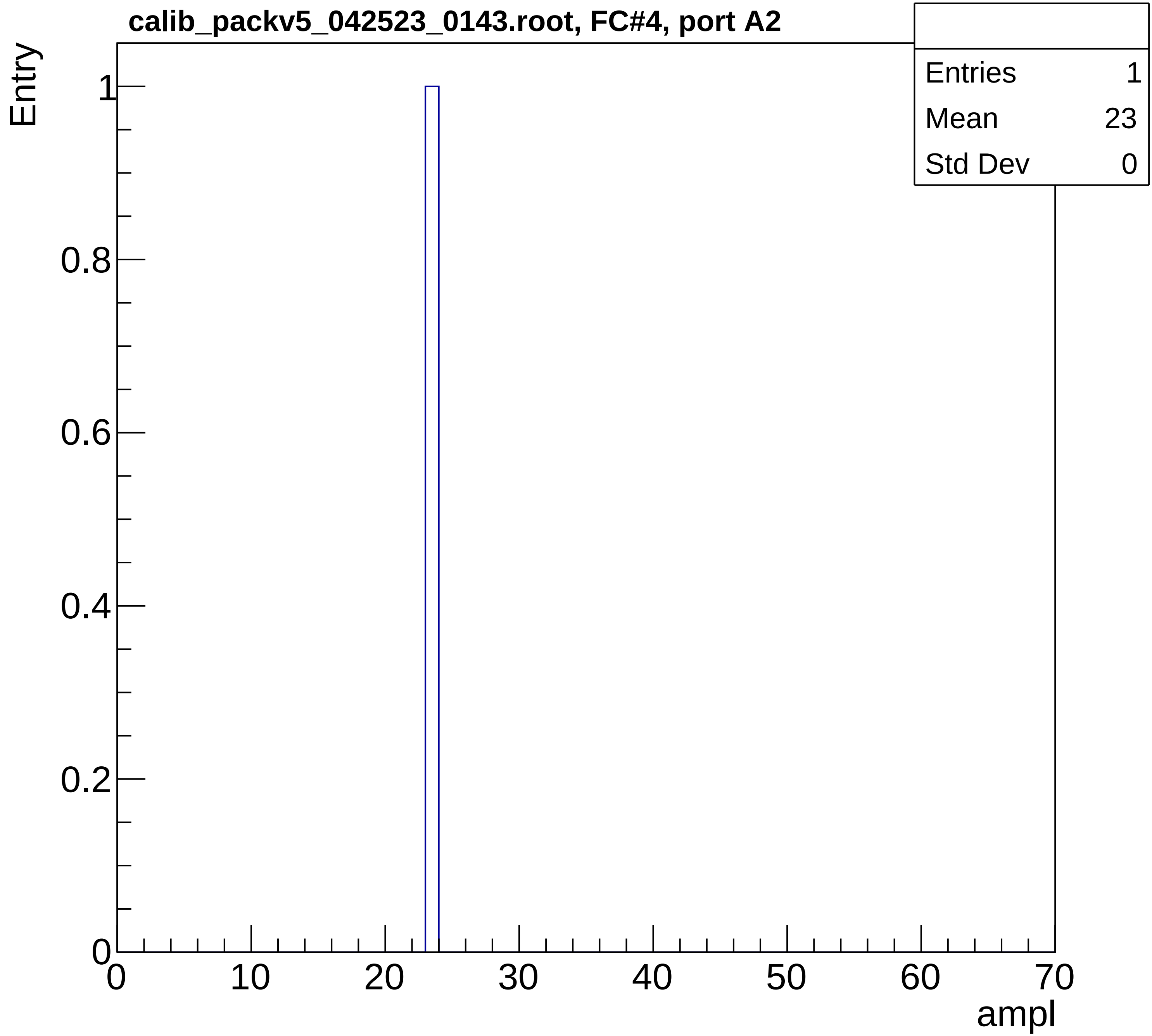

B1L100S, U21-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.51
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

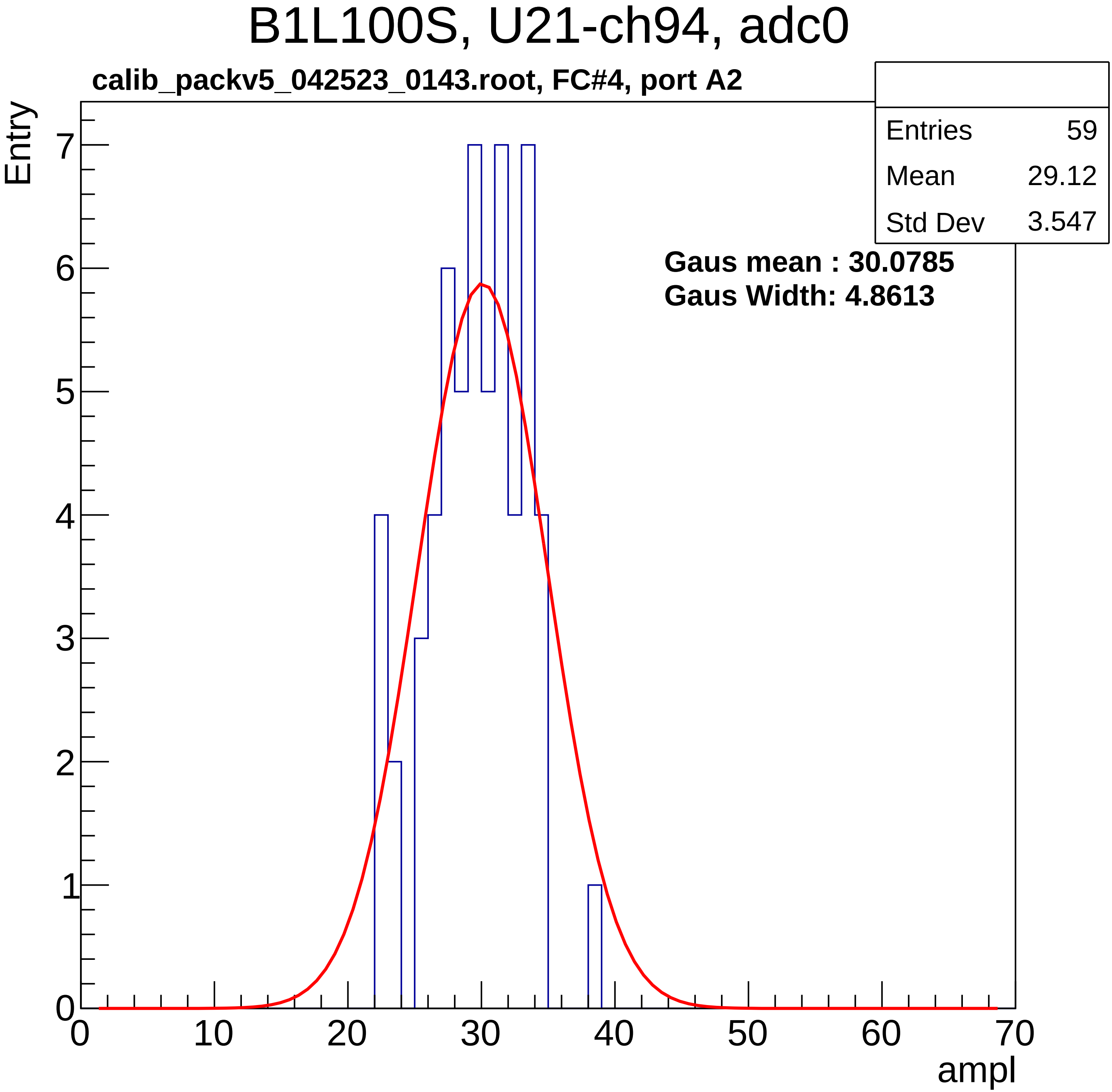
B1L100S, U21-ch93, adc5

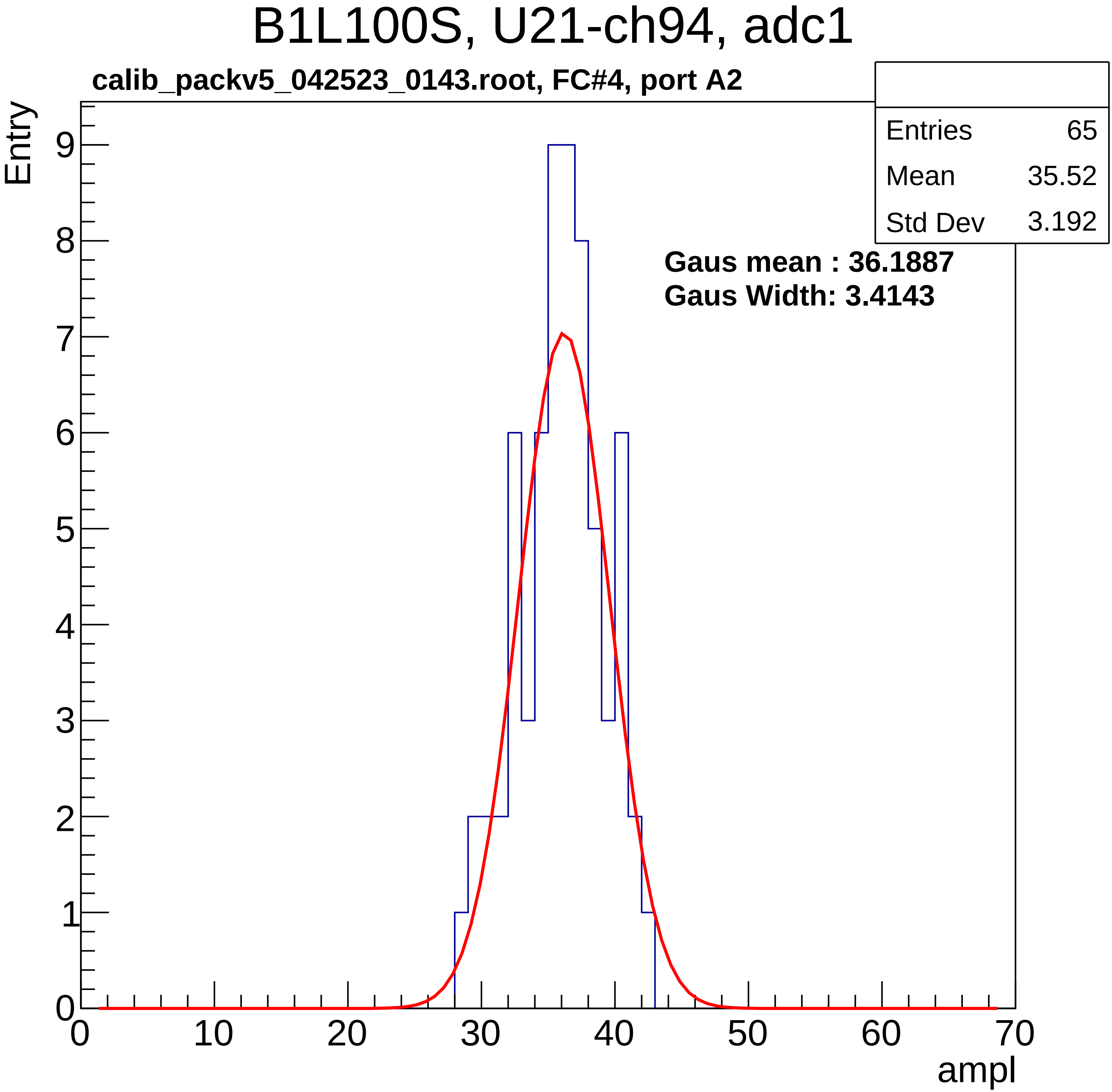


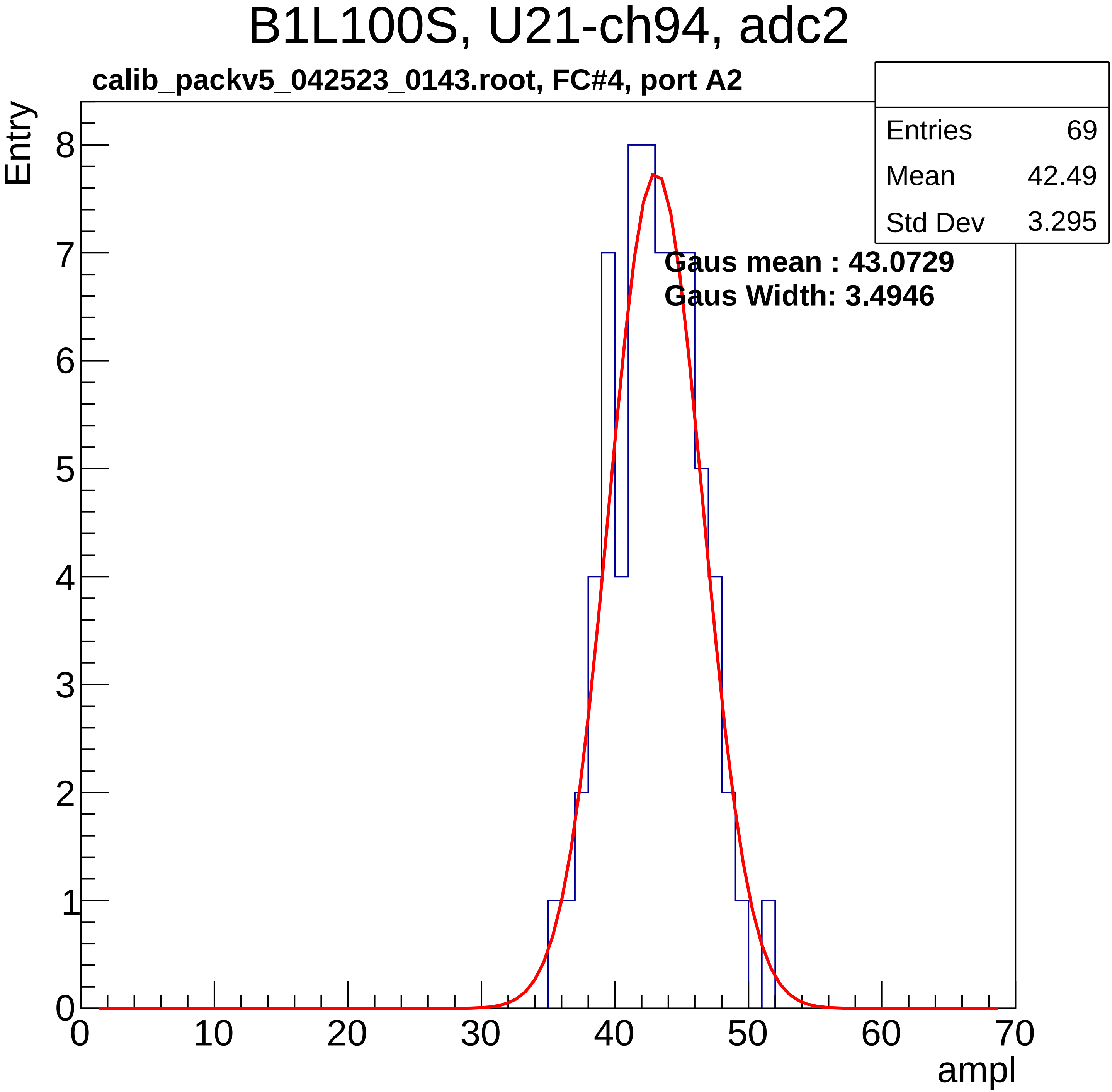

B1L100S, U21-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 3
Mean 40.67
Std Dev 28.8
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

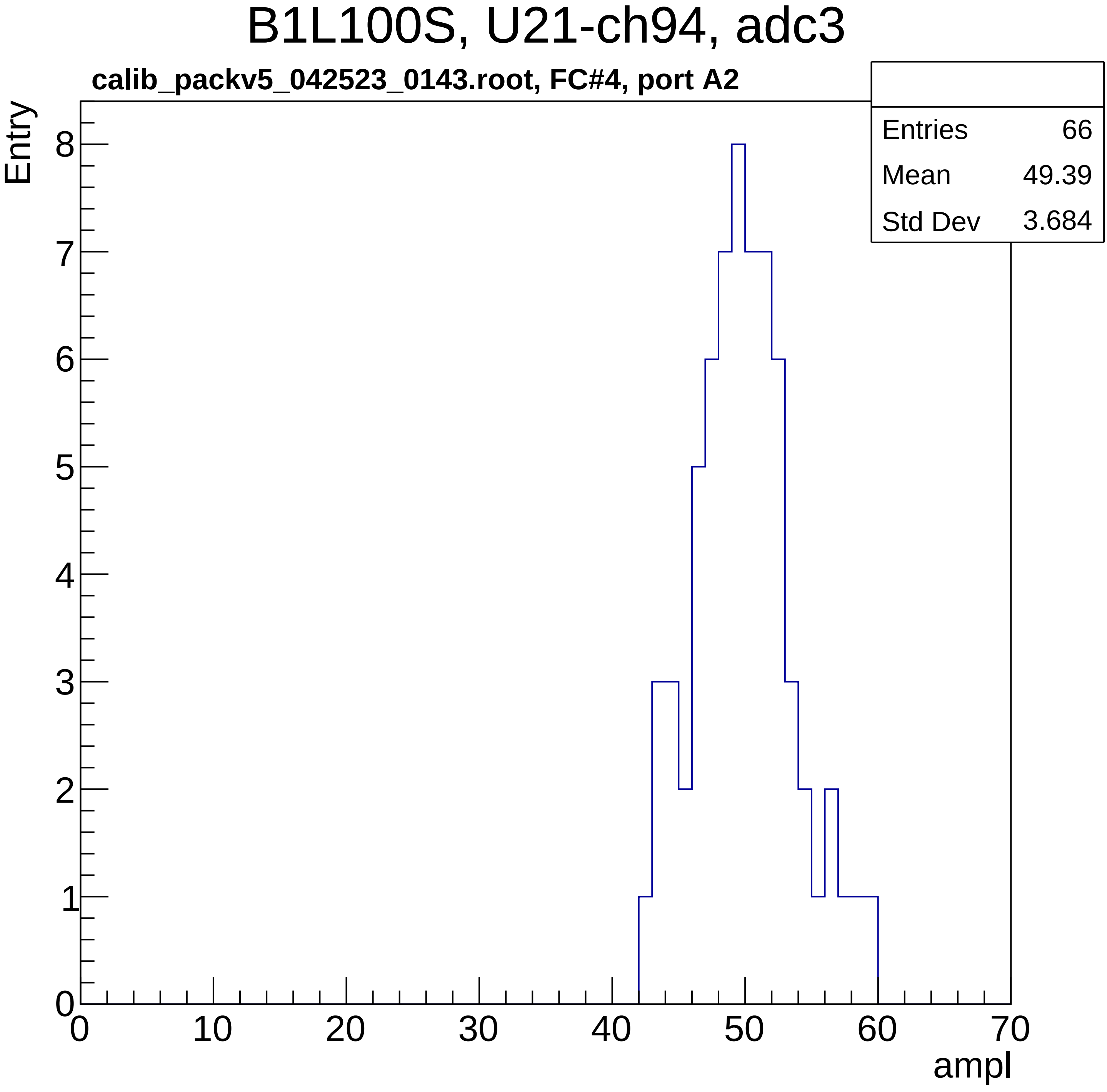
B1L100S, U21-ch93, adc7

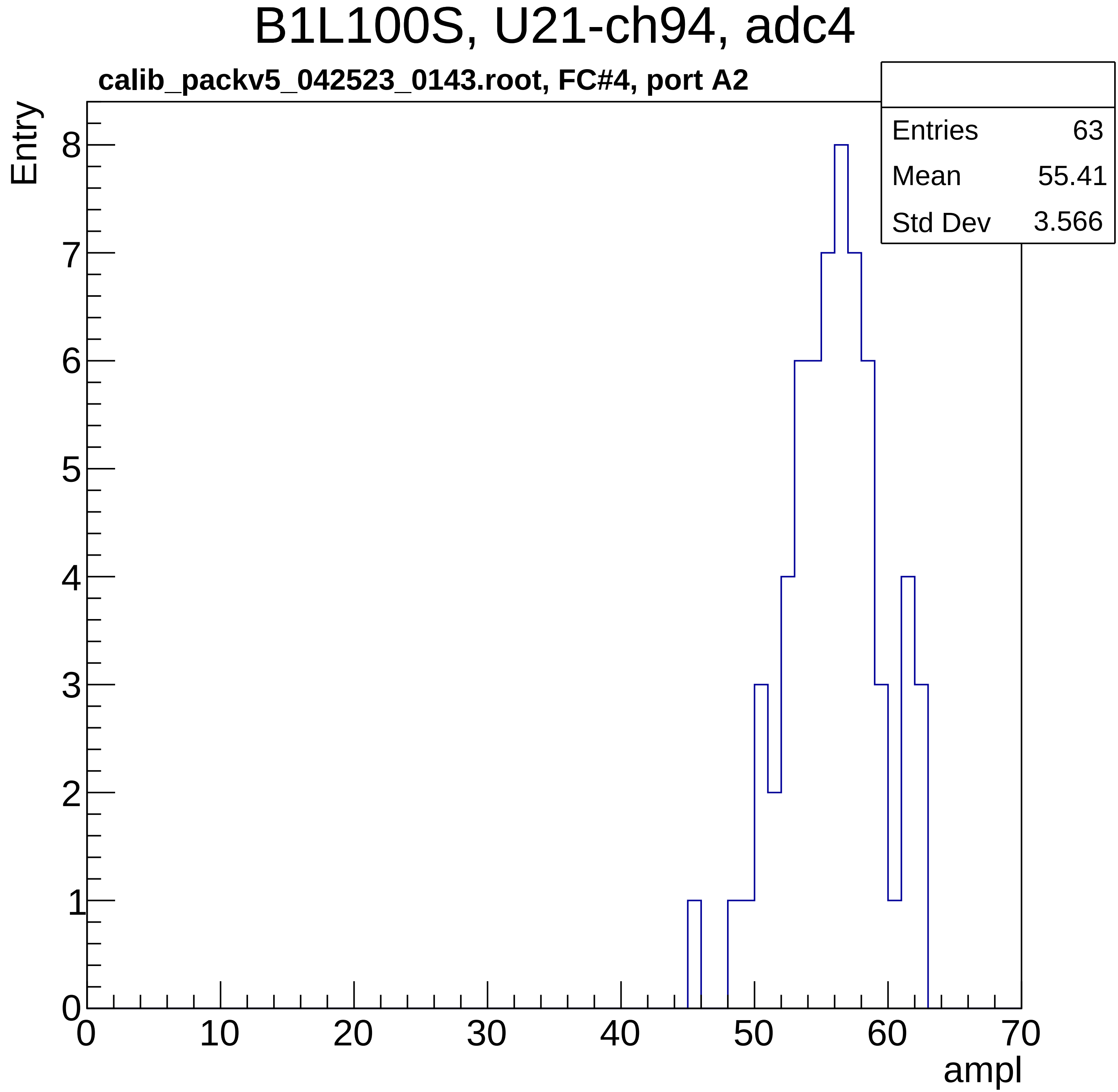



B1L100S, U21-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 59
Mean 29.12
Std Dev 3.547
Gaus mean : 30.0785
Gaus Width: 4.8613
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

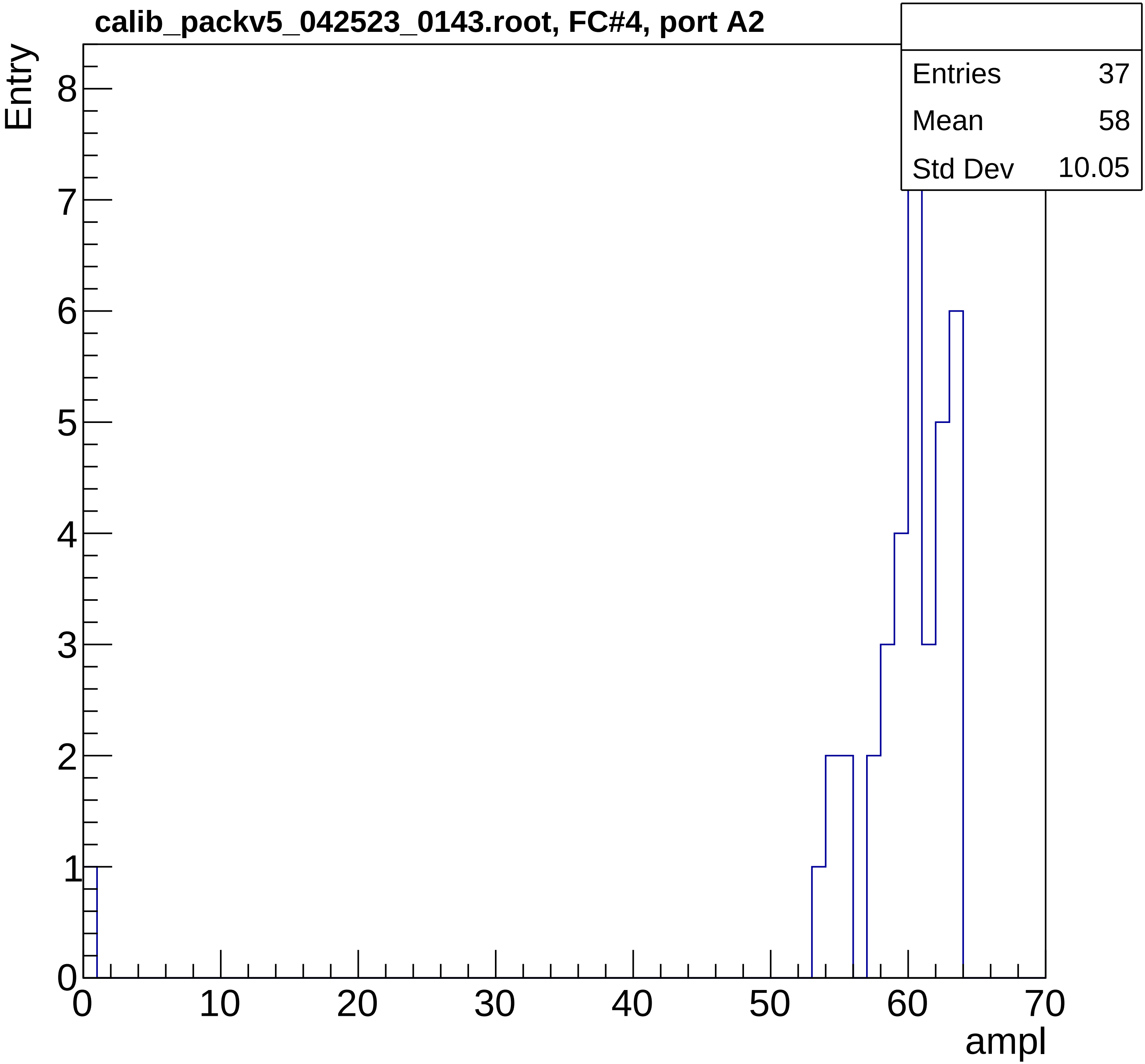

B1L100S, U21-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 65
Mean 35.52
Std Dev 3.192
Gaus mean : 36.1887
Gaus Width: 3.4143
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.49
Std Dev 3.295
Gaus mean : 43.0729
Gaus Width: 3.4946
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

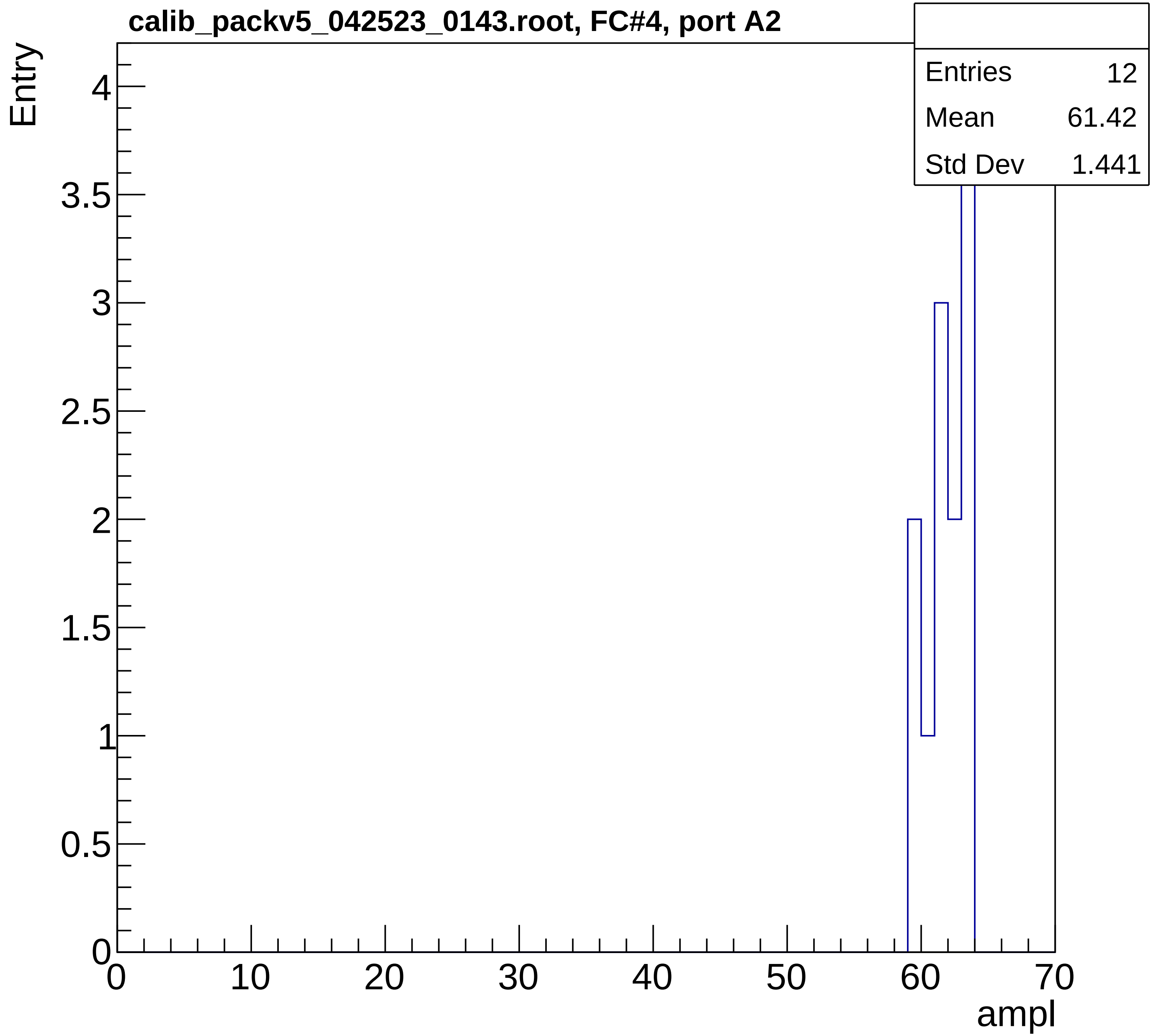

B1L100S, U21-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.39
Std Dev 3.684
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.41
Std Dev 3.566
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch94, adc5

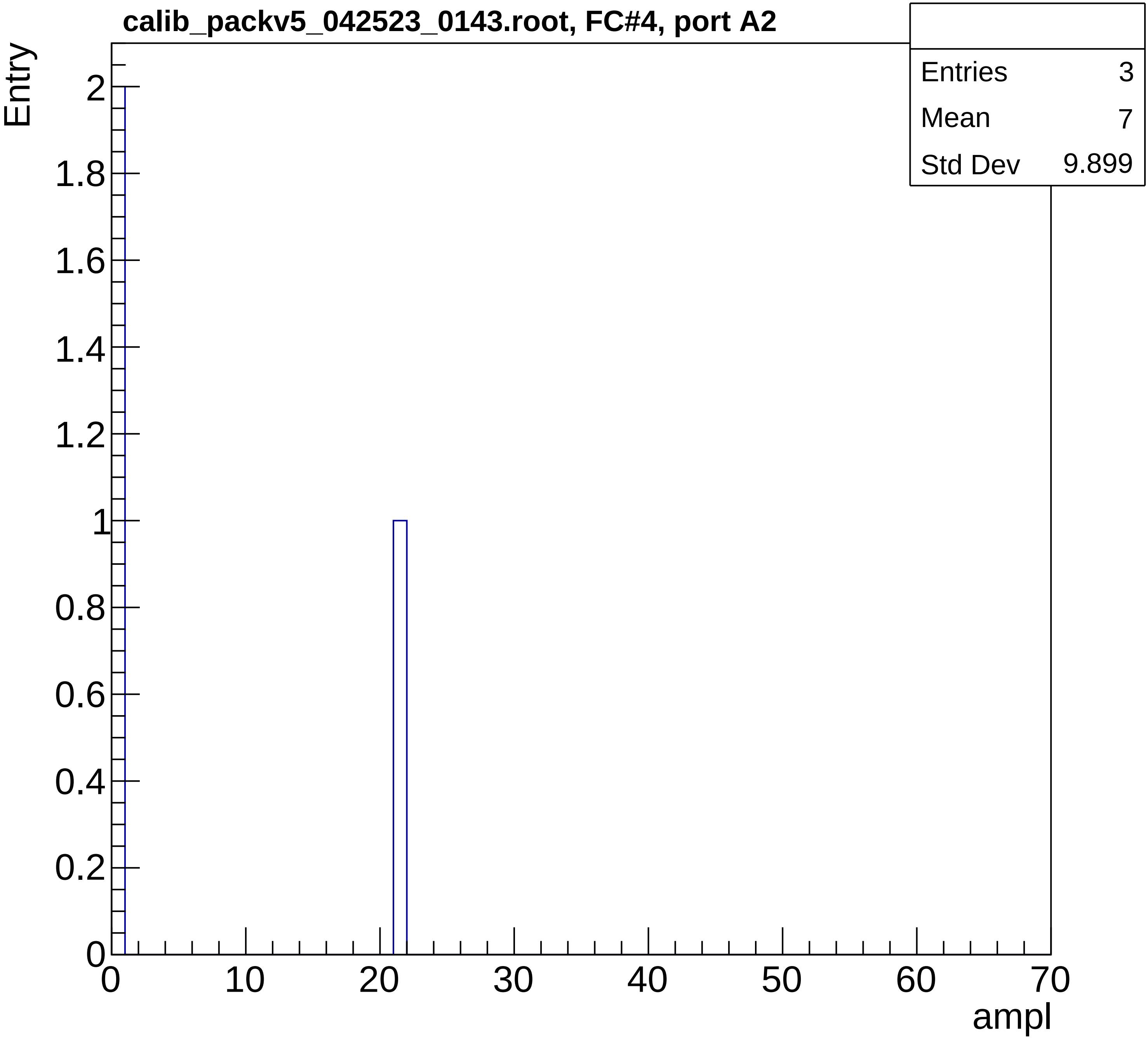


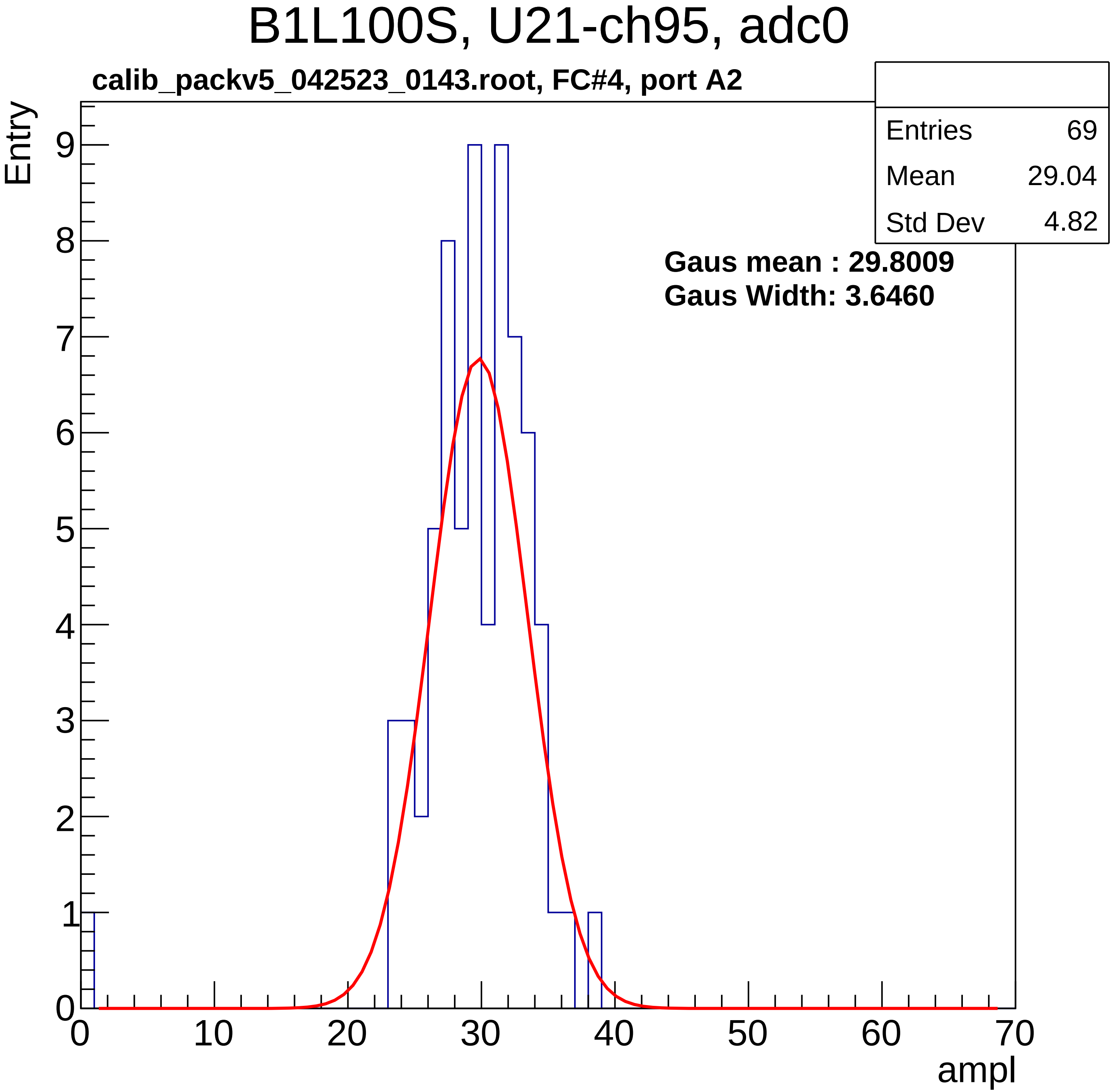
B1L100S, U21-ch94, adc6

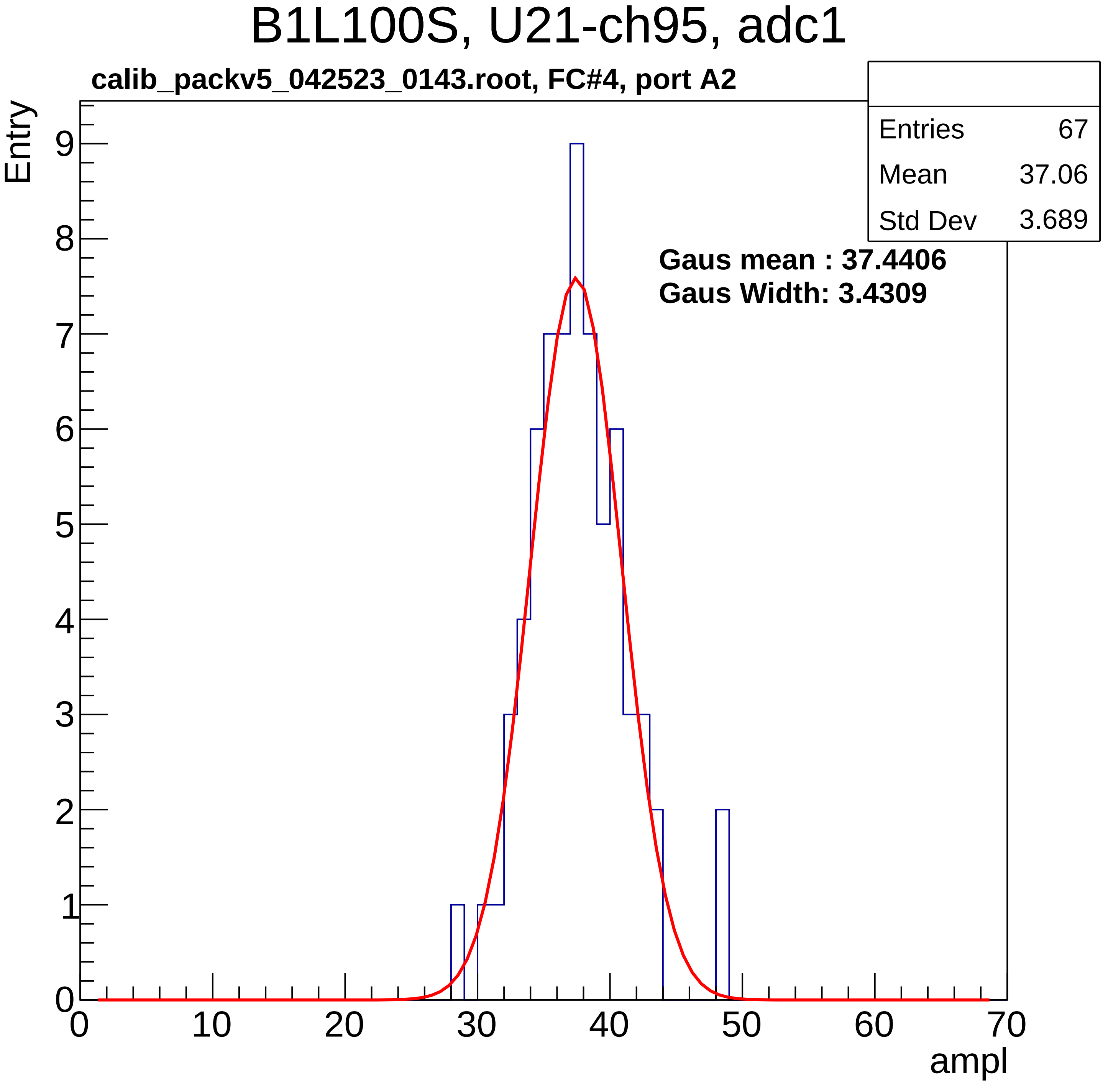


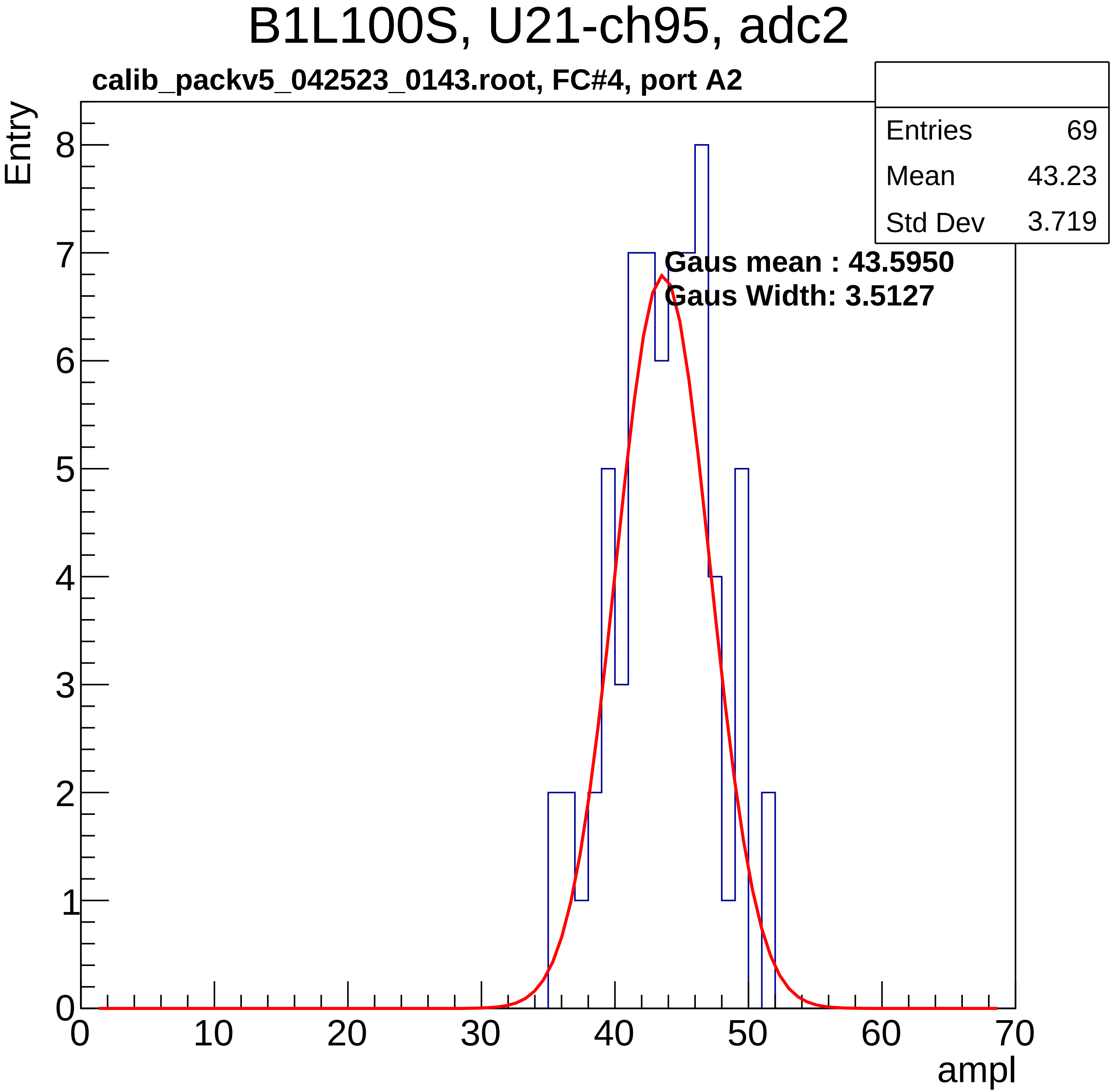
B1L100S, U21-ch94, adc7

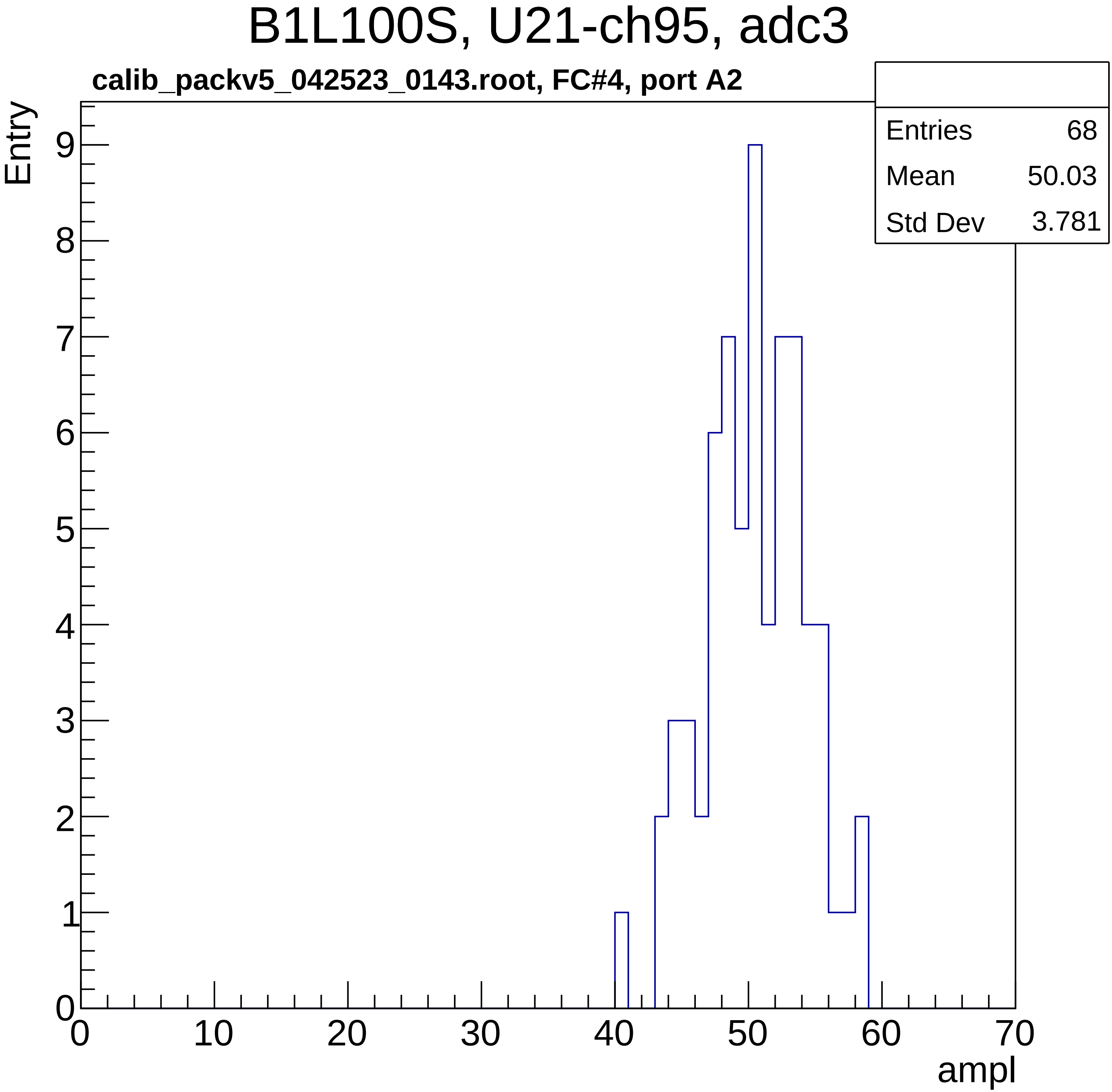
calib_packv5_042523_0143.root, FC#4, port A2



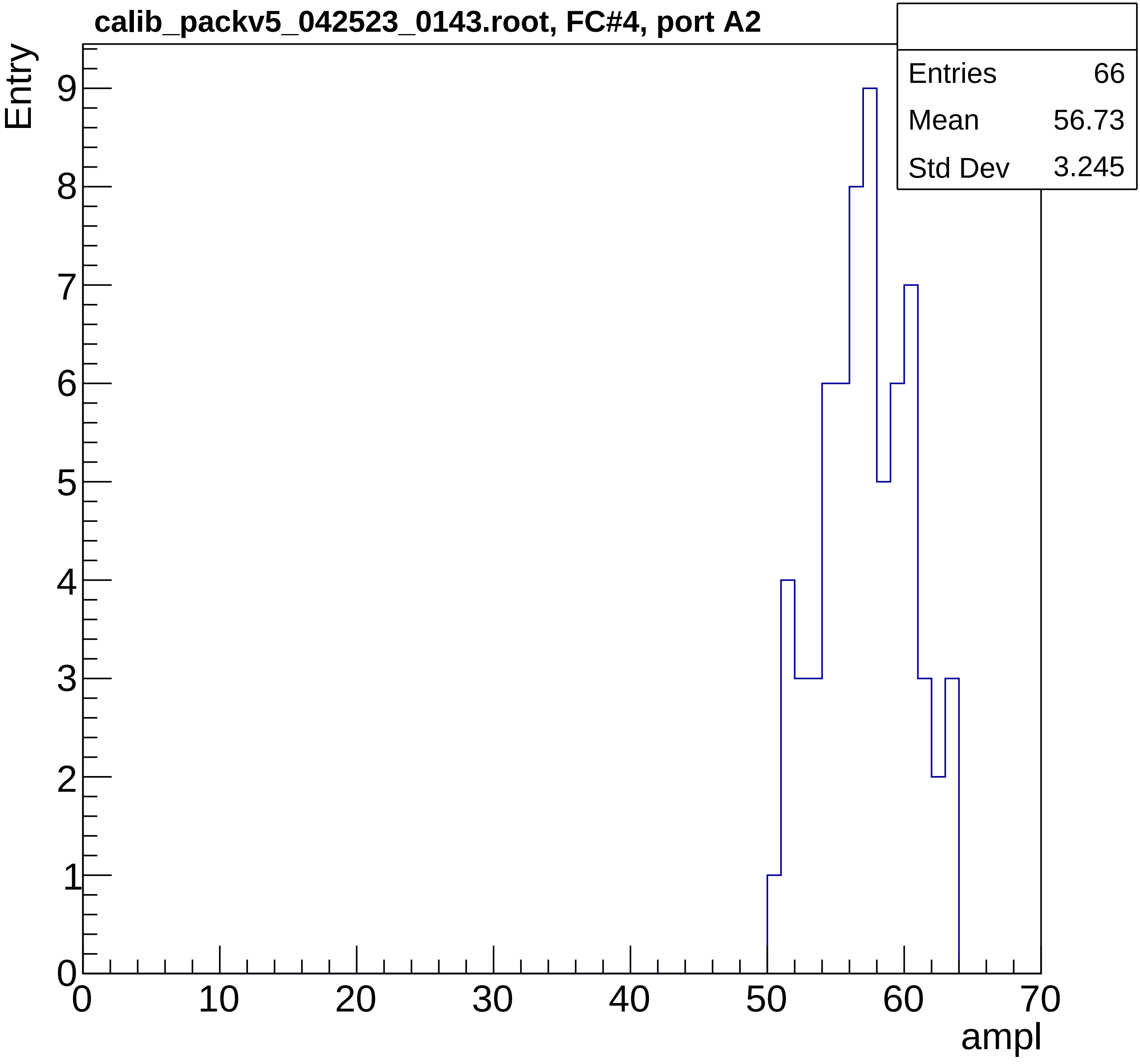

B1L100S, U21-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 69
Mean 29.04
Std Dev 4.82
Gaus mean : 29.8009
Gaus Width: 3.6460
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

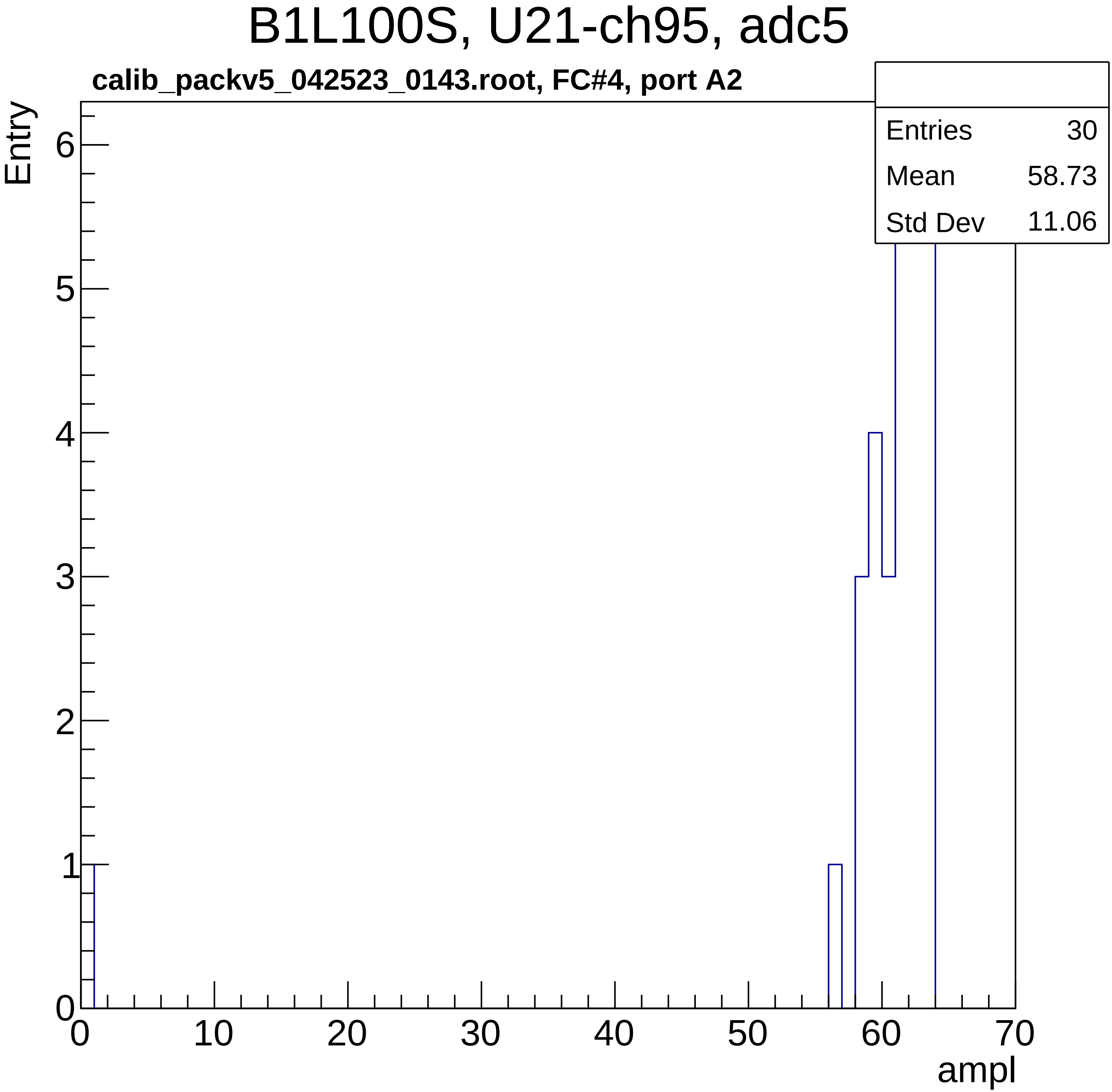

B1L100S, U21-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.06
Std Dev 3.689
Gaus mean : 37.4406
Gaus Width: 3.4309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

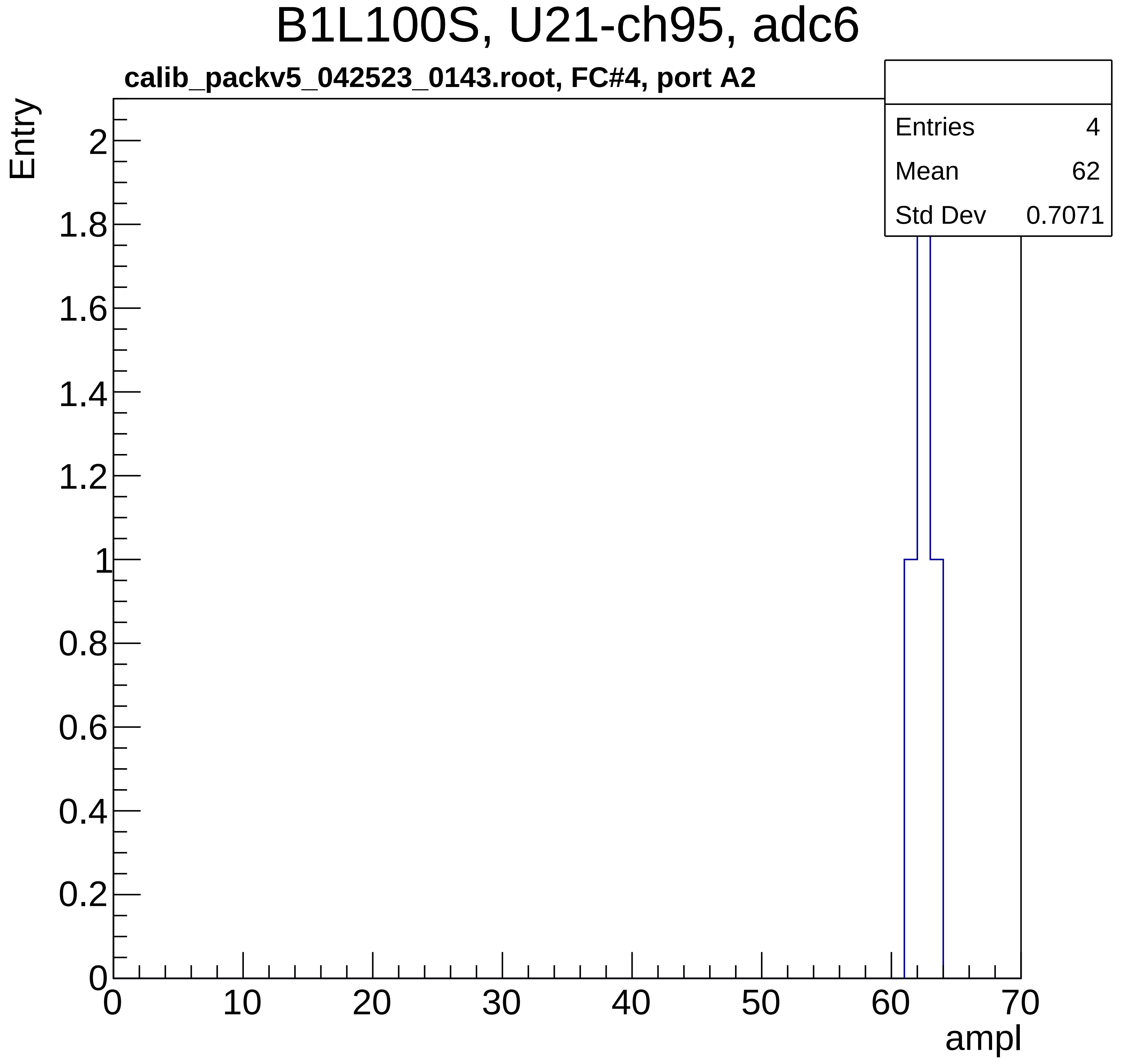

B1L100S, U21-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.23
Std Dev 3.719
Gaus mean : 43.5950
Gaus Width: 3.5127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.03
Std Dev 3.781
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

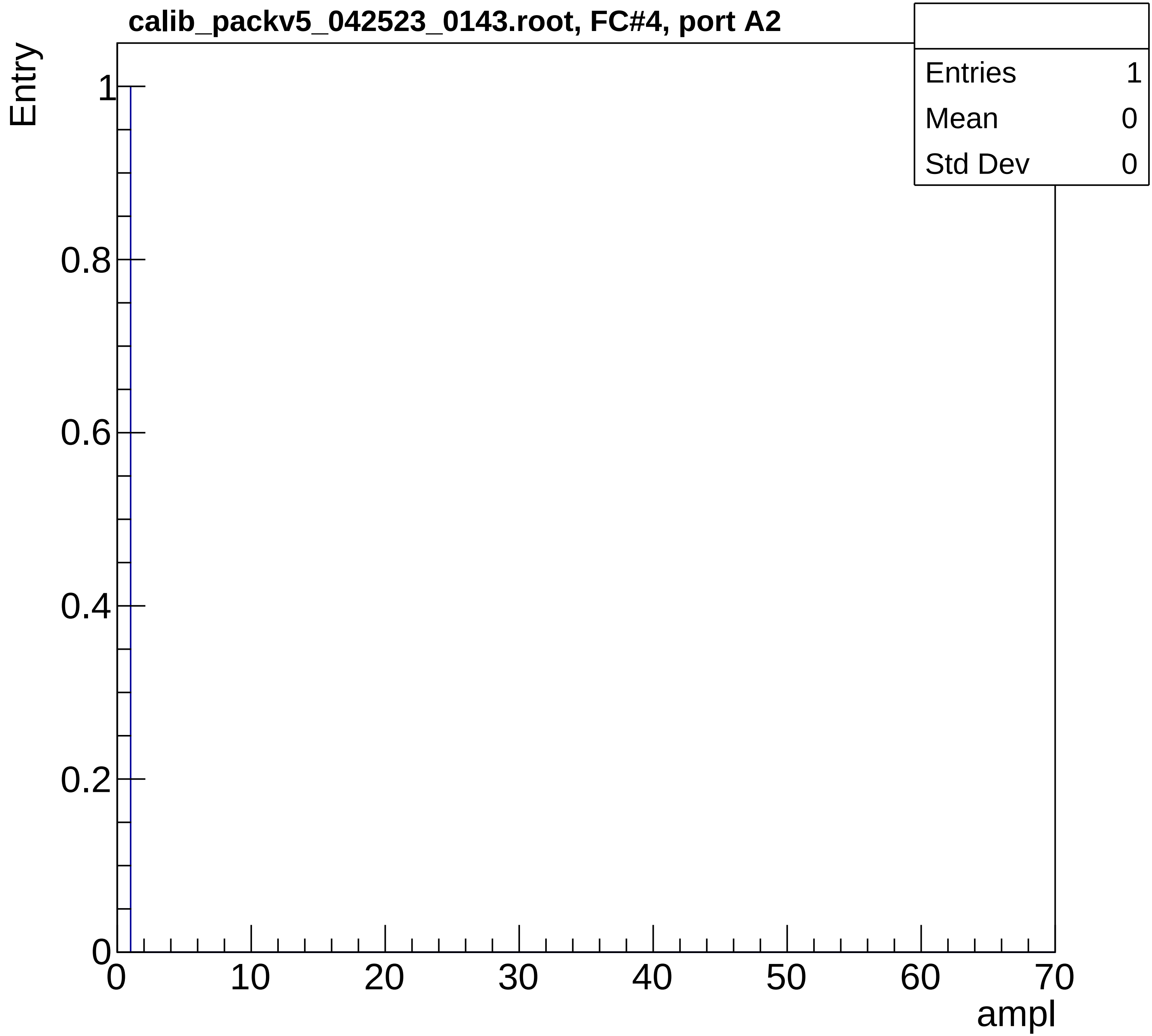
B1L100S, U21-ch95, adc4

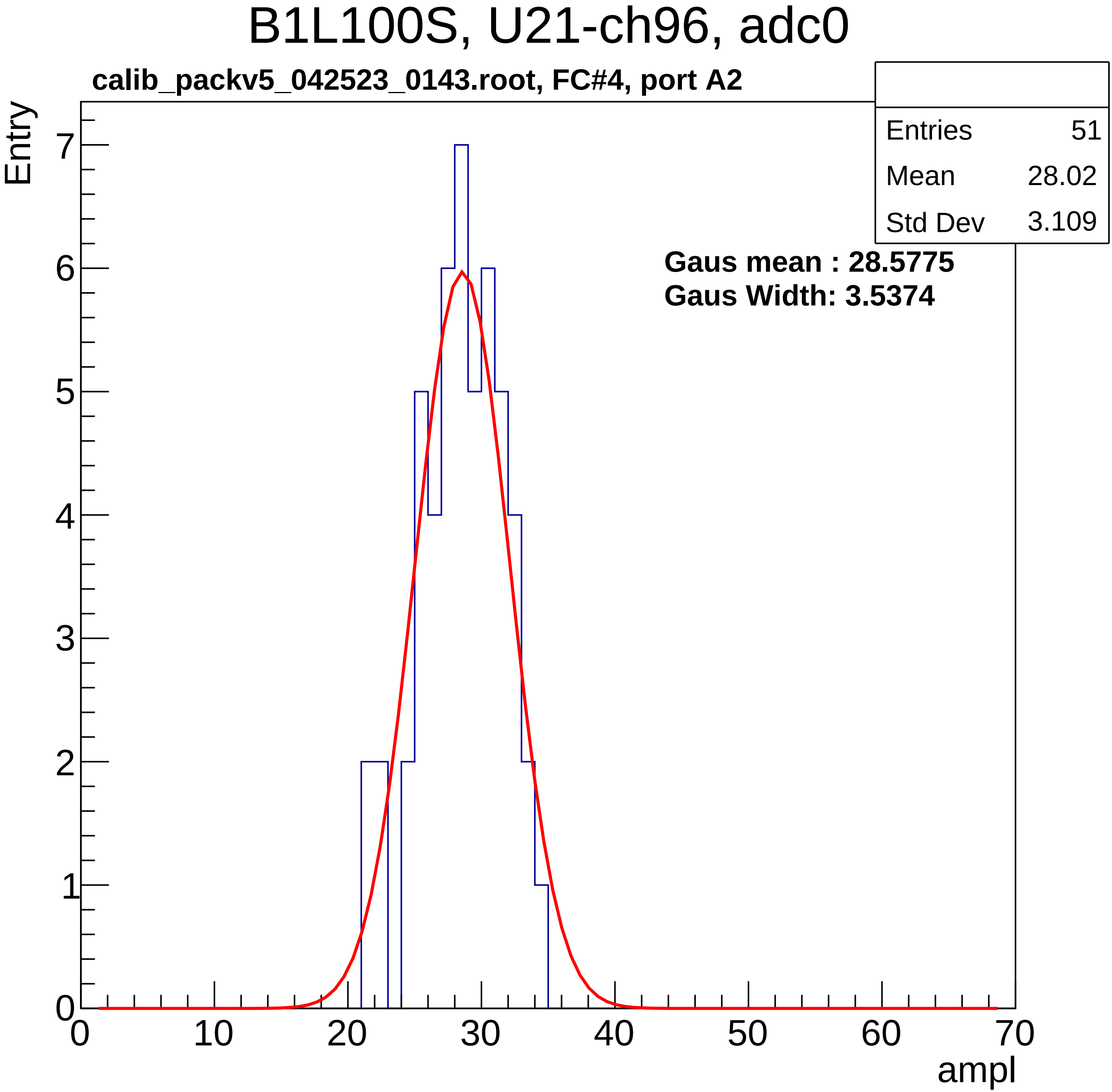


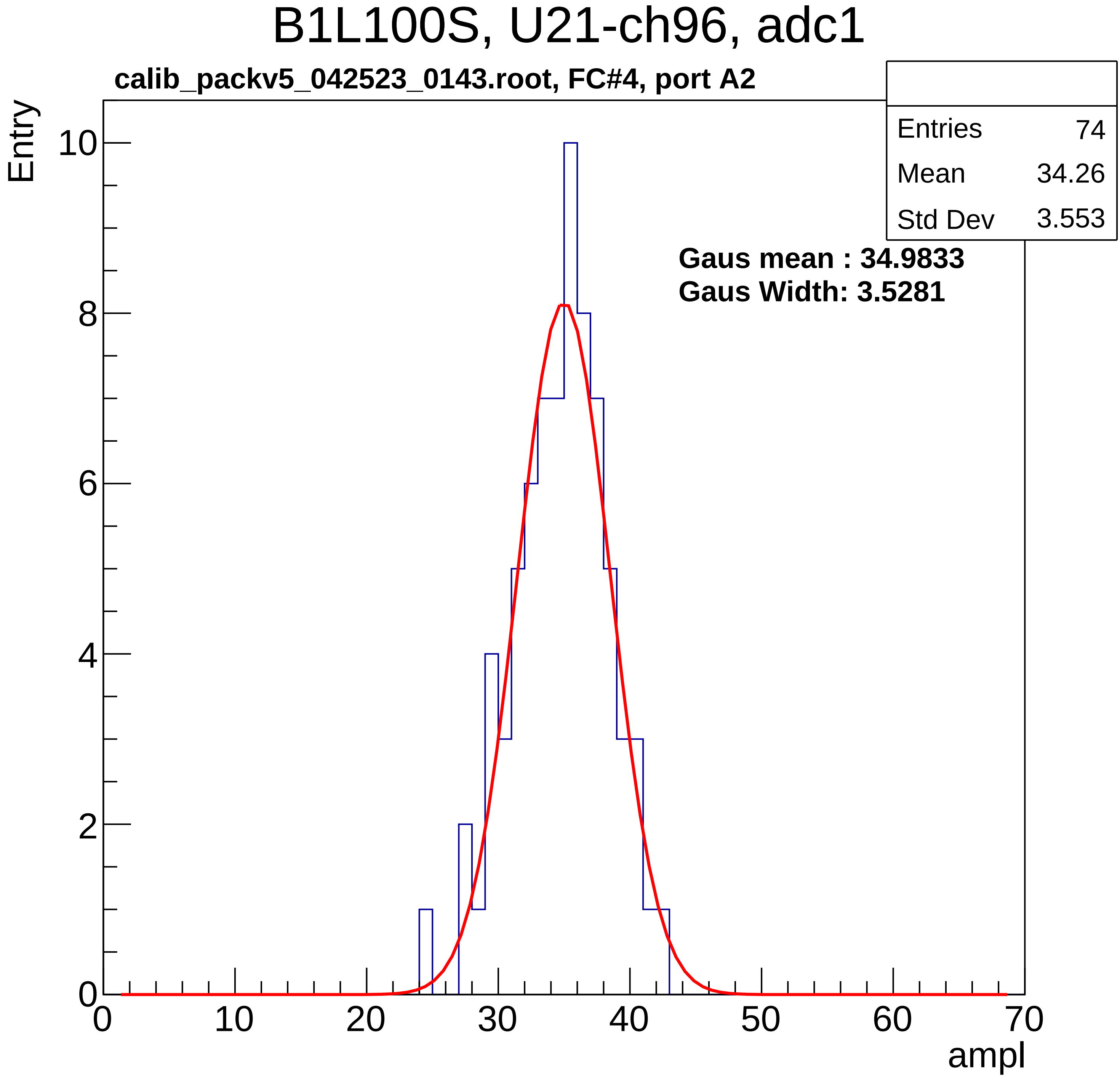

B1L100S, U21-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 30
Mean 58.73
Std Dev 11.06
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

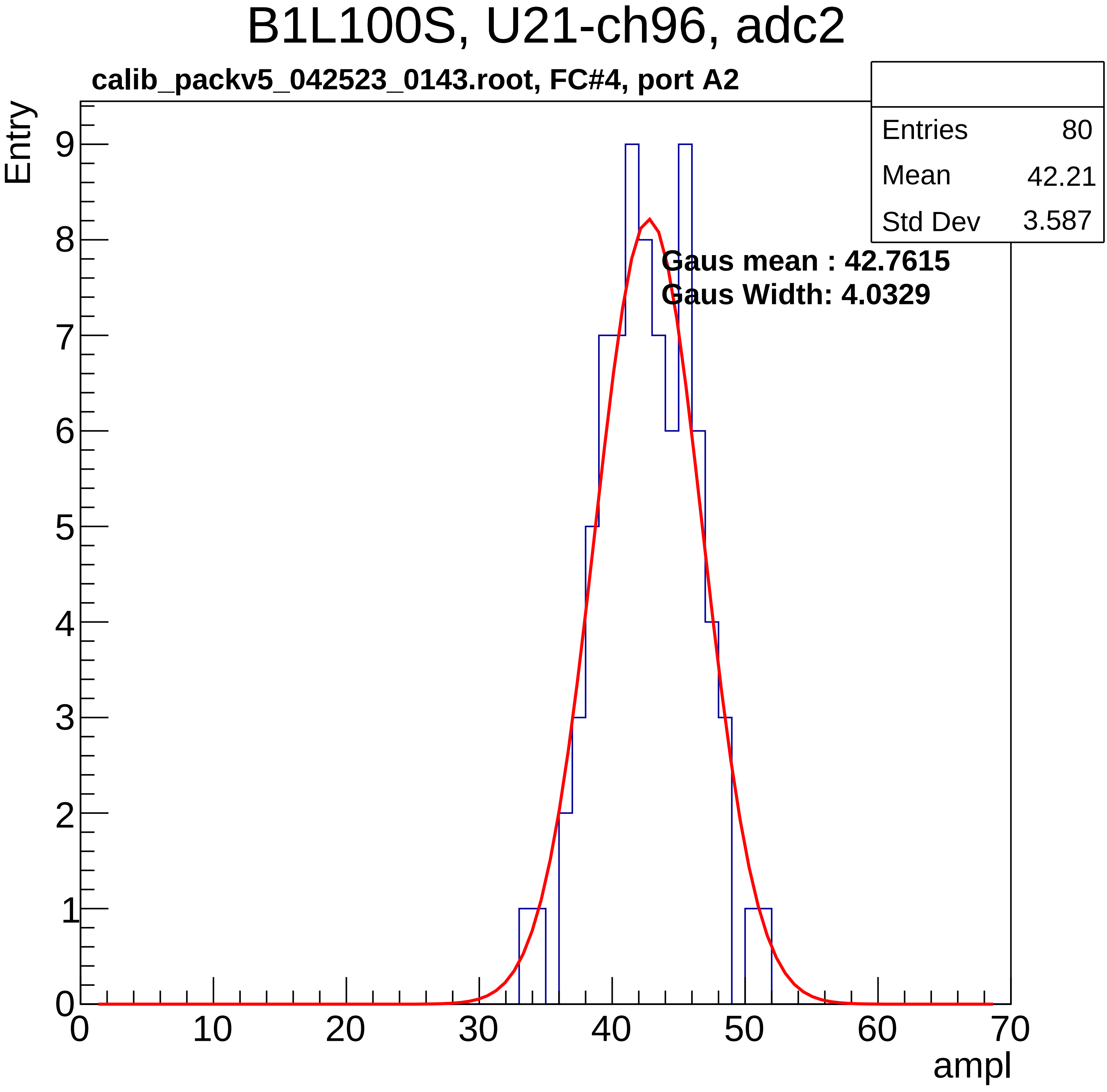

B1L100S, U21-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 4
Mean 62
Std Dev 0.7071
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

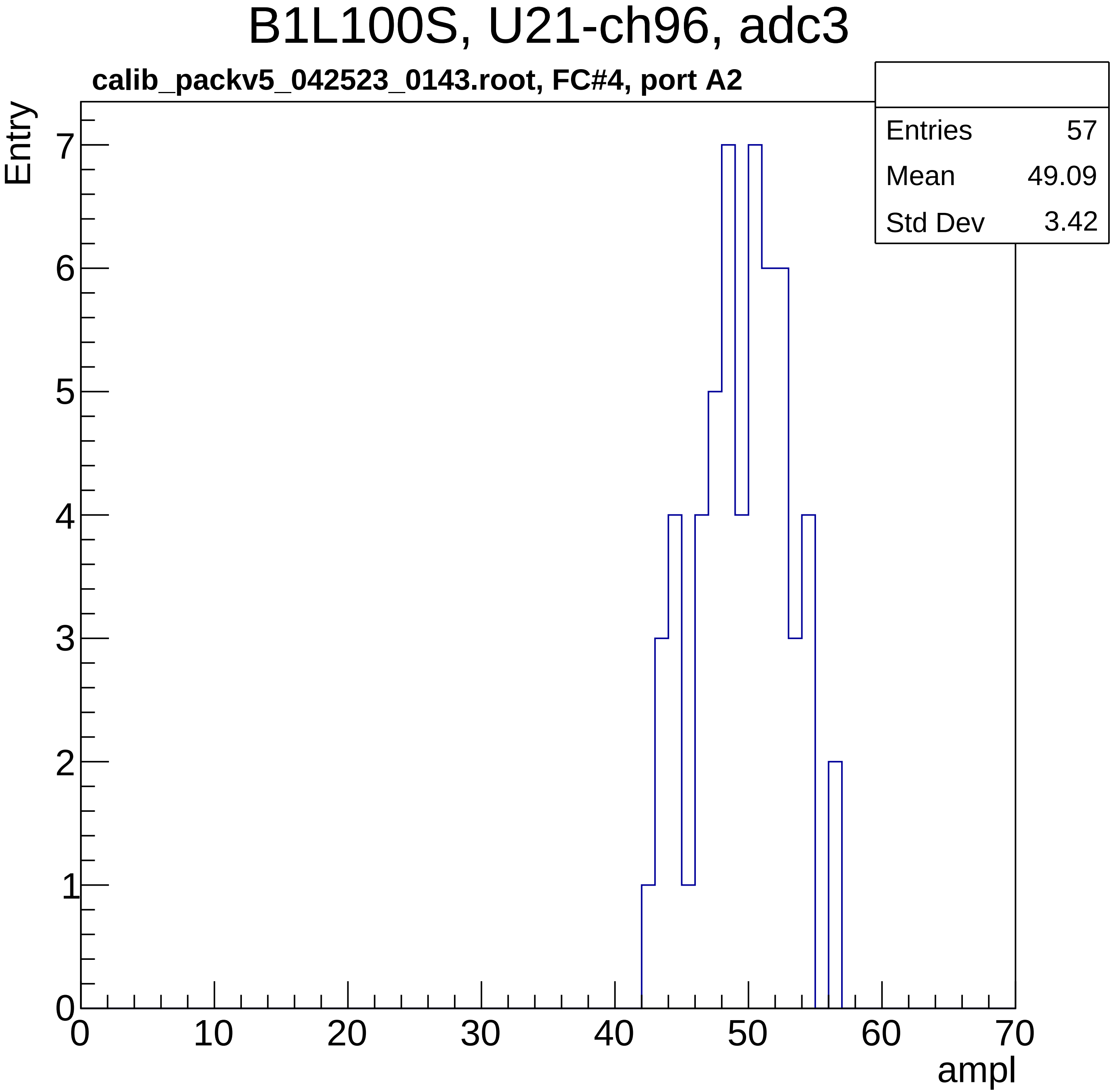
B1L100S, U21-ch95, adc7

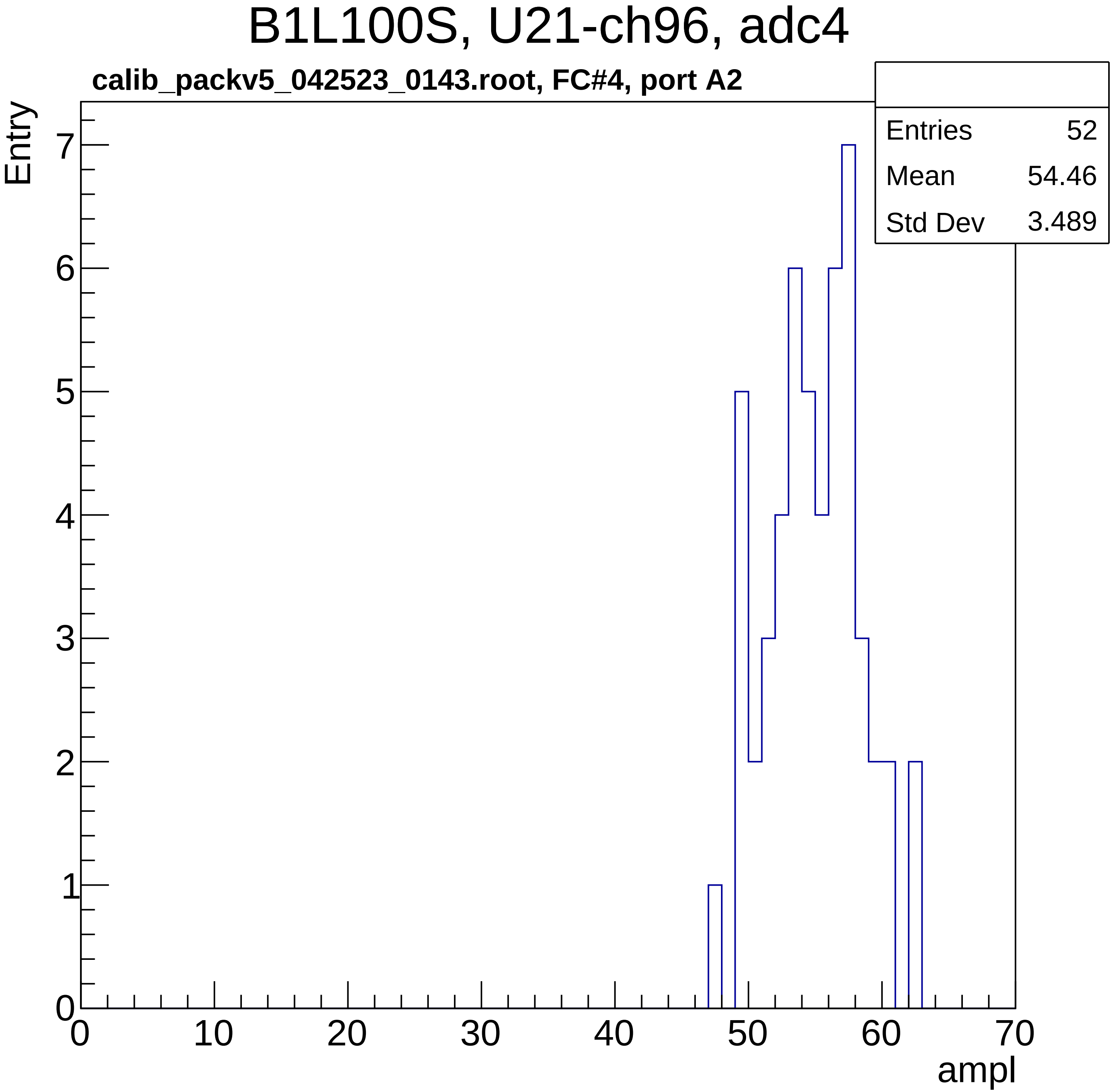


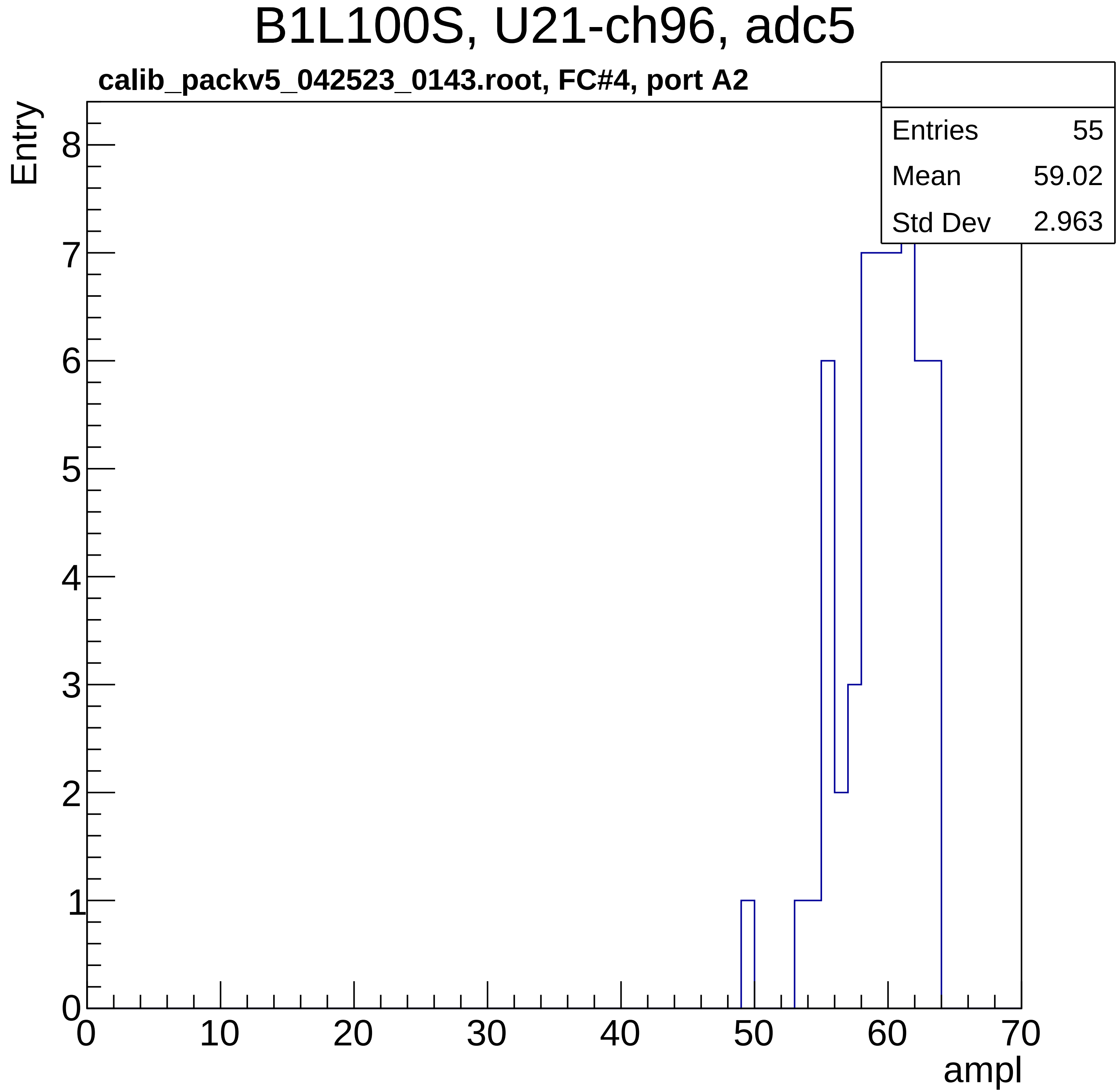

B1L100S, U21-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 28.02
Std Dev 3.109
Gaus mean : 28.5775
Gaus Width: 3.5374
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

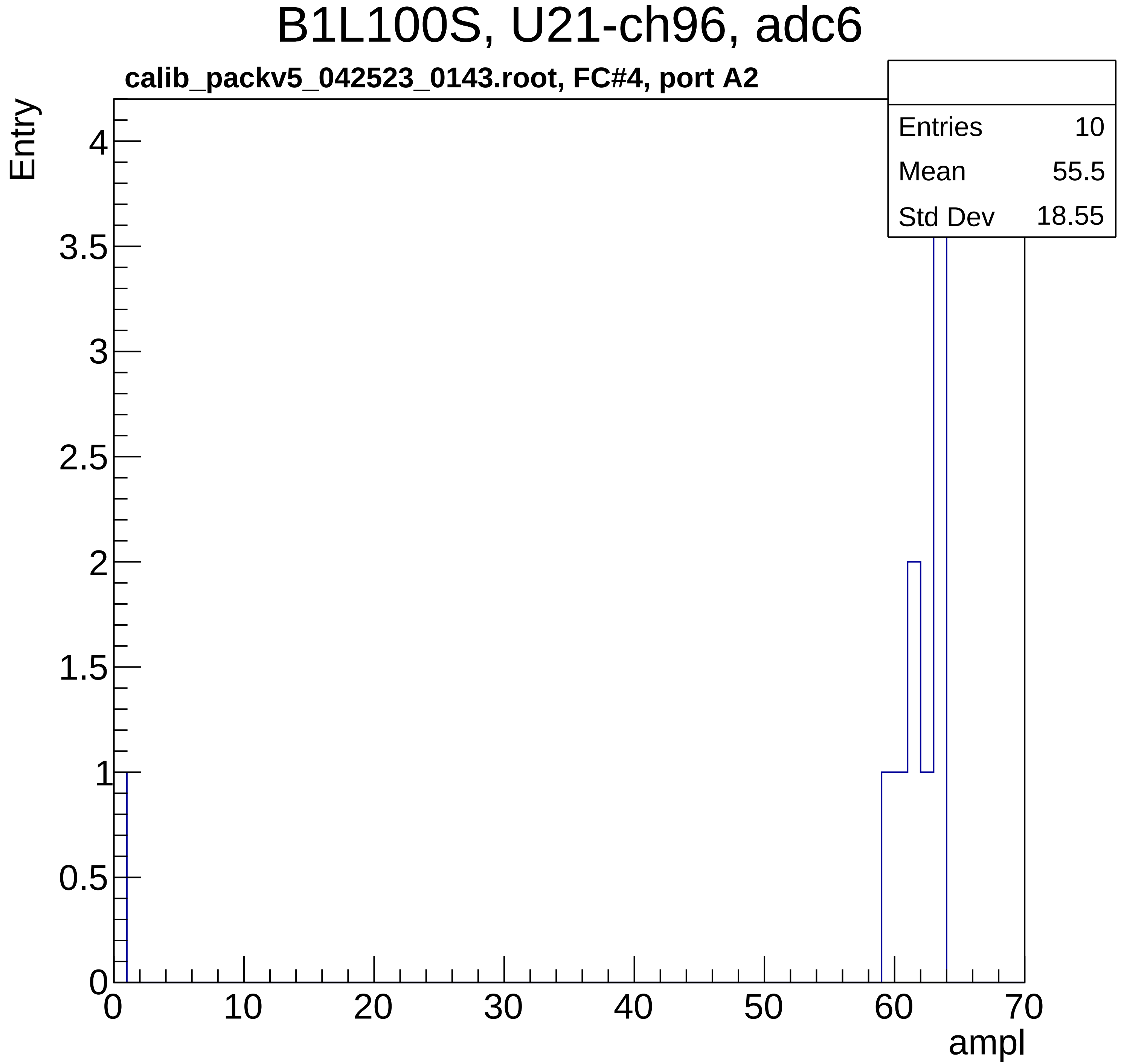

B1L100S, U21-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 74
Mean 34.26
Std Dev 3.553
Gaus mean : 34.9833
Gaus Width: 3.5281
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 80
Mean 42.21
Std Dev 3.587
Gaus mean : 42.7615
Gaus Width: 4.0329
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.09
Std Dev 3.42
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.46
Std Dev 3.489
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

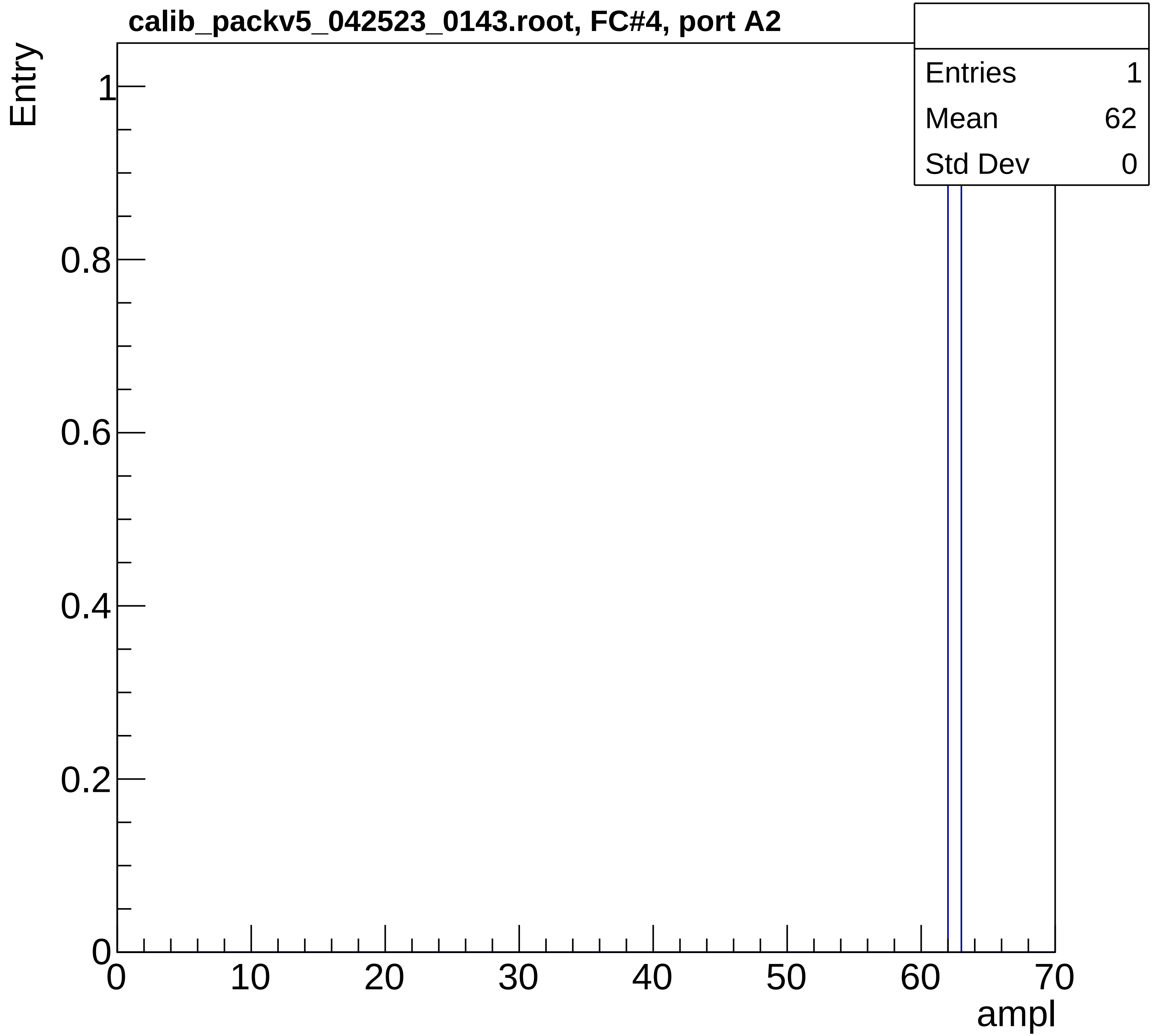

B1L100S, U21-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 55
Mean 59.02
Std Dev 2.963
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

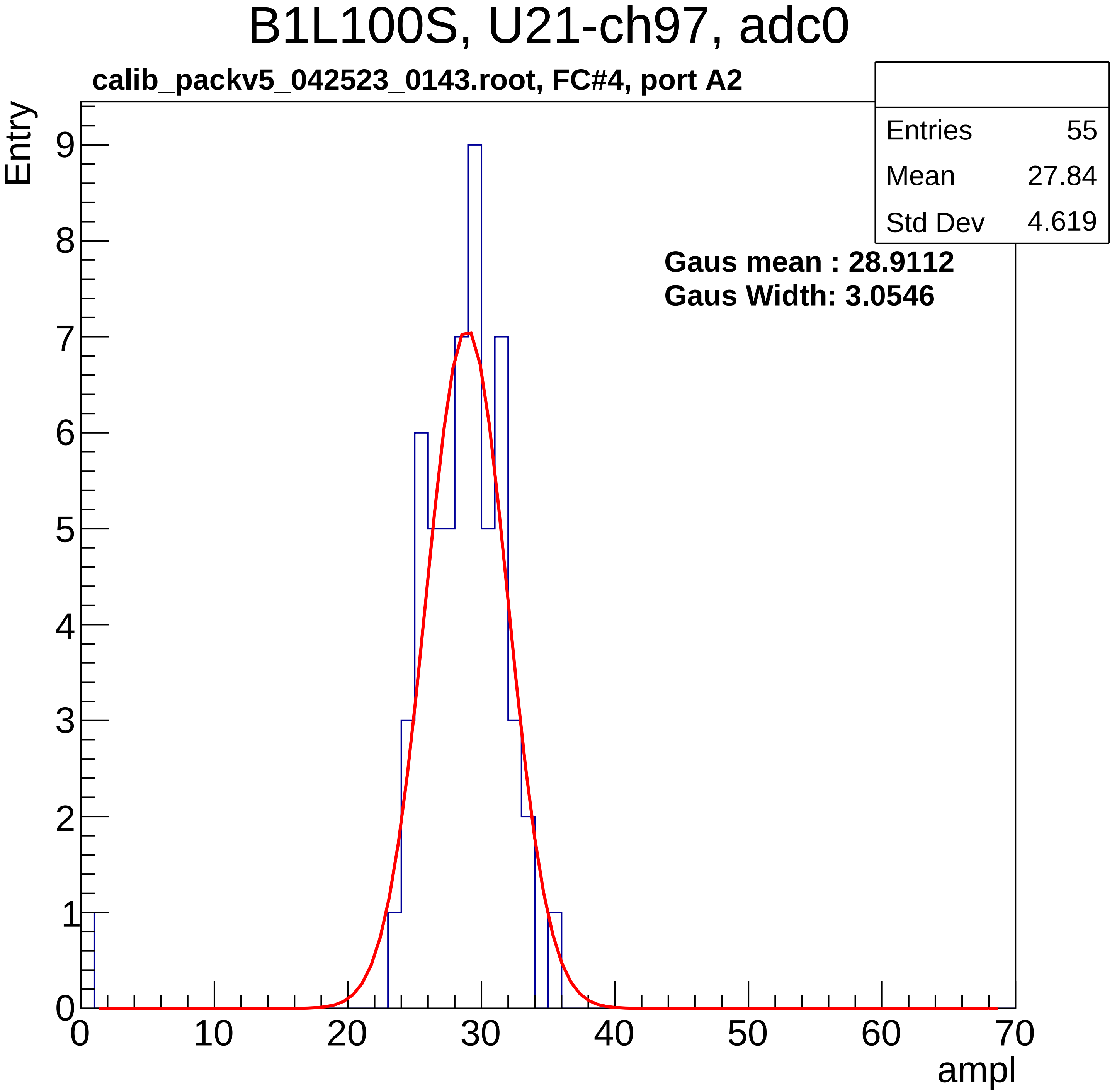


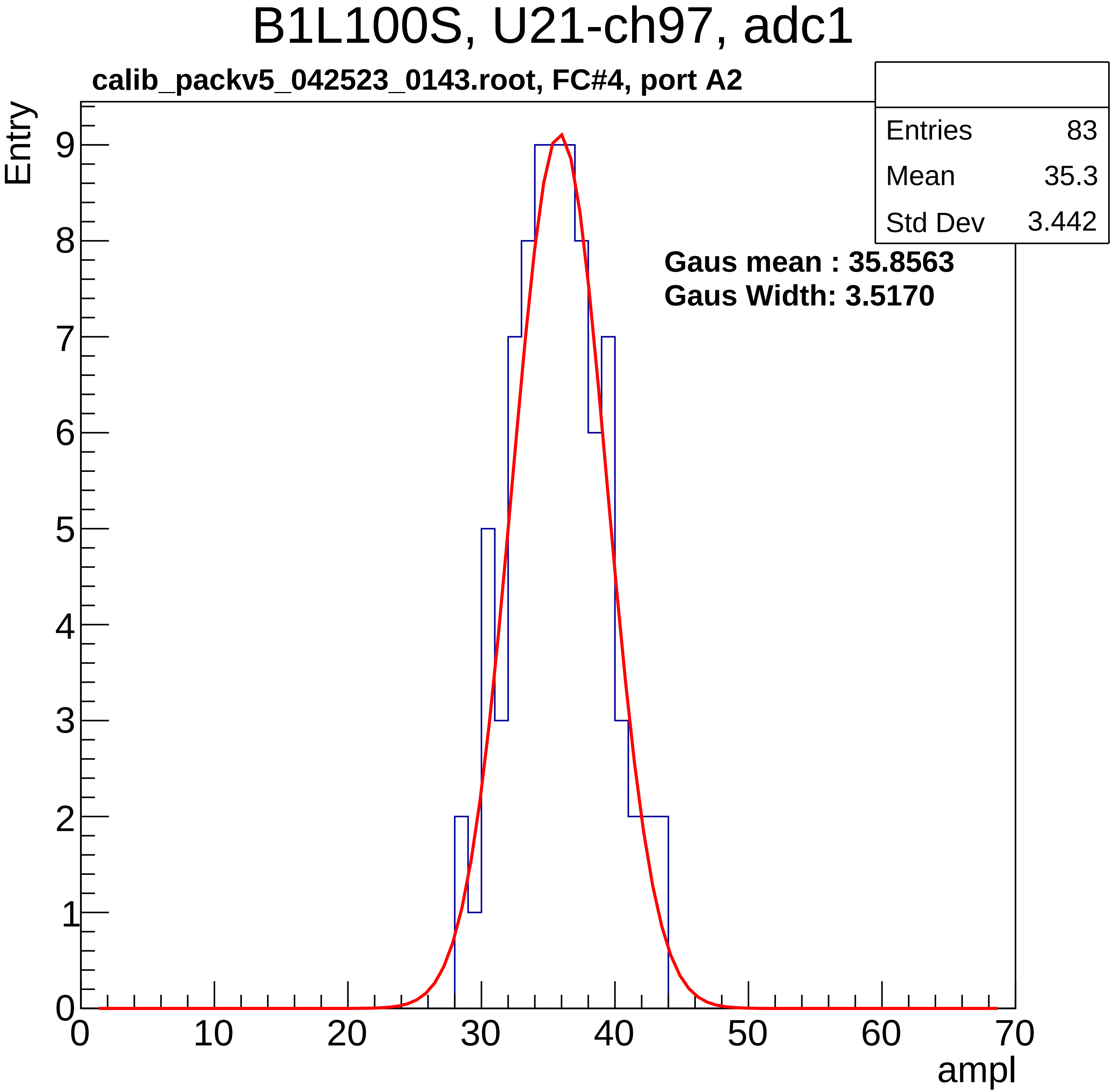
B1L100S, U21-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 10
Mean 55.5
Std Dev 18.55
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

B1L100S, U21-ch96, adc7

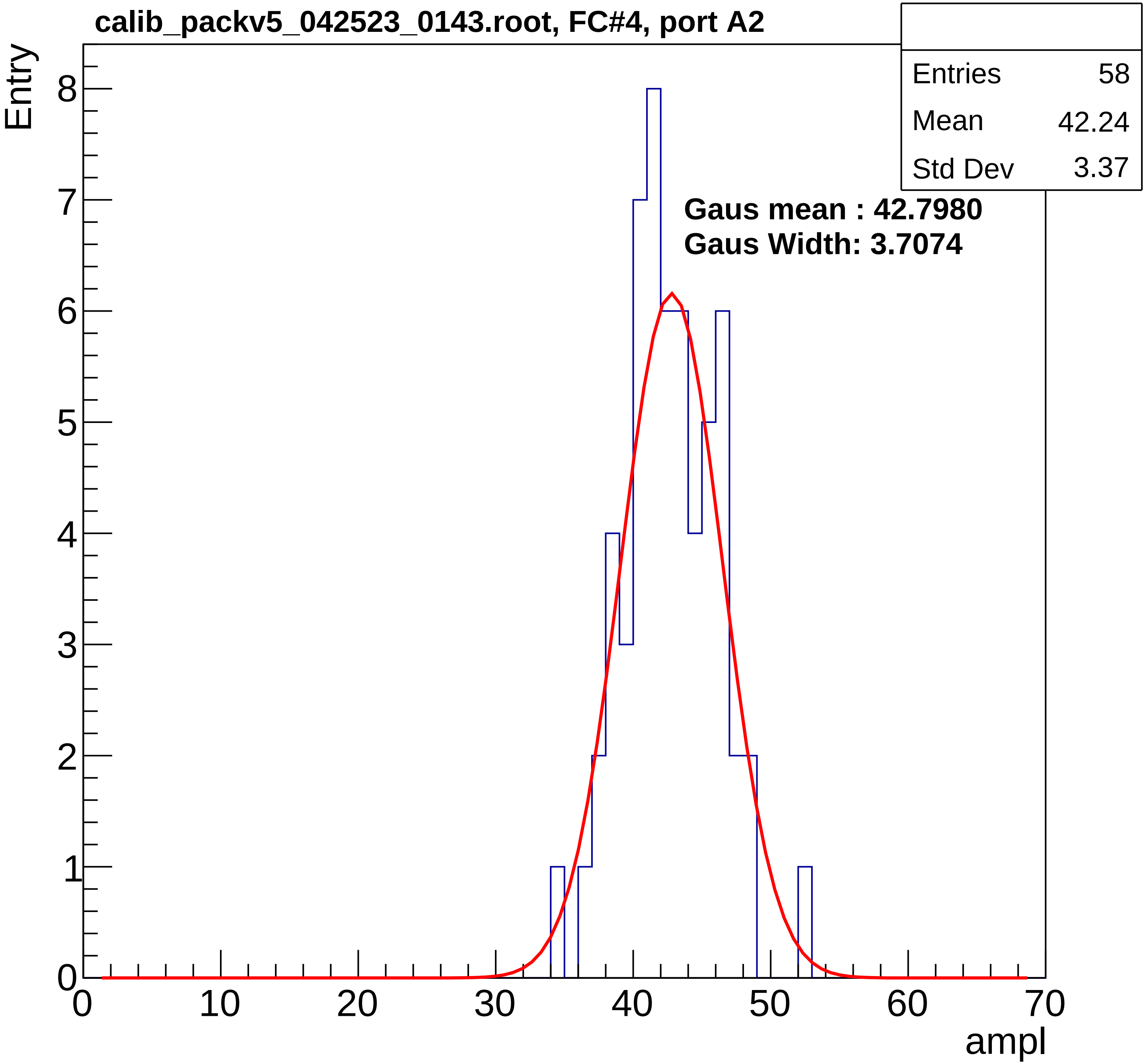
calib_packv5_042523_0143.root, FC#4, port A2

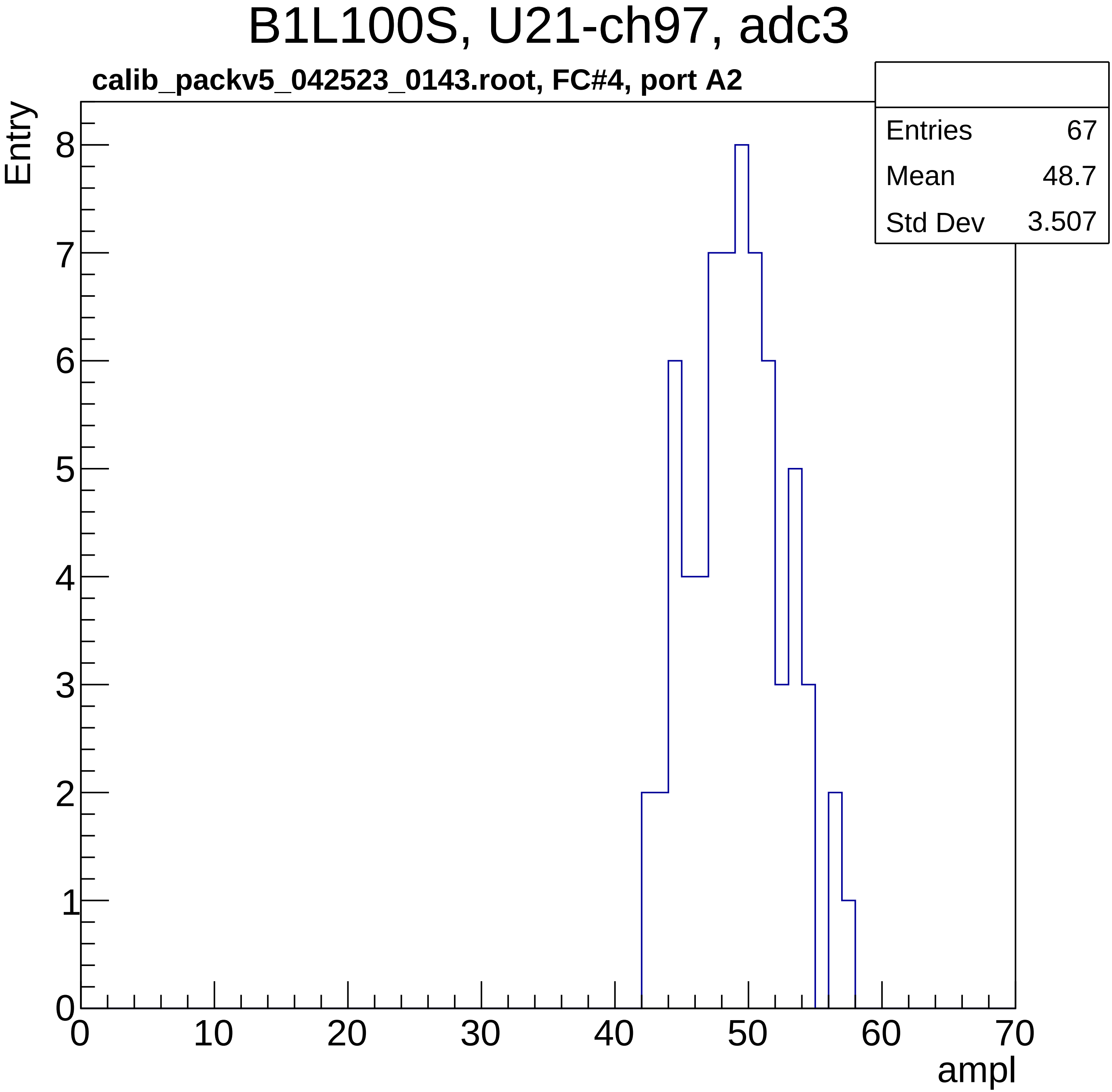



B1L100S, U21-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 55
Mean 27.84
Std Dev 4.619
Gaus mean : 28.9112
Gaus Width: 3.0546
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 35.3
Std Dev 3.442
Gaus mean : 35.8563
Gaus Width: 3.5170
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

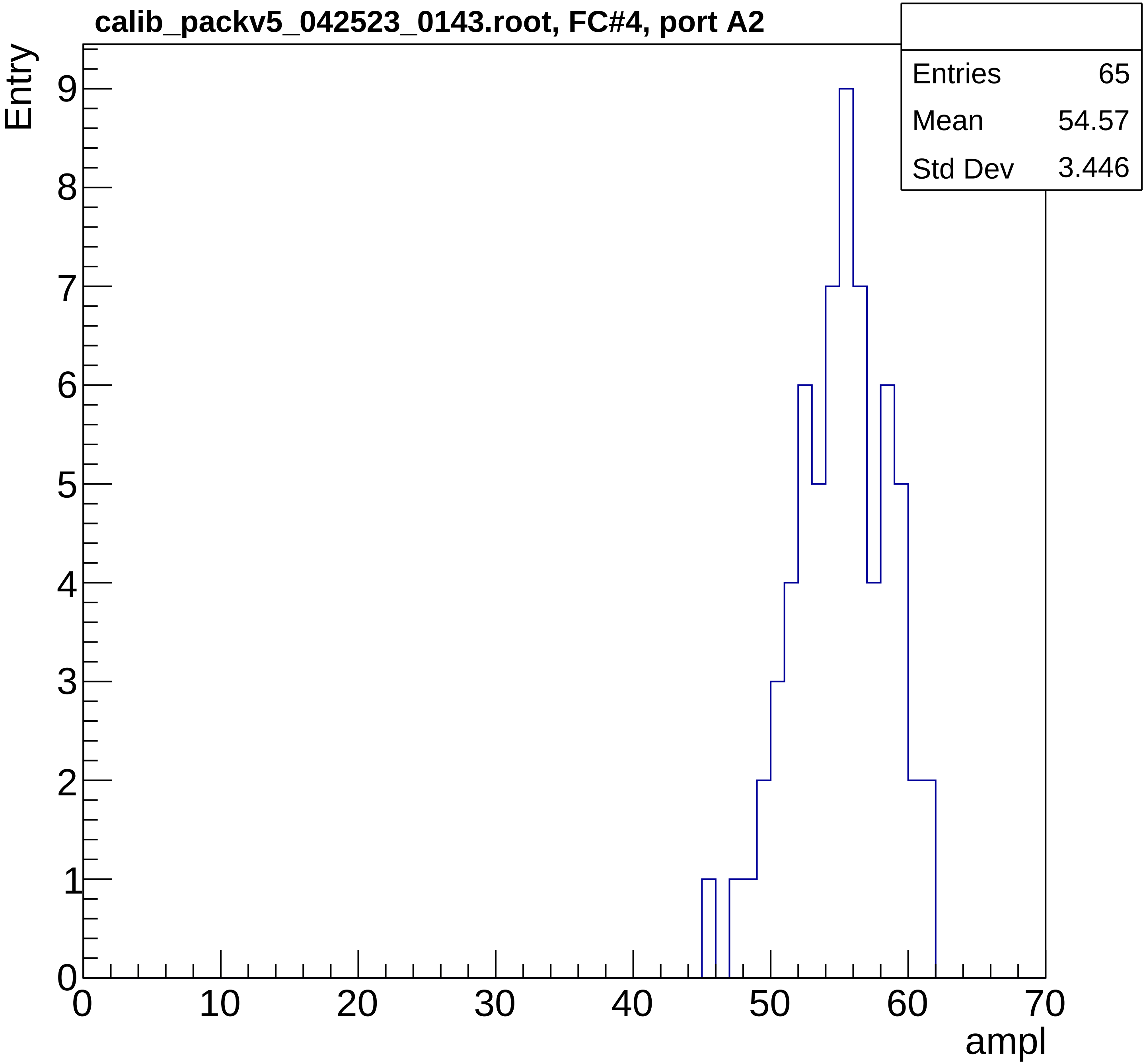
B1L100S, U21-ch97, adc2

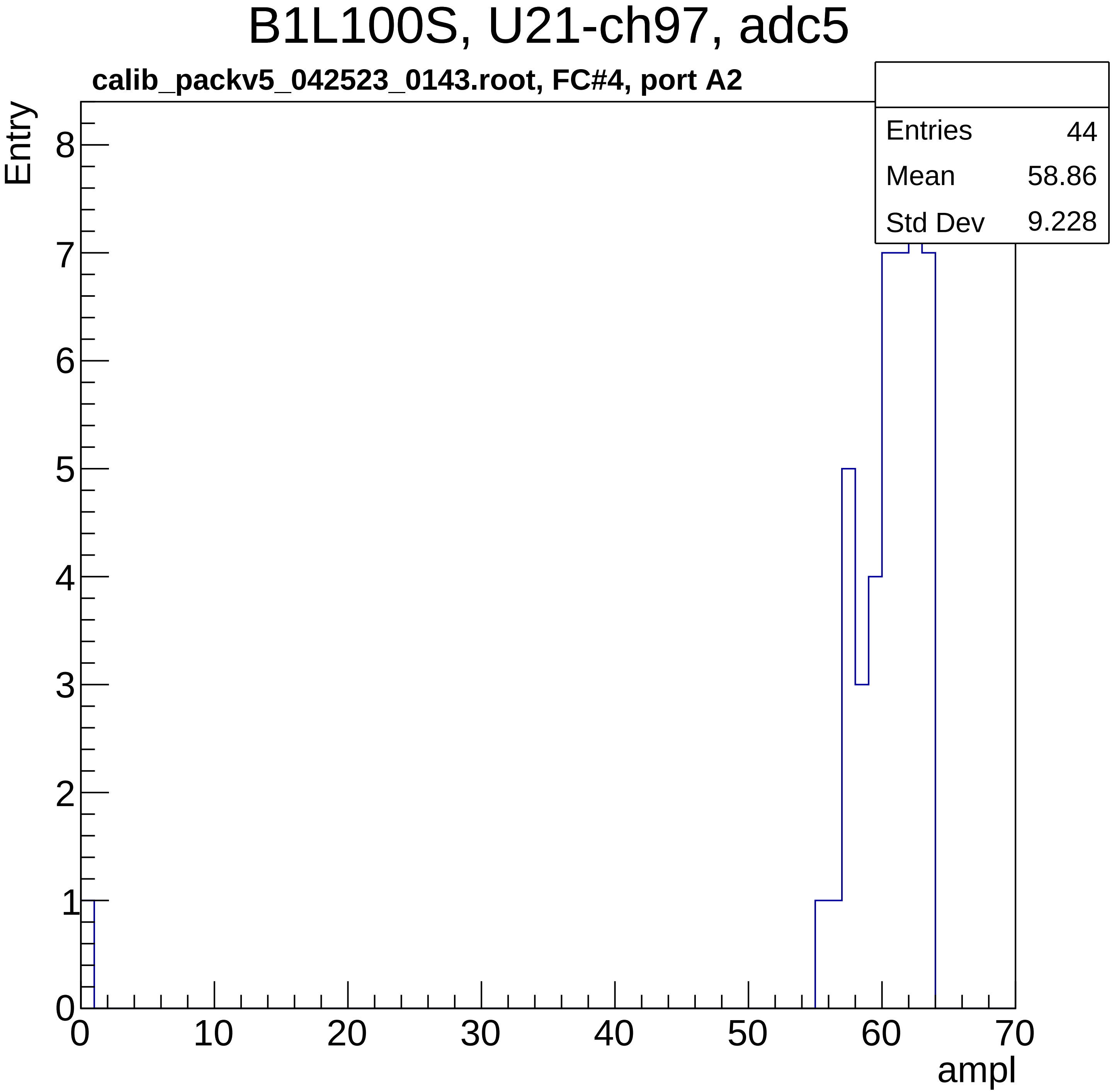


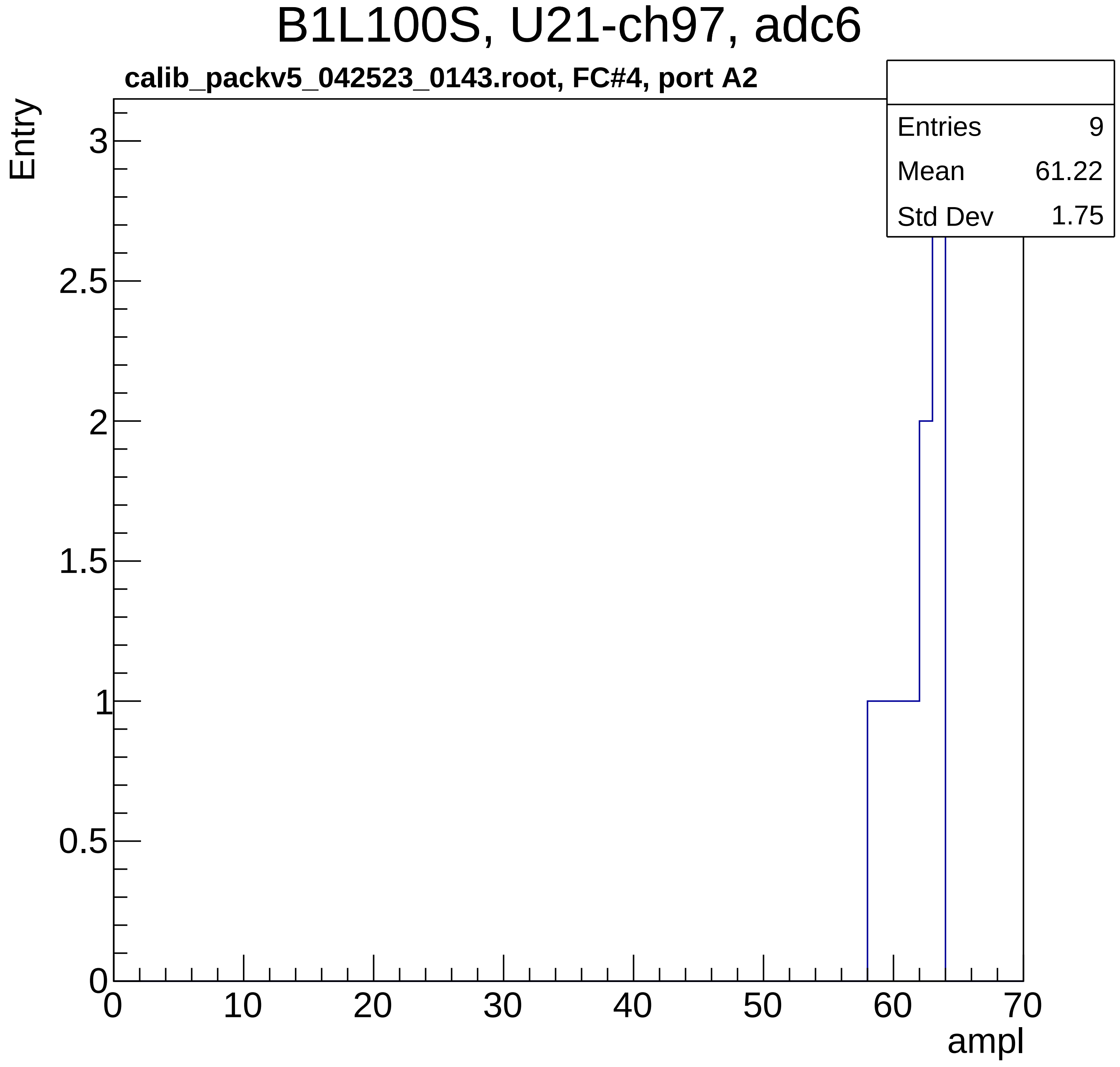

B1L100S, U21-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 48.7
Std Dev 3.507
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

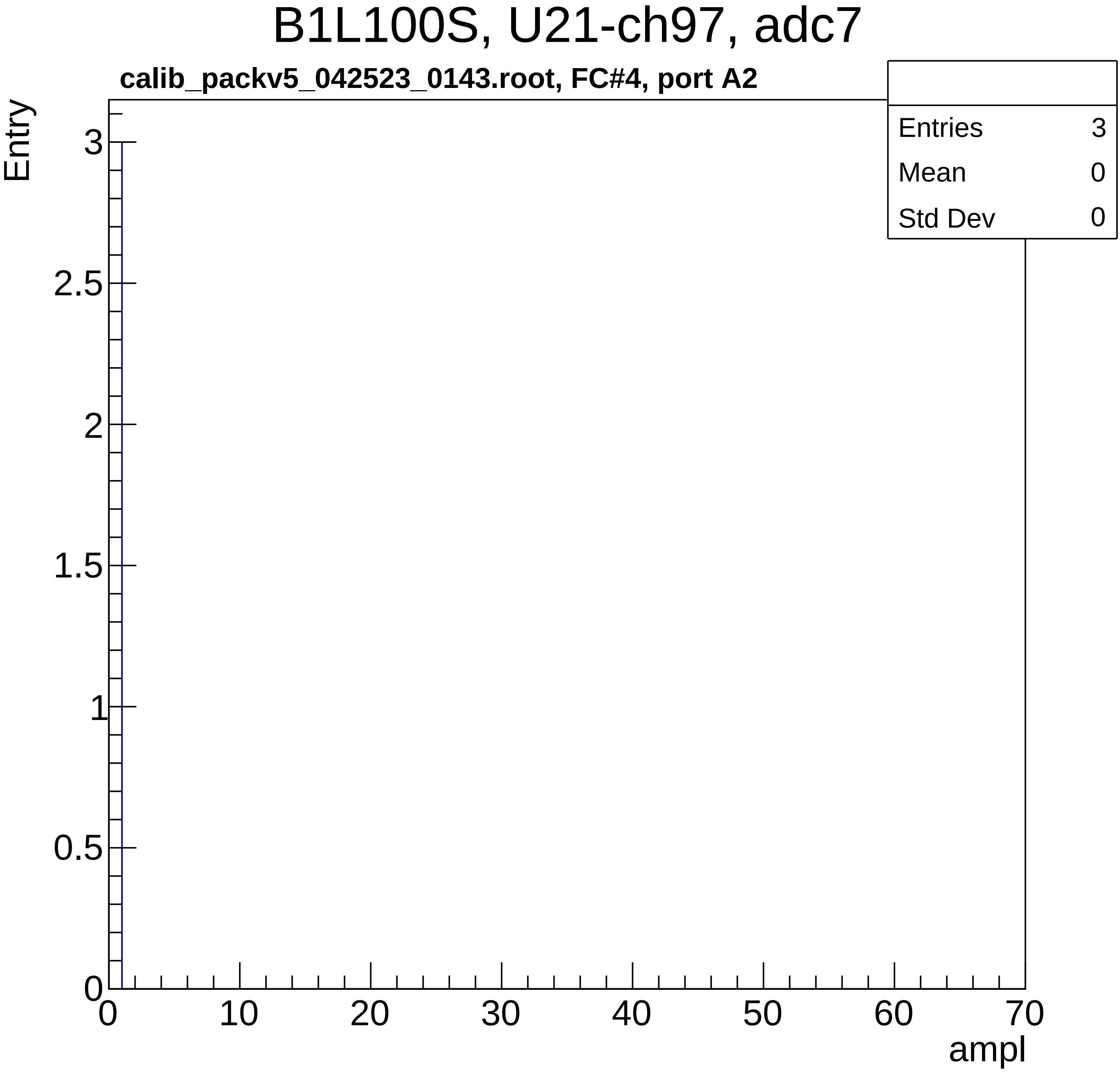
B1L100S, U21-ch97, adc4

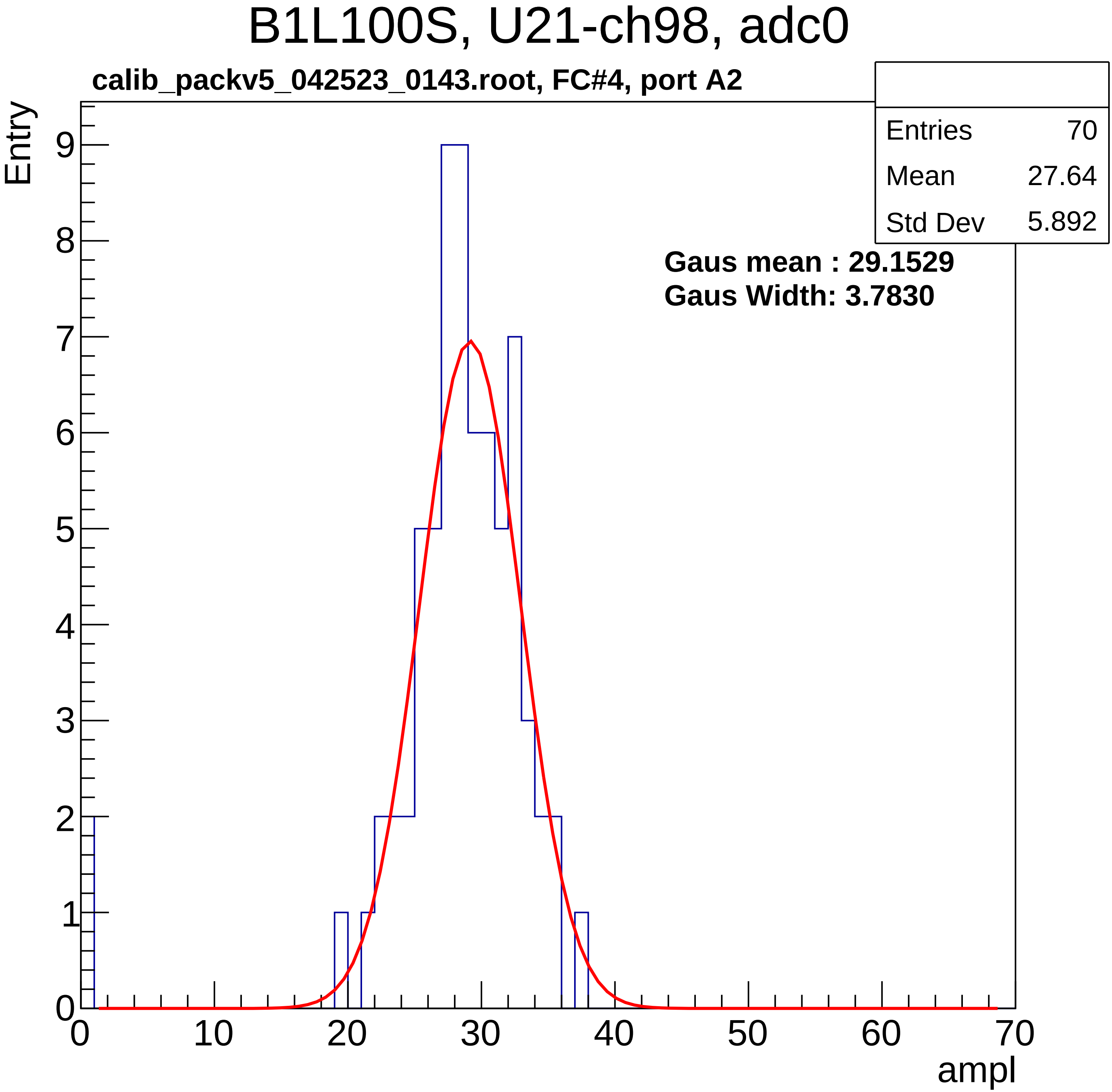
calib_packv5_042523_0143.root, FC#4, port A2

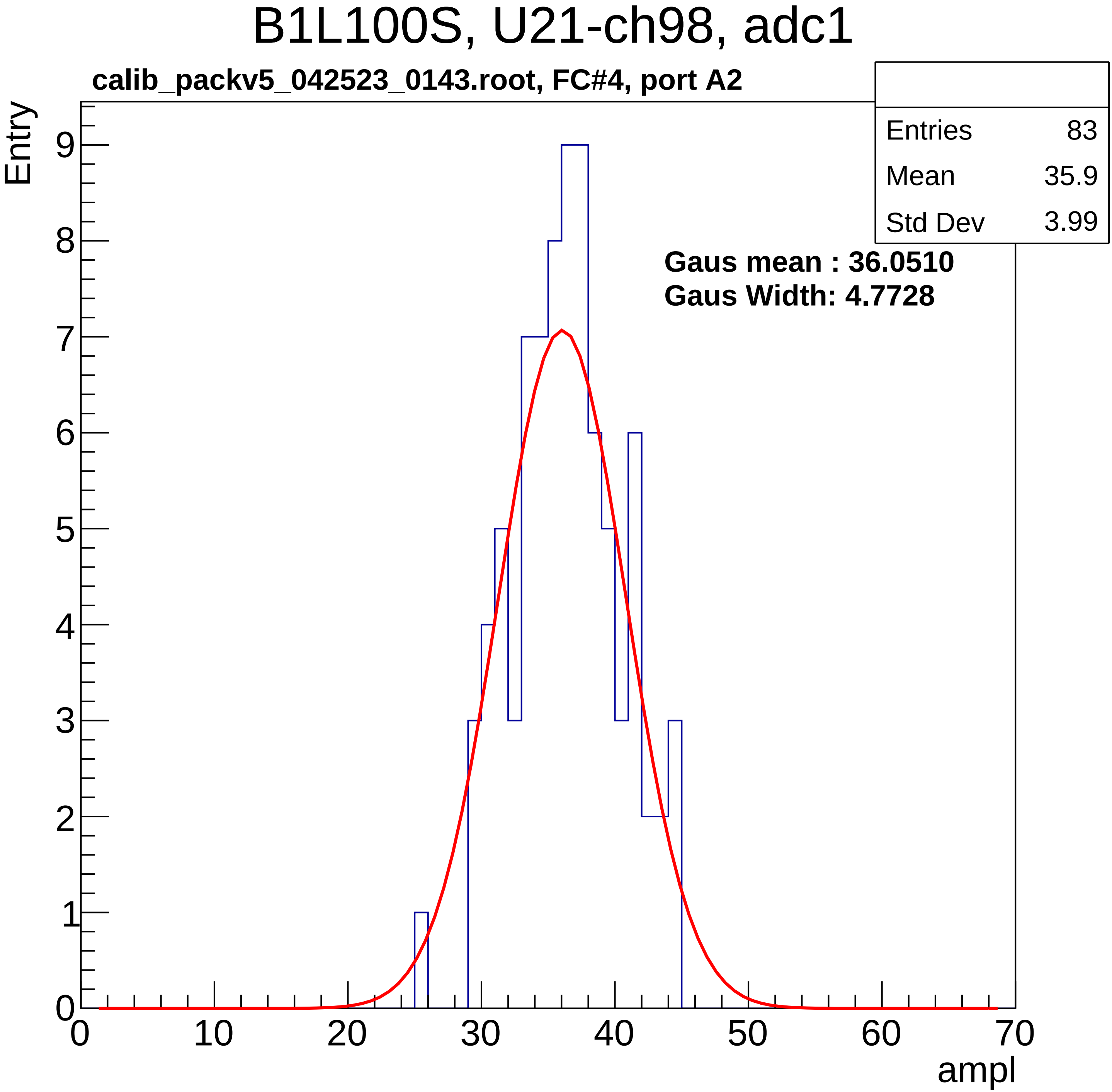


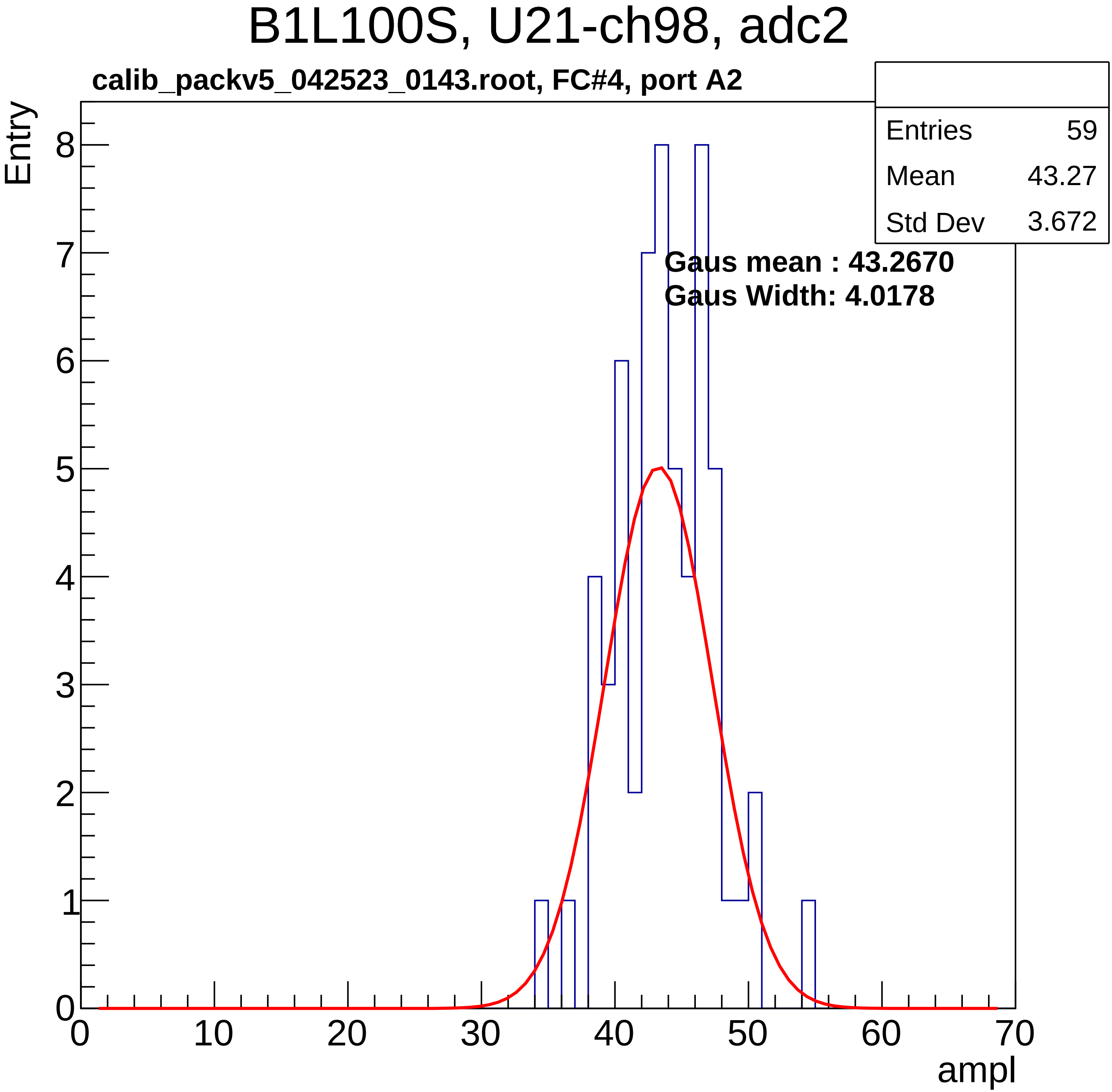

B1L100S, U21-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl
Entries 44
Mean 58.86
Std Dev 9.228


B1L100S, U21-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.22
Std Dev 1.75
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

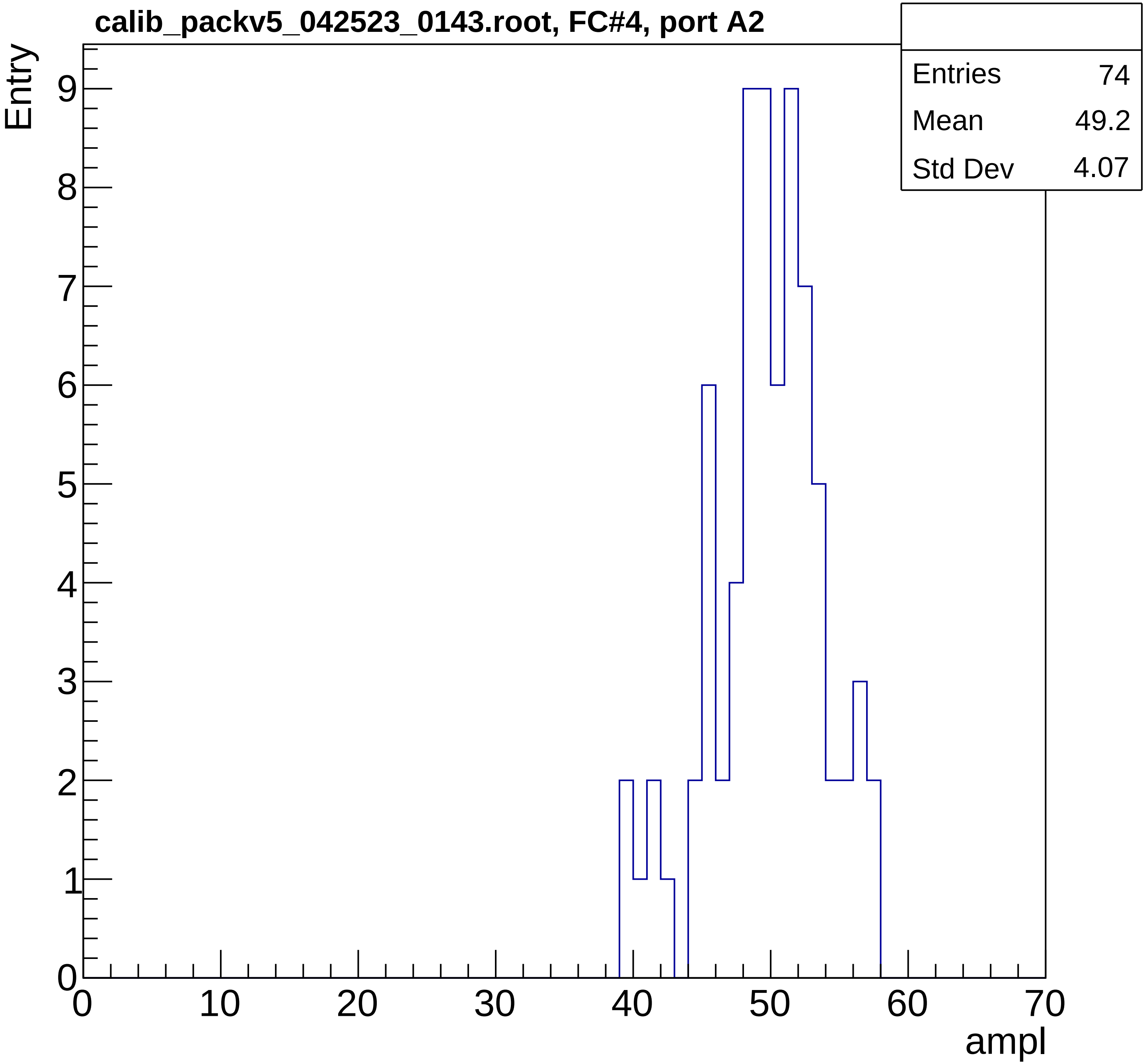

B1L100S, U21-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

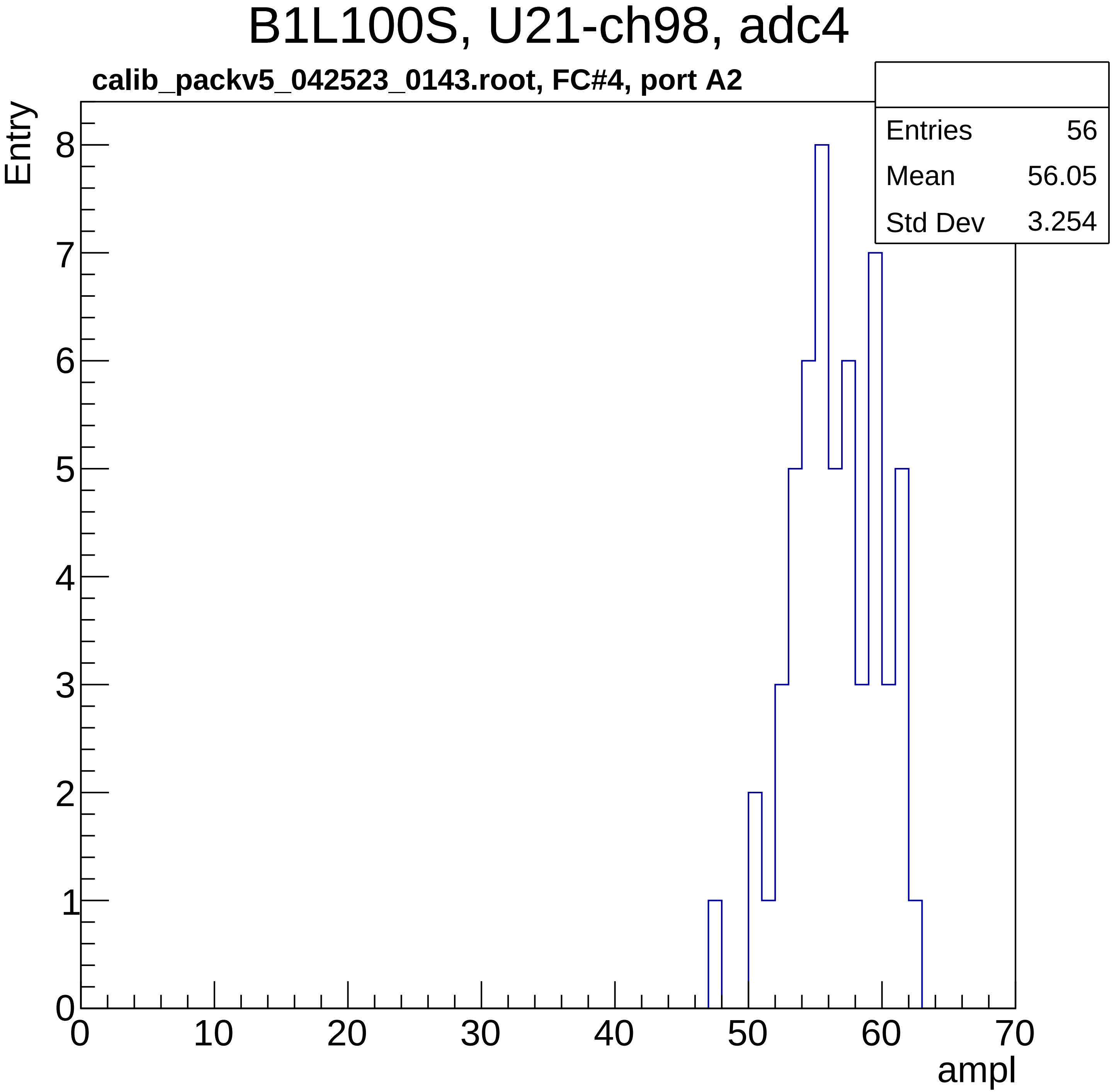

B1L100S, U21-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 27.64
Std Dev 5.892
Gaus mean : 29.1529
Gaus Width: 3.7830
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

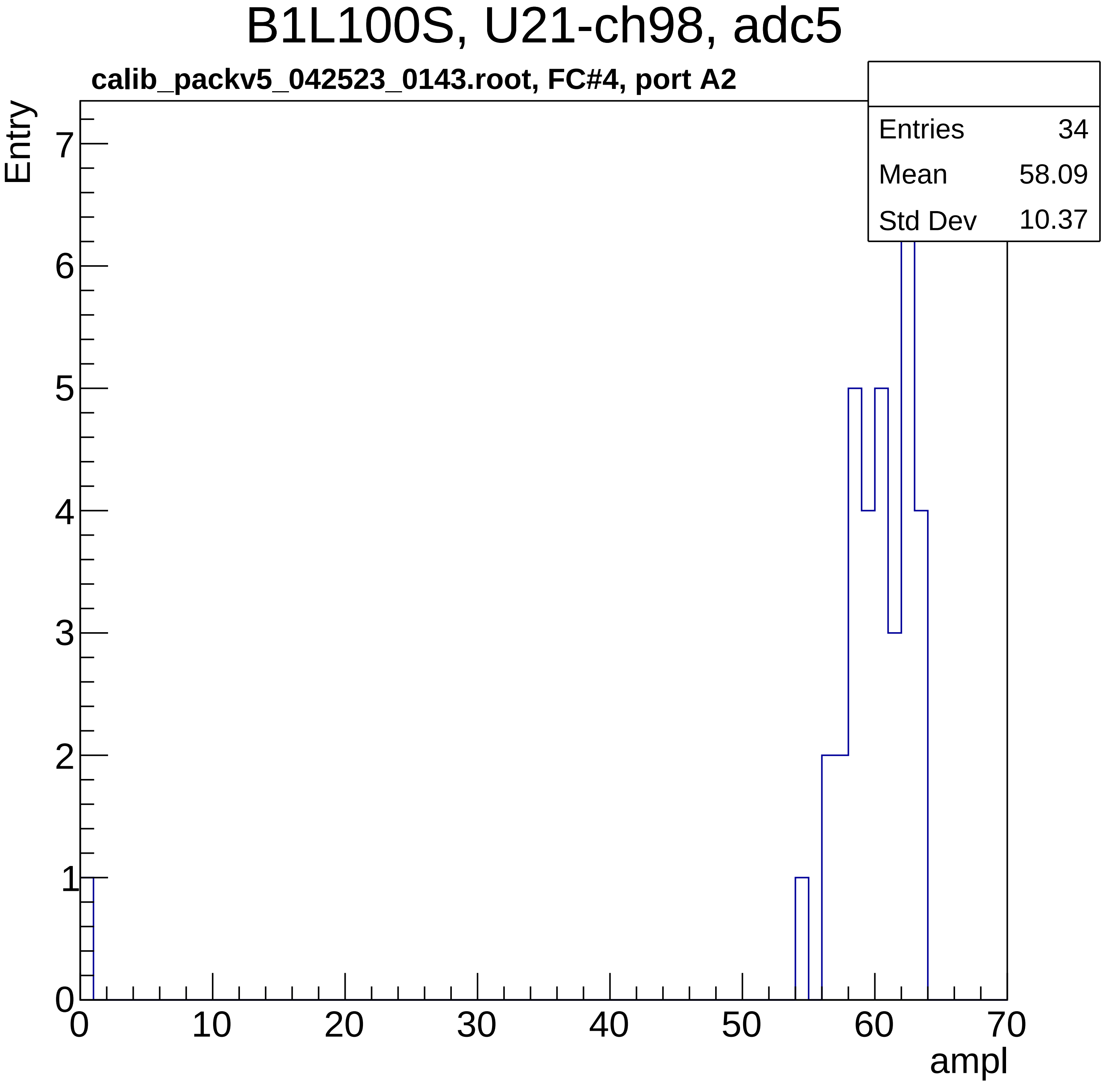

B1L100S, U21-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 35.9
Std Dev 3.99
Gaus mean : 36.0510
Gaus Width: 4.7728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.27
Std Dev 3.672
Gaus mean : 43.2670
Gaus Width: 4.0178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

B1L100S, U21-ch98, adc3

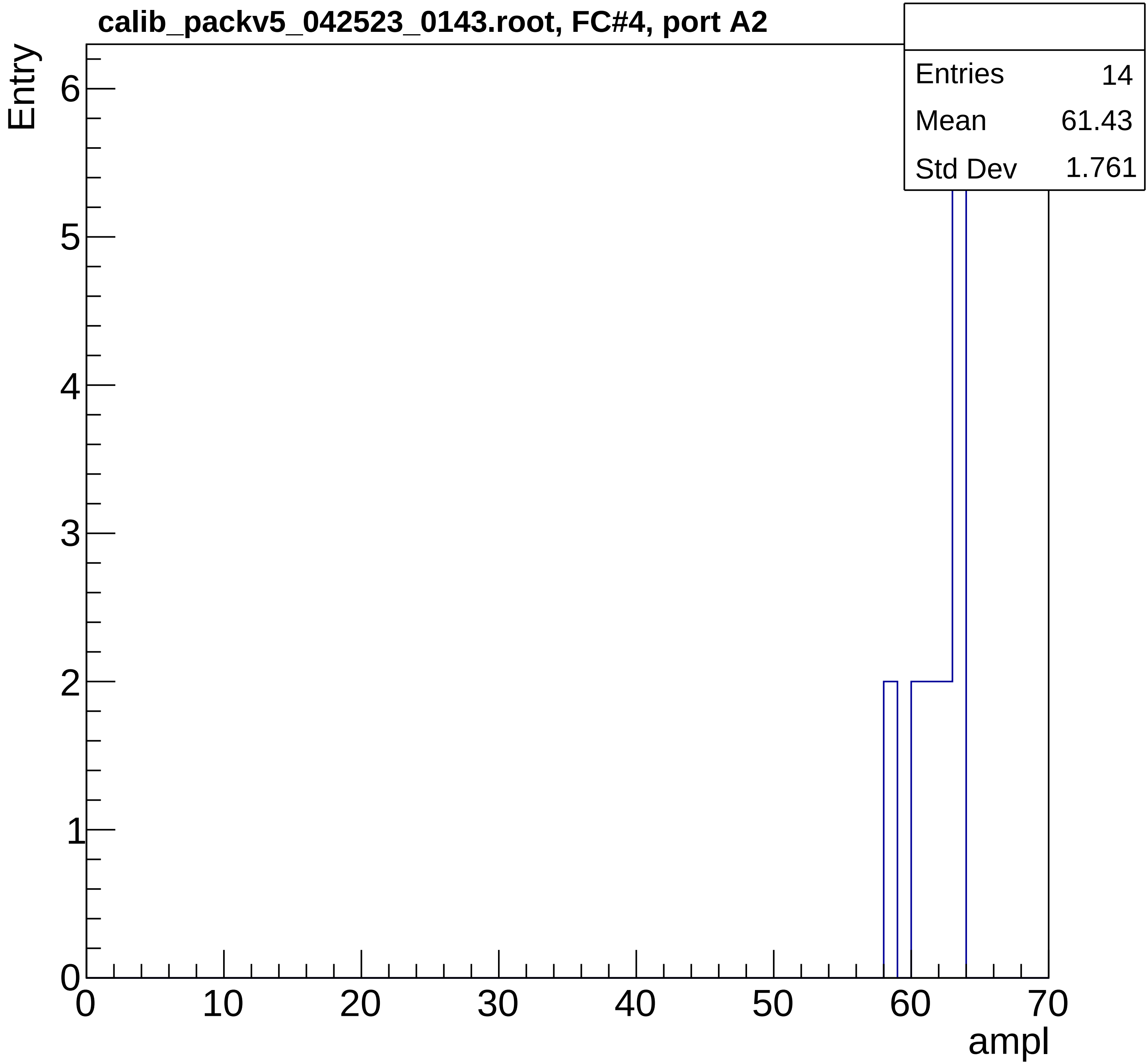


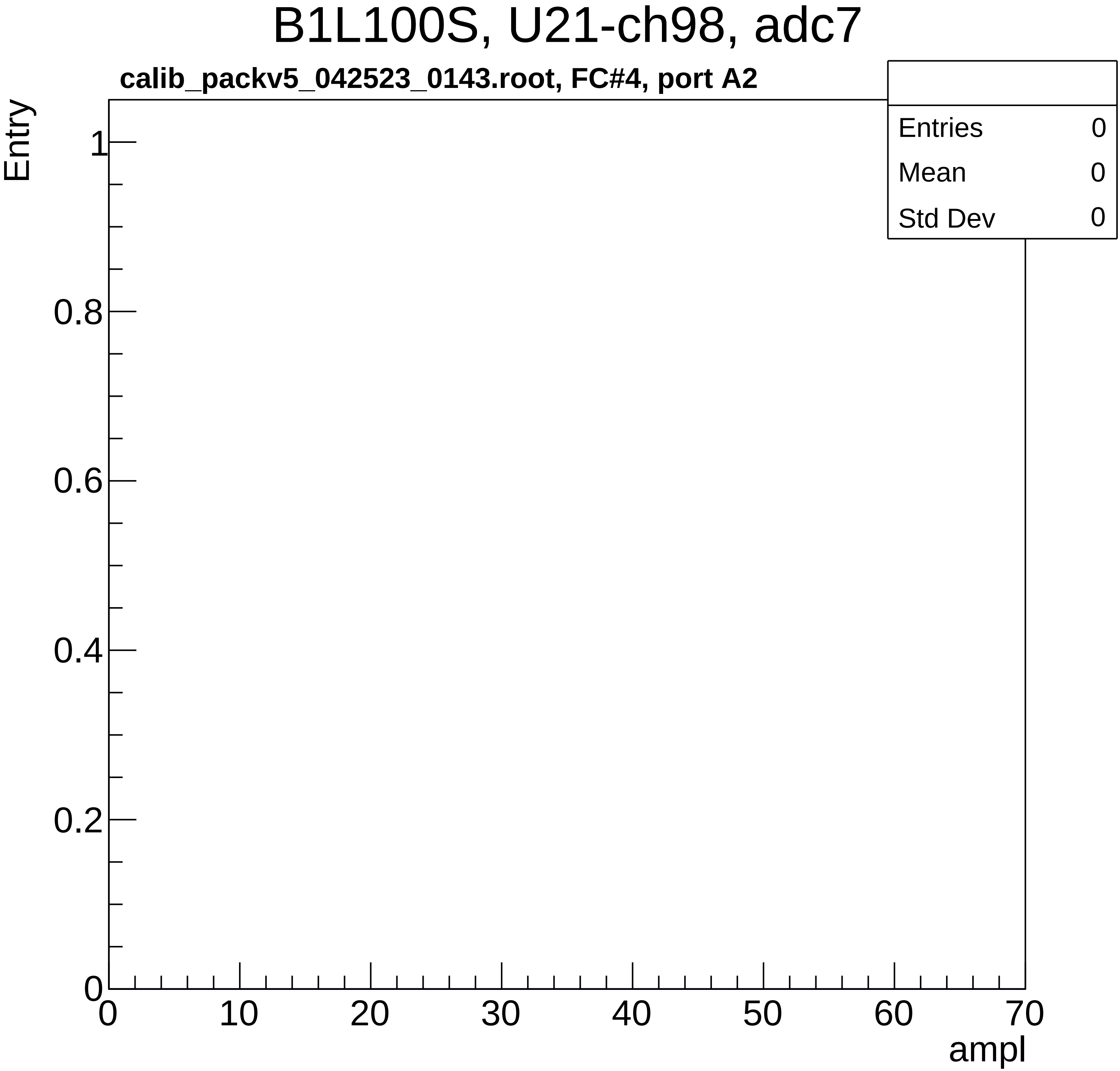

B1L100S, U21-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.05
Std Dev 3.254
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

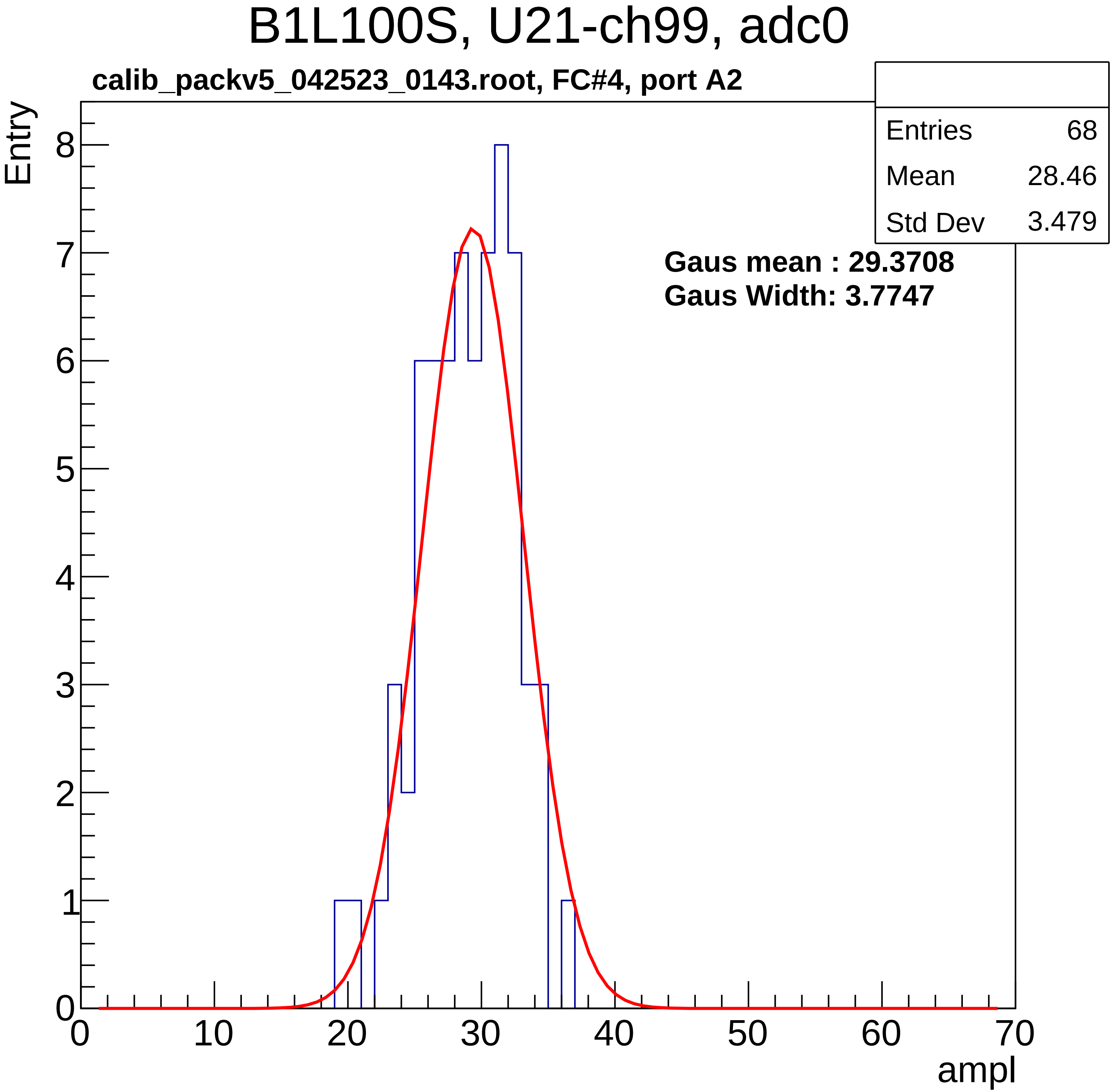


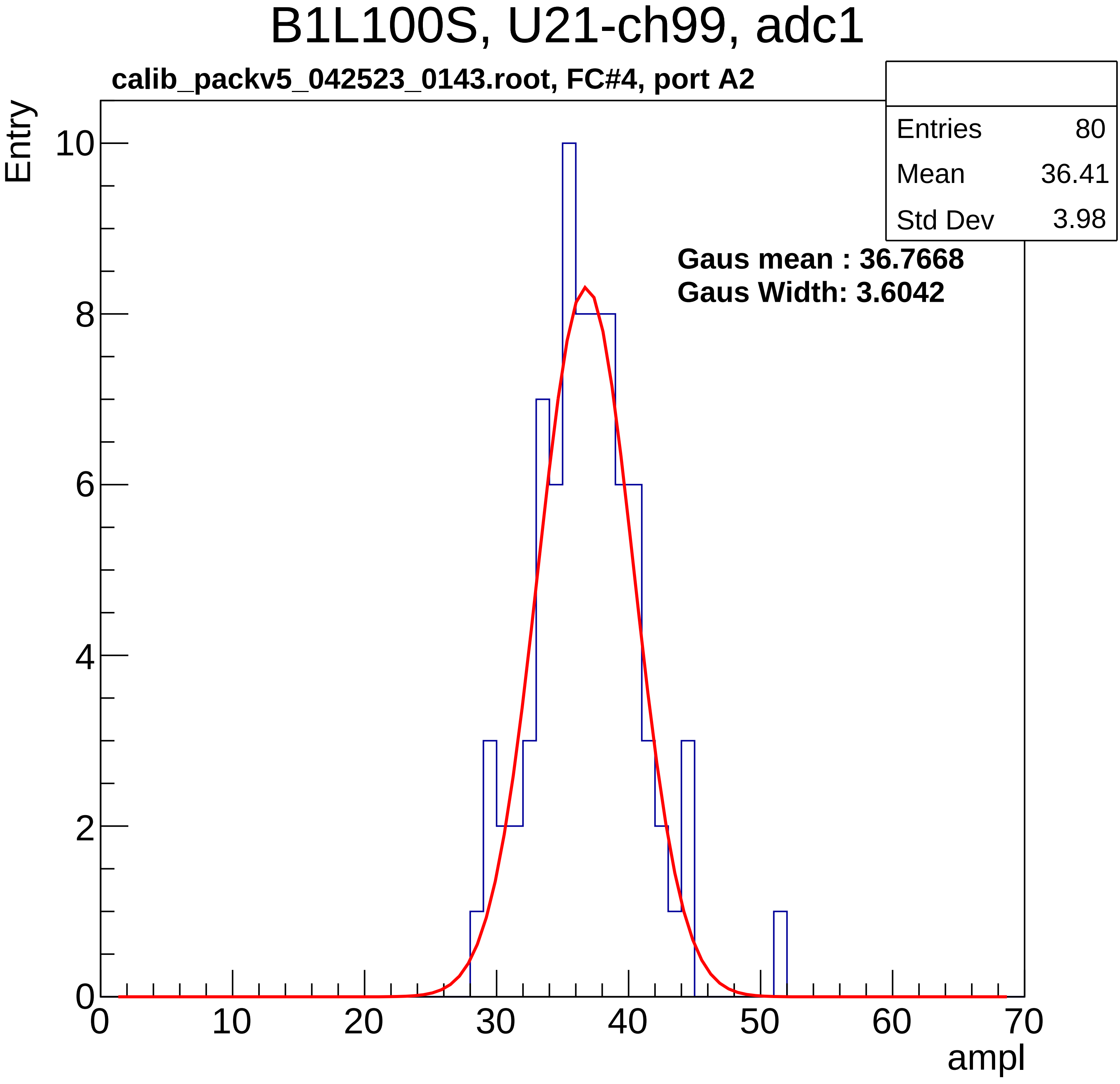
B1L100S, U21-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.09
Std Dev 10.37
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

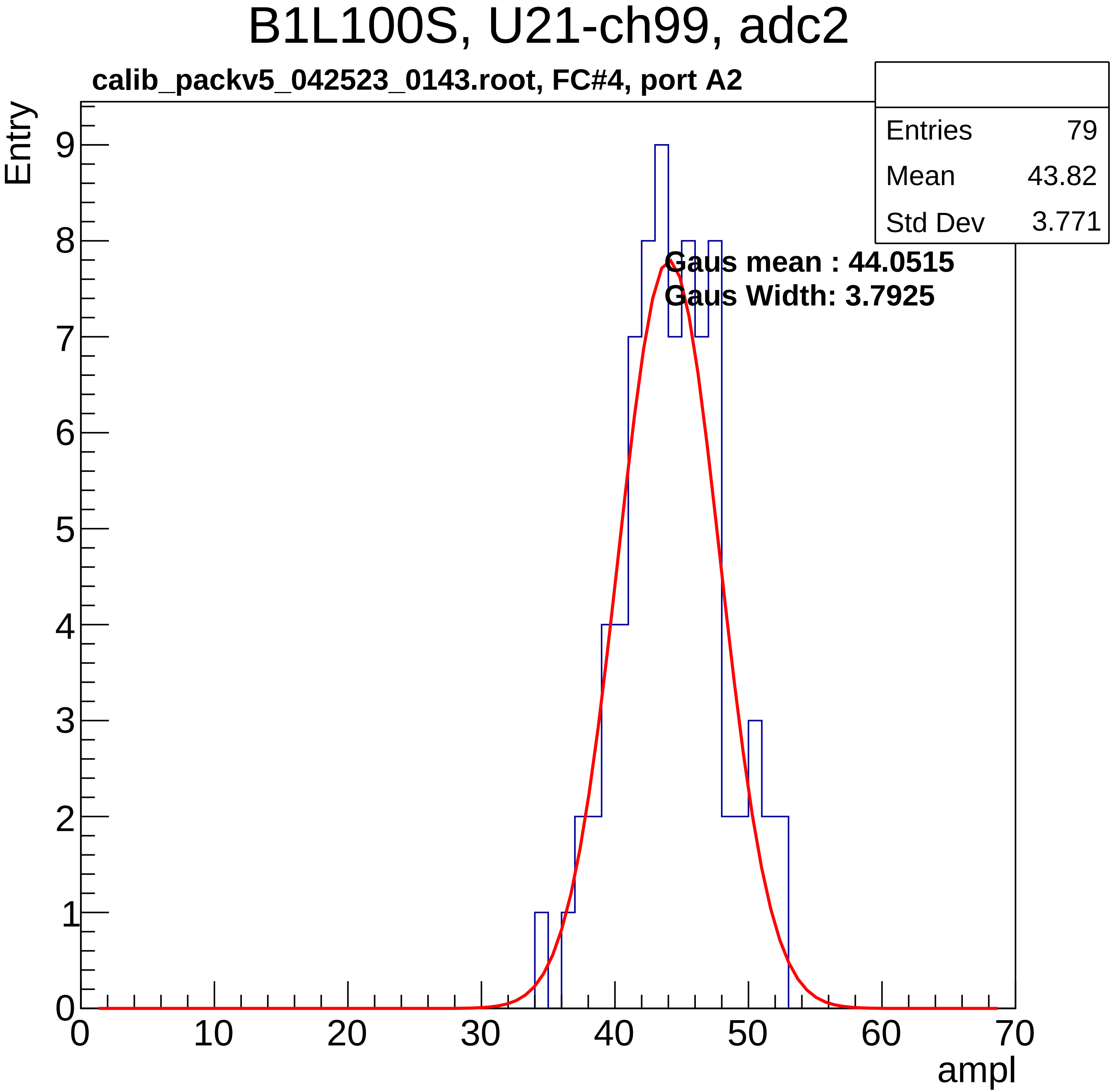
B1L100S, U21-ch98, adc6




B1L100S, U21-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

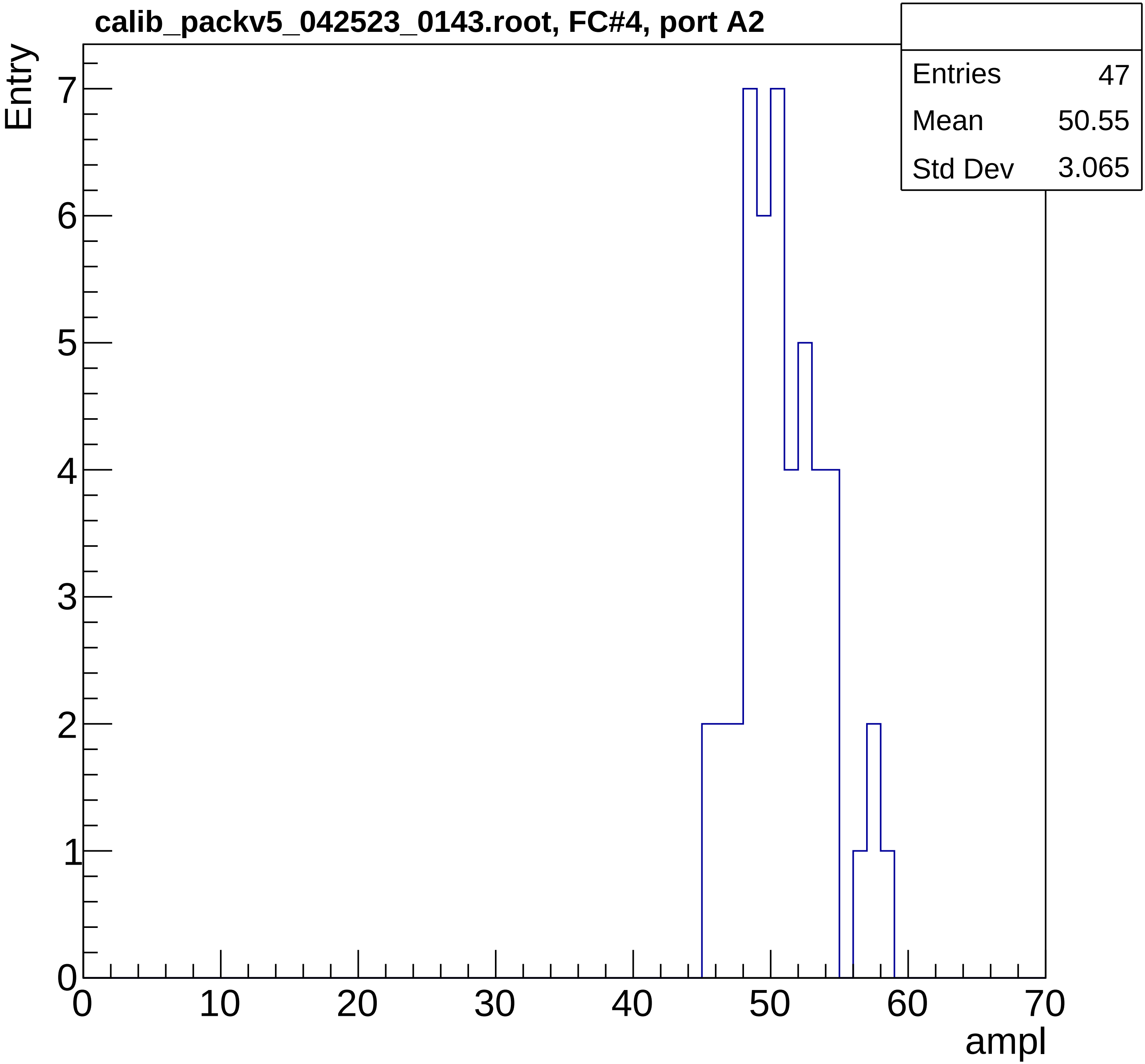

B1L100S, U21-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 68
Mean 28.46
Std Dev 3.479
Gaus mean : 29.3708
Gaus Width: 3.7747
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

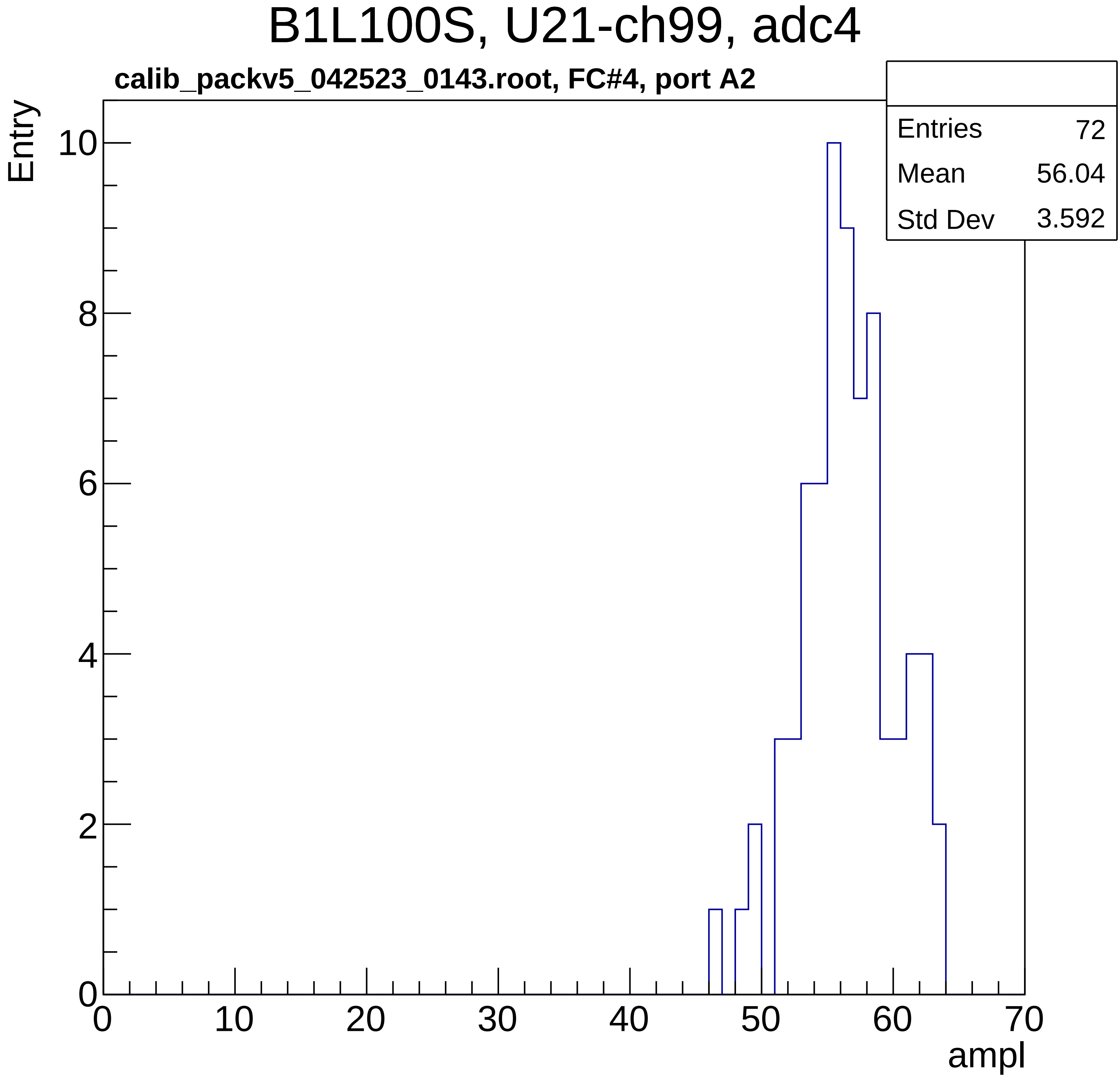

B1L100S, U21-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.41
Std Dev 3.98
Gaus mean : 36.7668
Gaus Width: 3.6042
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

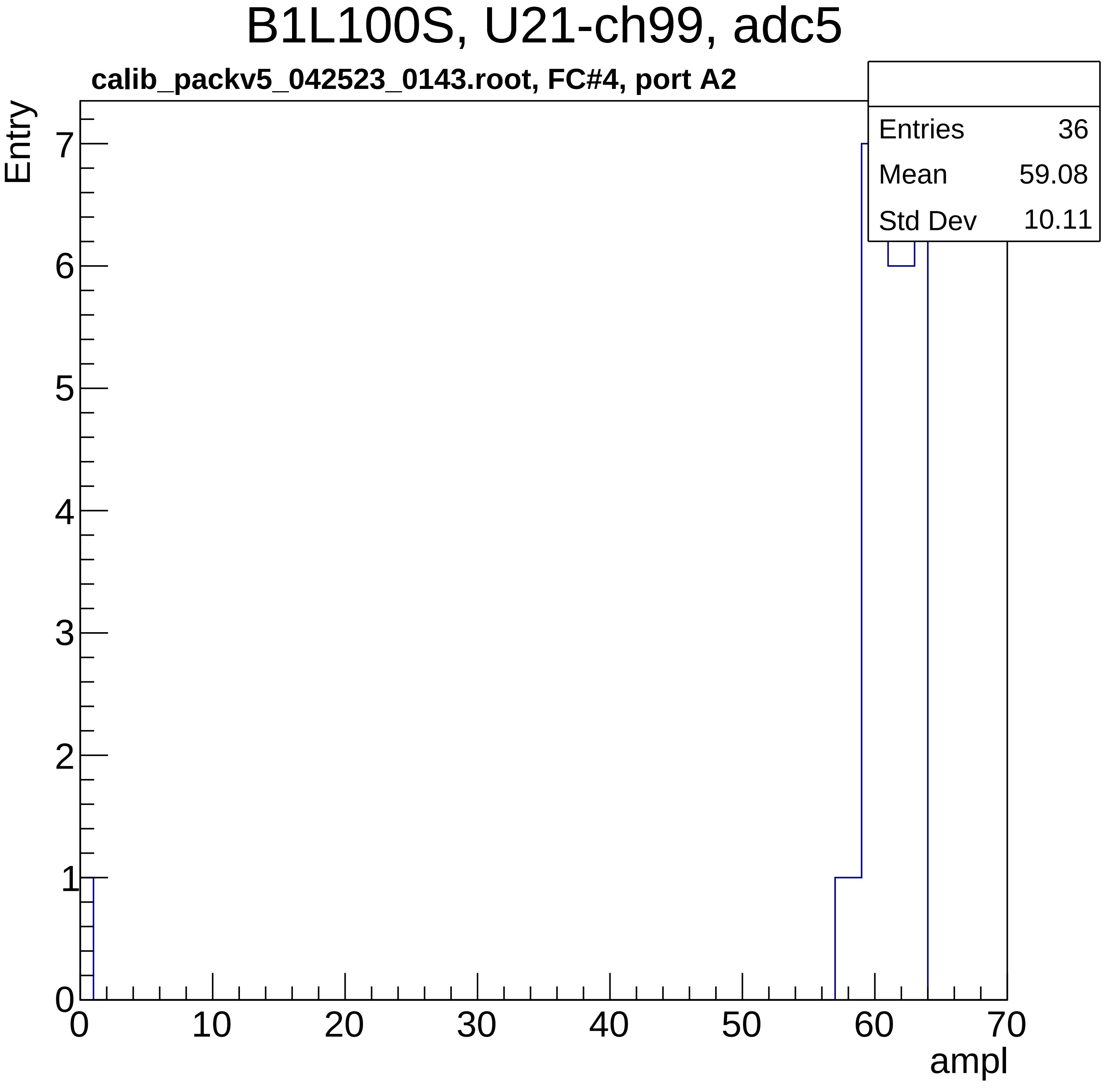

B1L100S, U21-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 79
Mean 43.82
Std Dev 3.771
Gaus mean : 44.0515
Gaus Width: 3.7925
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

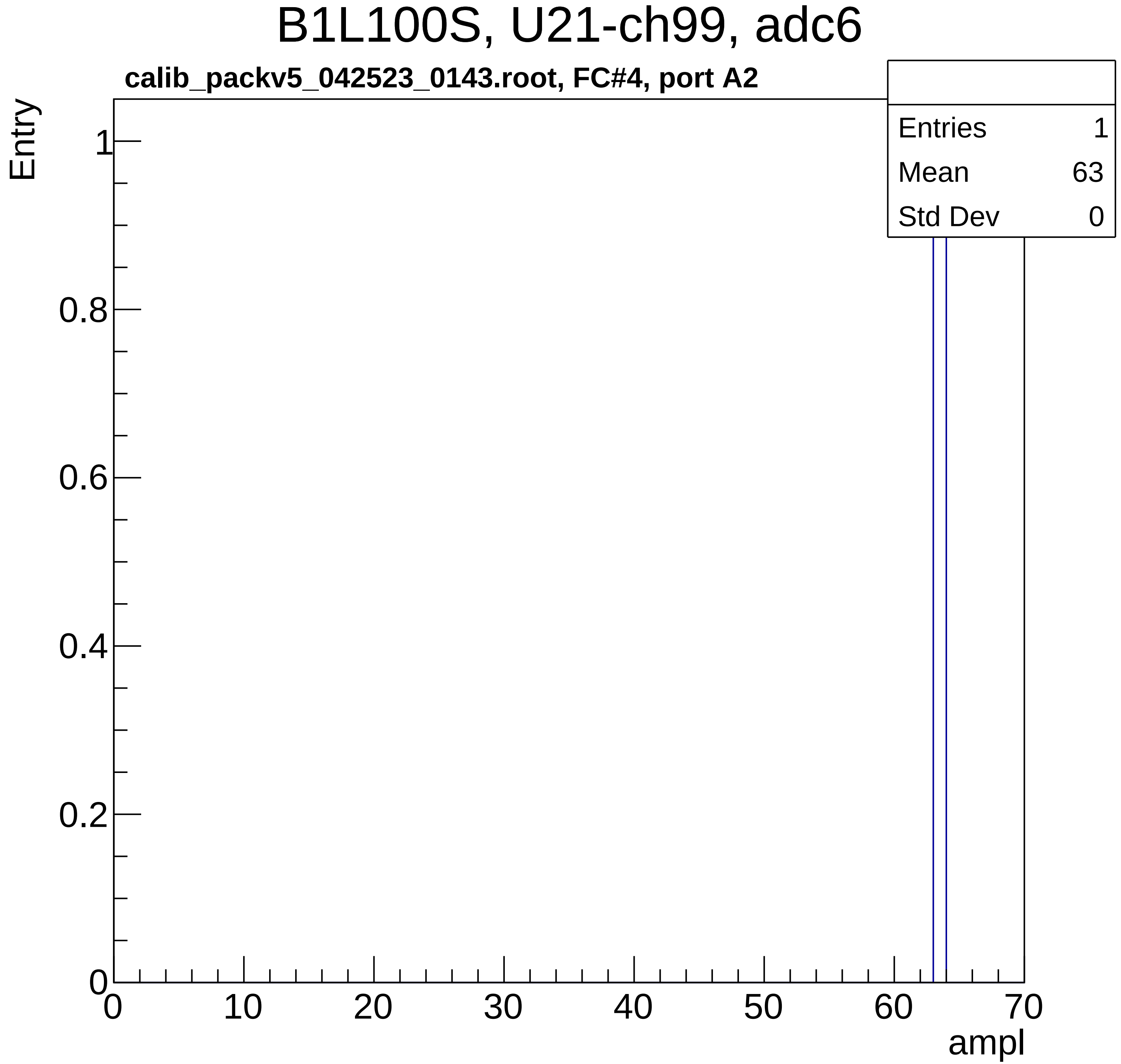
B1L100S, U21-ch99, adc3

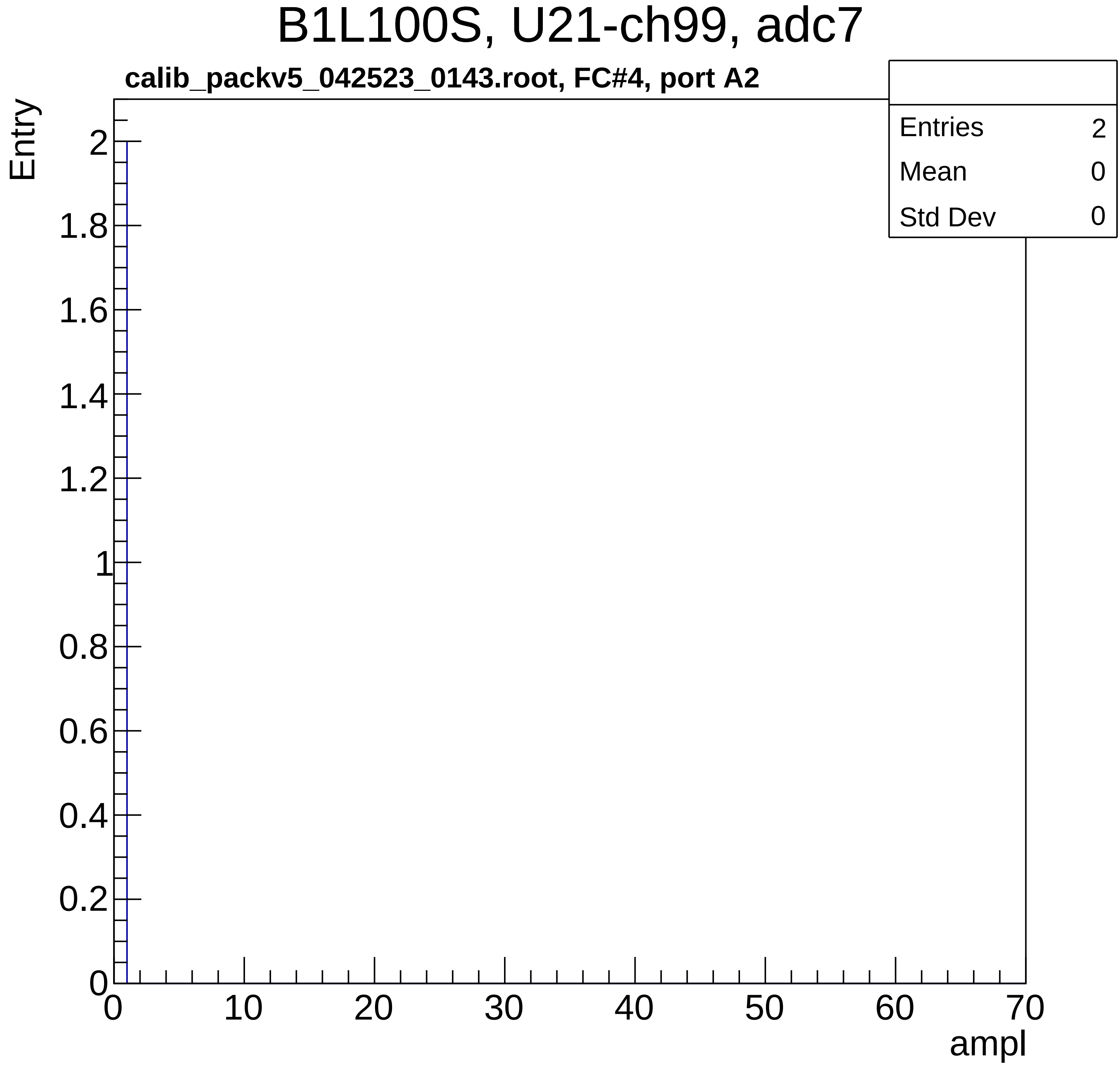
calib_packv5_042523_0143.root, FC#4, port A2




B1L100S, U21-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 56.04
Std Dev 3.592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

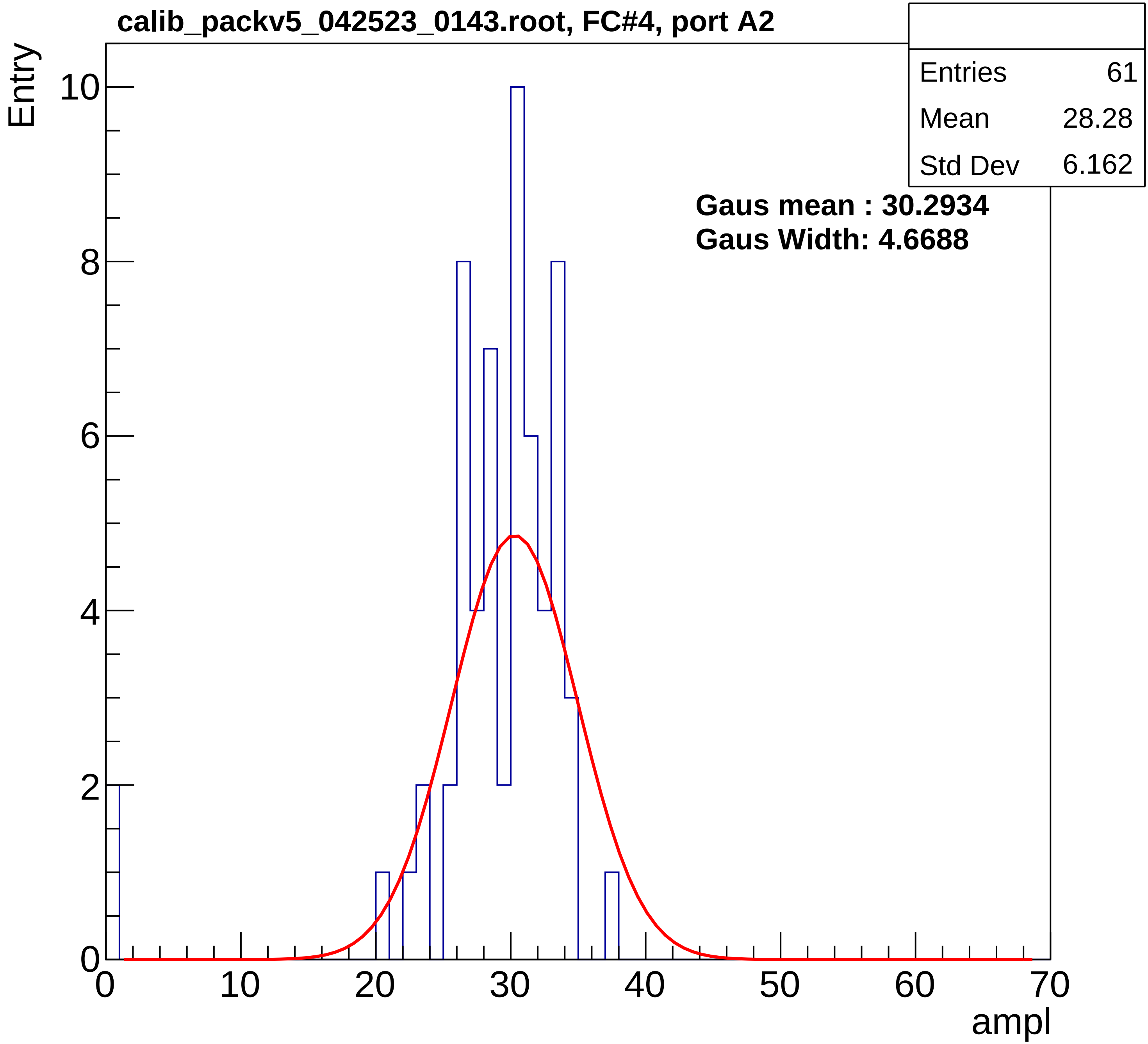

B1L100S, U21-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.08
Std Dev 10.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 1
Mean 63
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

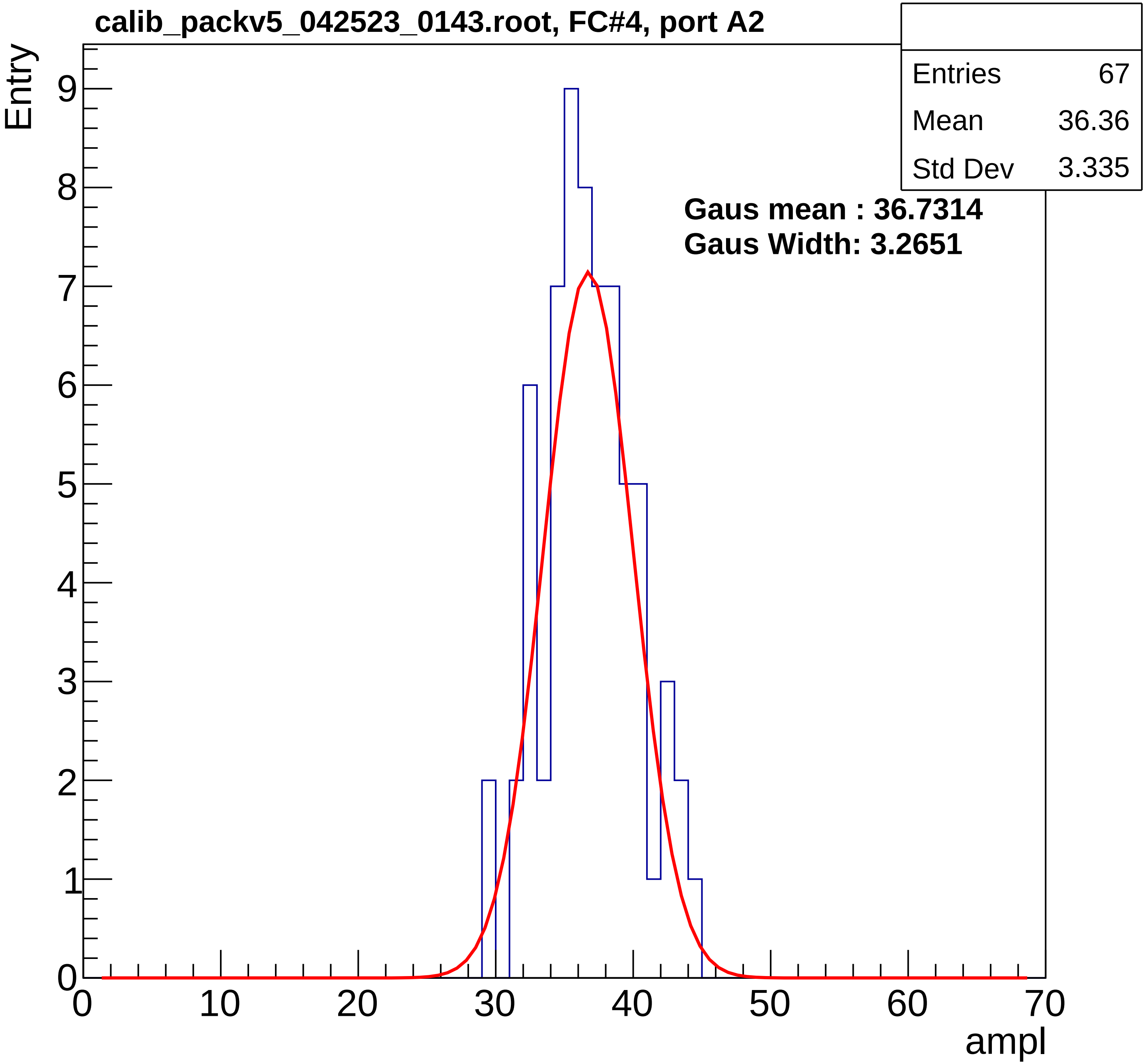

B1L100S, U21-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

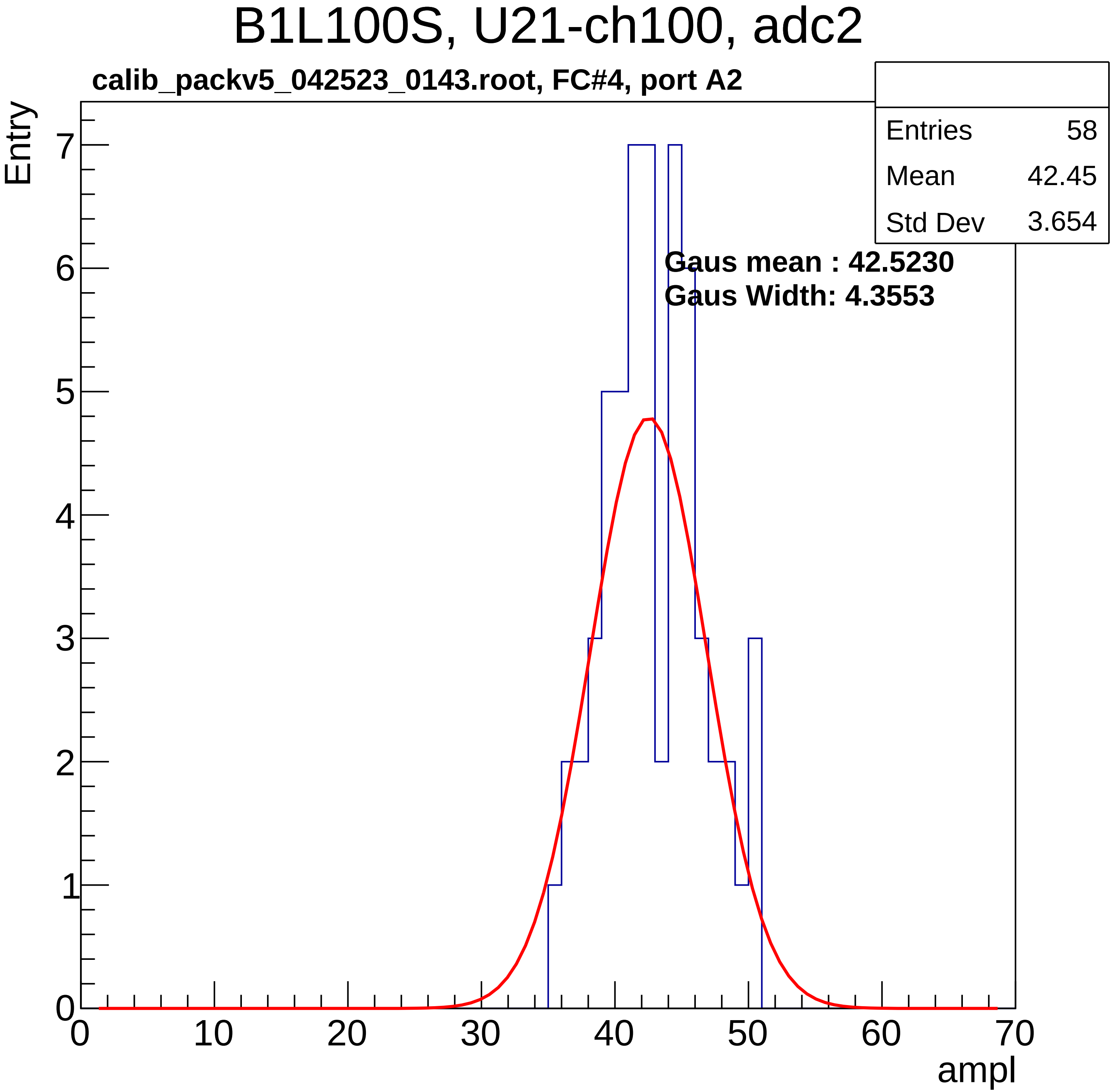
B1L100S, U21-ch100, adc0

calib_packv5_042523_0143.root, FC#4, port A2



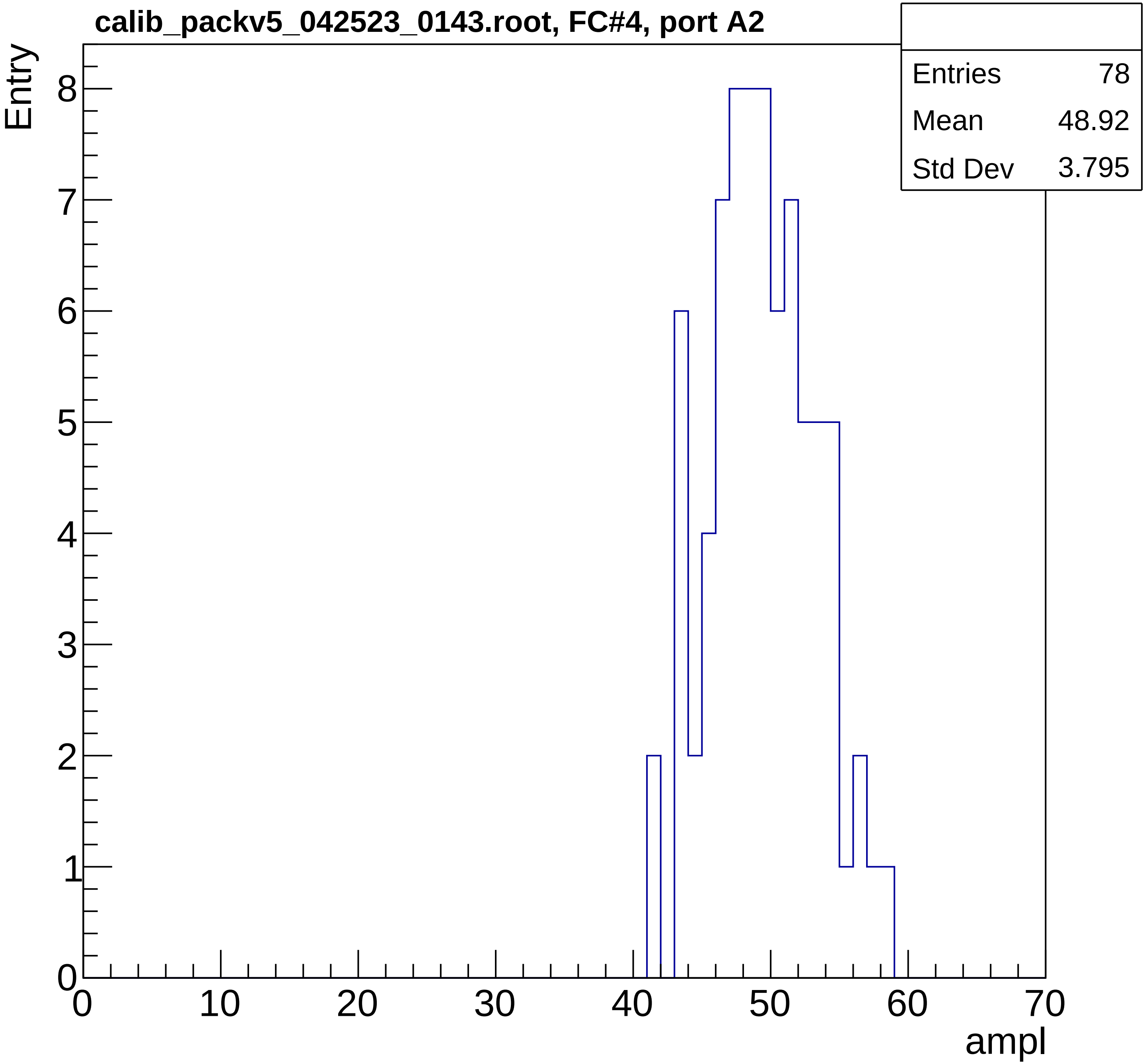
B1L100S, U21-ch100, adc1




B1L100S, U21-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 42.45
Std Dev 3.654
Gaus mean : 42.5230
Gaus Width: 4.3553
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

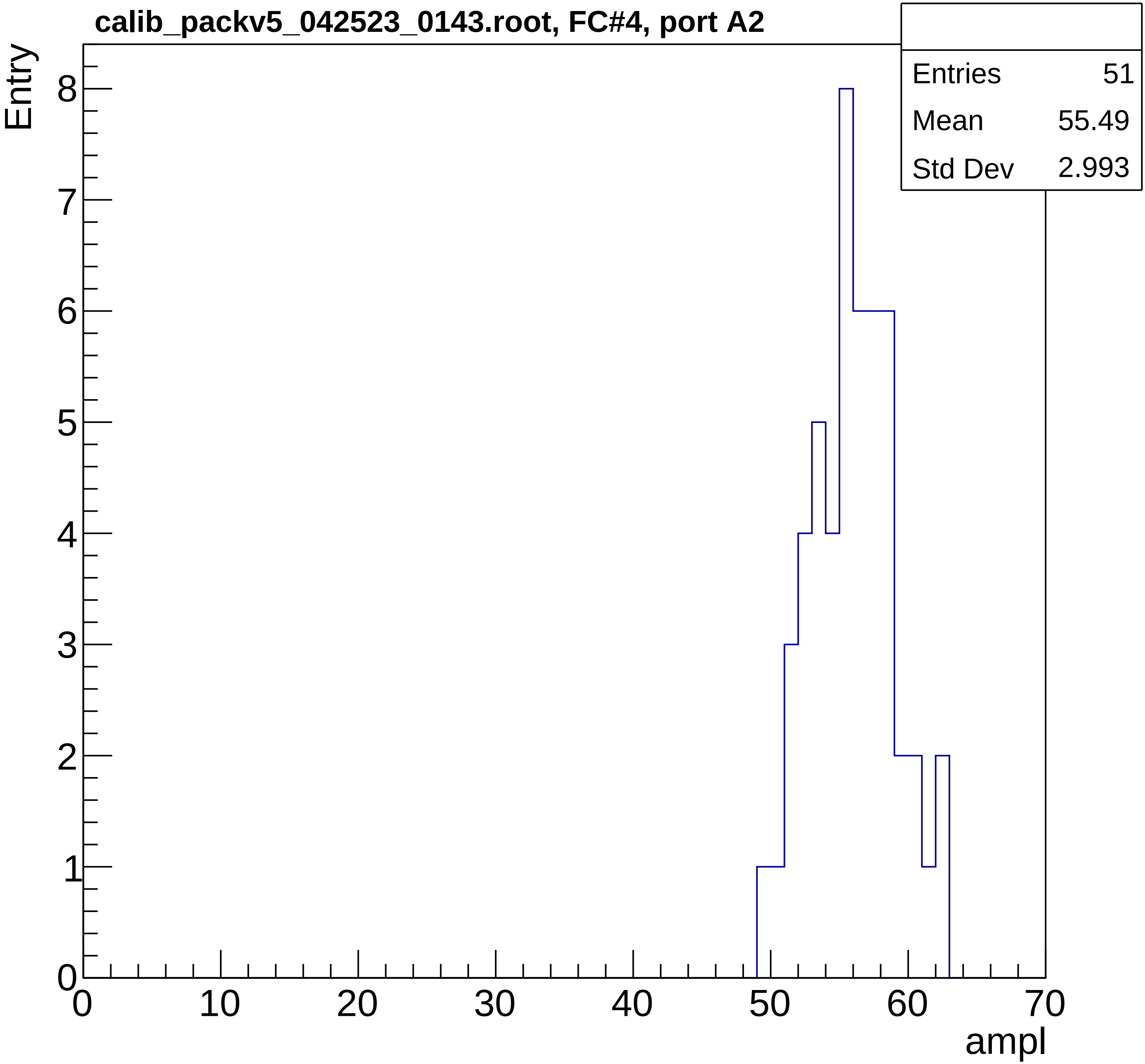
B1L100S, U21-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2



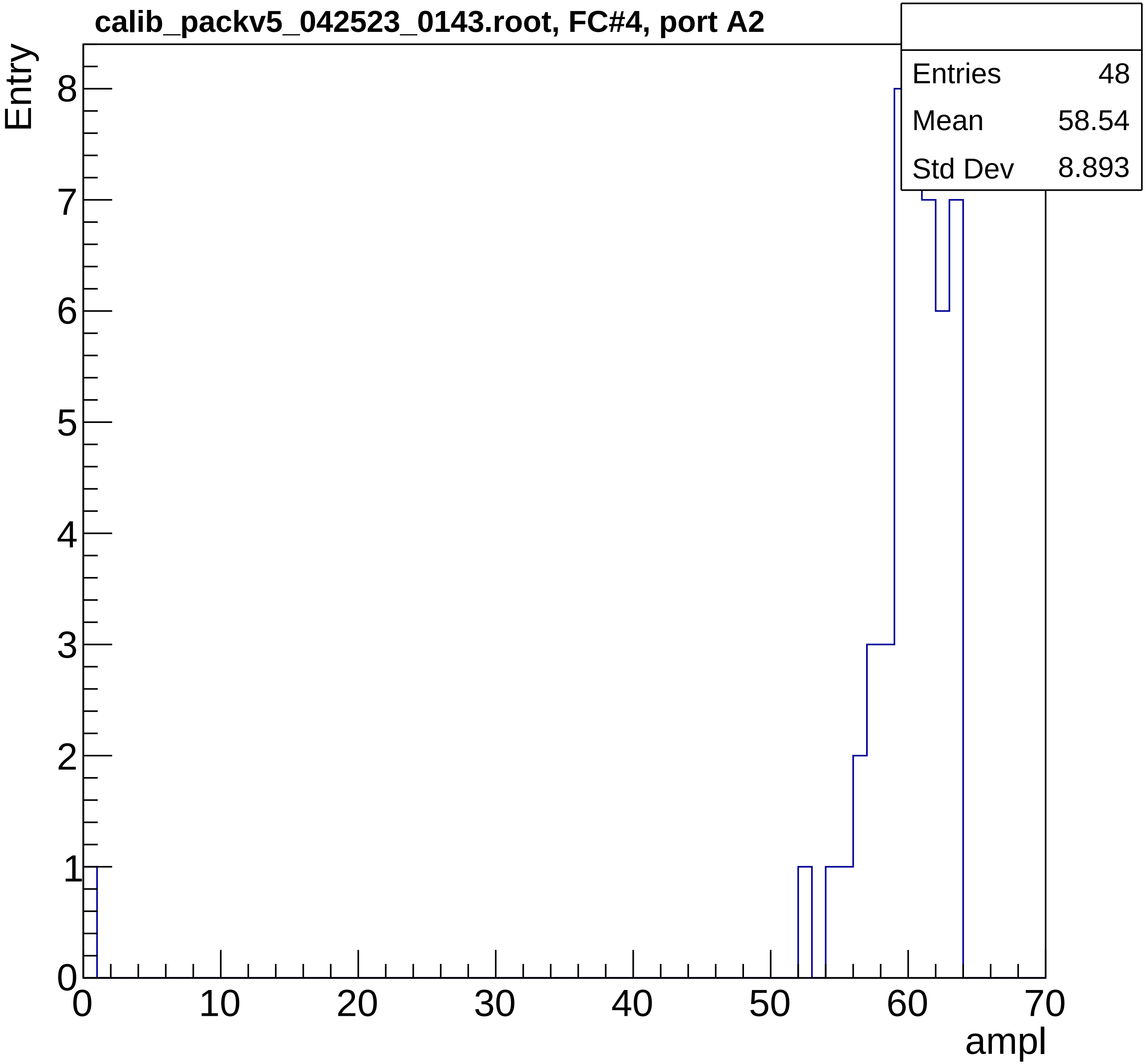
B1L100S, U21-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

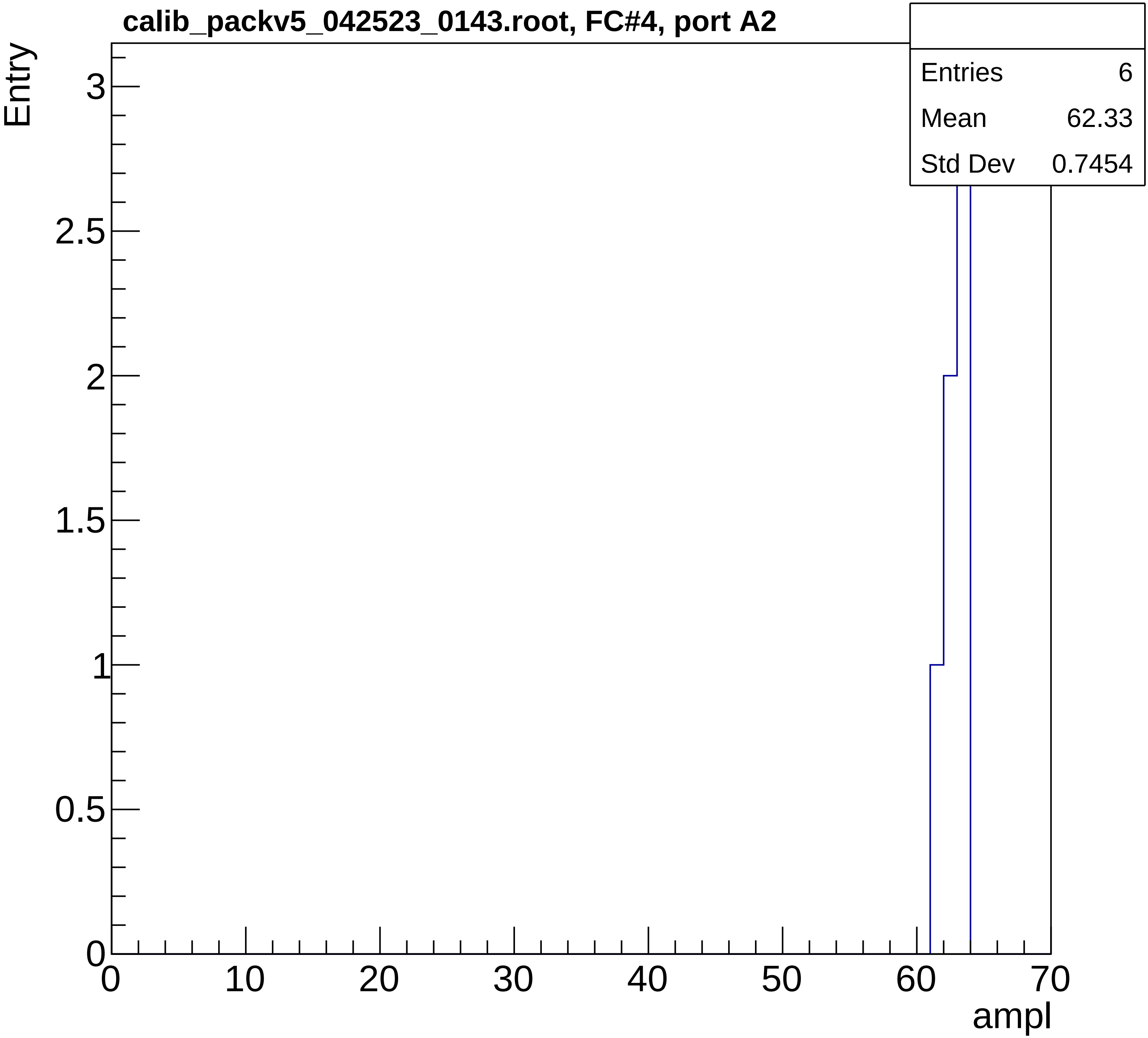


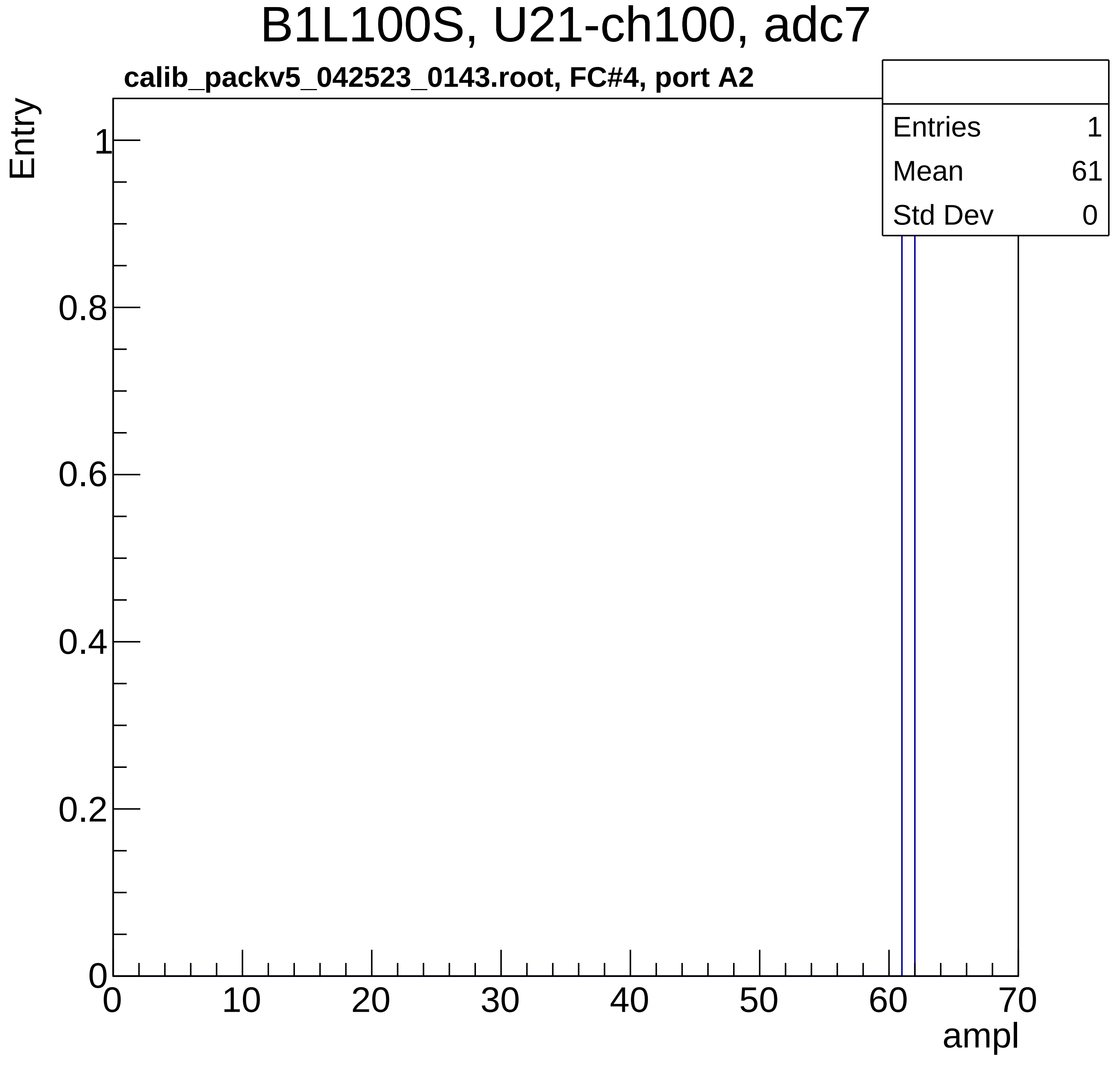
B1L100S, U21-ch100, adc5

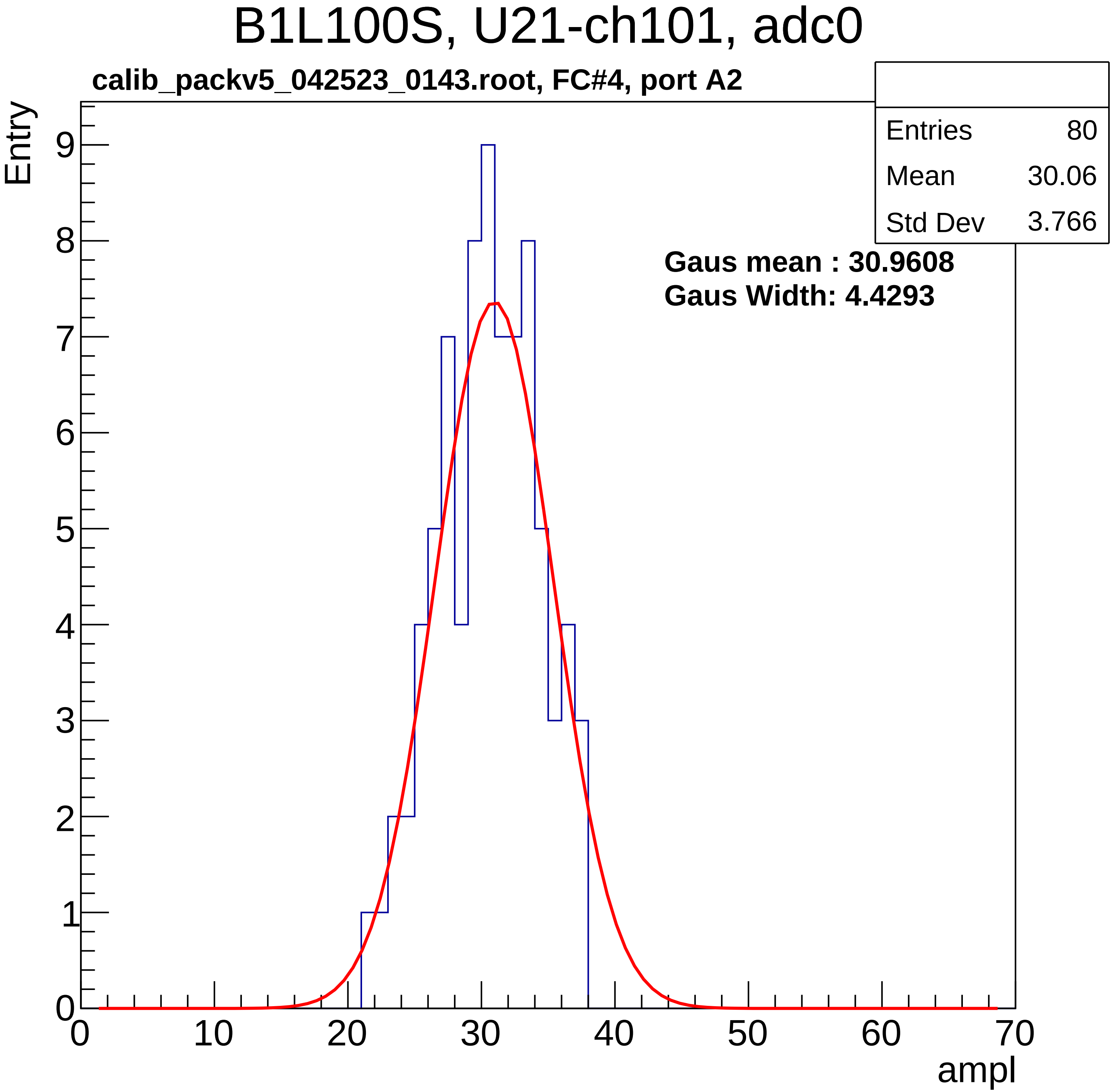
calib_packv5_042523_0143.root, FC#4, port A2

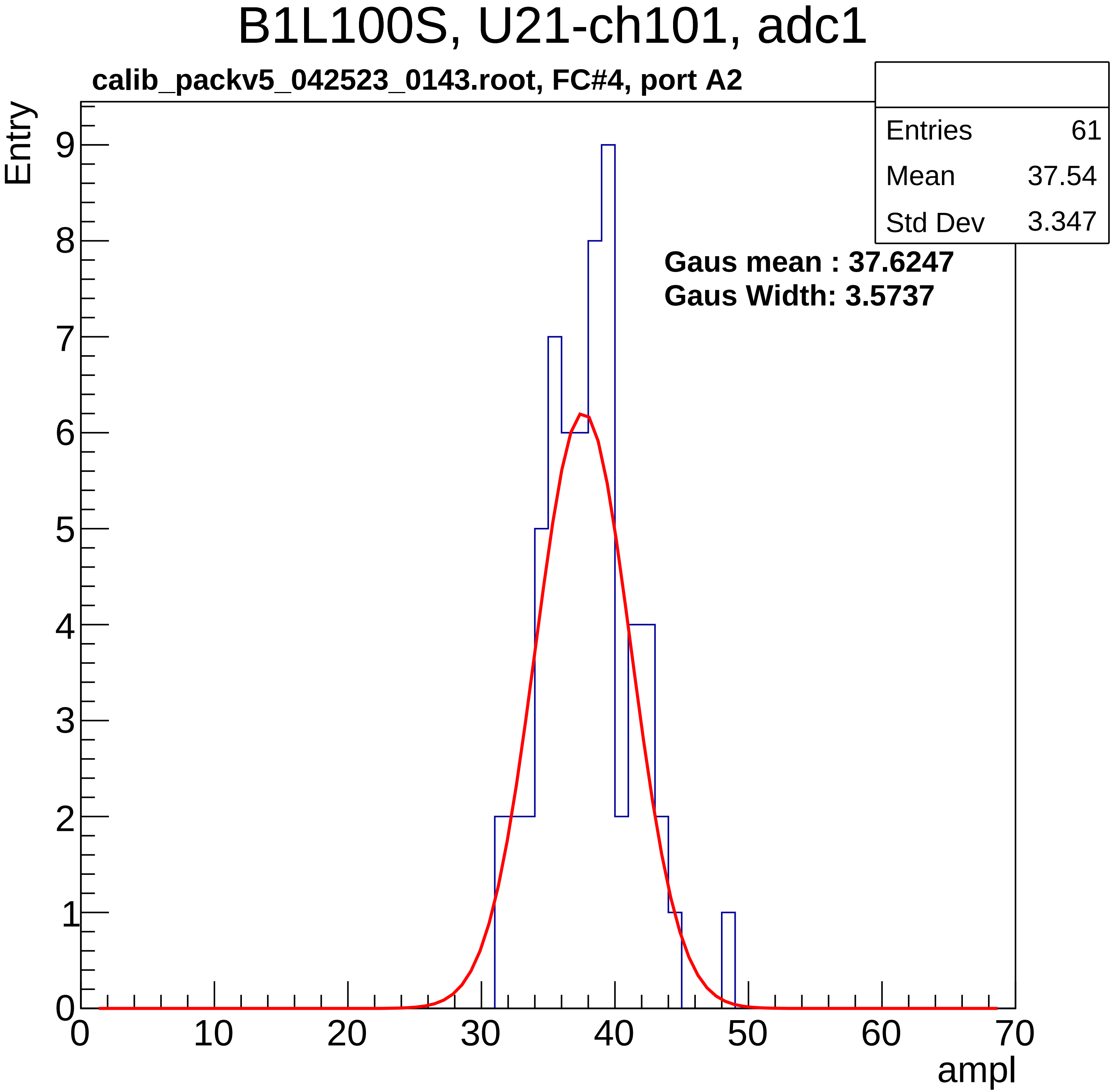


B1L100S, U21-ch100, adc6

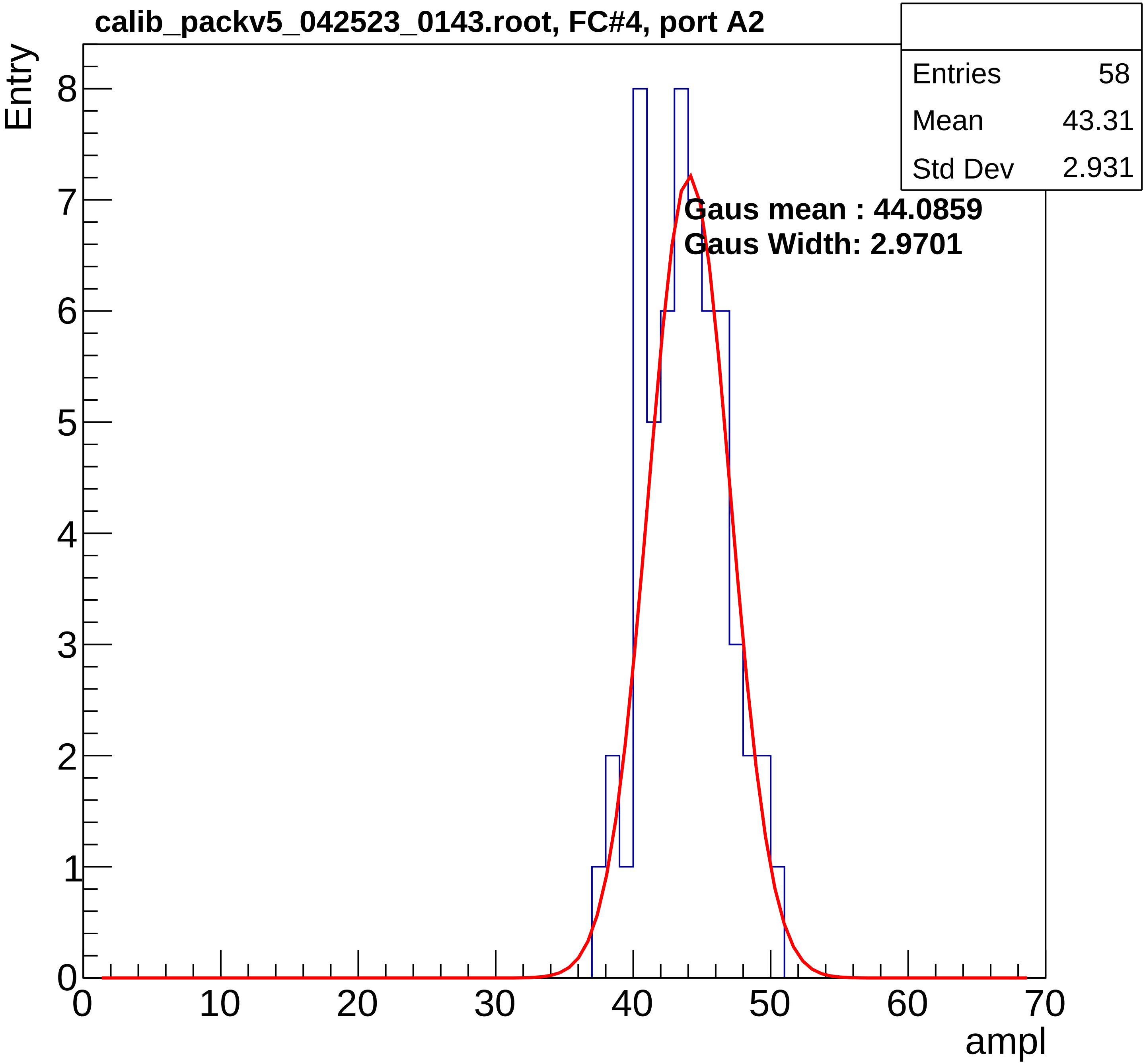



B1L100S, U21-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 1
Mean 61
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

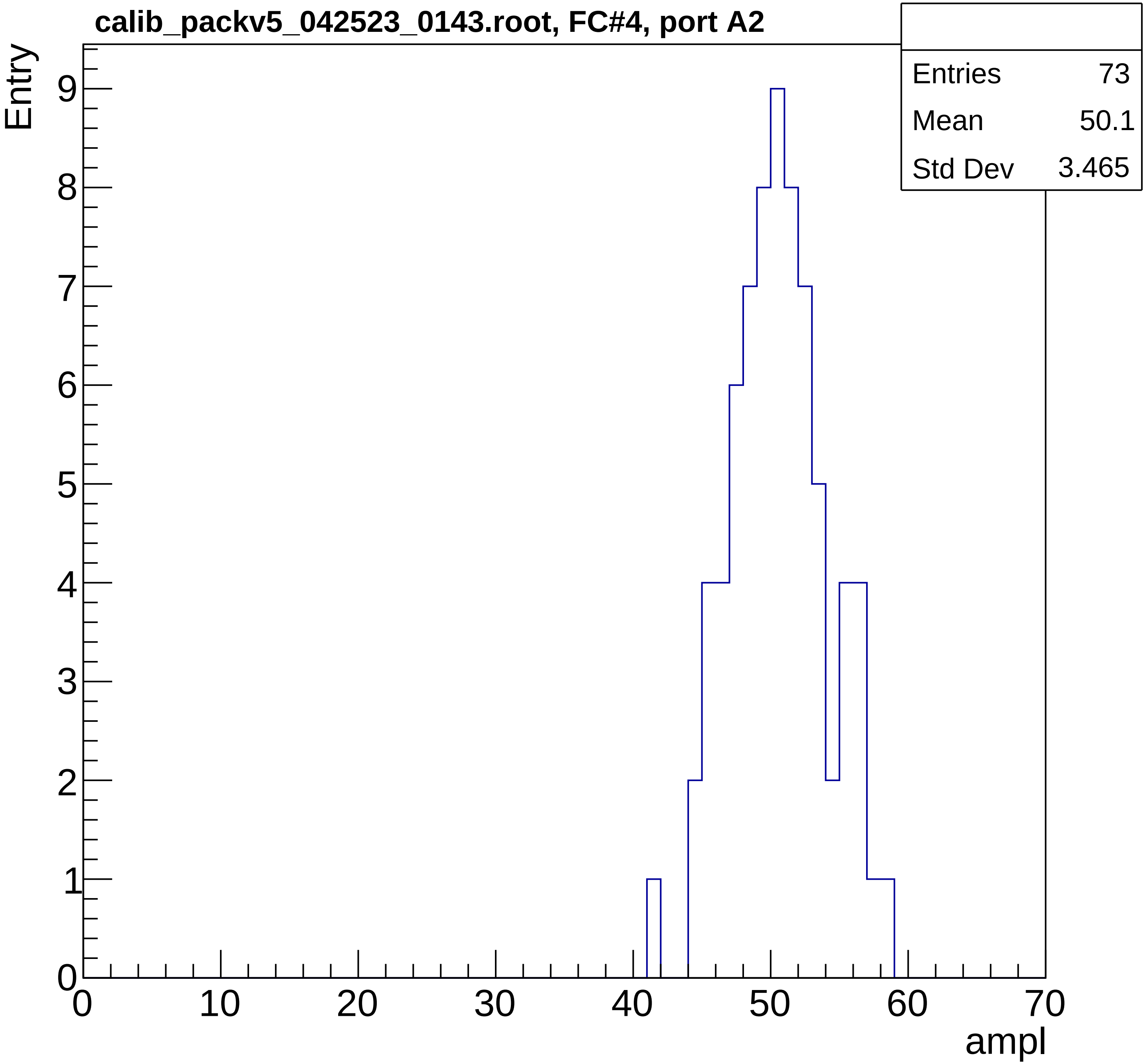

B1L100S, U21-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 30.06
Std Dev 3.766
Gaus mean : 30.9608
Gaus Width: 4.4293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 61
Mean 37.54
Std Dev 3.347
Gaus mean : 37.6247
Gaus Width: 3.5737
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch101, adc2

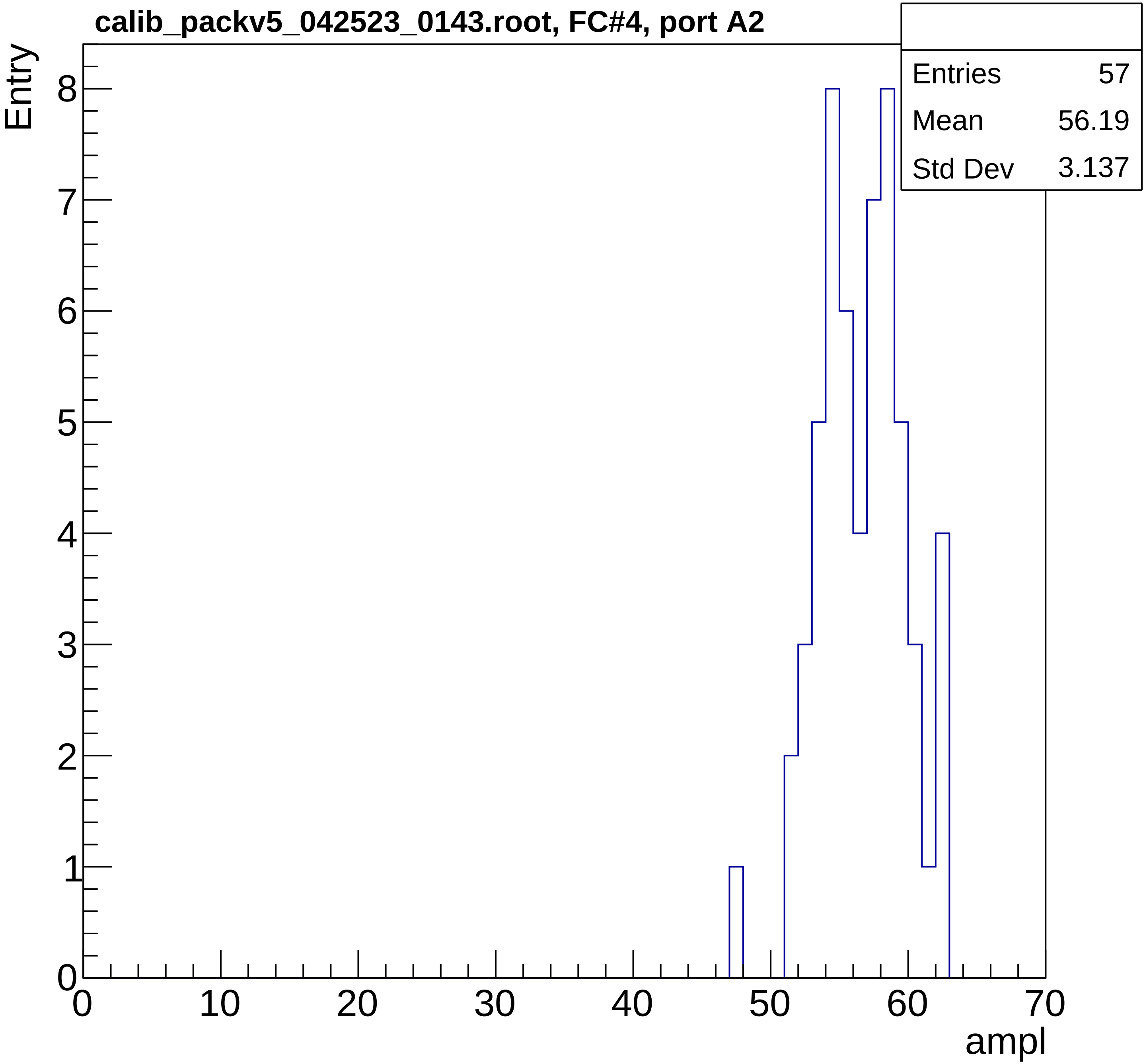


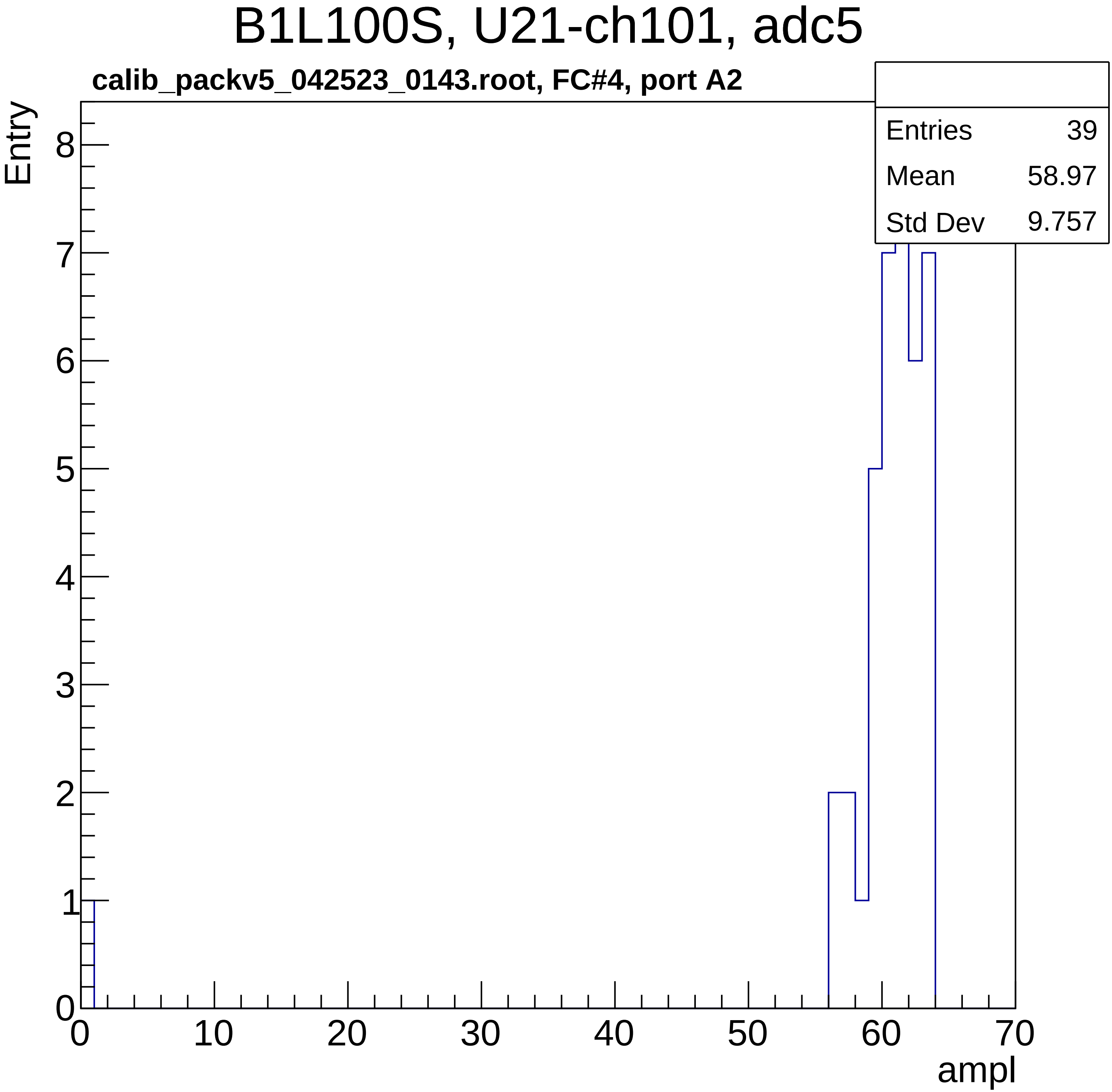
B1L100S, U21-ch101, adc3

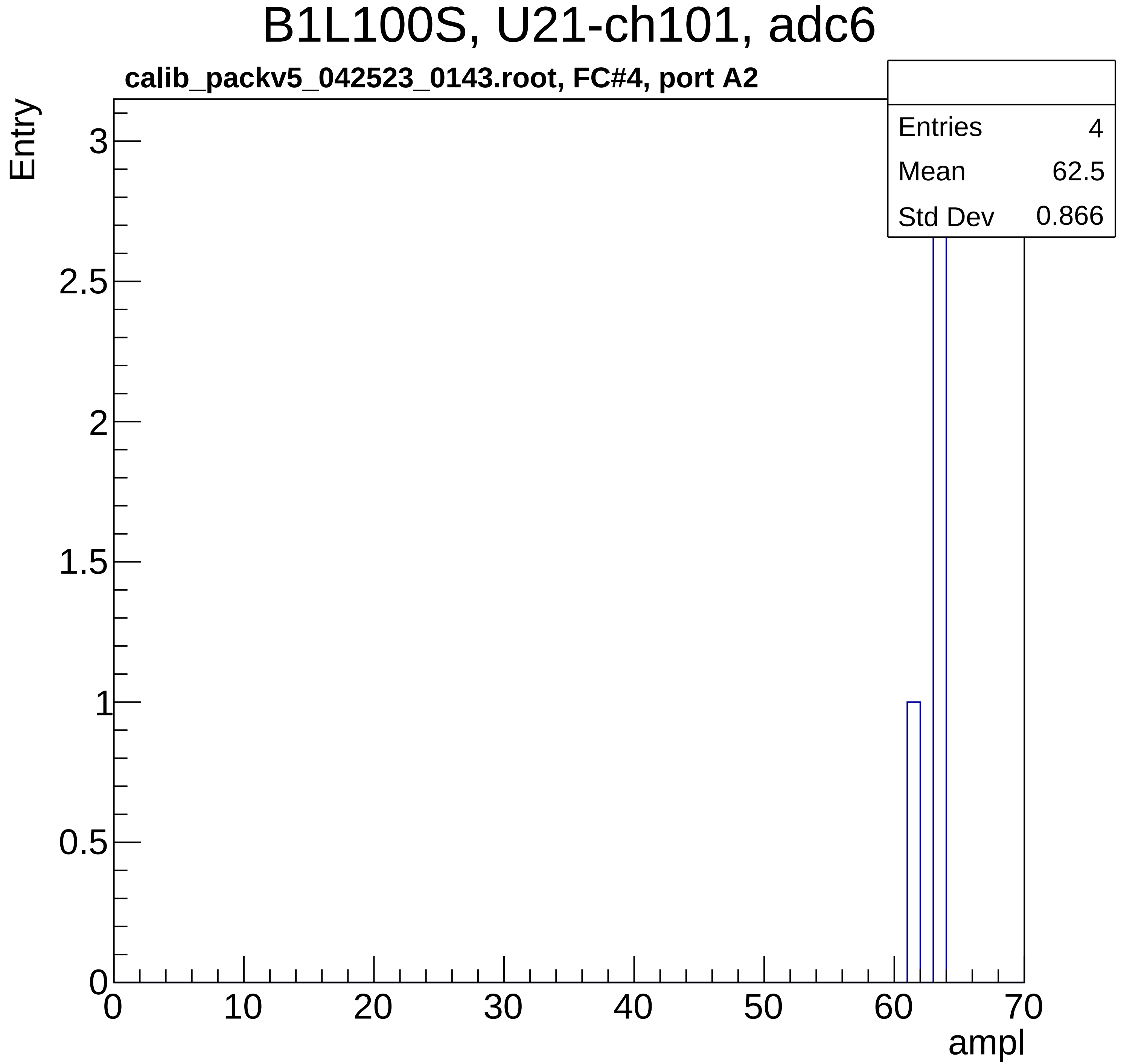


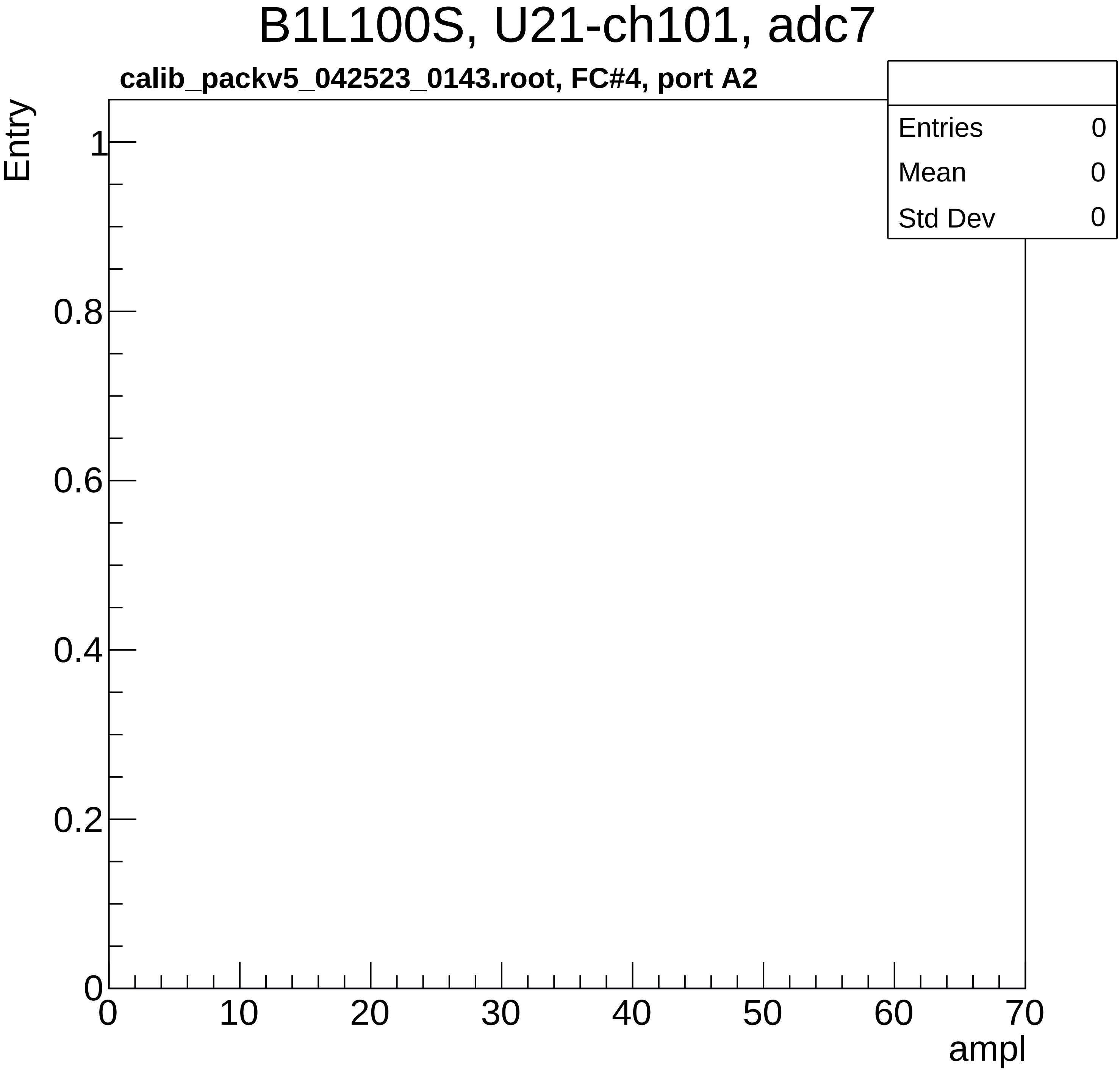
B1L100S, U21-ch101, adc4

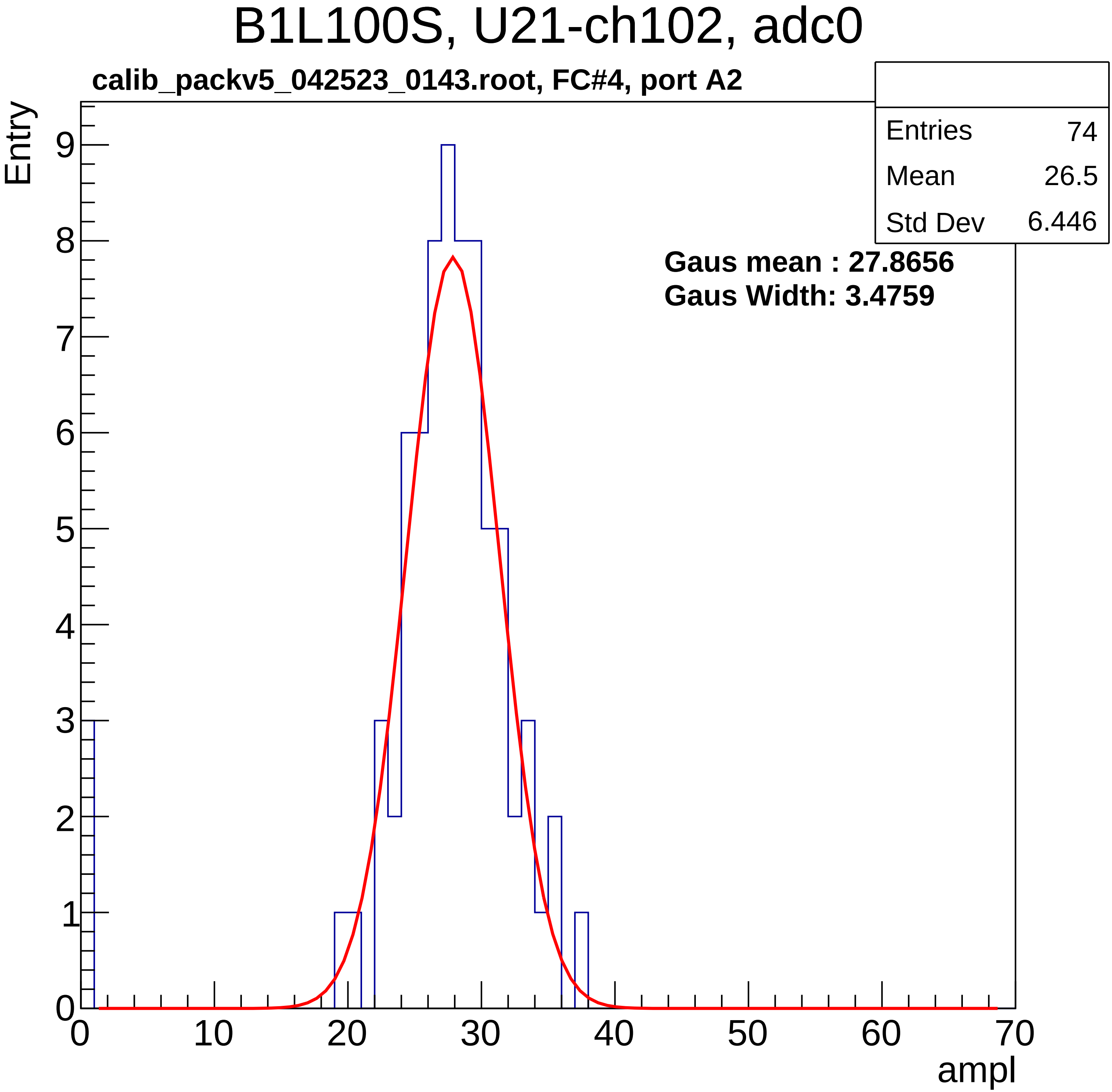
calib_packv5_042523_0143.root, FC#4, port A2

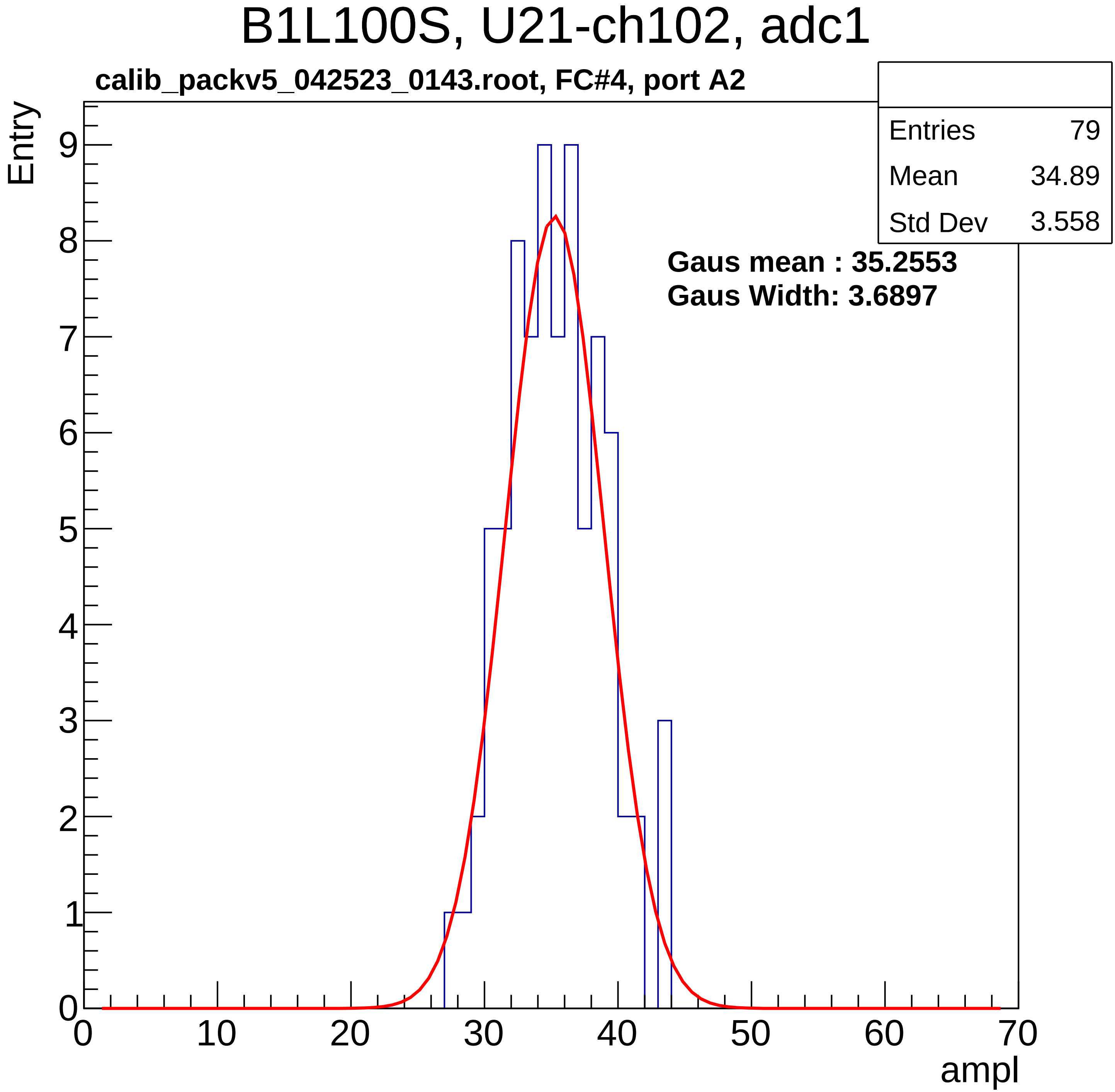


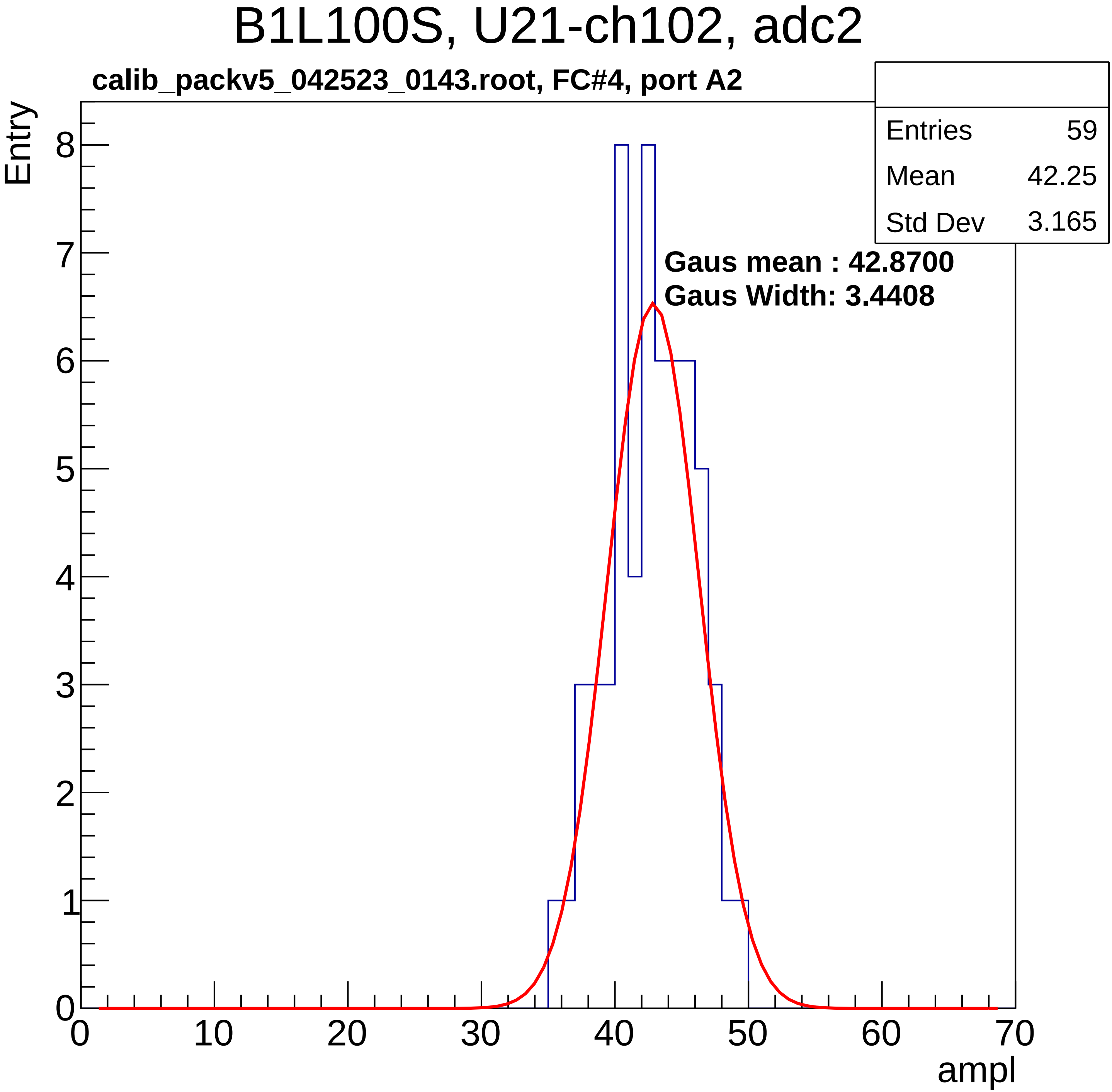

B1L100S, U21-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.97
Std Dev 9.757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

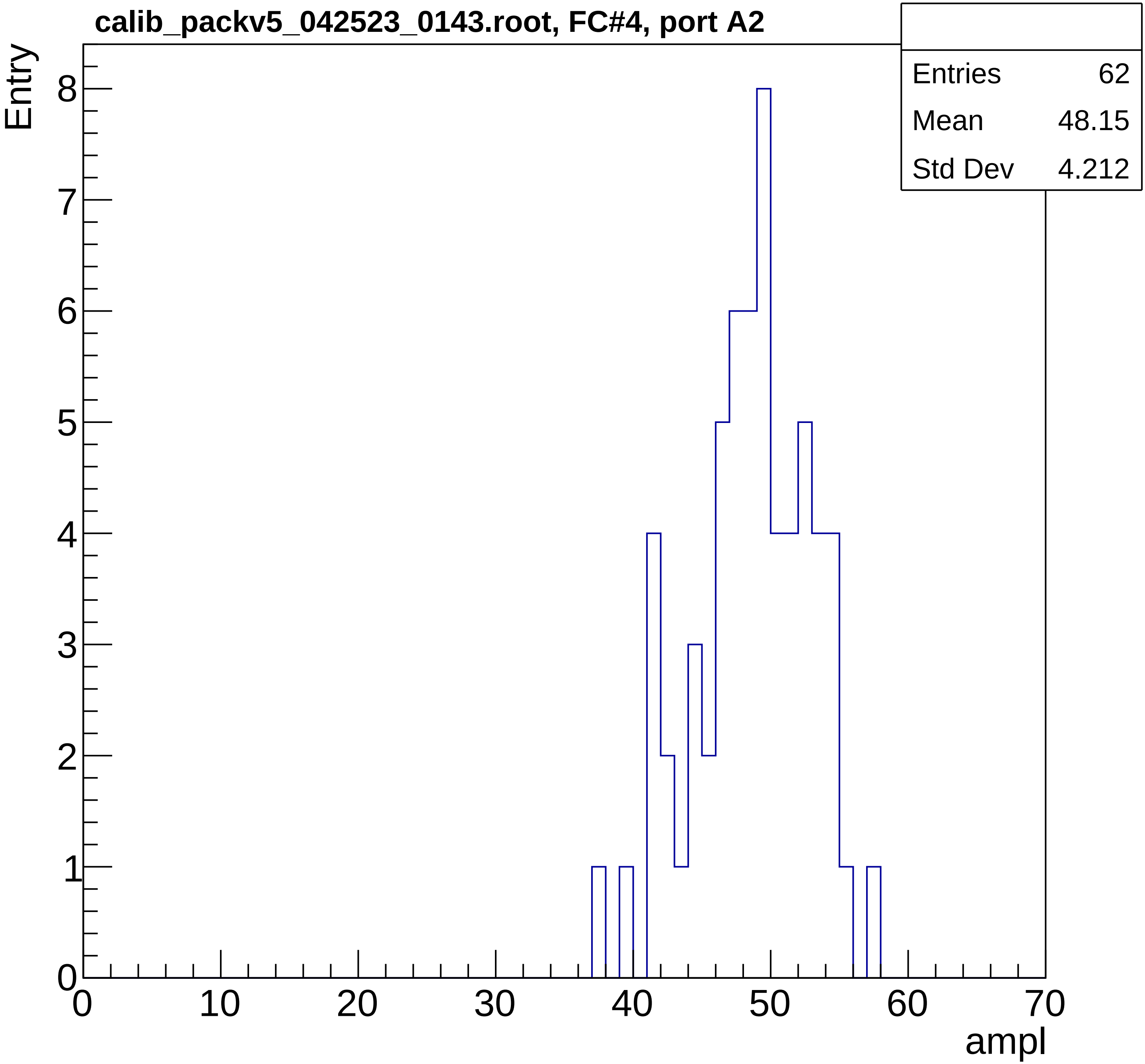

B1L100S, U21-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 74
Mean 26.5
Std Dev 6.446
Gaus mean : 27.8656
Gaus Width: 3.4759
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

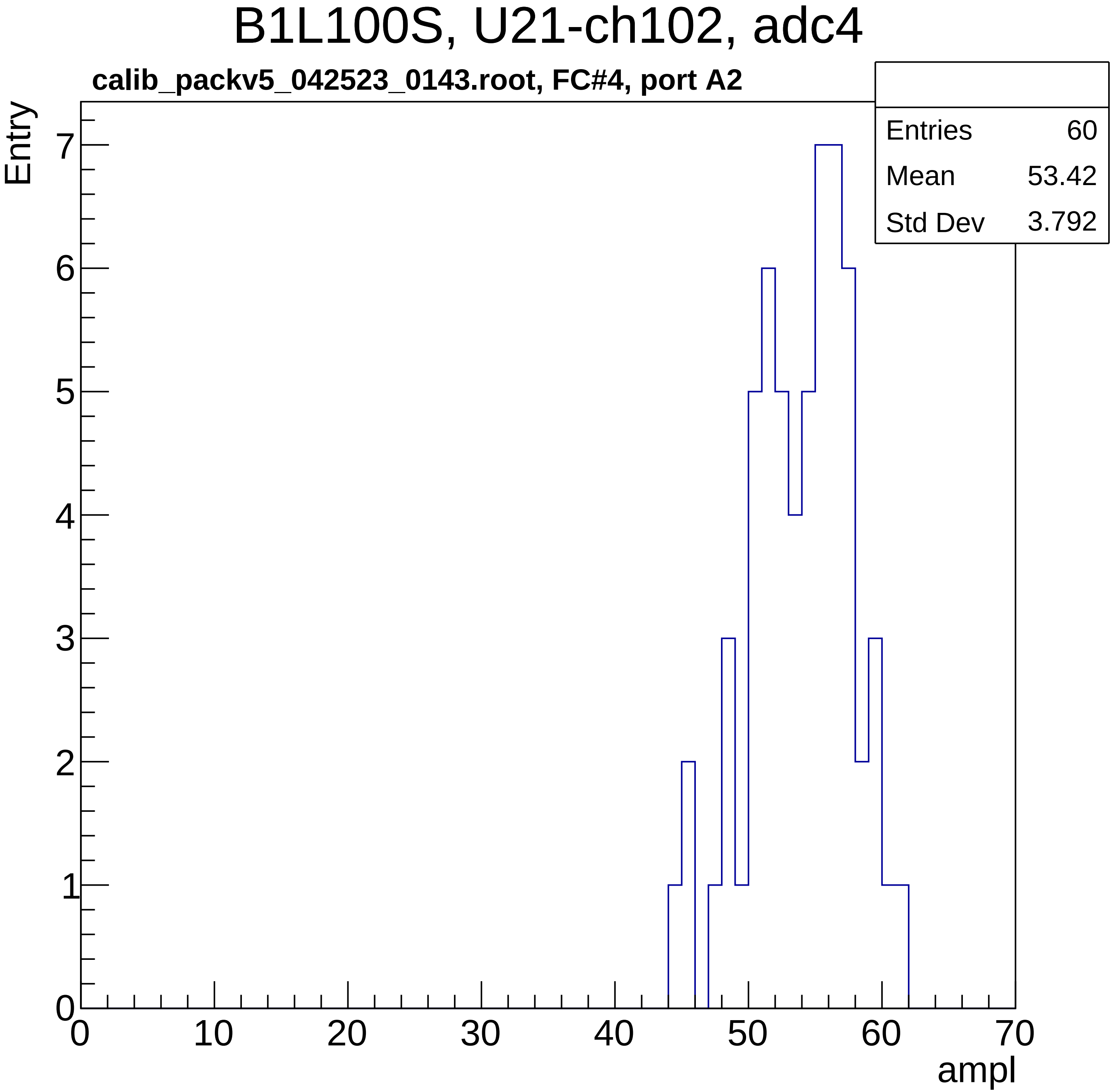

B1L100S, U21-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 34.89
Std Dev 3.558
Gaus mean : 35.2553
Gaus Width: 3.6897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
Entries 59
Mean 42.25
Std Dev 3.165
Gaus mean : 42.8700
Gaus Width: 3.4408

B1L100S, U21-ch102, adc3

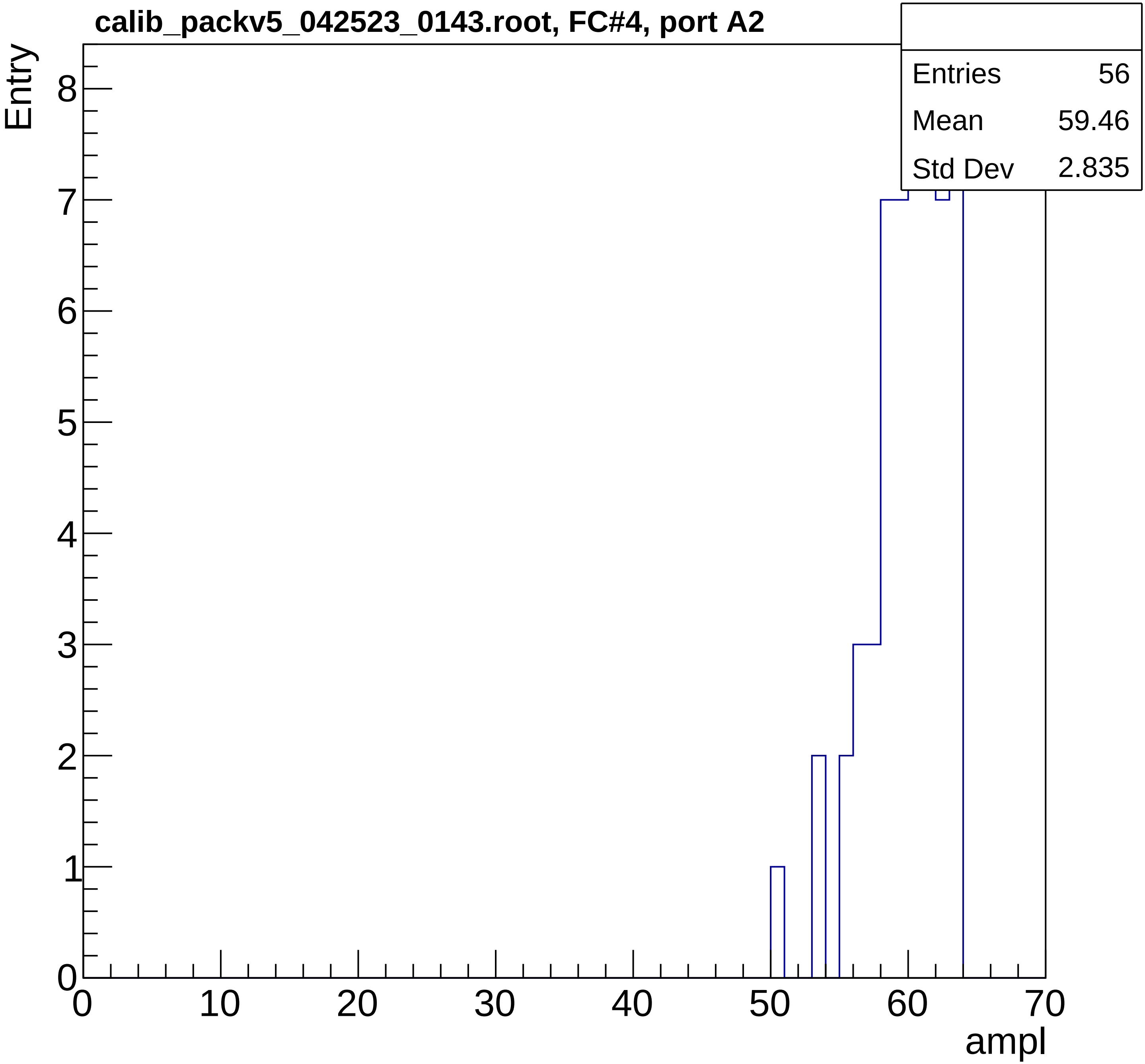
calib_packv5_042523_0143.root, FC#4, port A2

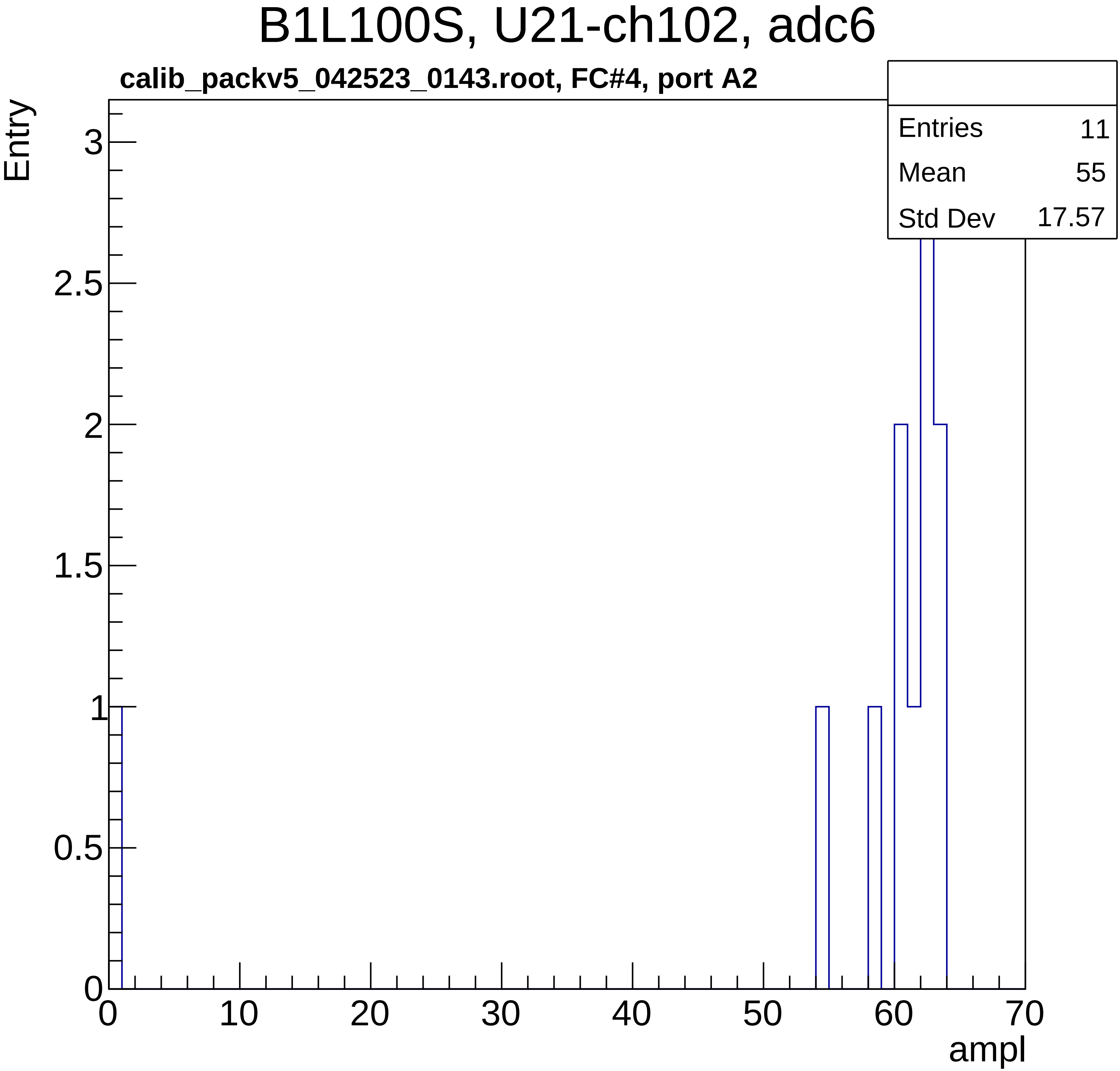


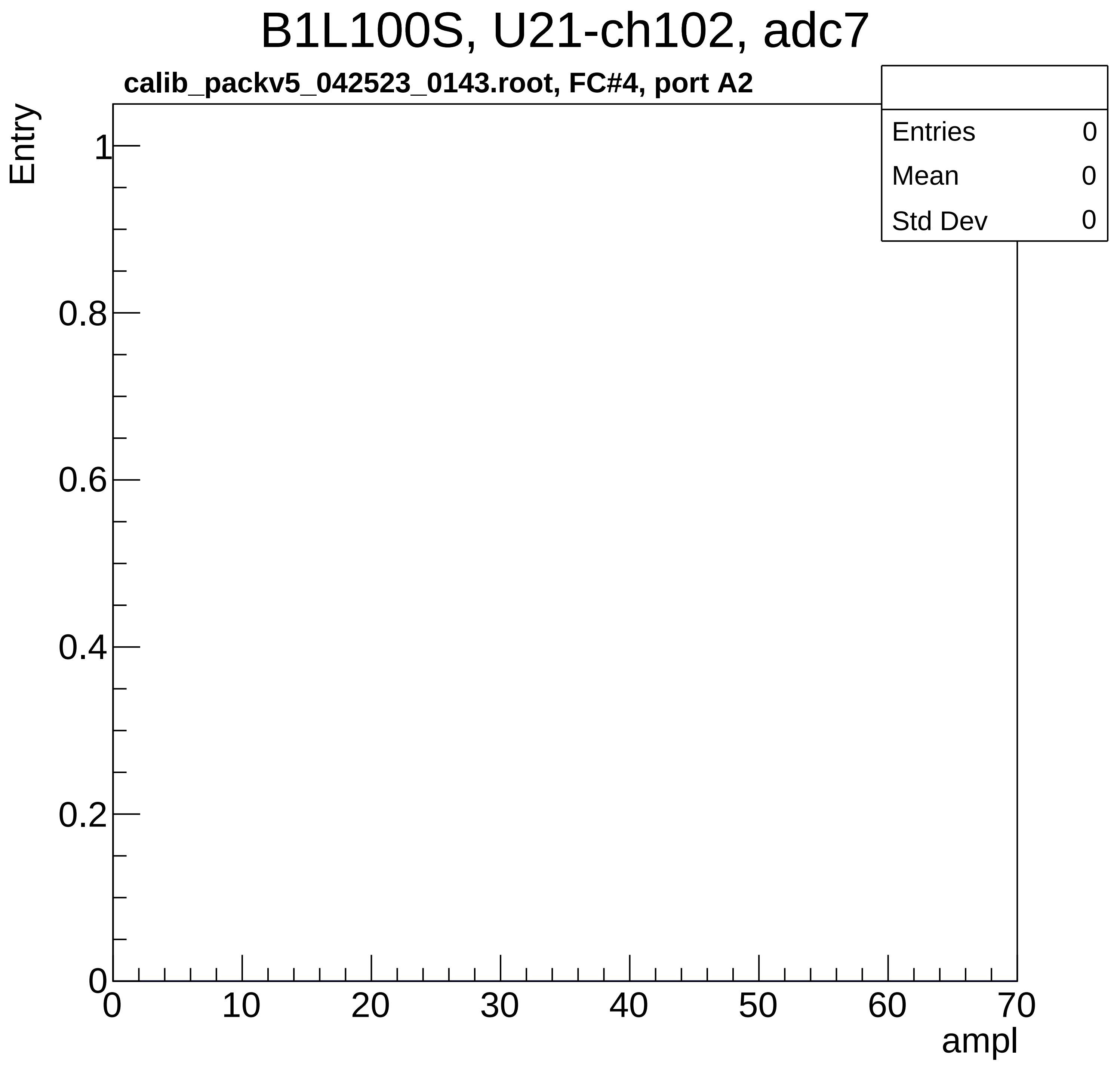

B1L100S, U21-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 53.42
Std Dev 3.792
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

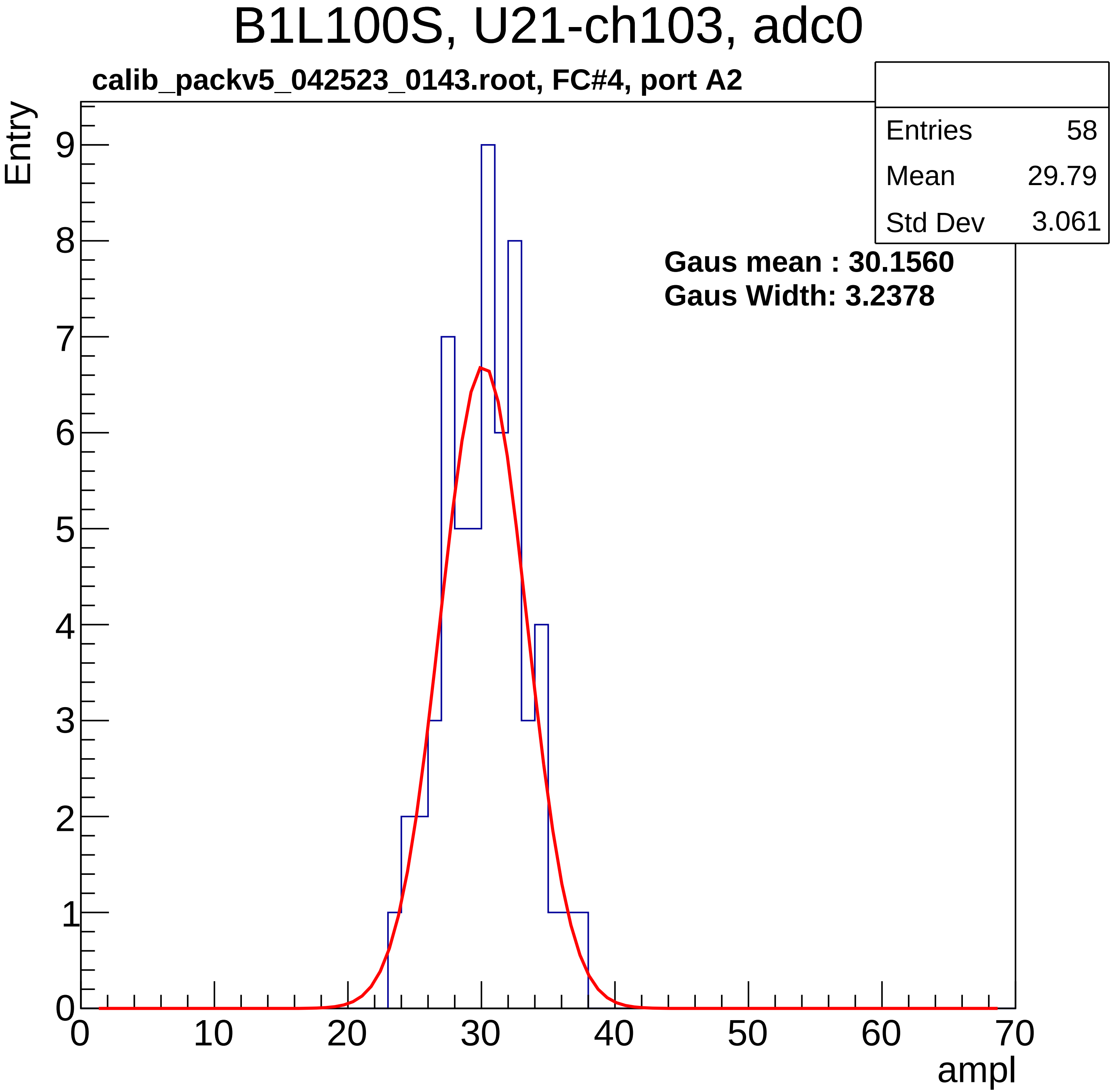
B1L100S, U21-ch102, adc5

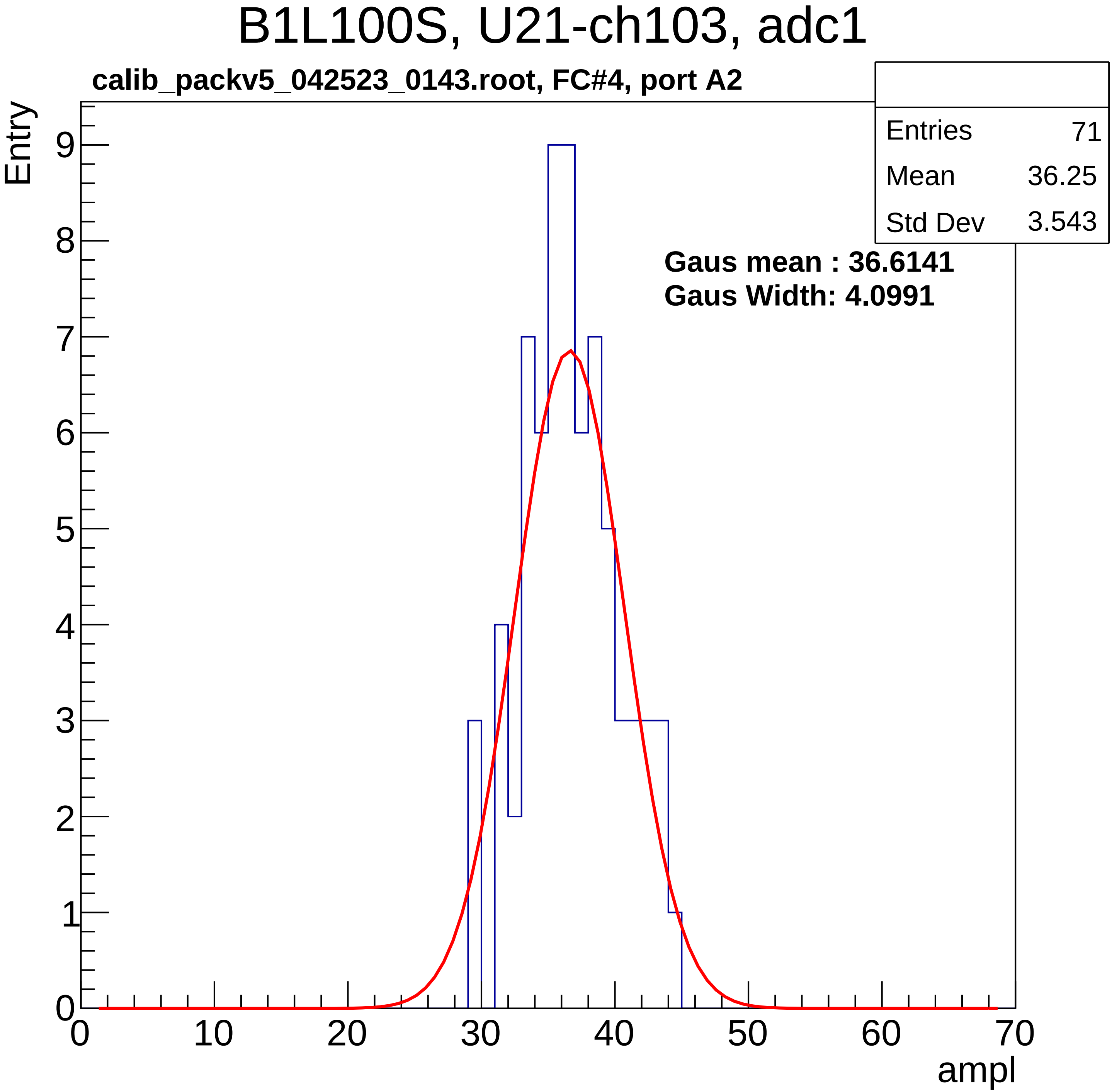
calib_packv5_042523_0143.root, FC#4, port A2

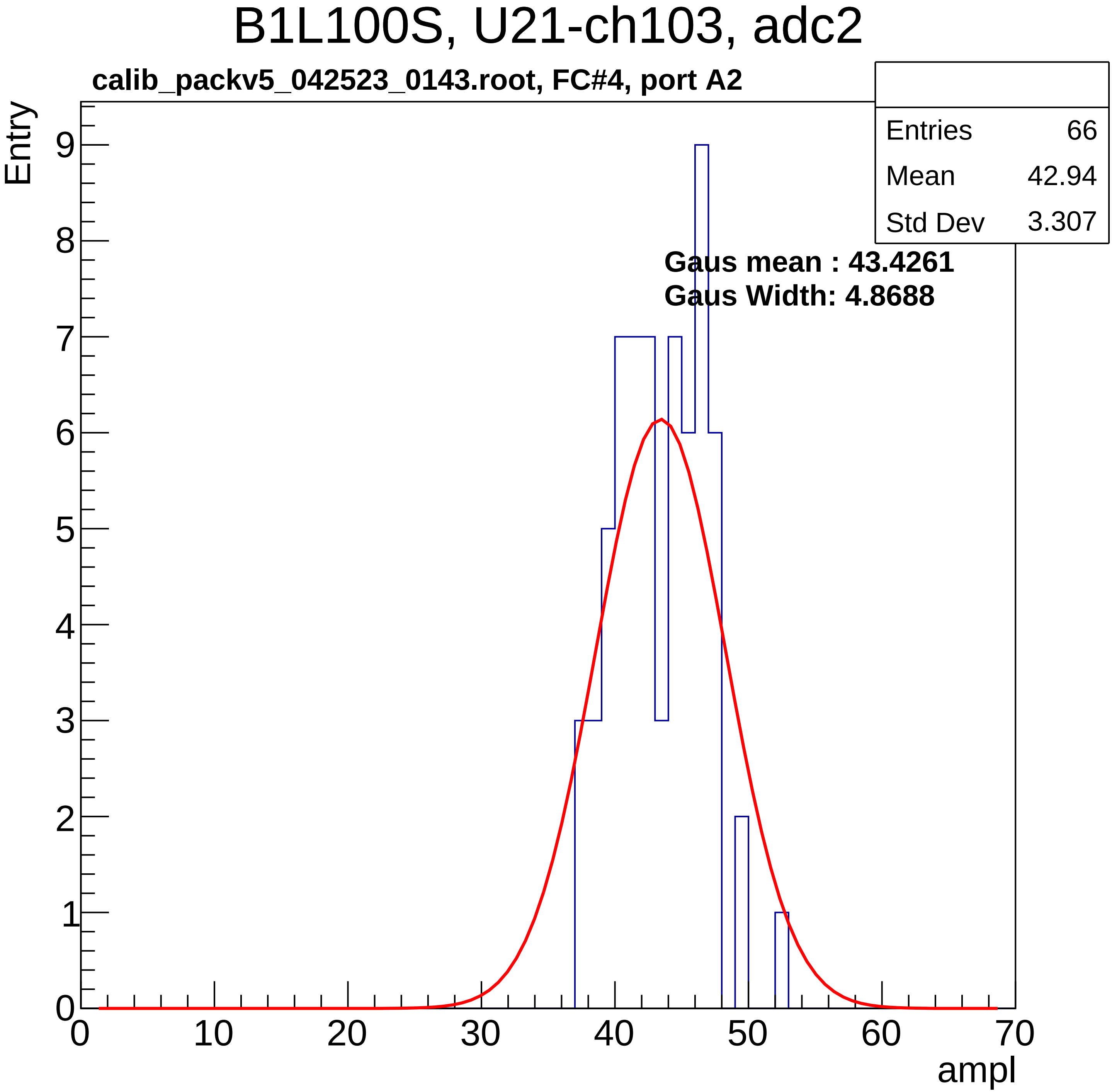



B1L100S, U21-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 11
Mean 55
Std Dev 17.57
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

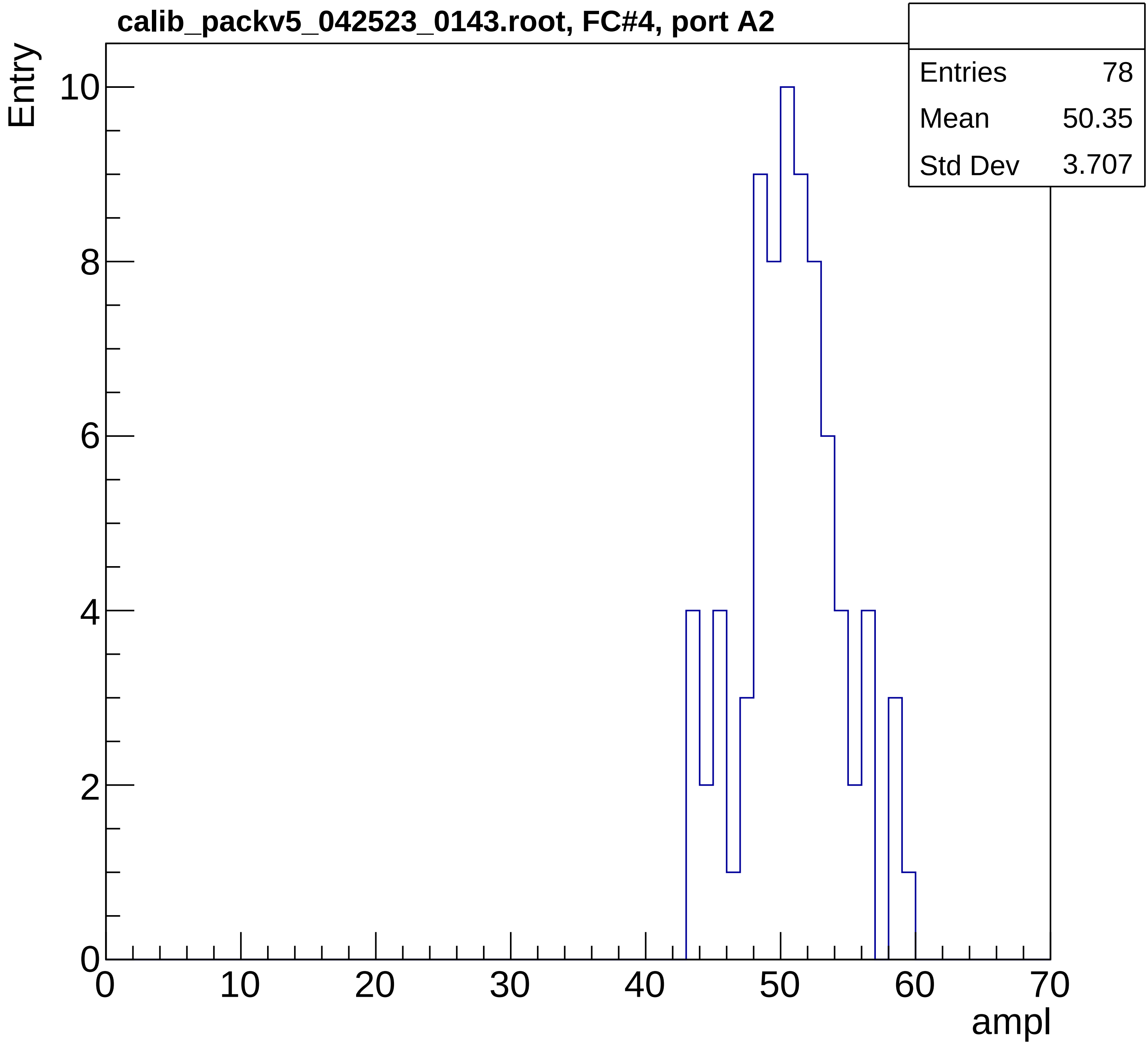

B1L100S, U21-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 29.79
Std Dev 3.061
Gaus mean : 30.1560
Gaus Width: 3.2378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

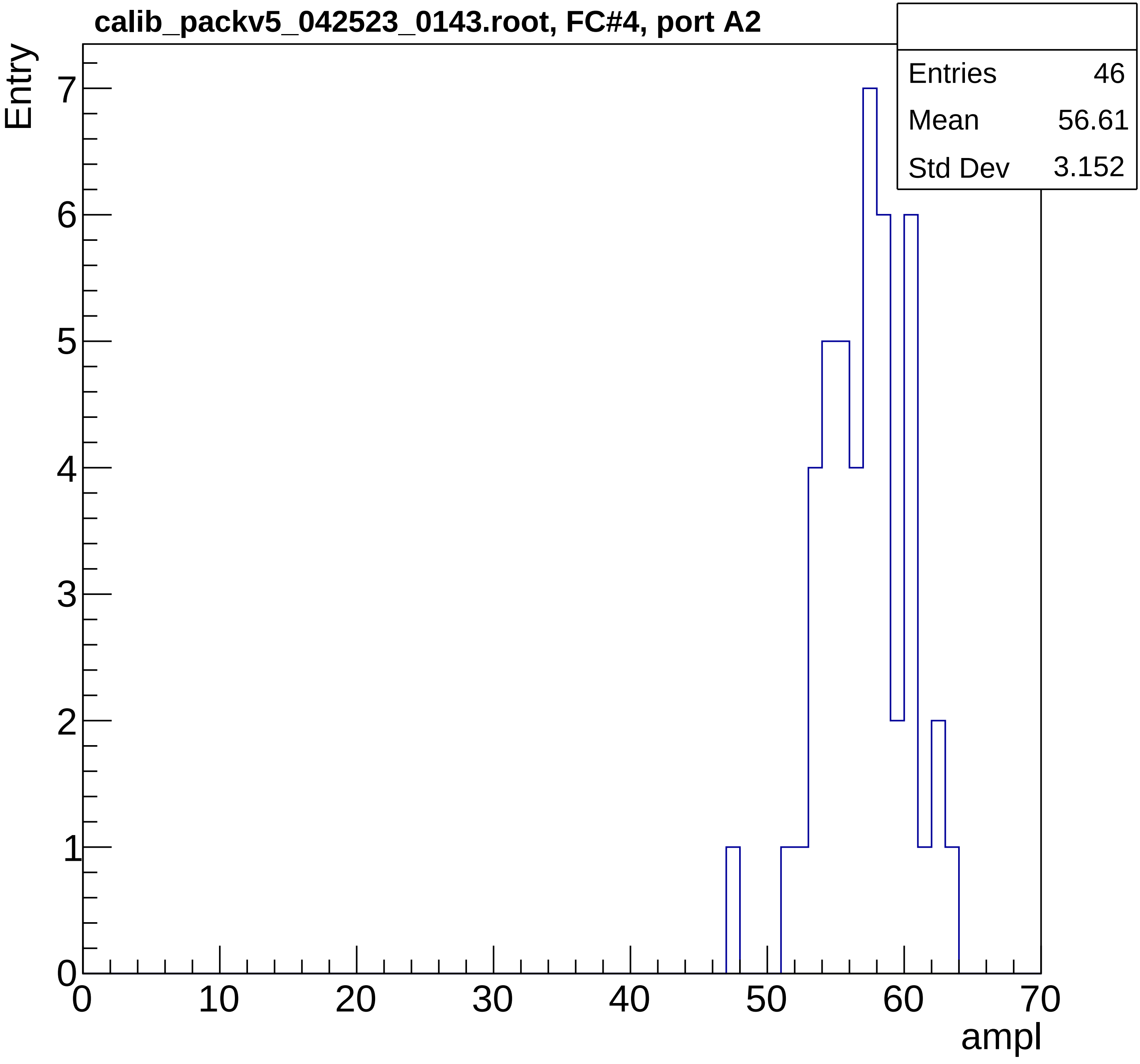

B1L100S, U21-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
Entries 71
Mean 36.25
Std Dev 3.543
Gaus mean : 36.6141
Gaus Width: 4.0991

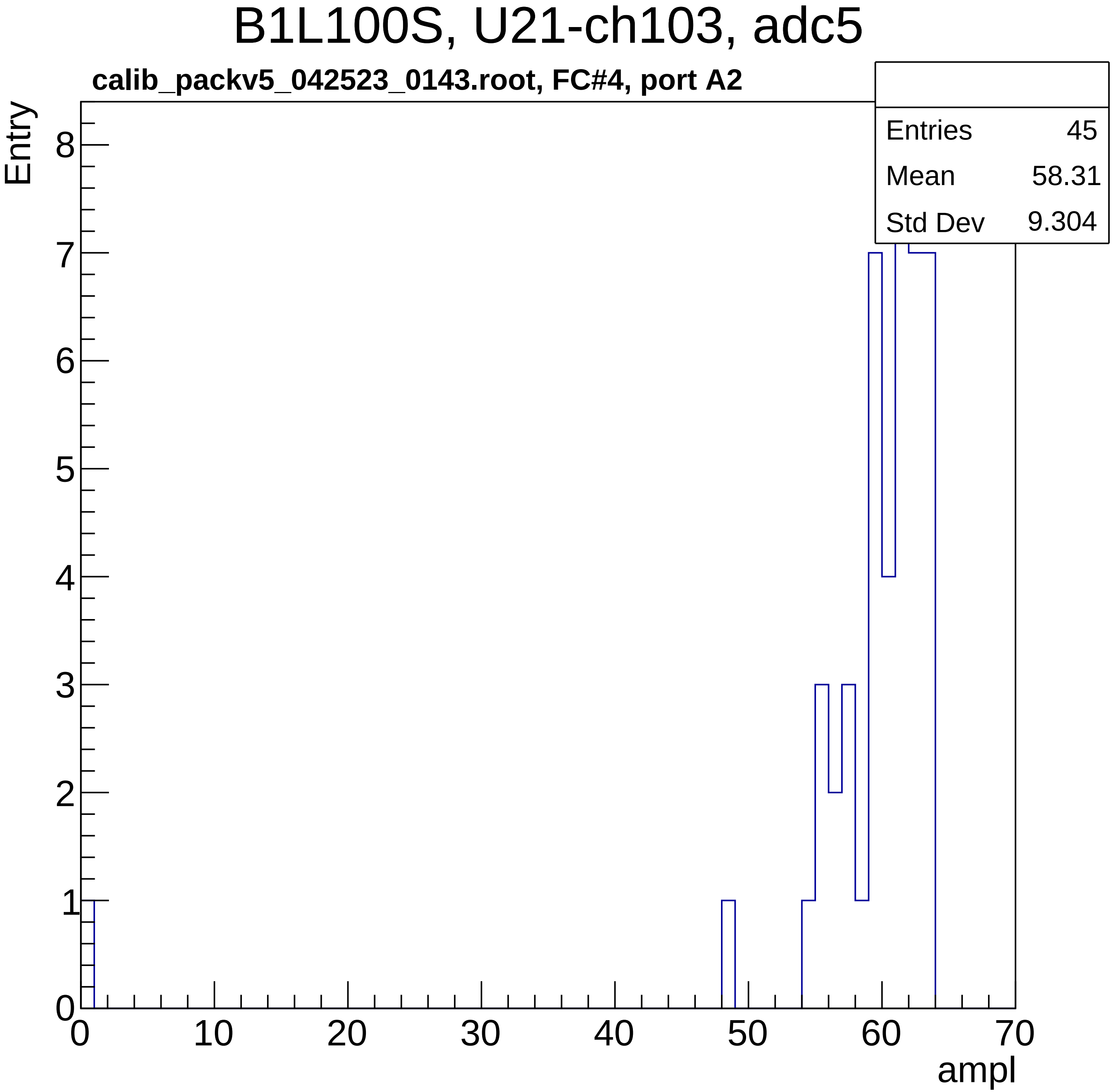

B1L100S, U21-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 66
Mean 42.94
Std Dev 3.307
Gaus mean : 43.4261
Gaus Width: 4.8688
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch103, adc3



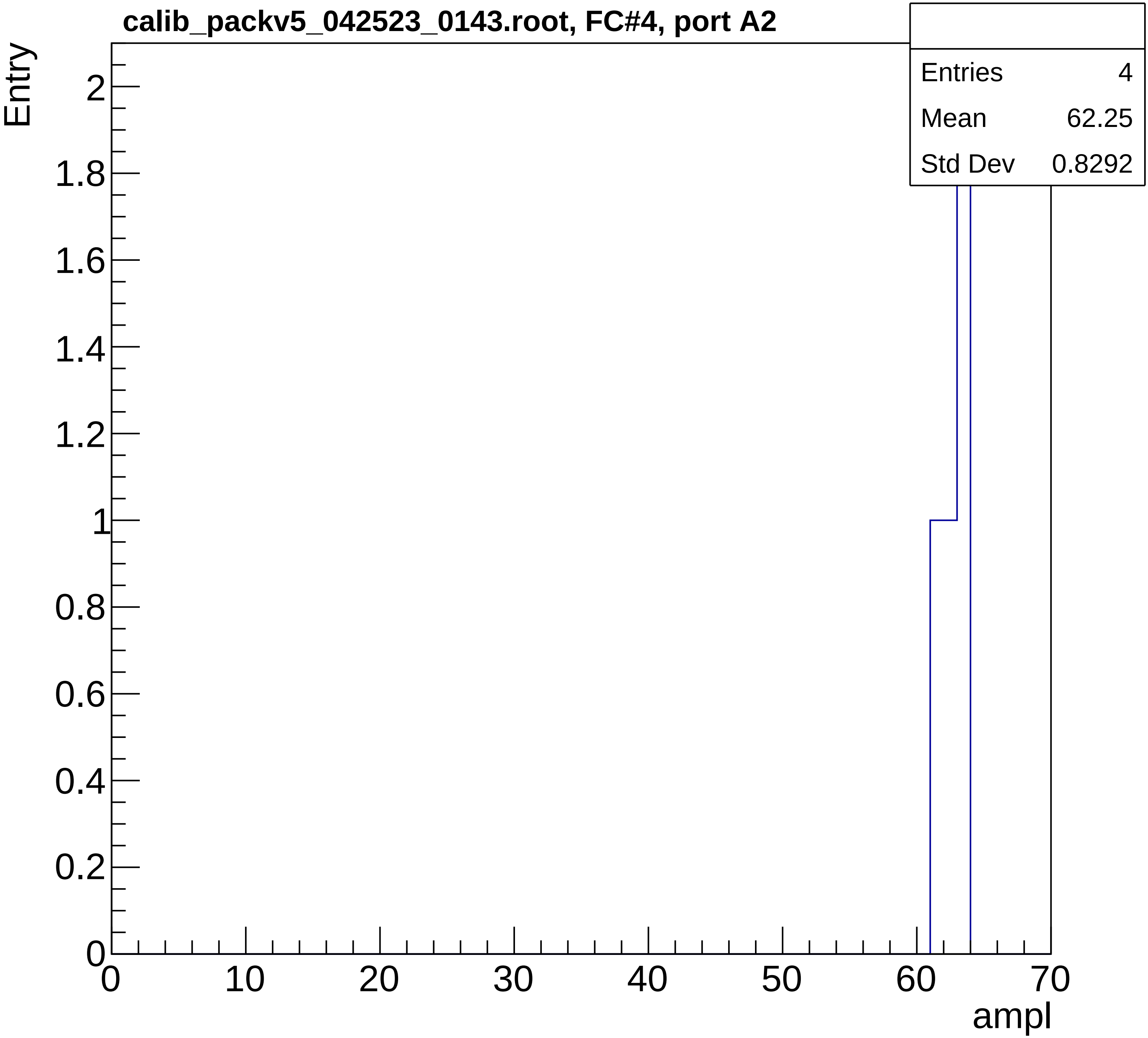
B1L100S, U21-ch103, adc4

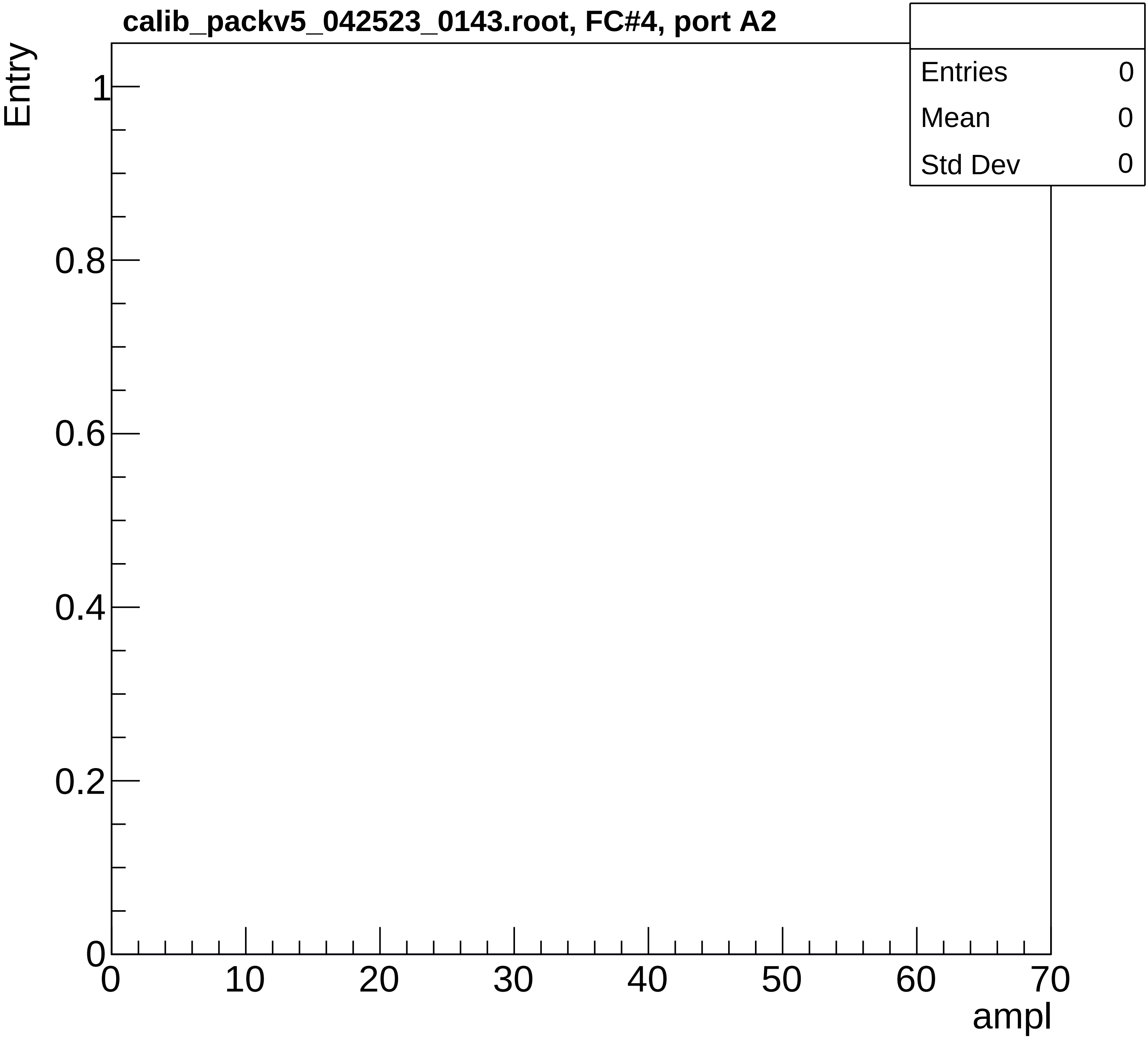




B1L100S, U21-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.31
Std Dev 9.304
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

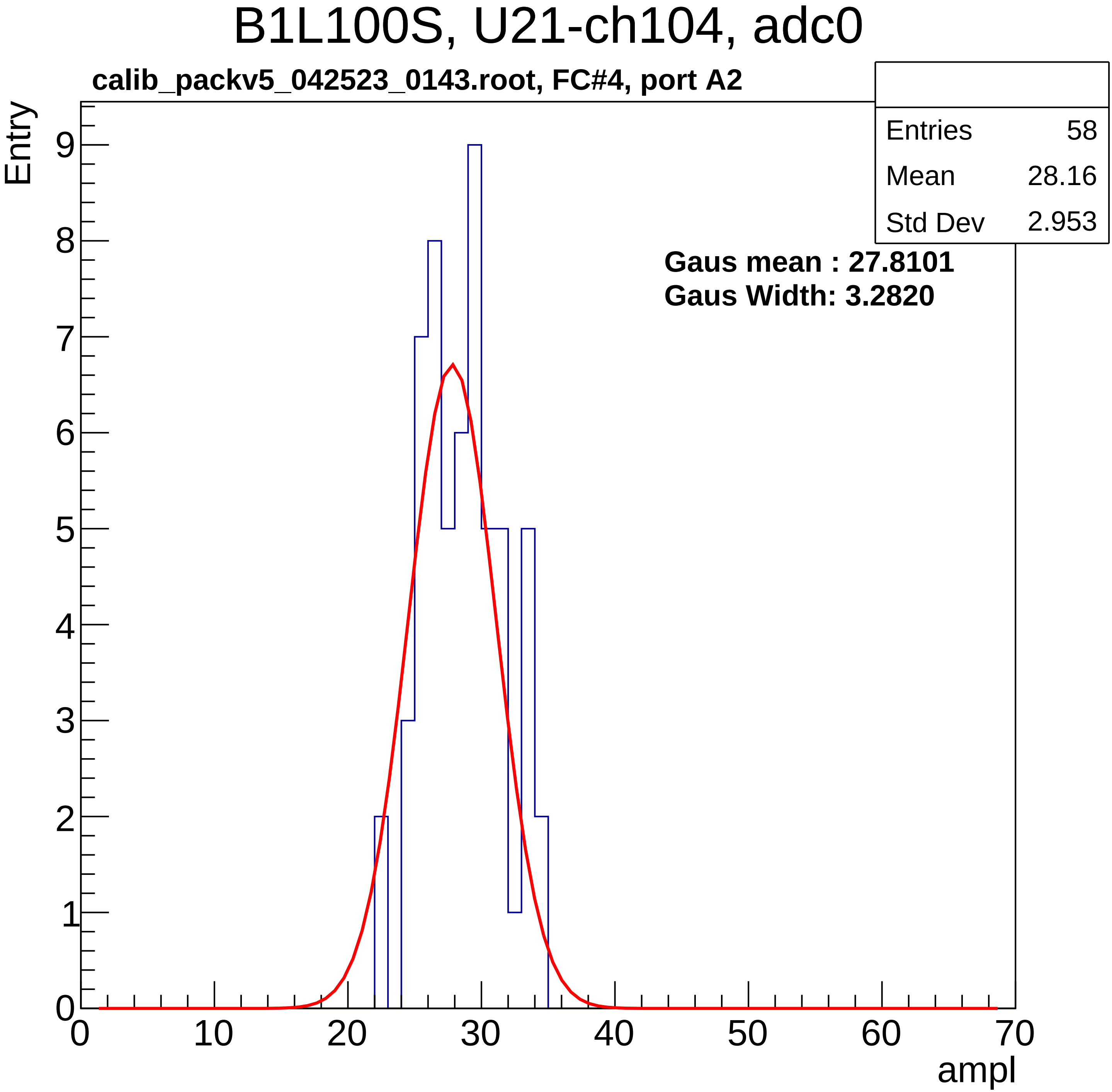
B1L100S, U21-ch103, adc6



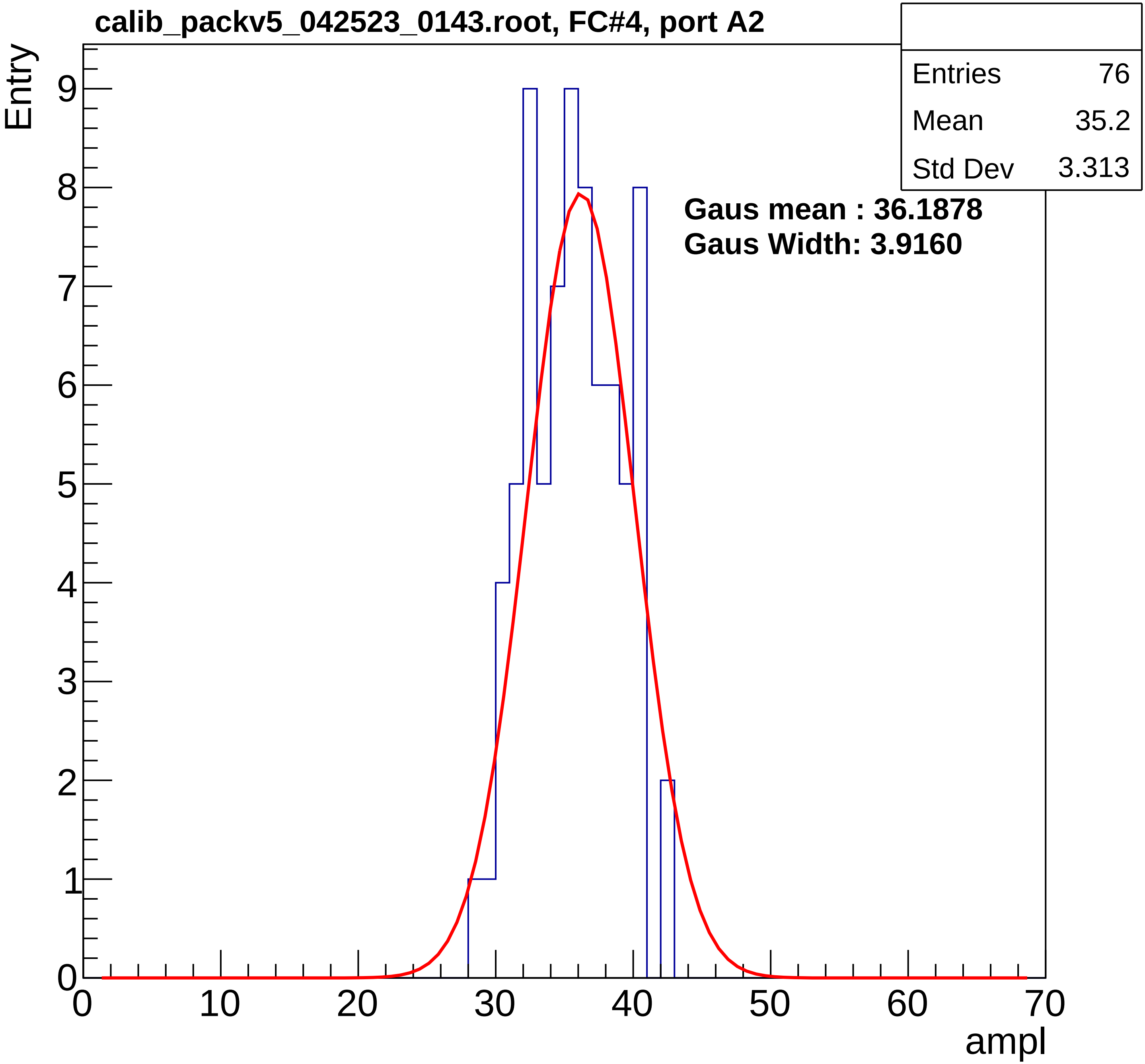


B1L100S, U21-ch103, adc7

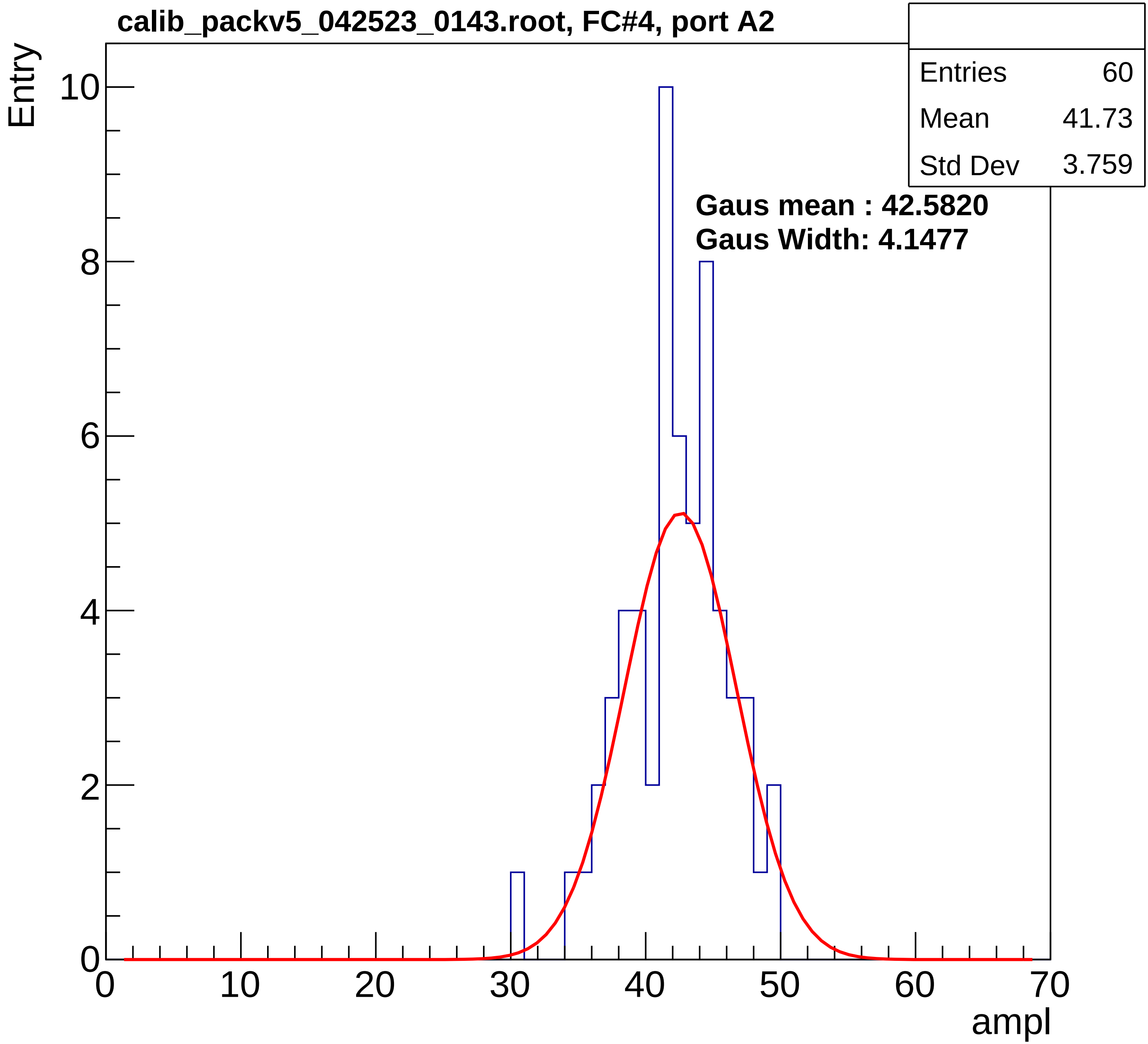
calib_packv5_042523_0143.root, FC#4, port A2

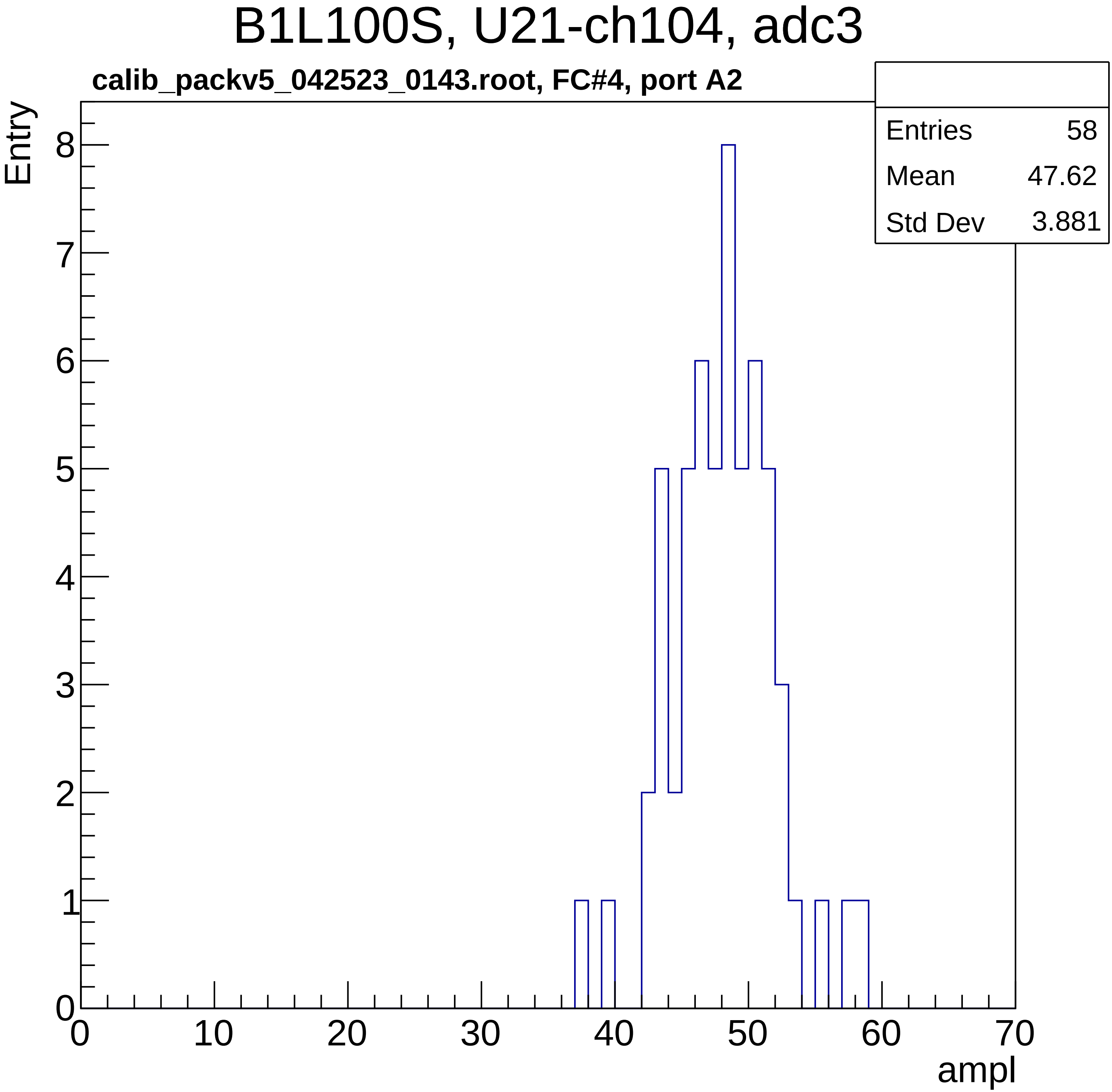

B1L100S, U21-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 28.16
Std Dev 2.953
Gaus mean : 27.8101
Gaus Width: 3.2820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

B1L100S, U21-ch104, adc1

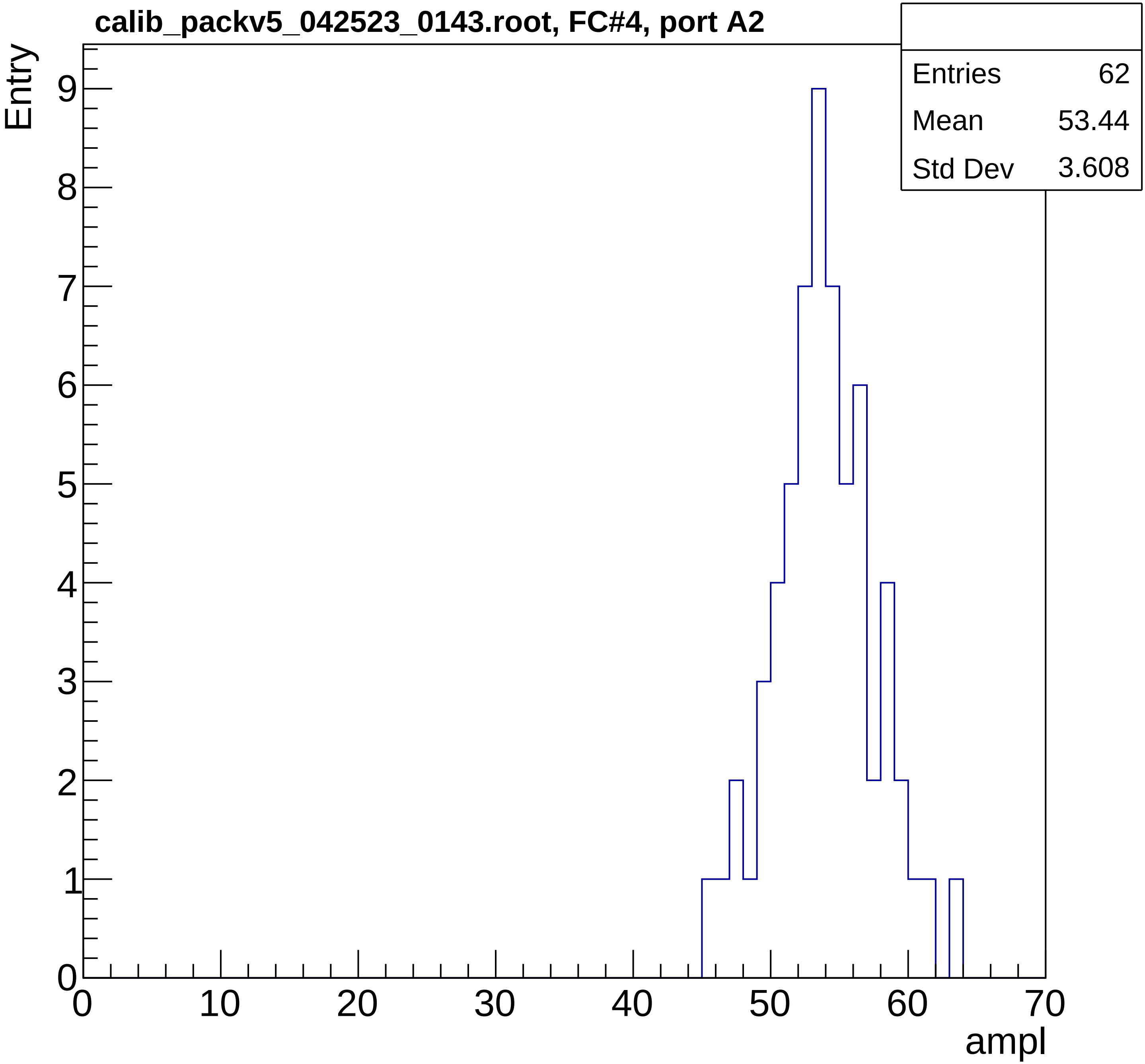


B1L100S, U21-ch104, adc2

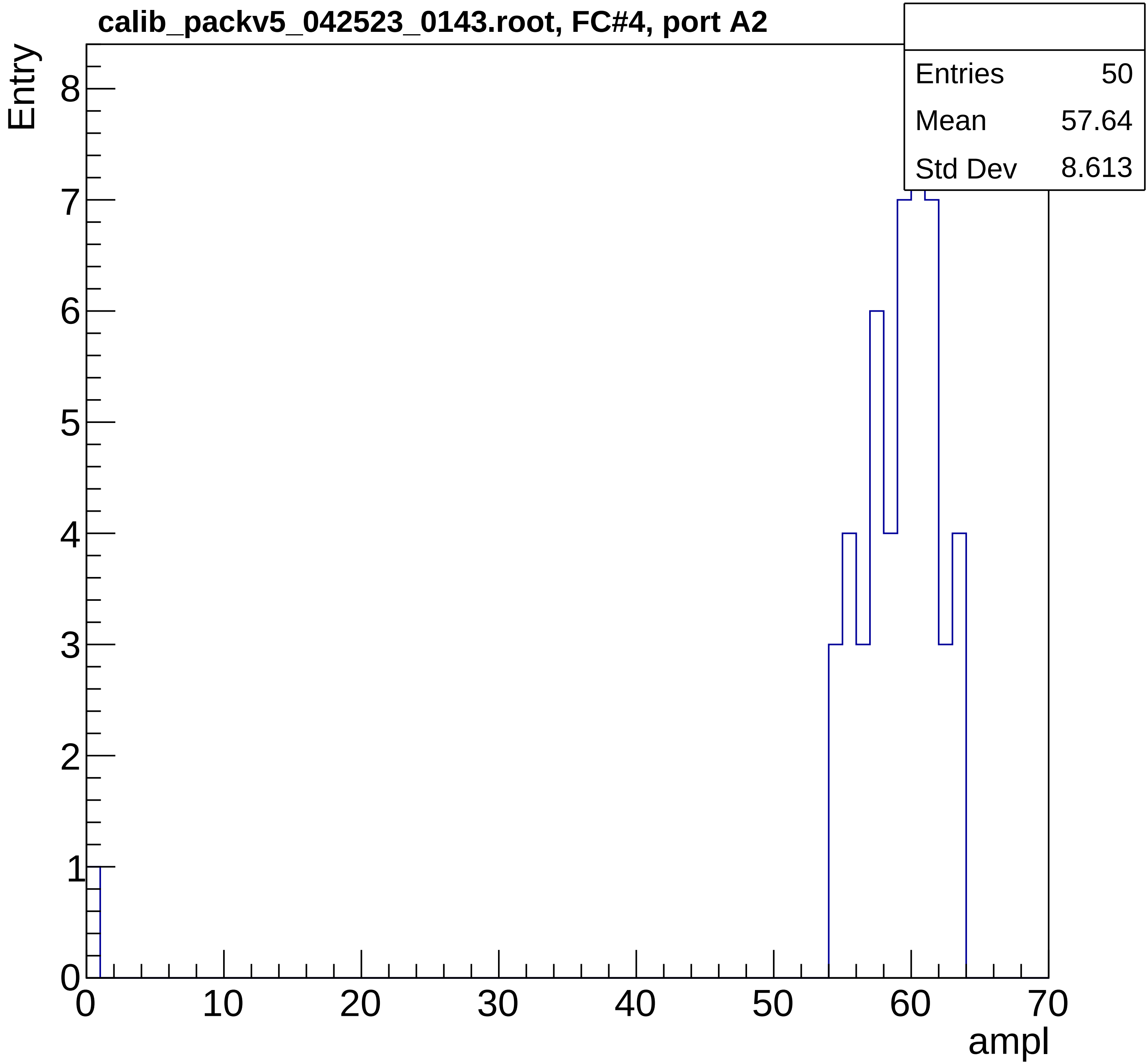



B1L100S, U21-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 47.62
Std Dev 3.881
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

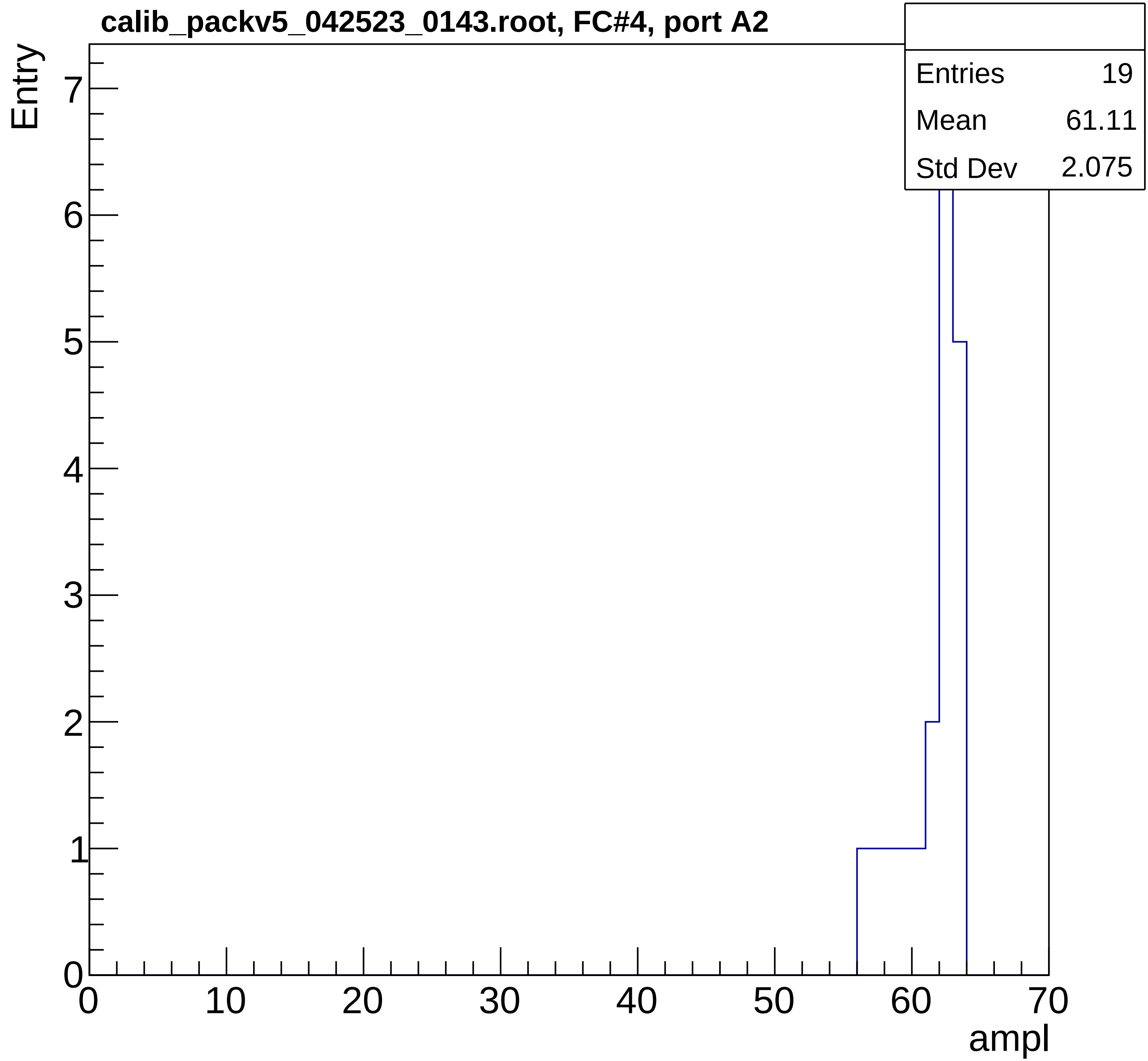
B1L100S, U21-ch104, adc4



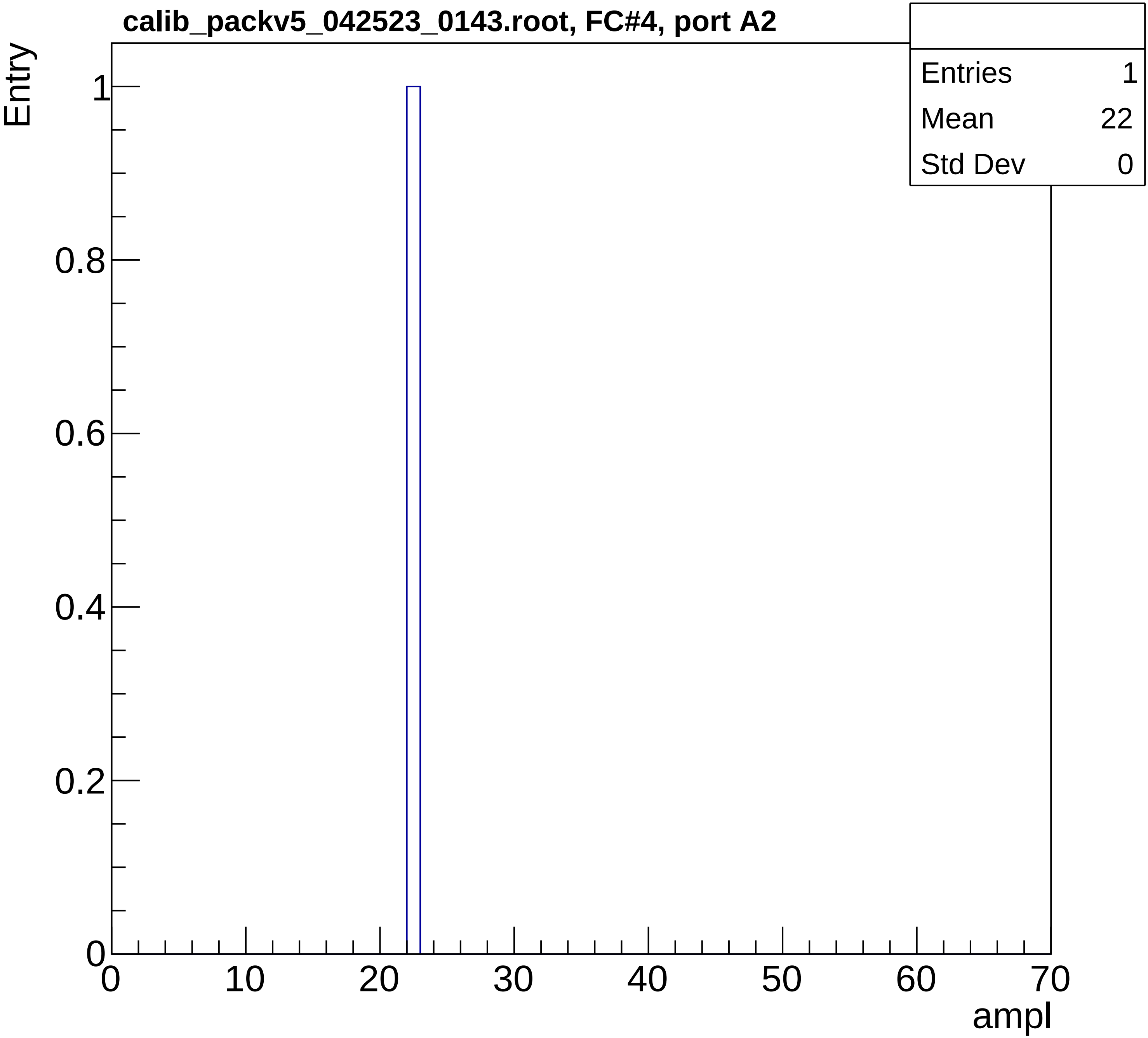
B1L100S, U21-ch104, adc5

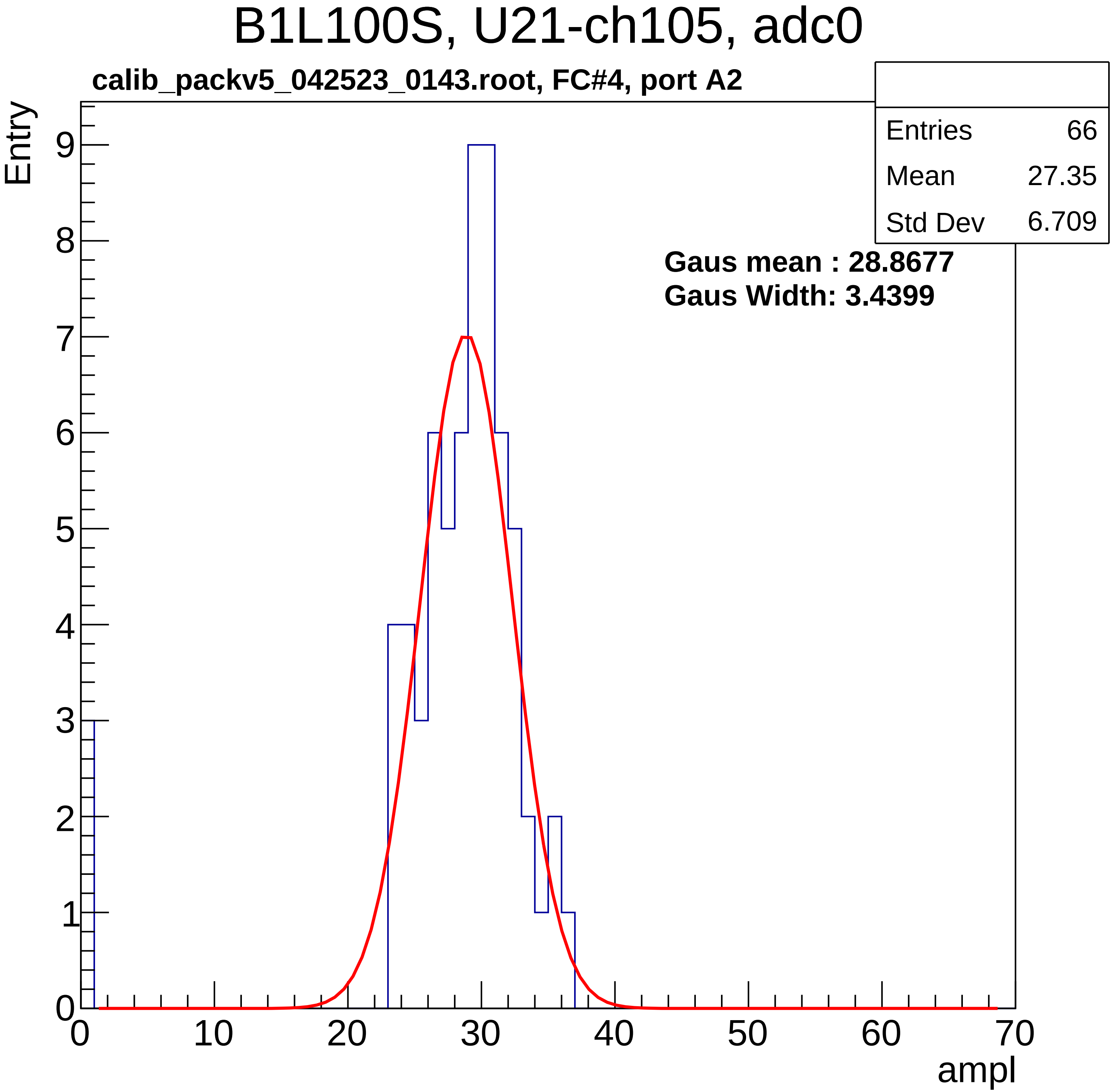


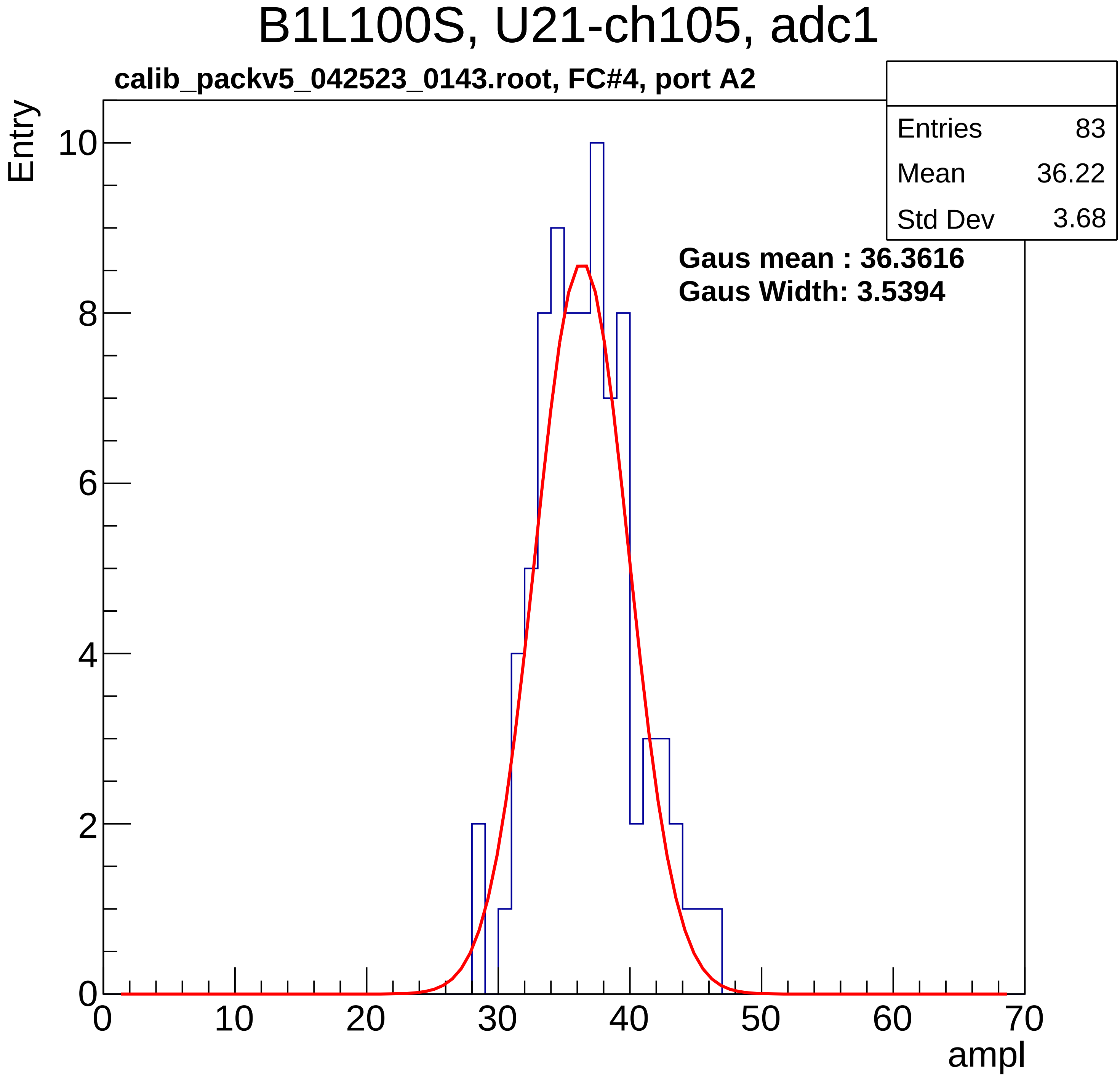
B1L100S, U21-ch104, adc6

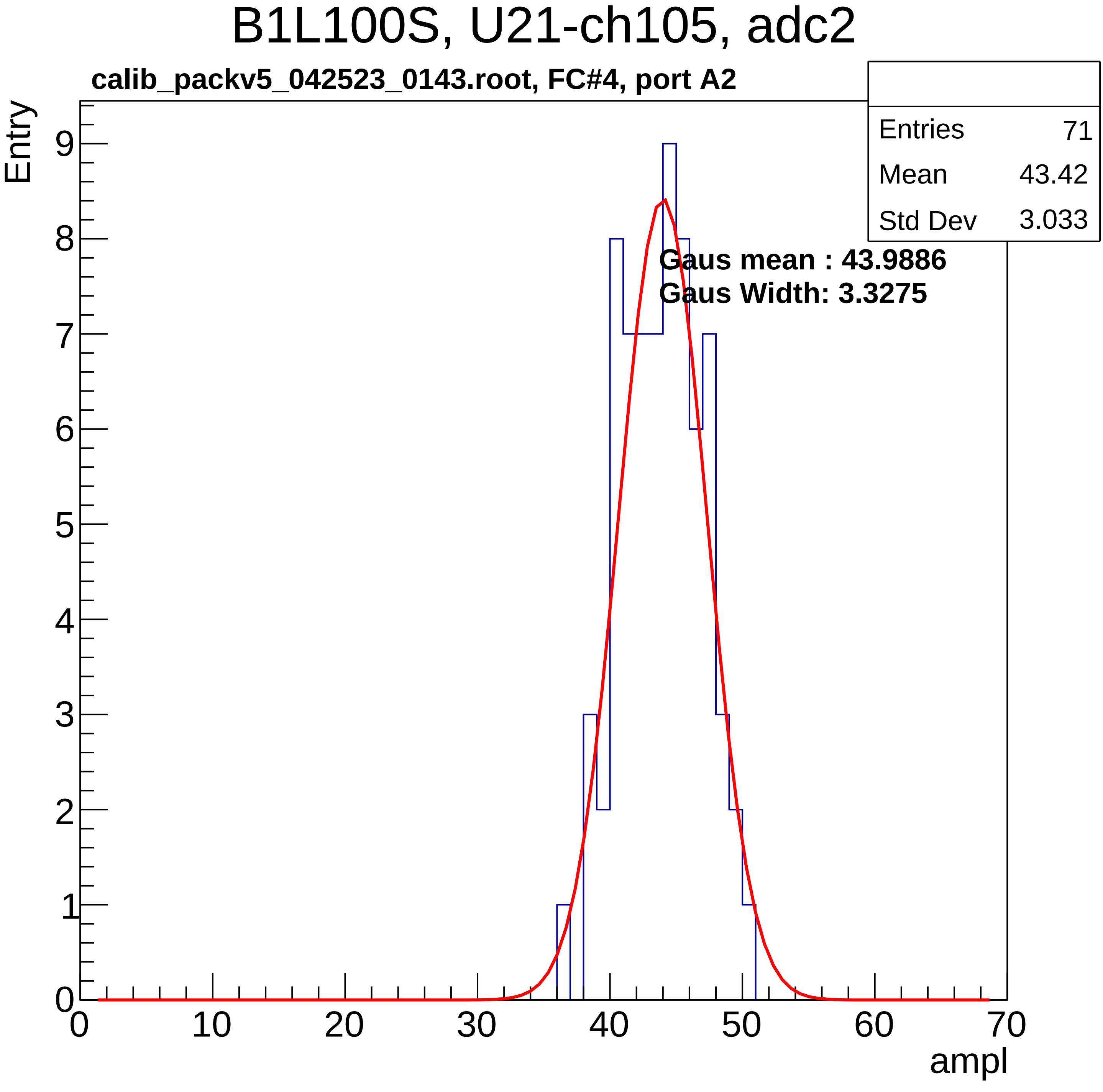


B1L100S, U21-ch104, adc7

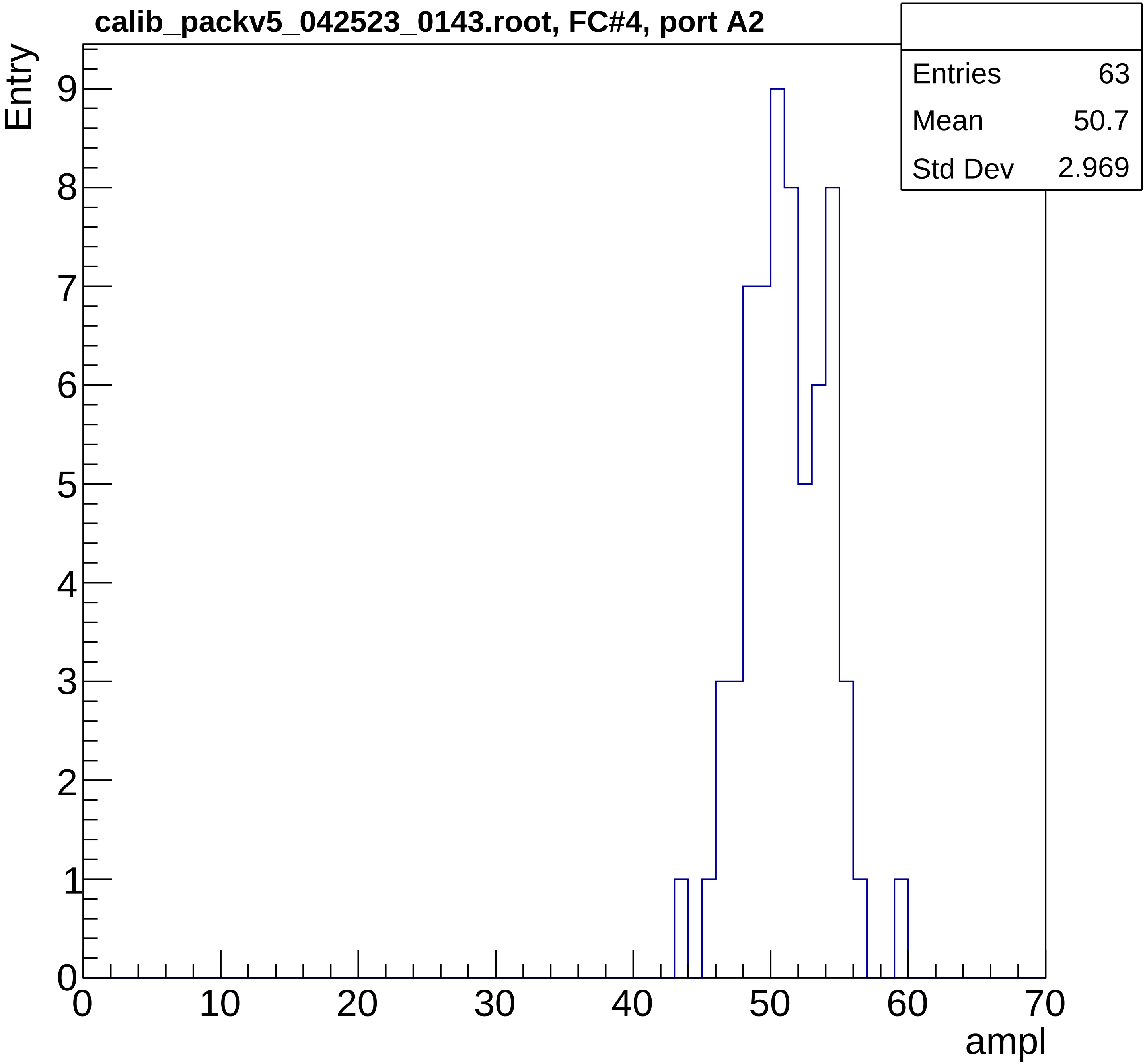


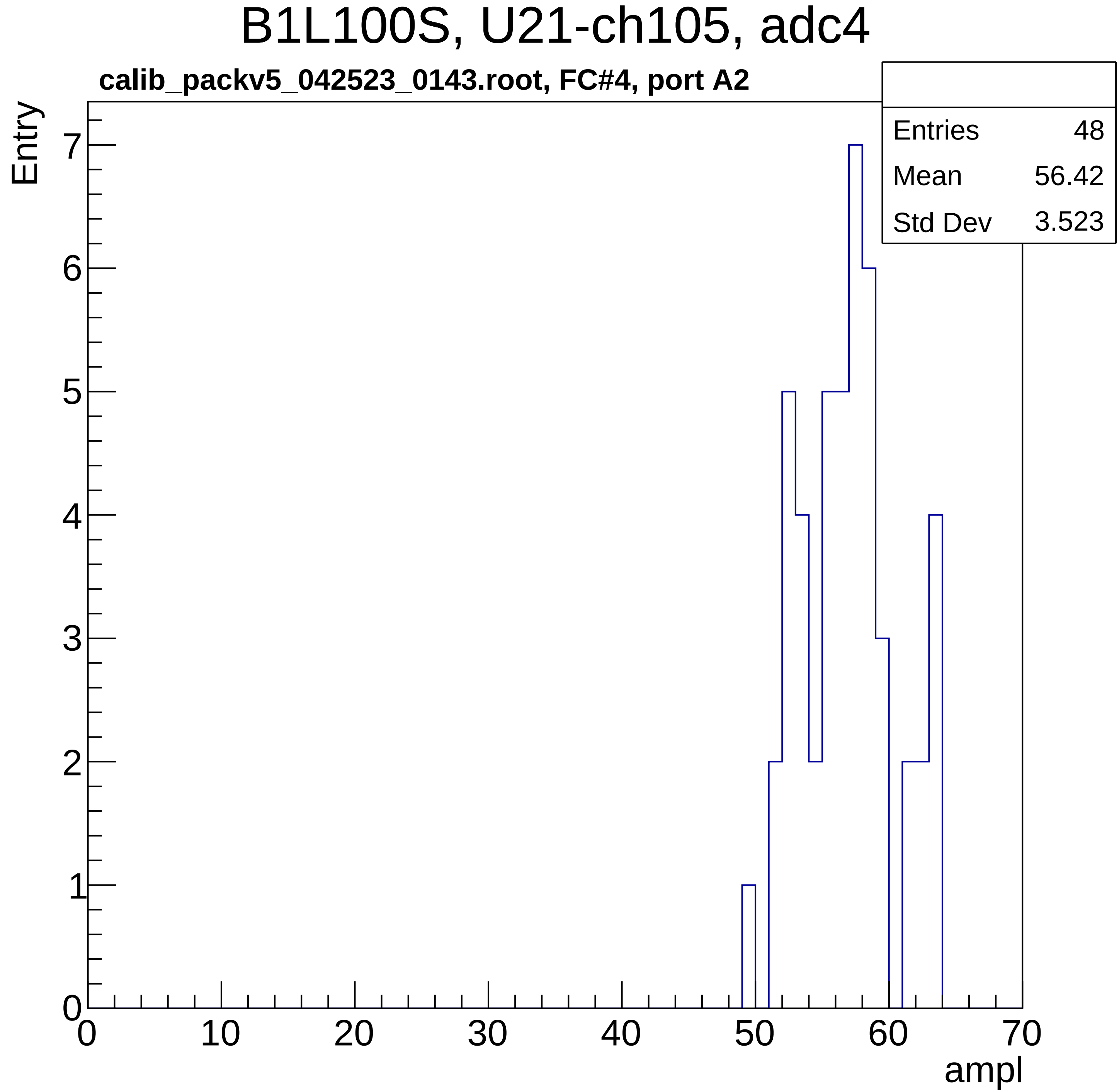

B1L100S, U21-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 27.35
Std Dev 6.709
Gaus mean : 28.8677
Gaus Width: 3.4399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.22
Std Dev 3.68
Gaus mean : 36.3616
Gaus Width: 3.5394
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

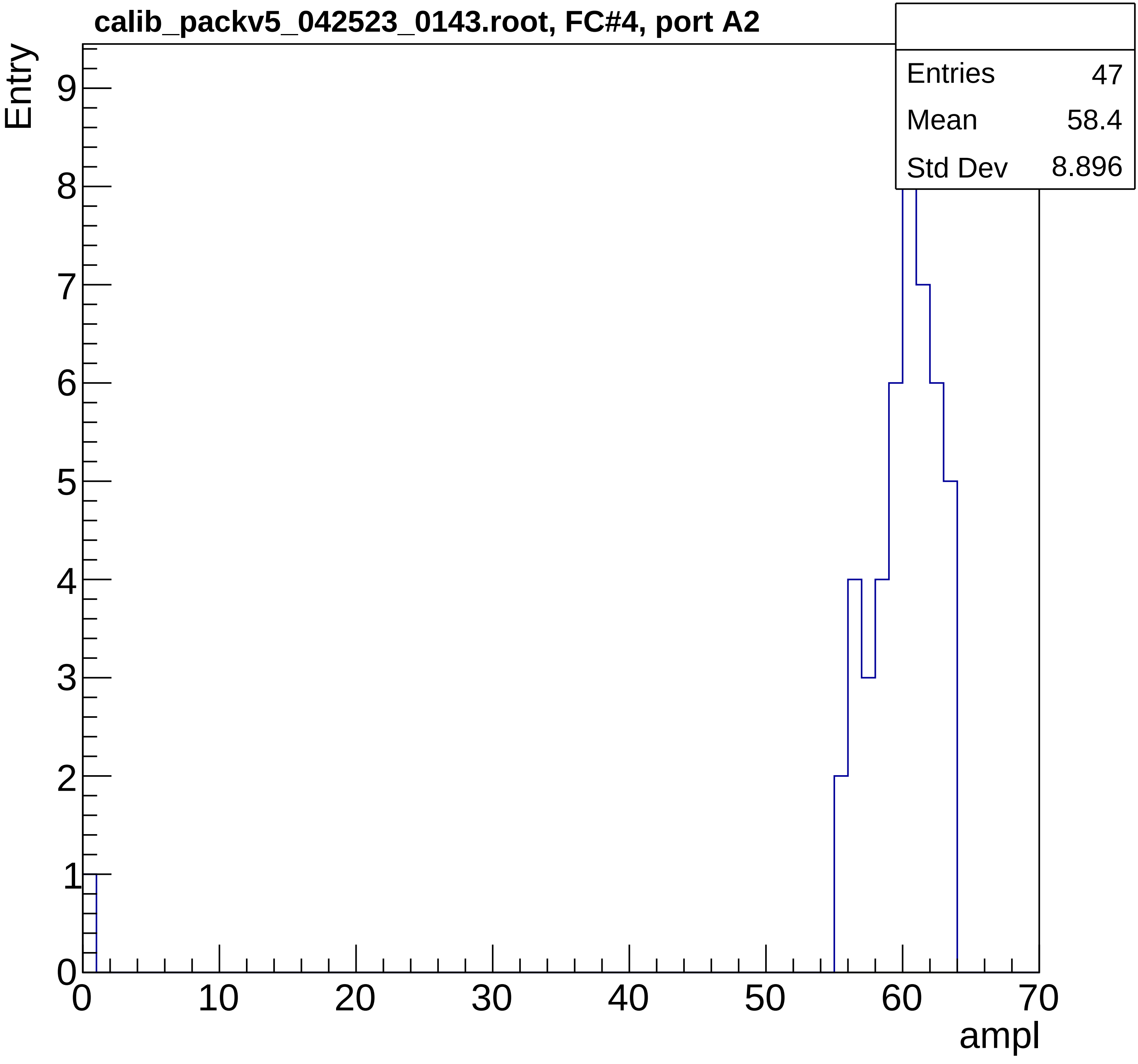

B1L100S, U21-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 43.42
Std Dev 3.033
Gaus mean : 43.9886
Gaus Width: 3.3275
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

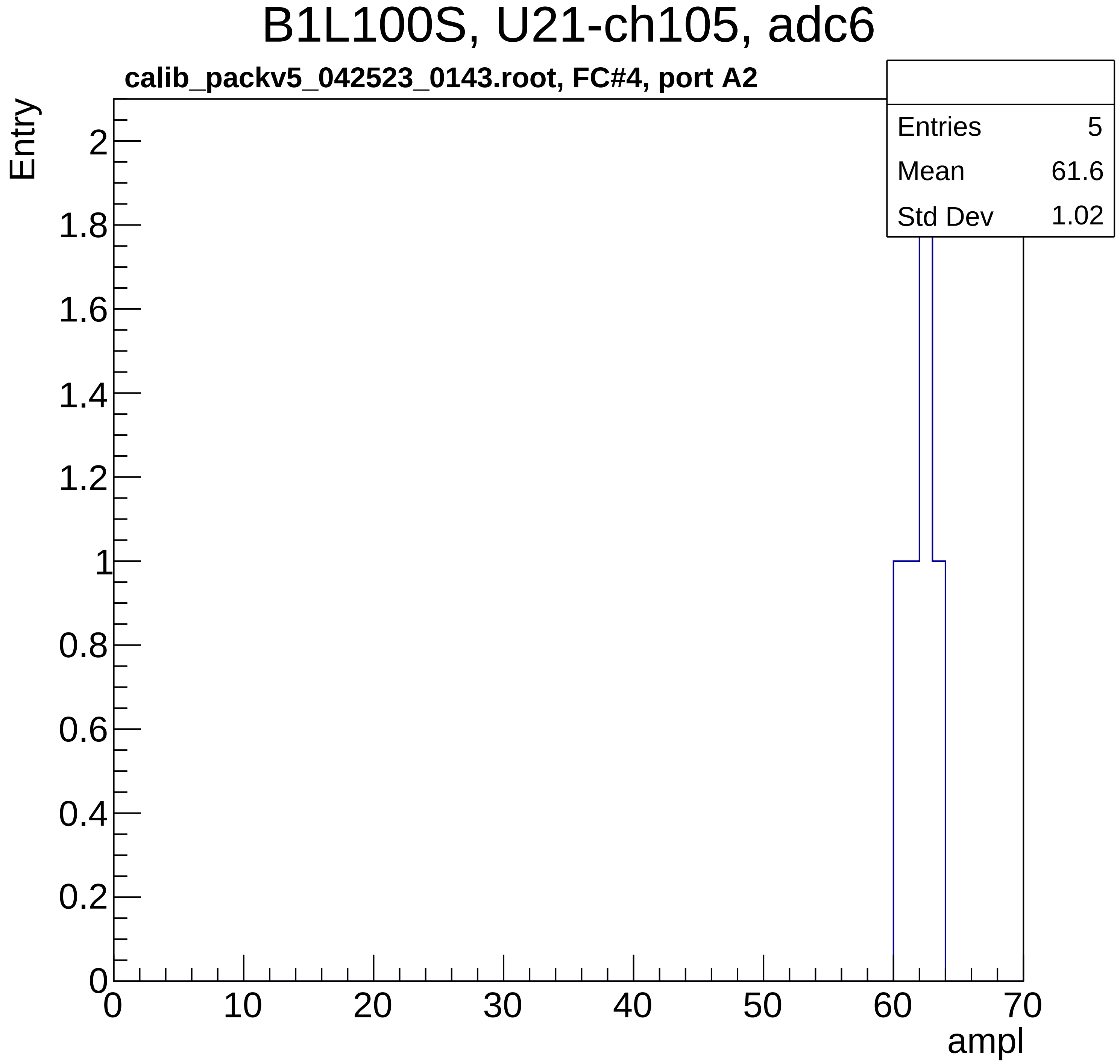
B1L100S, U21-ch105, adc3

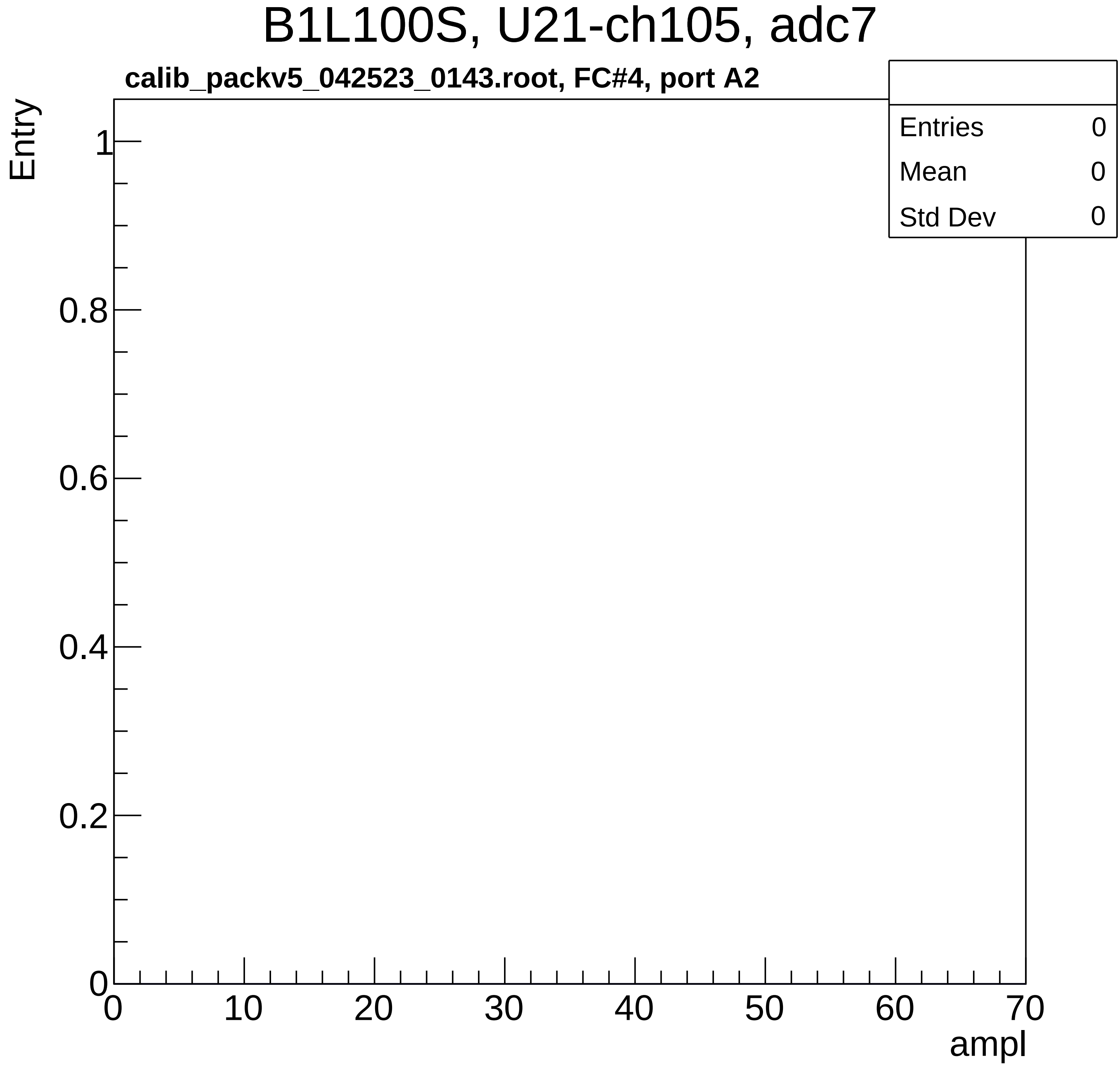


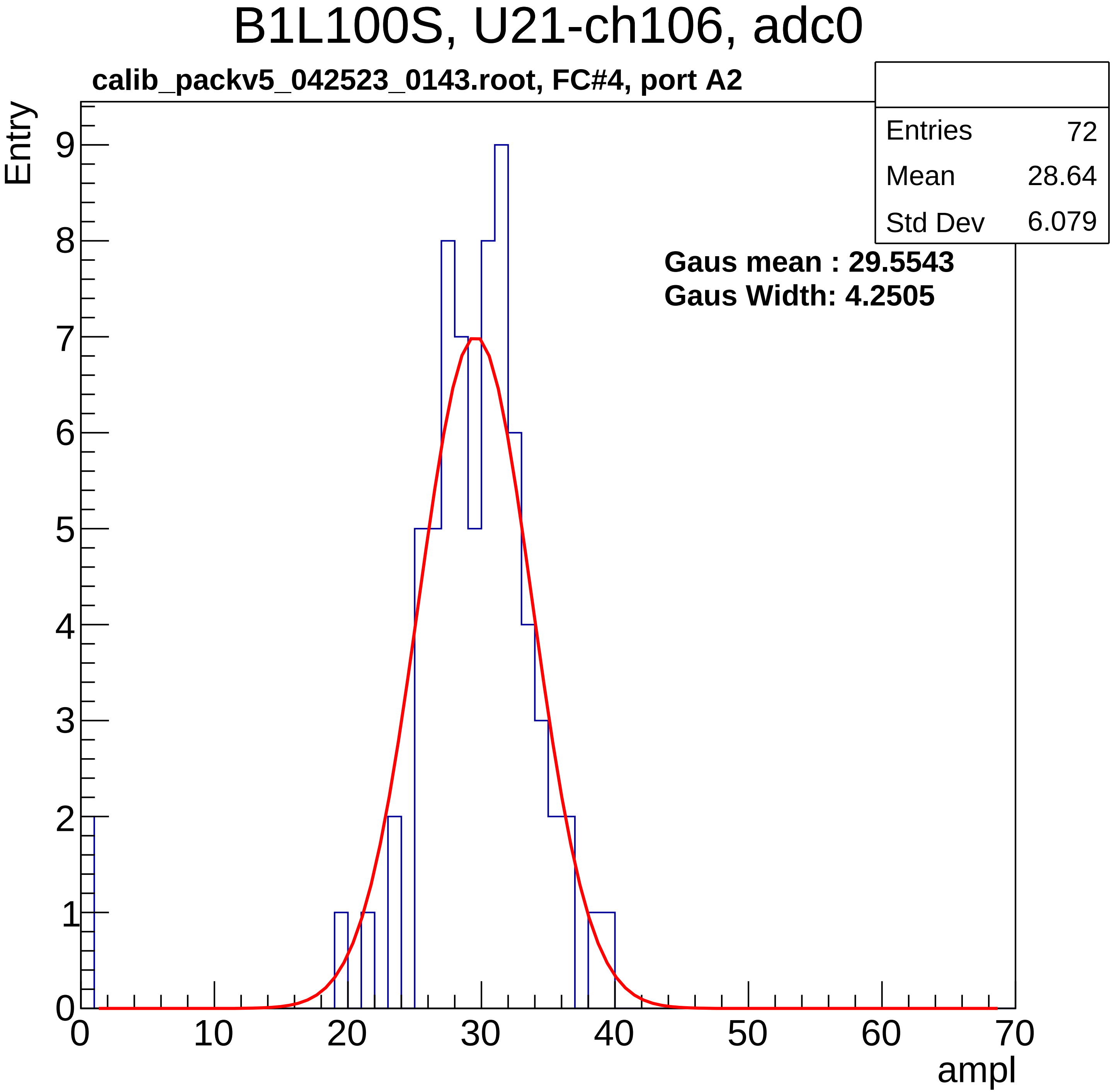

B1L100S, U21-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.42
Std Dev 3.523
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch105, adc5

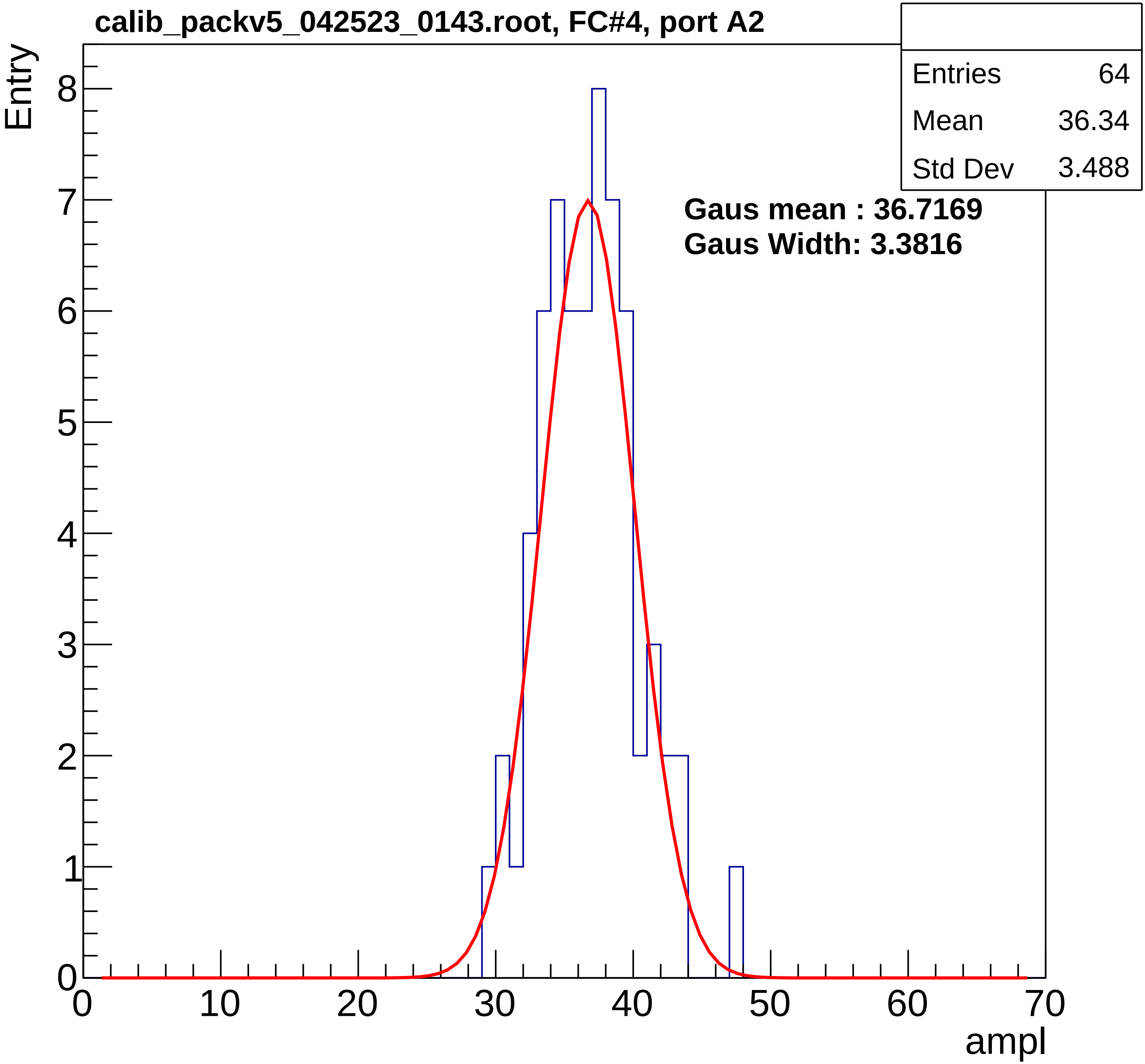


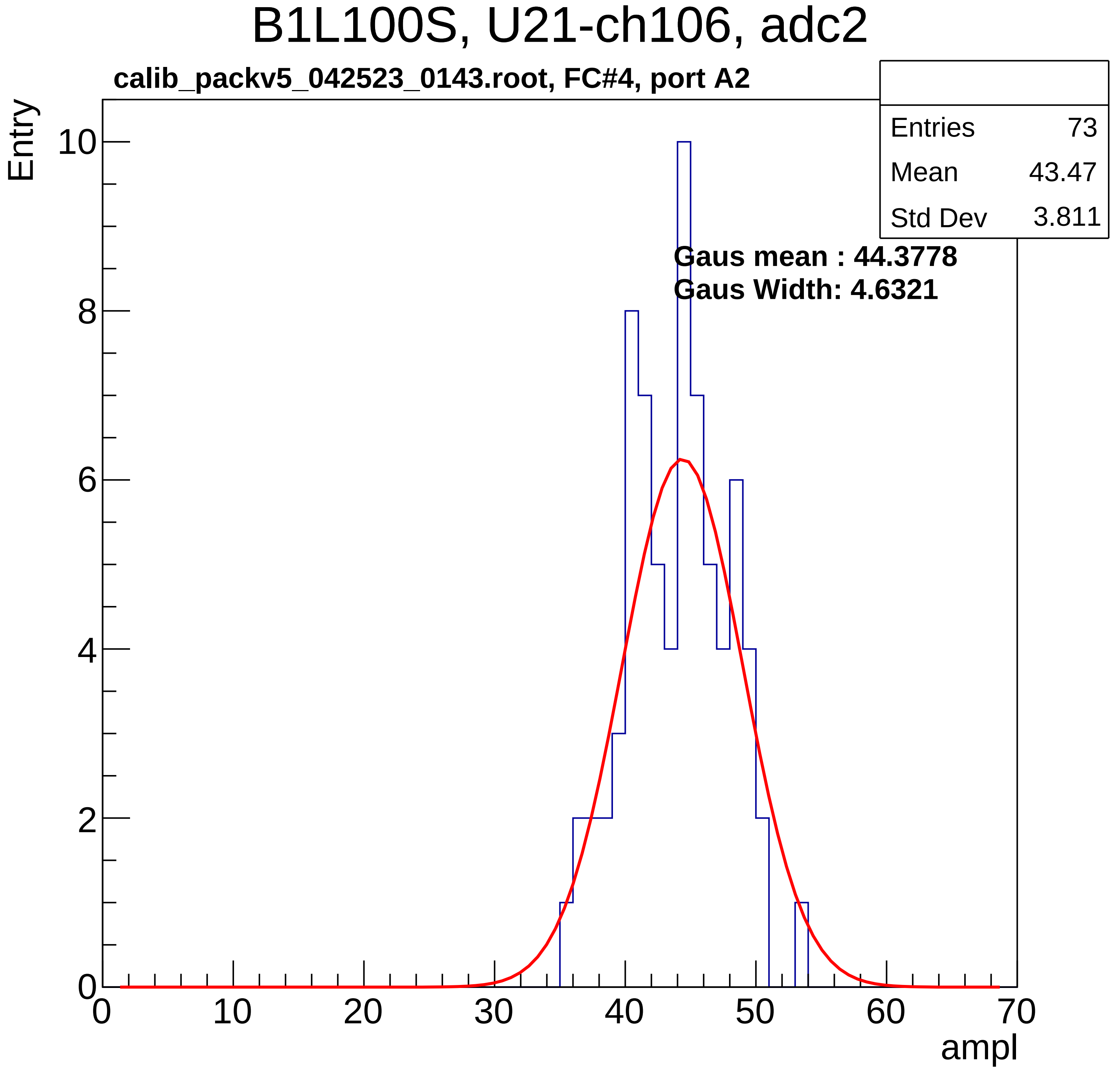

B1L100S, U21-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 5
Mean 61.6
Std Dev 1.02
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.64
Std Dev 6.079
Gaus mean : 29.5543
Gaus Width: 4.2505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

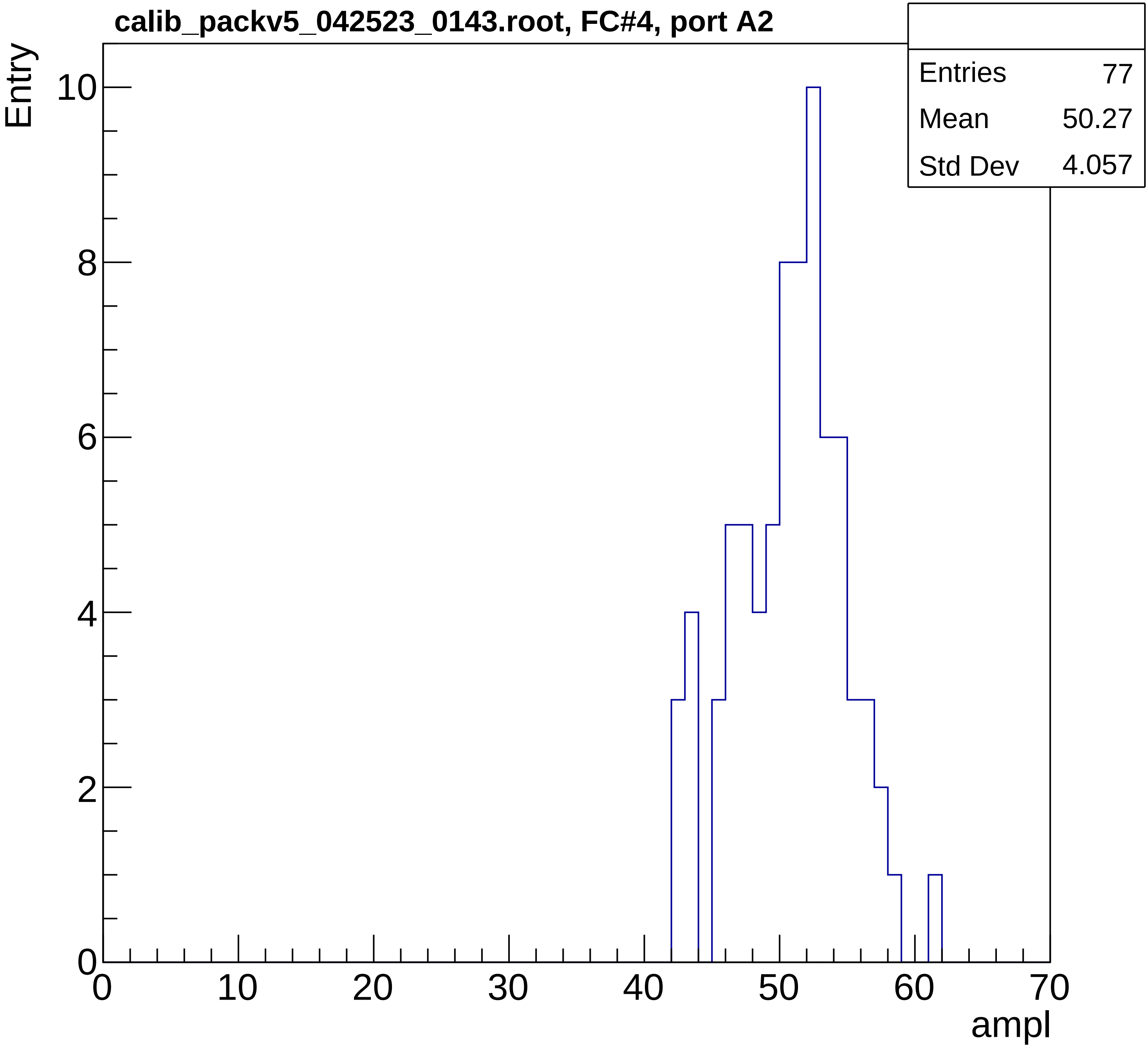
B1L100S, U21-ch106, adc1




B1L100S, U21-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.47
Std Dev 3.811
Gaus mean : 44.3778
Gaus Width: 4.6321
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

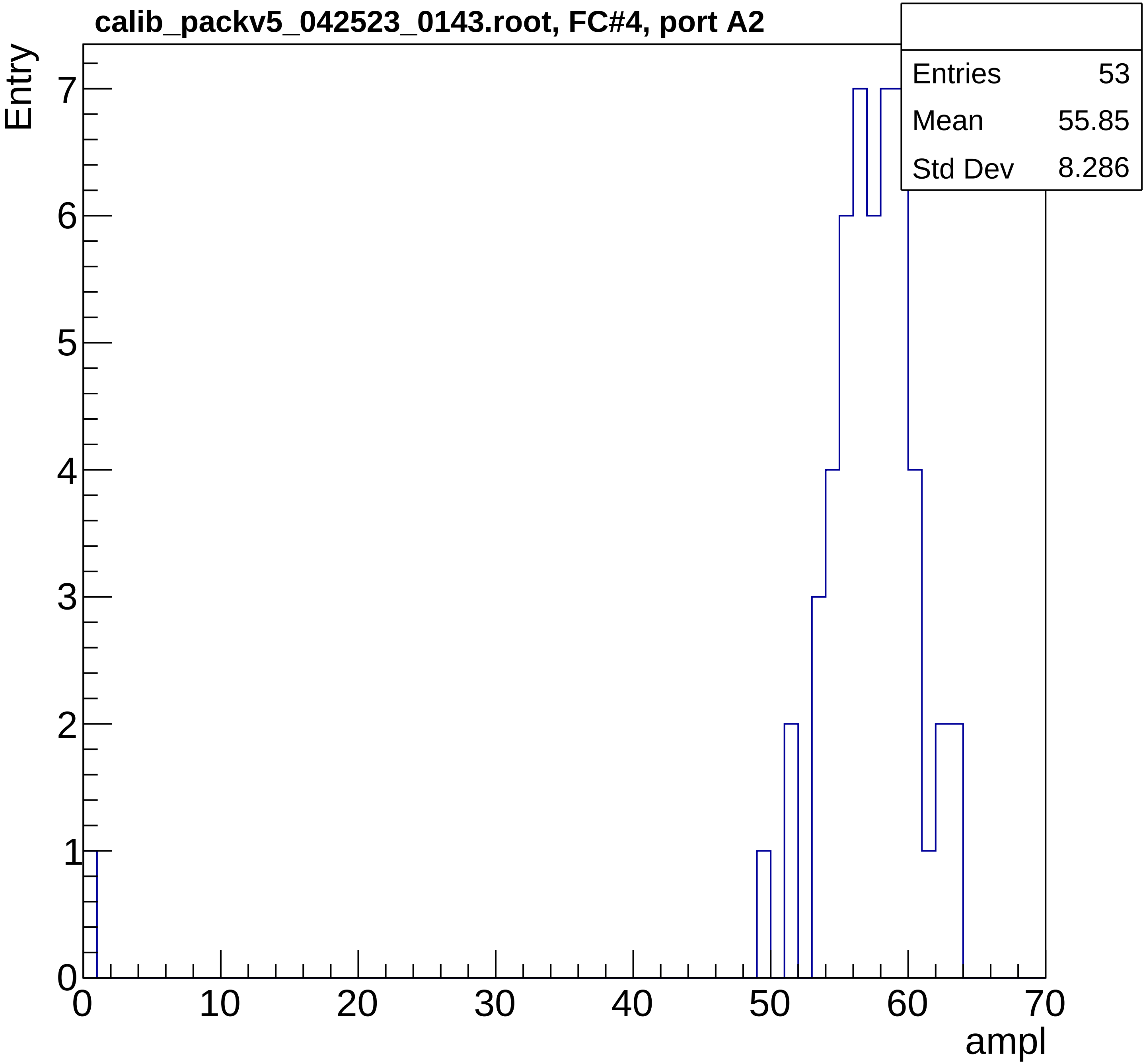
B1L100S, U21-ch106, adc3

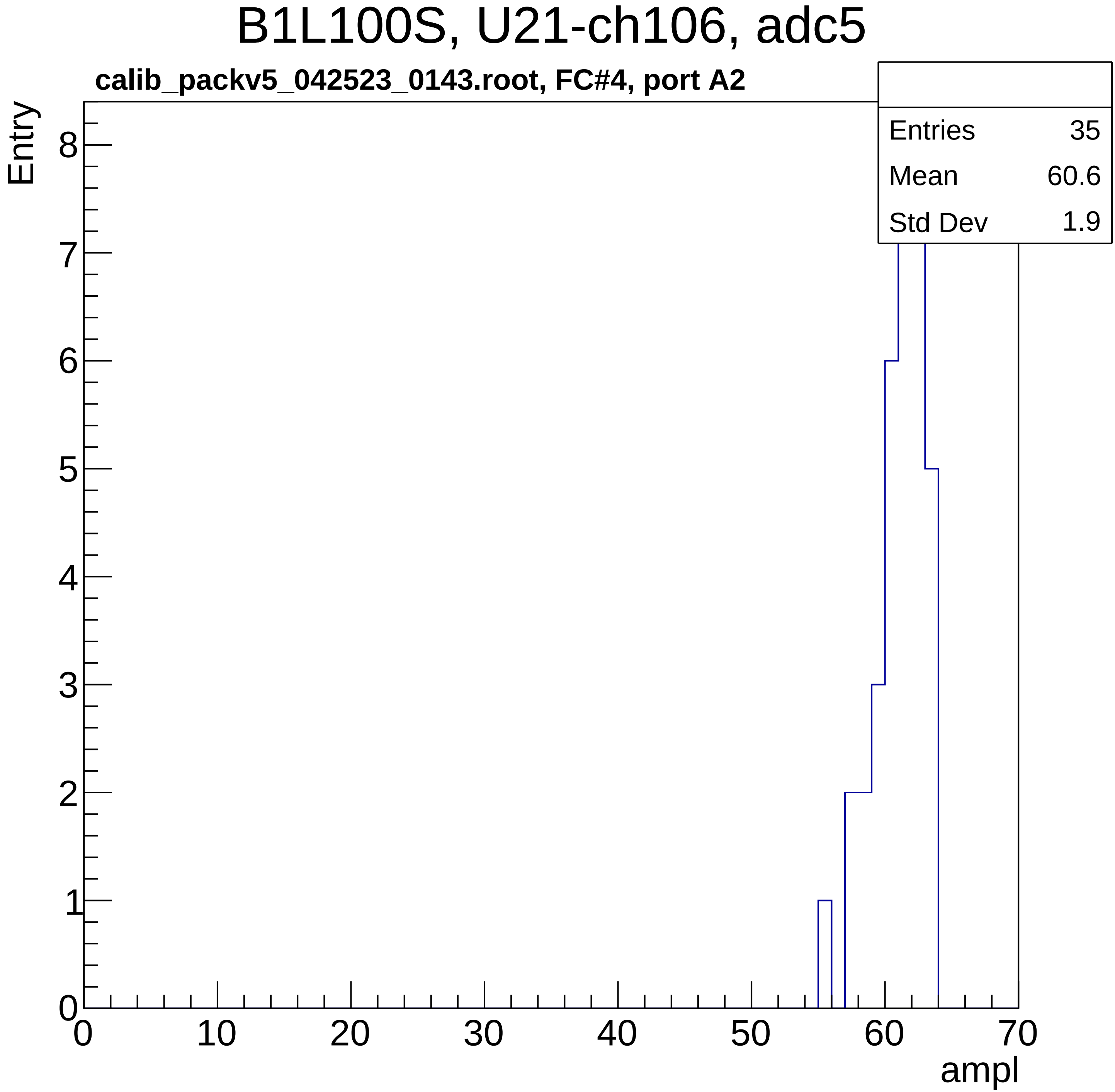
calib_packv5_042523_0143.root, FC#4, port A2

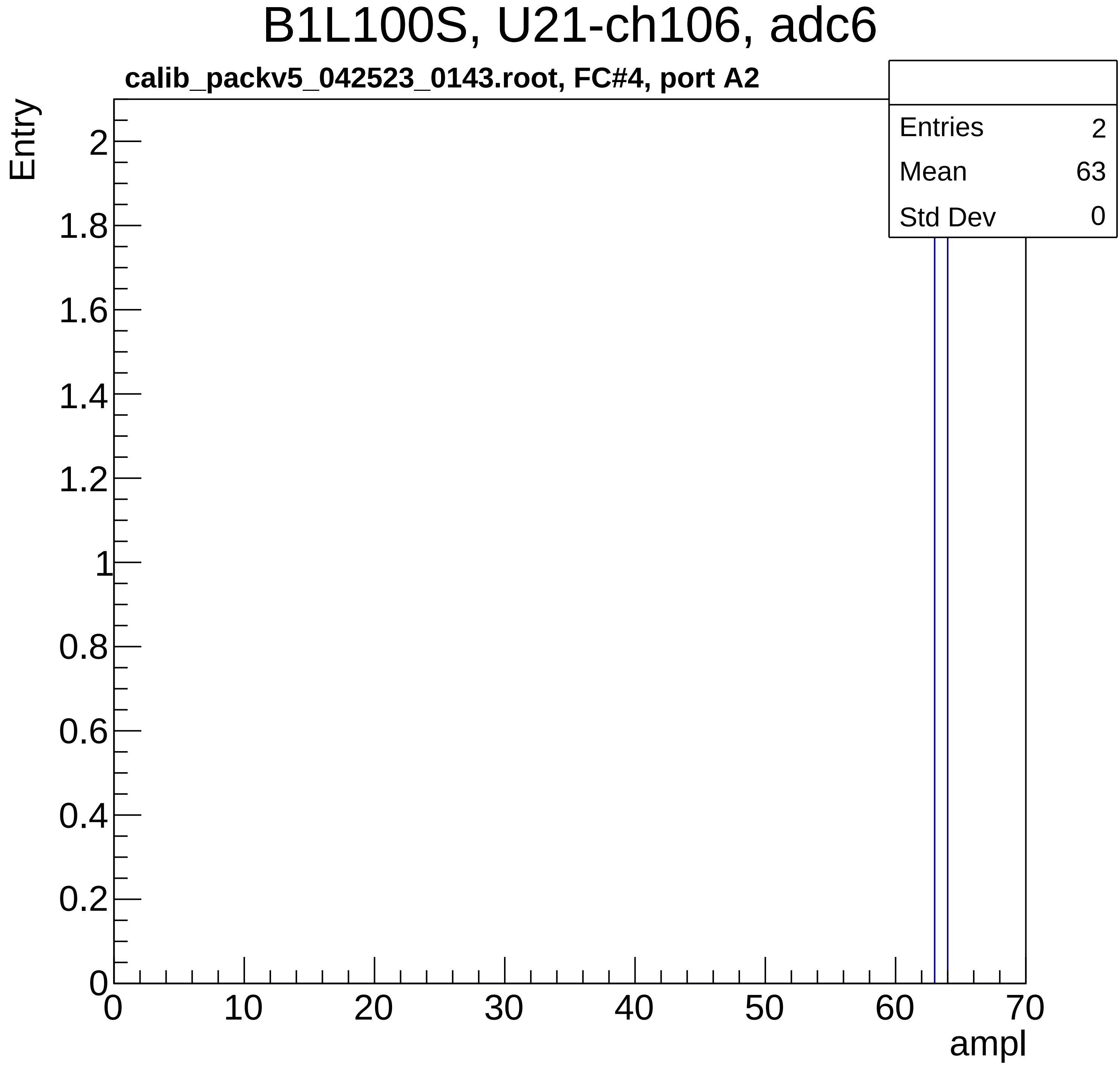


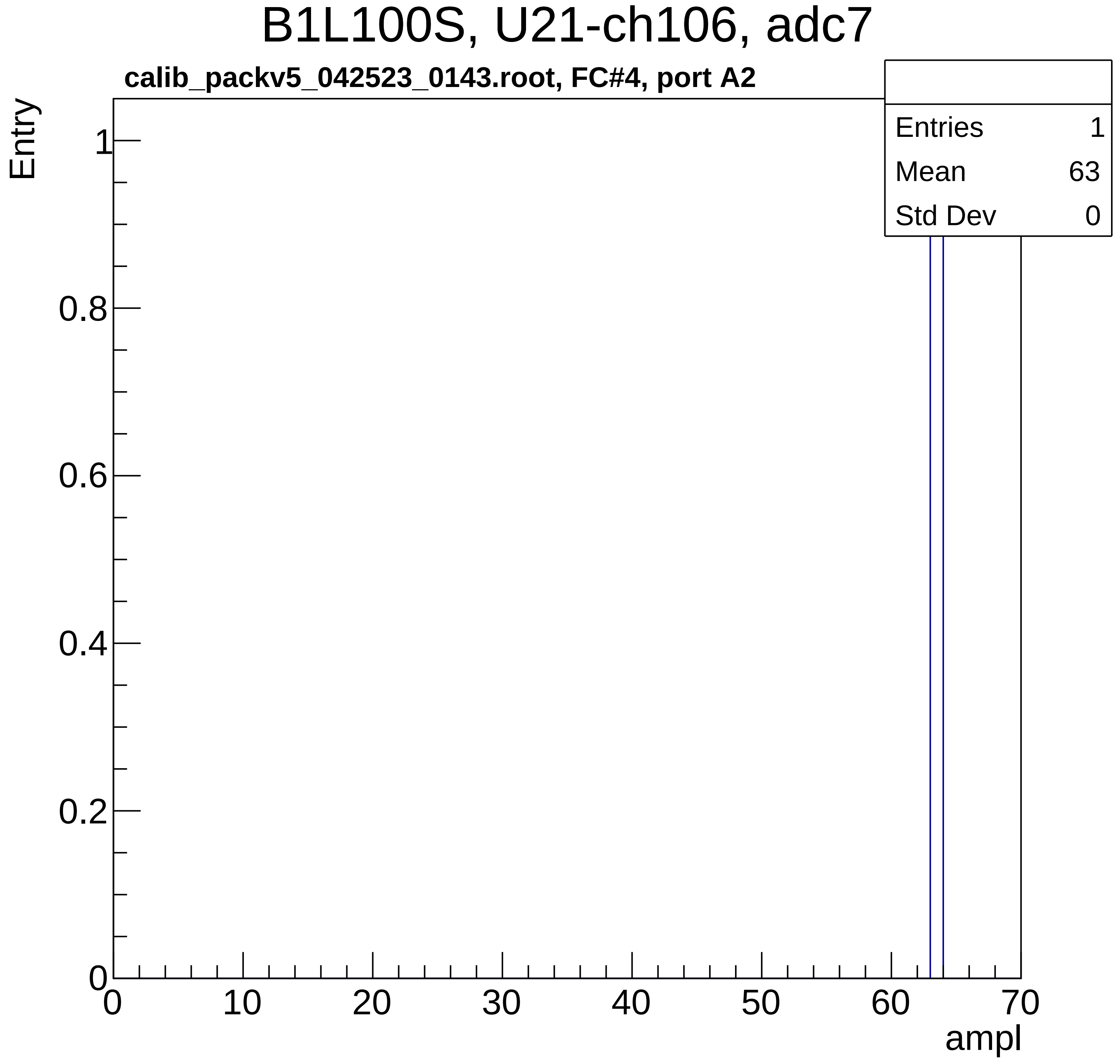
B1L100S, U21-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

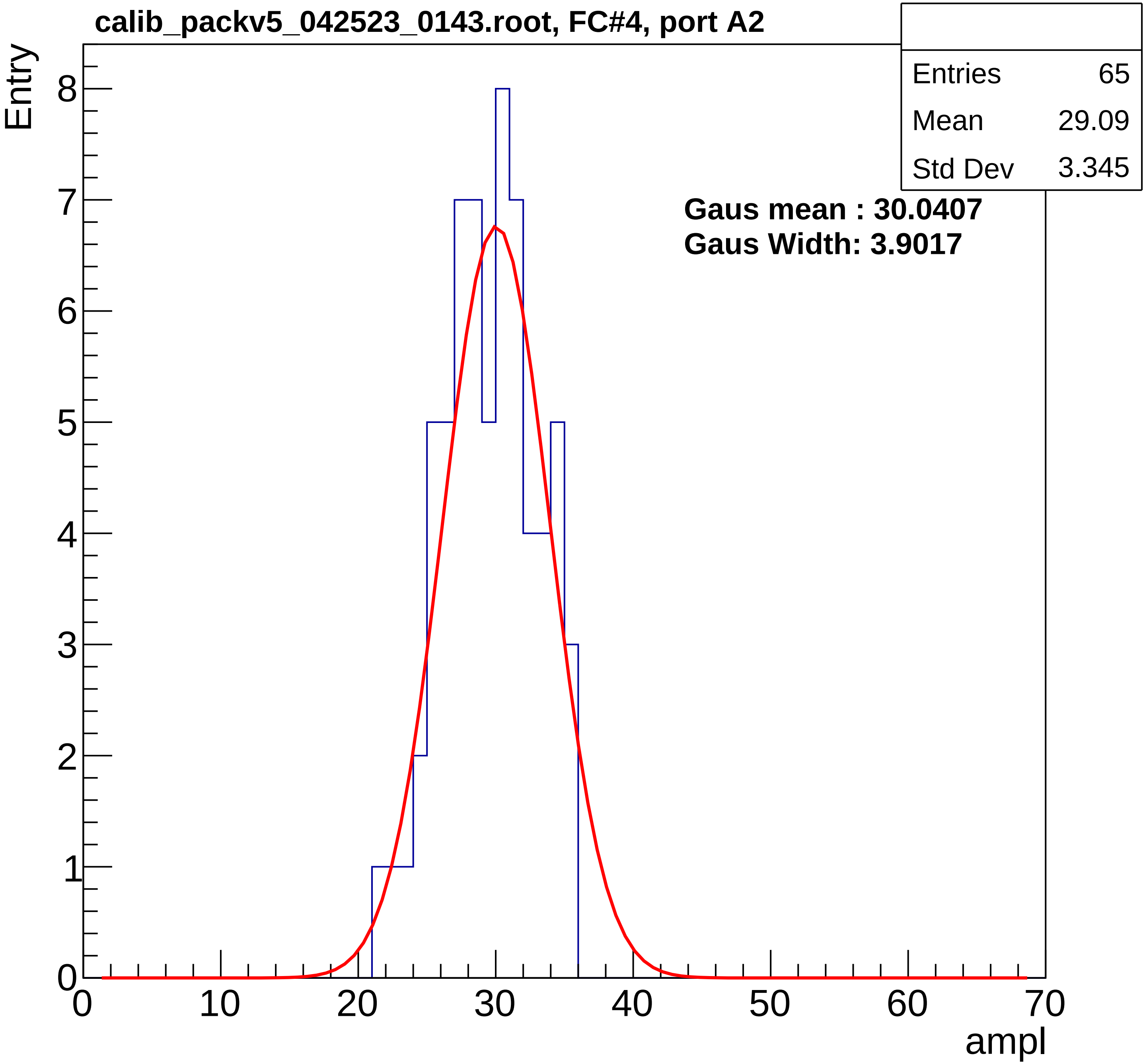


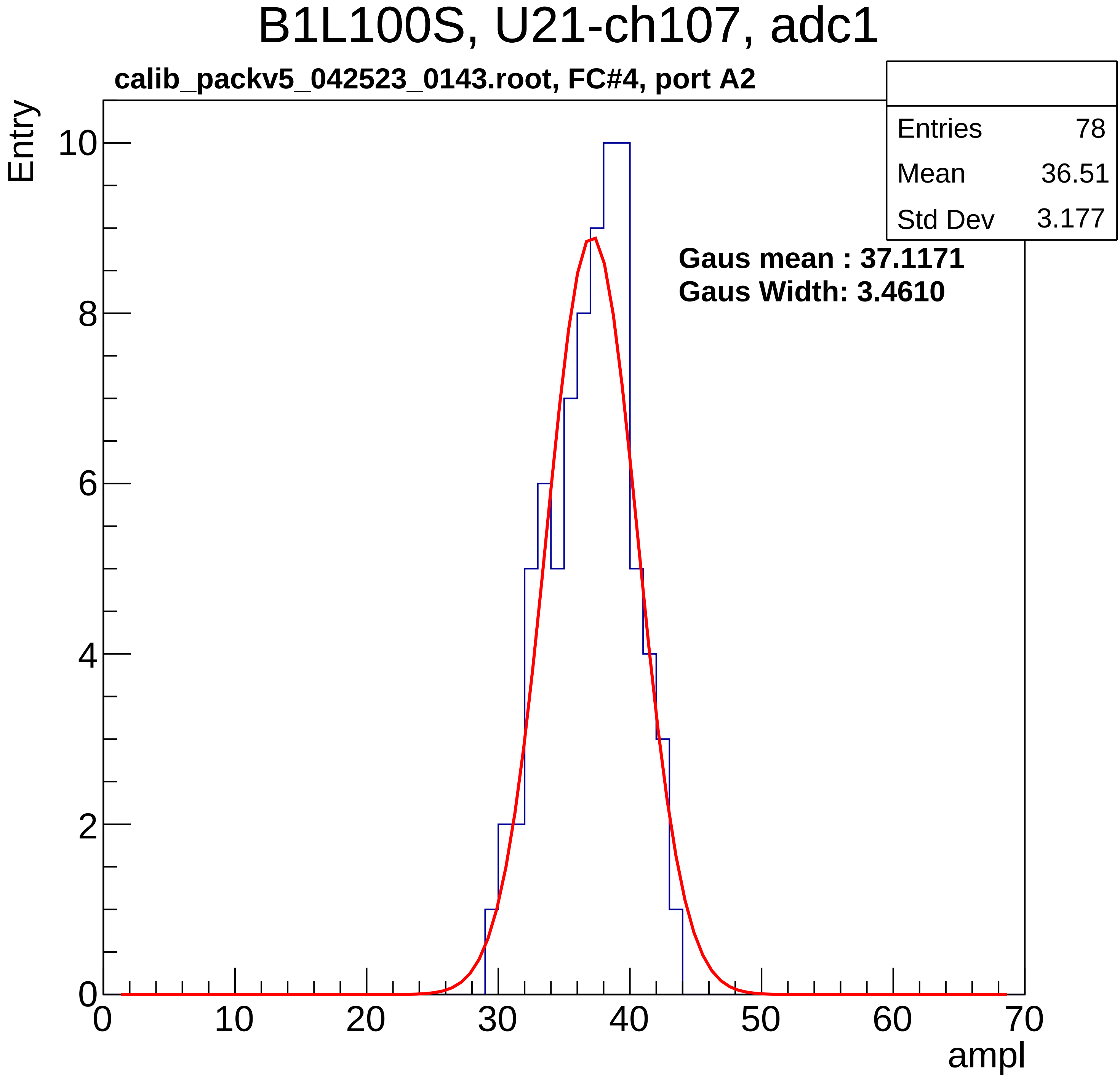

B1L100S, U21-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 60.6
Std Dev 1.9
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

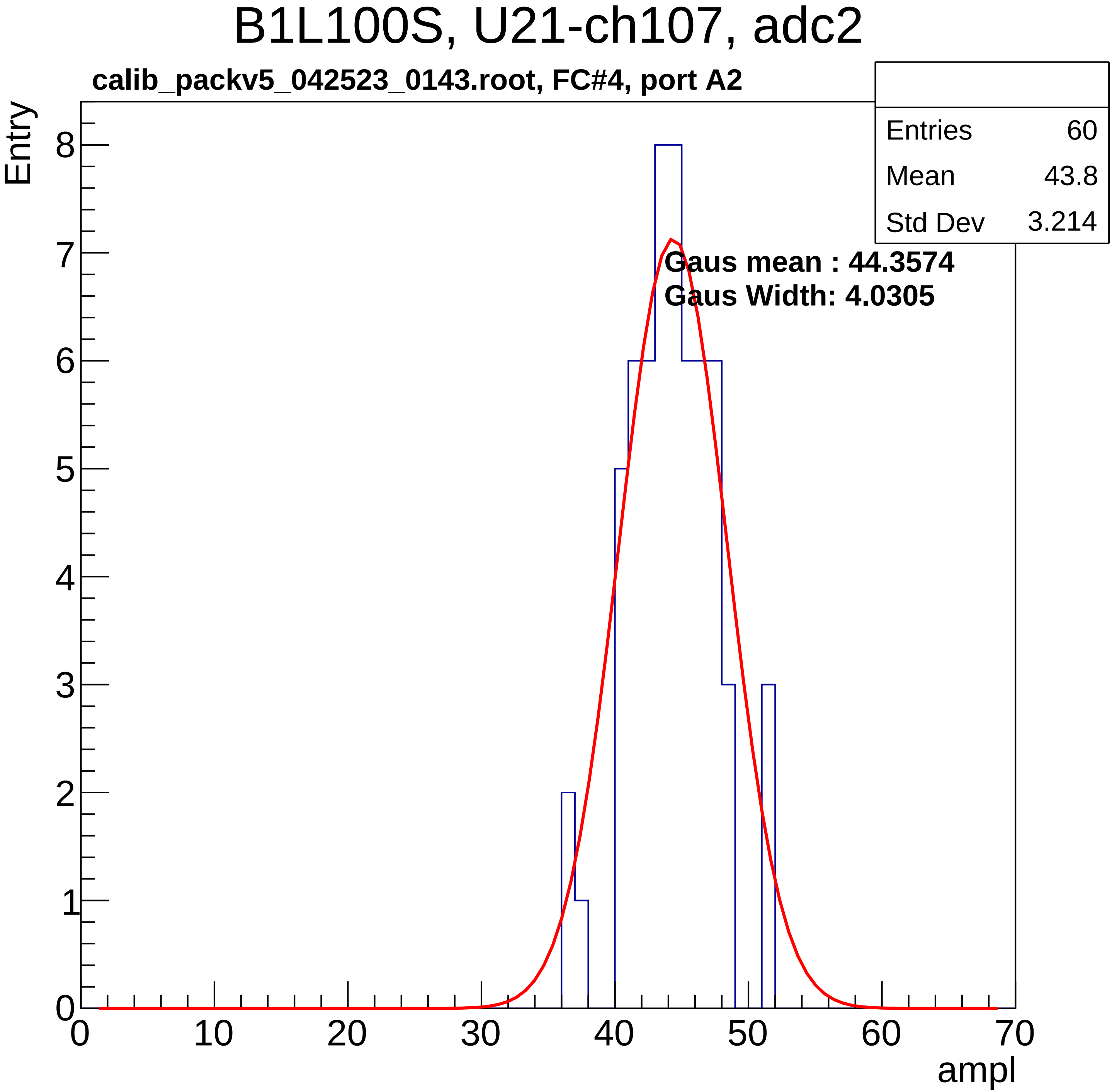

B1L100S, U21-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

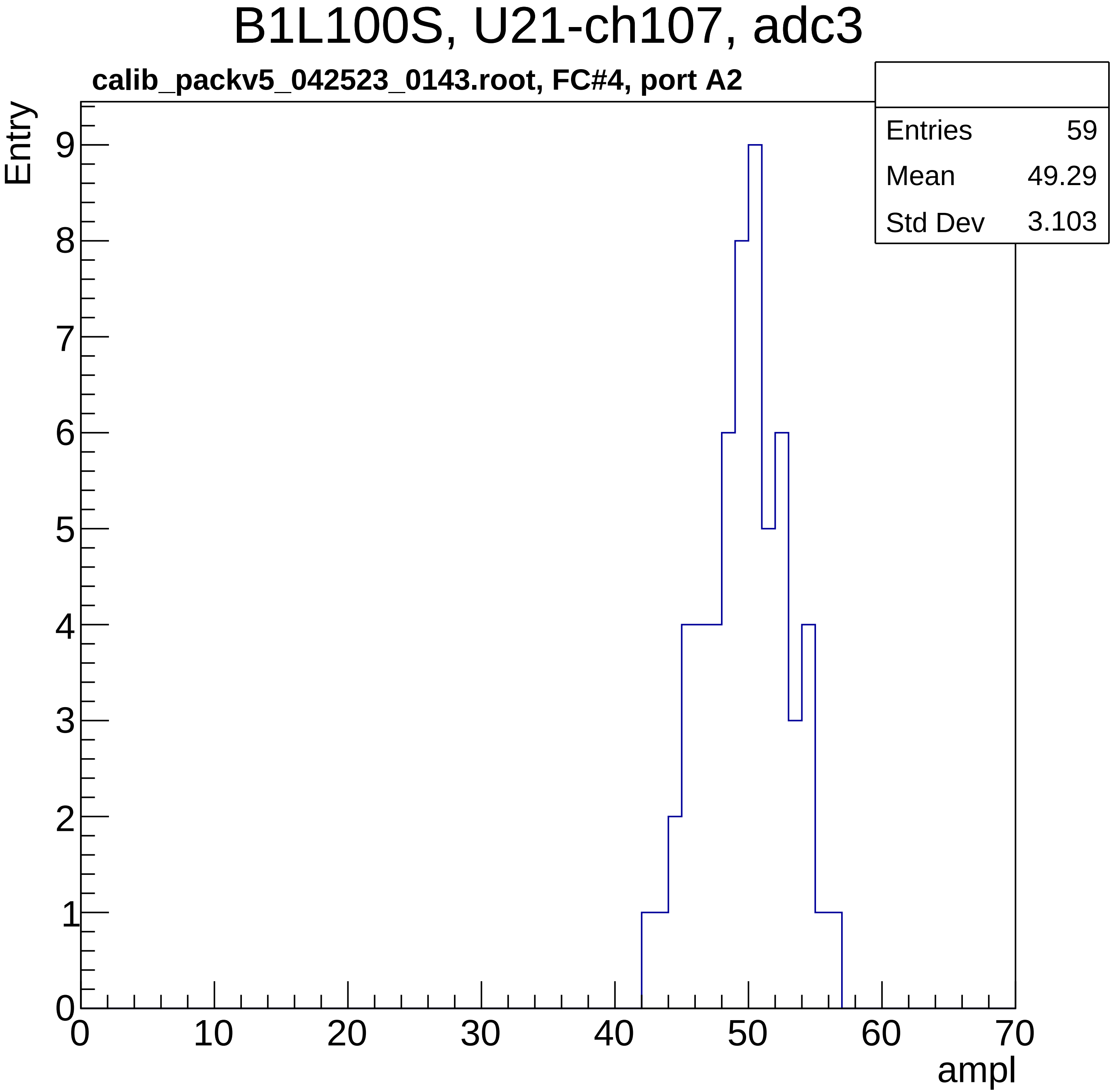

B1L100S, U21-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

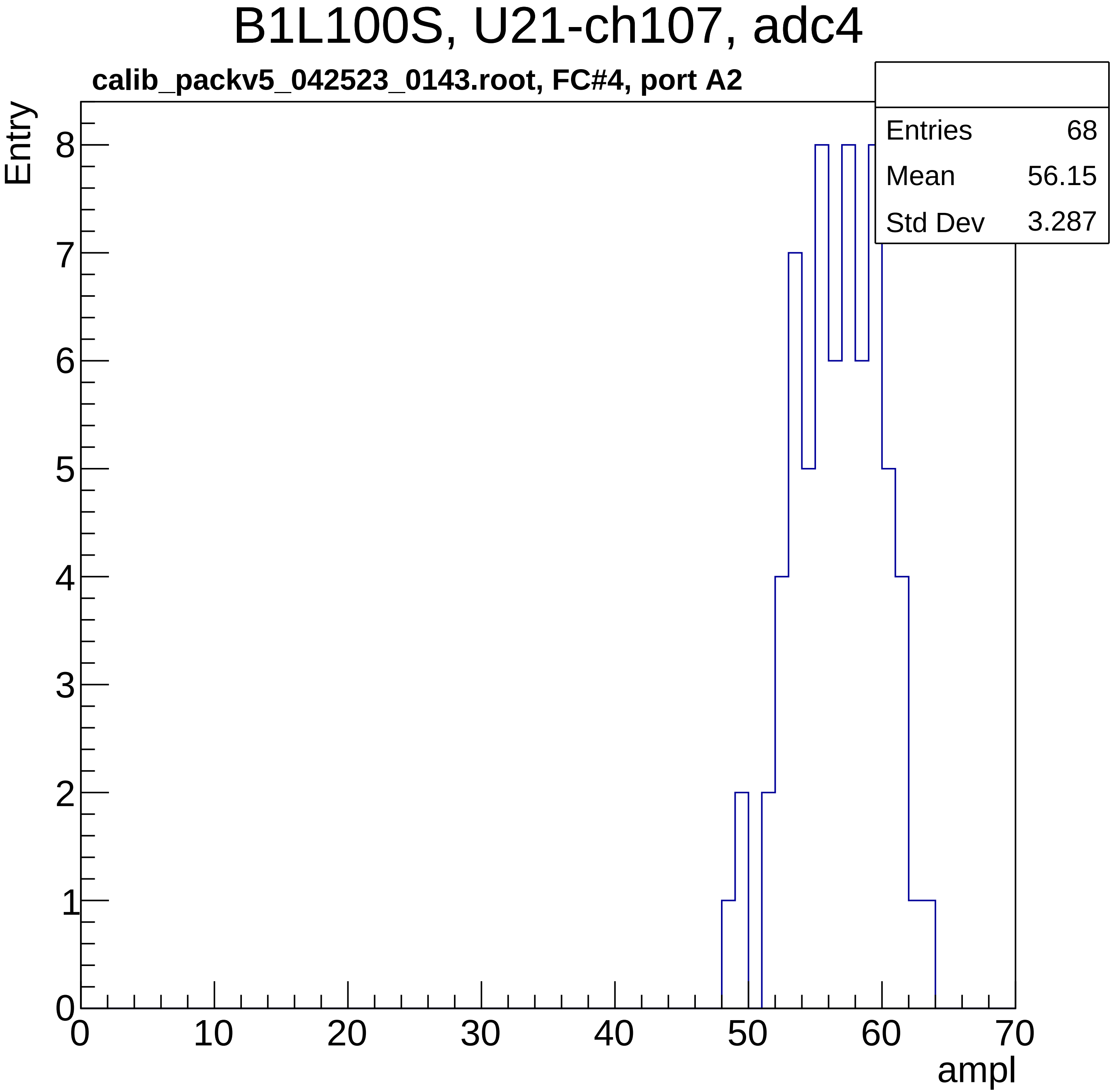
B1L100S, U21-ch107, adc0

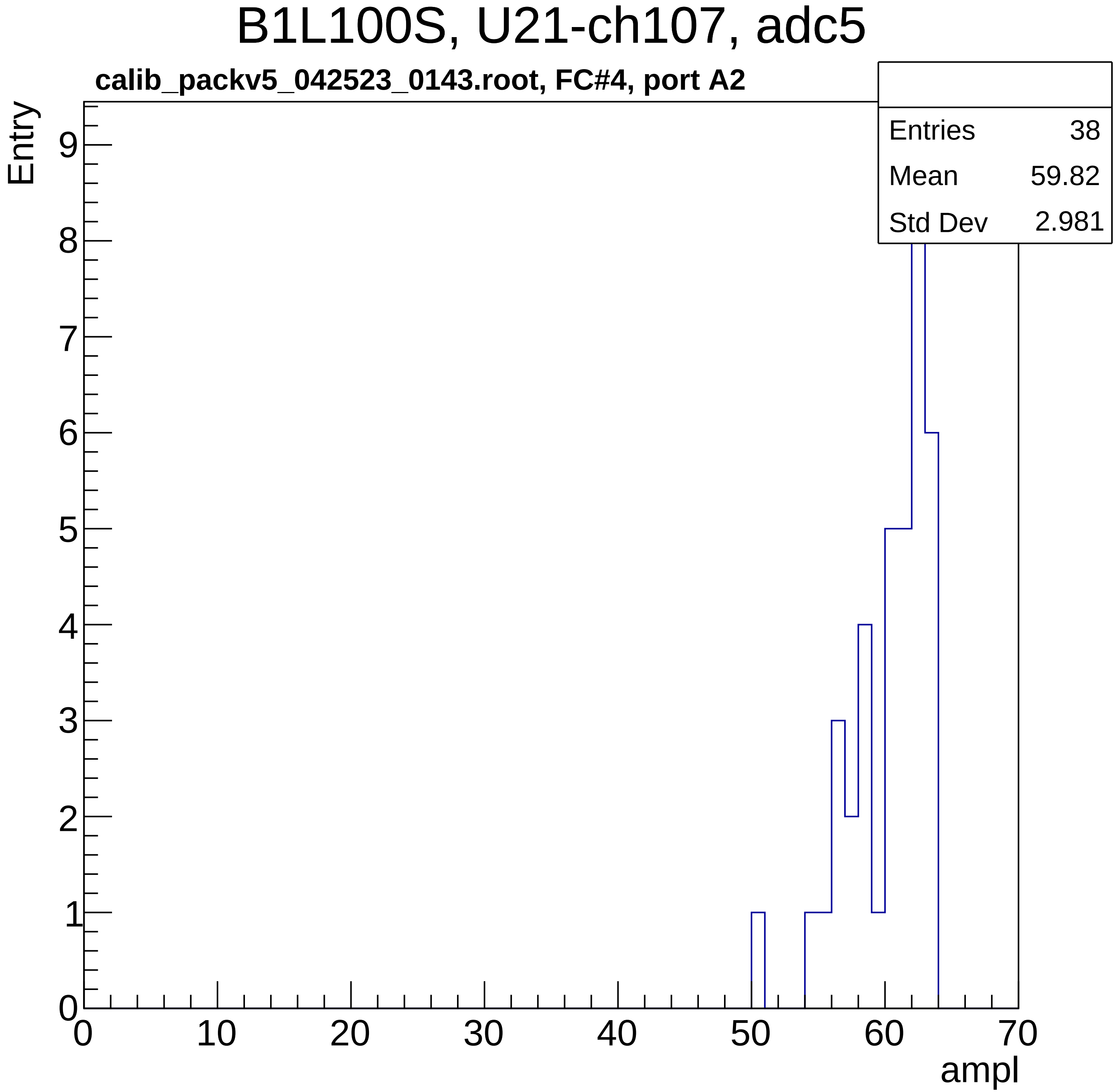



B1L100S, U21-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.51
Std Dev 3.177
Gaus mean : 37.1171
Gaus Width: 3.4610
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

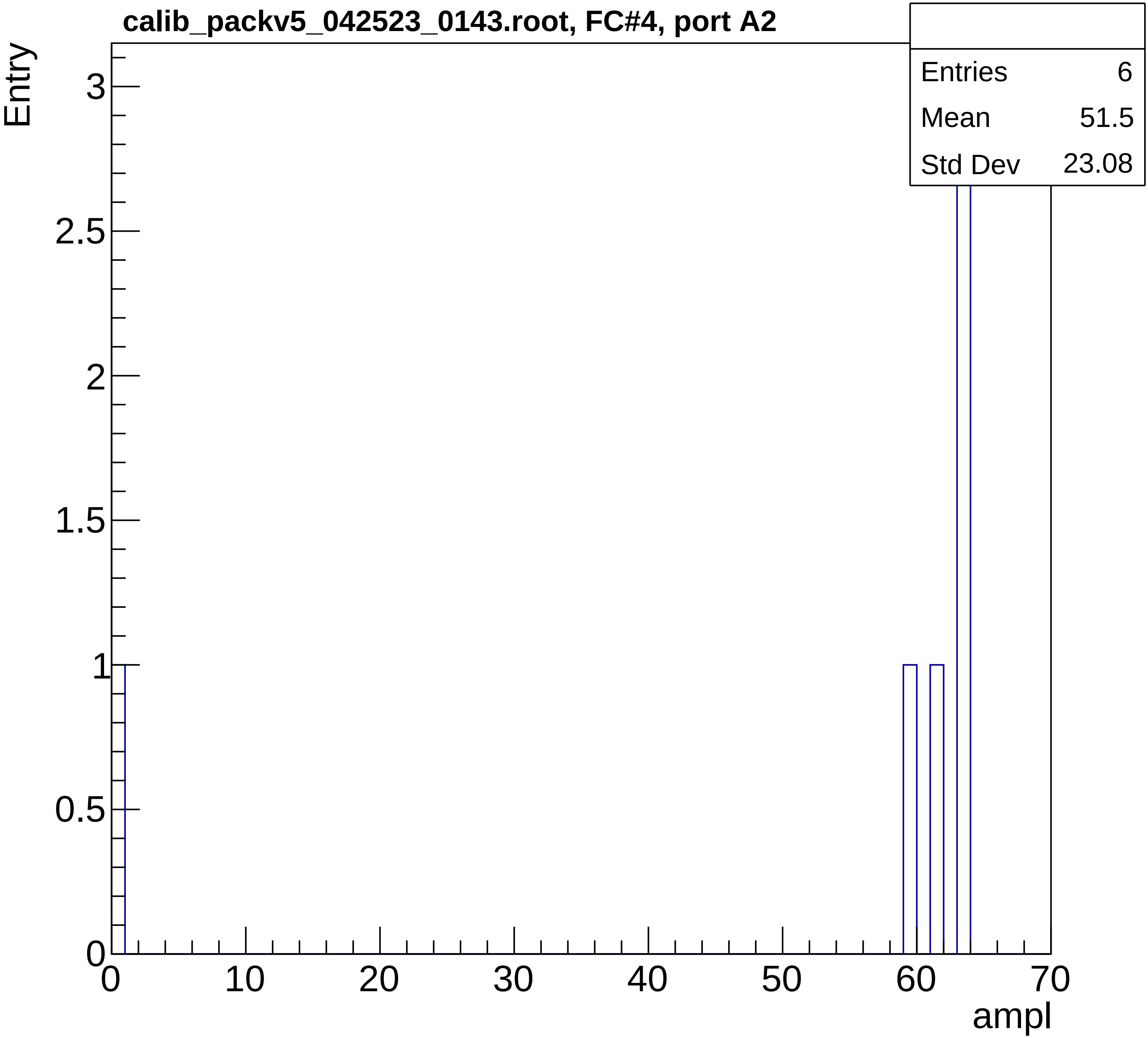

B1L100S, U21-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.8
Std Dev 3.214
Gaus mean : 44.3574
Gaus Width: 4.0305
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 49.29
Std Dev 3.103
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

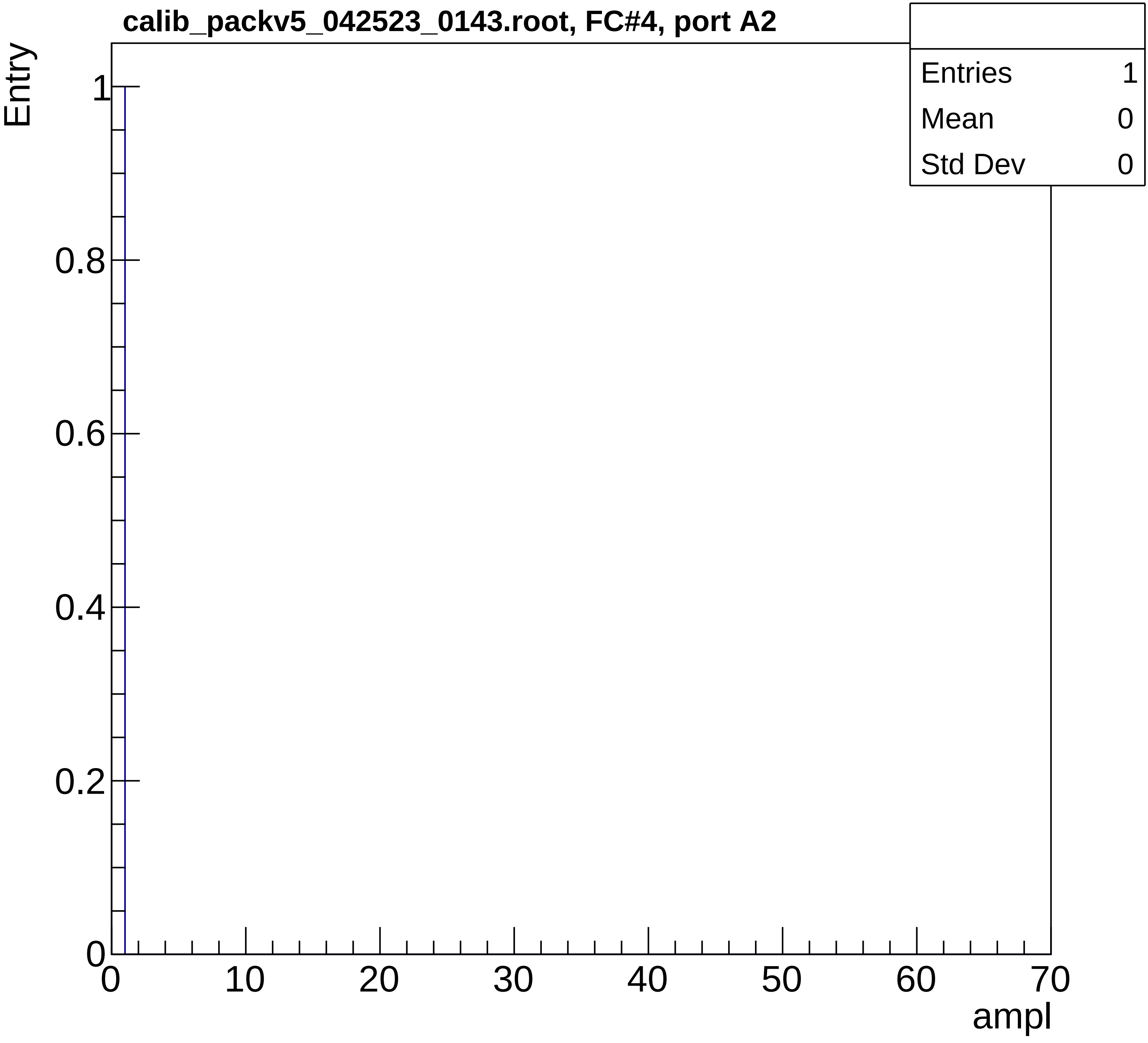

B1L100S, U21-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.15
Std Dev 3.287
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

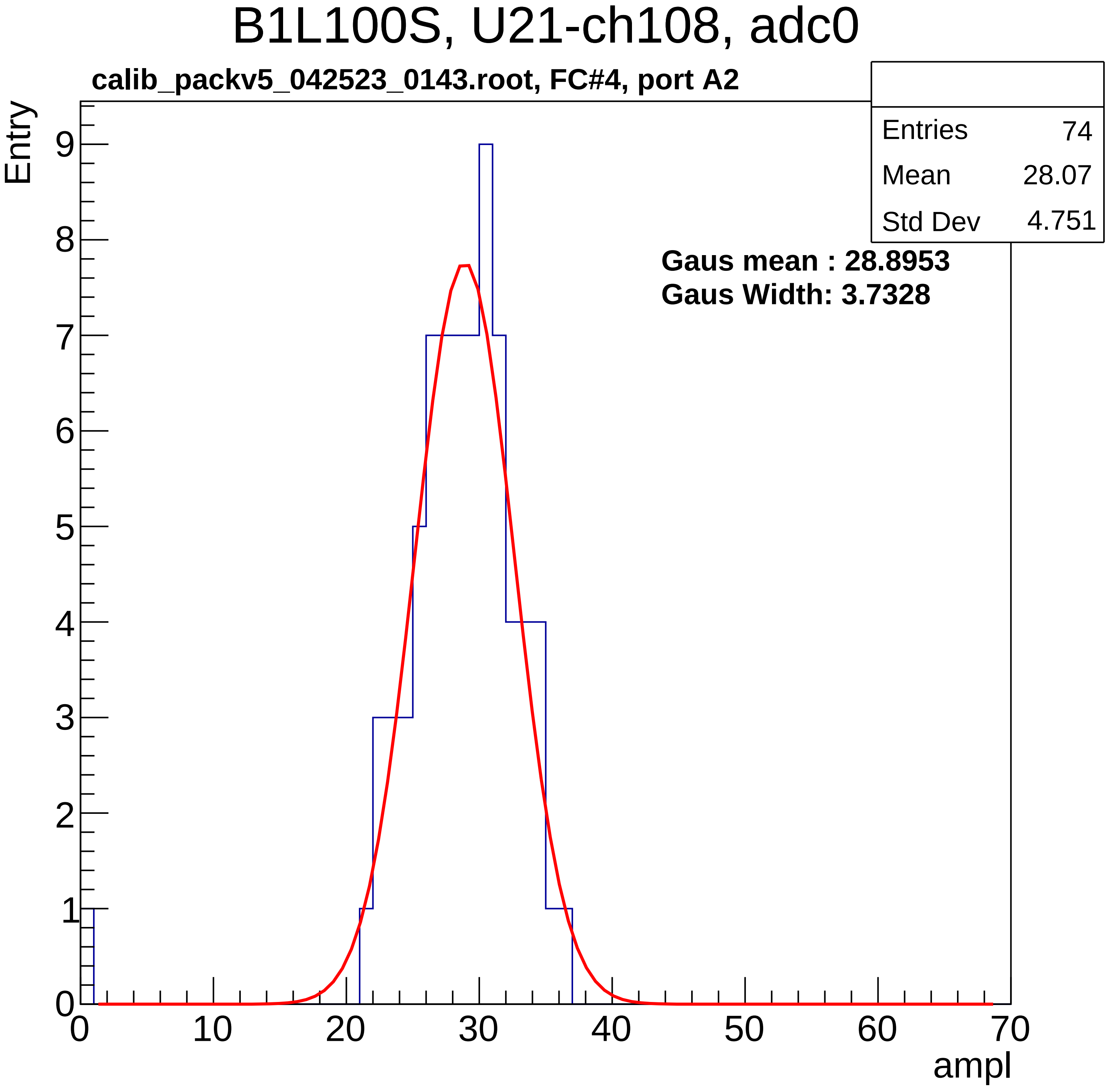

B1L100S, U21-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.82
Std Dev 2.981
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

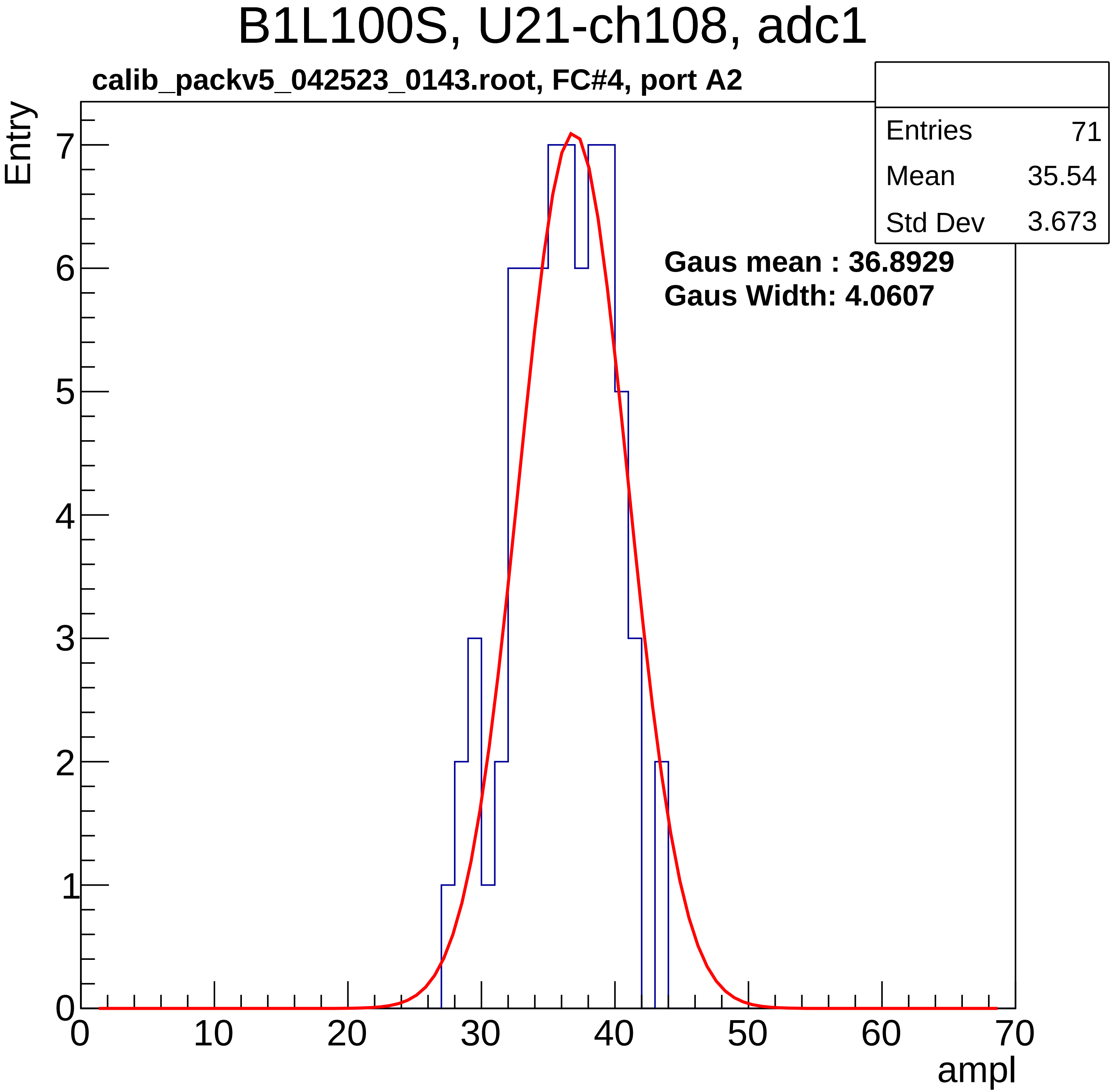
B1L100S, U21-ch107, adc6

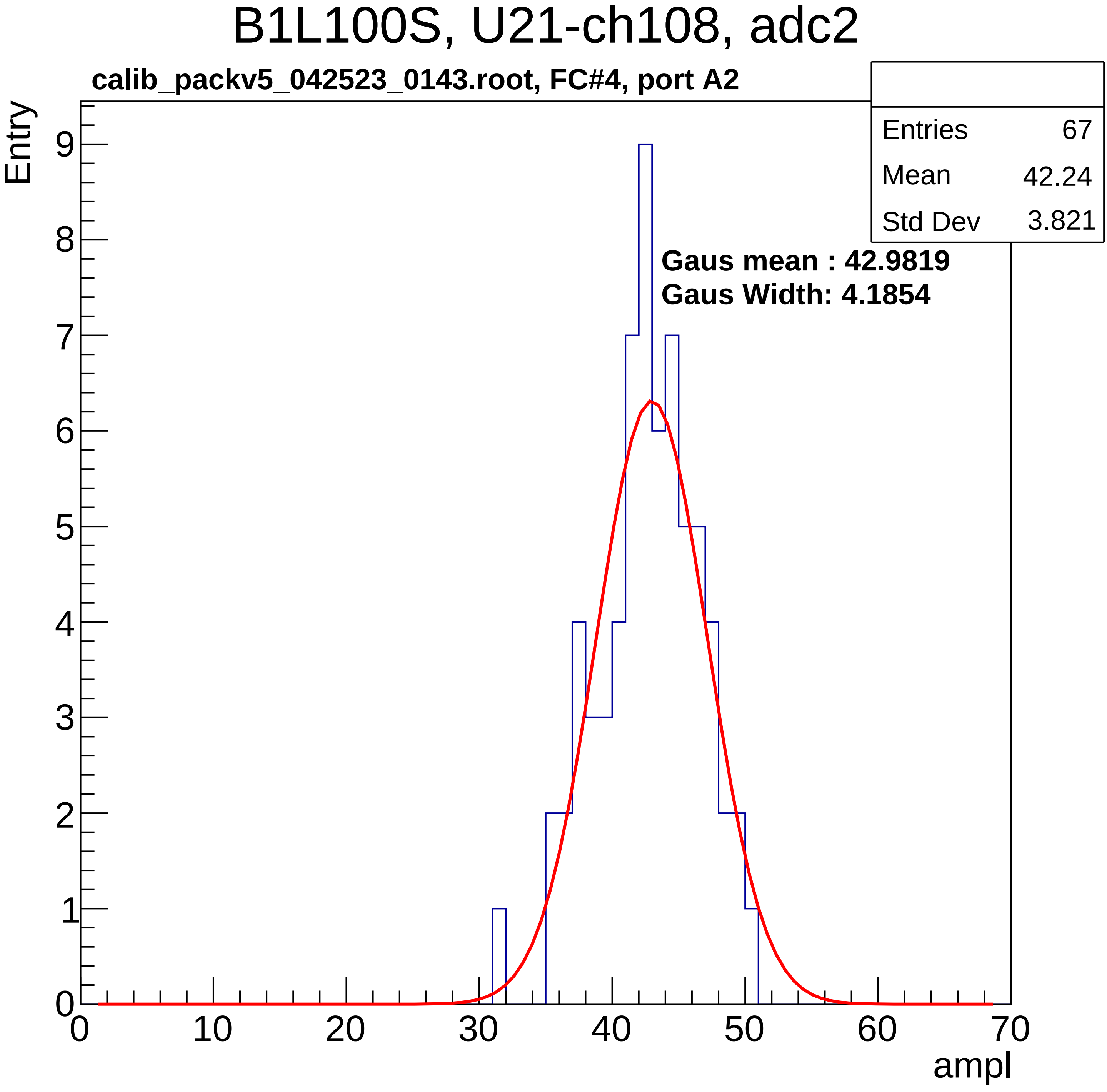


B1L100S, U21-ch107, adc7



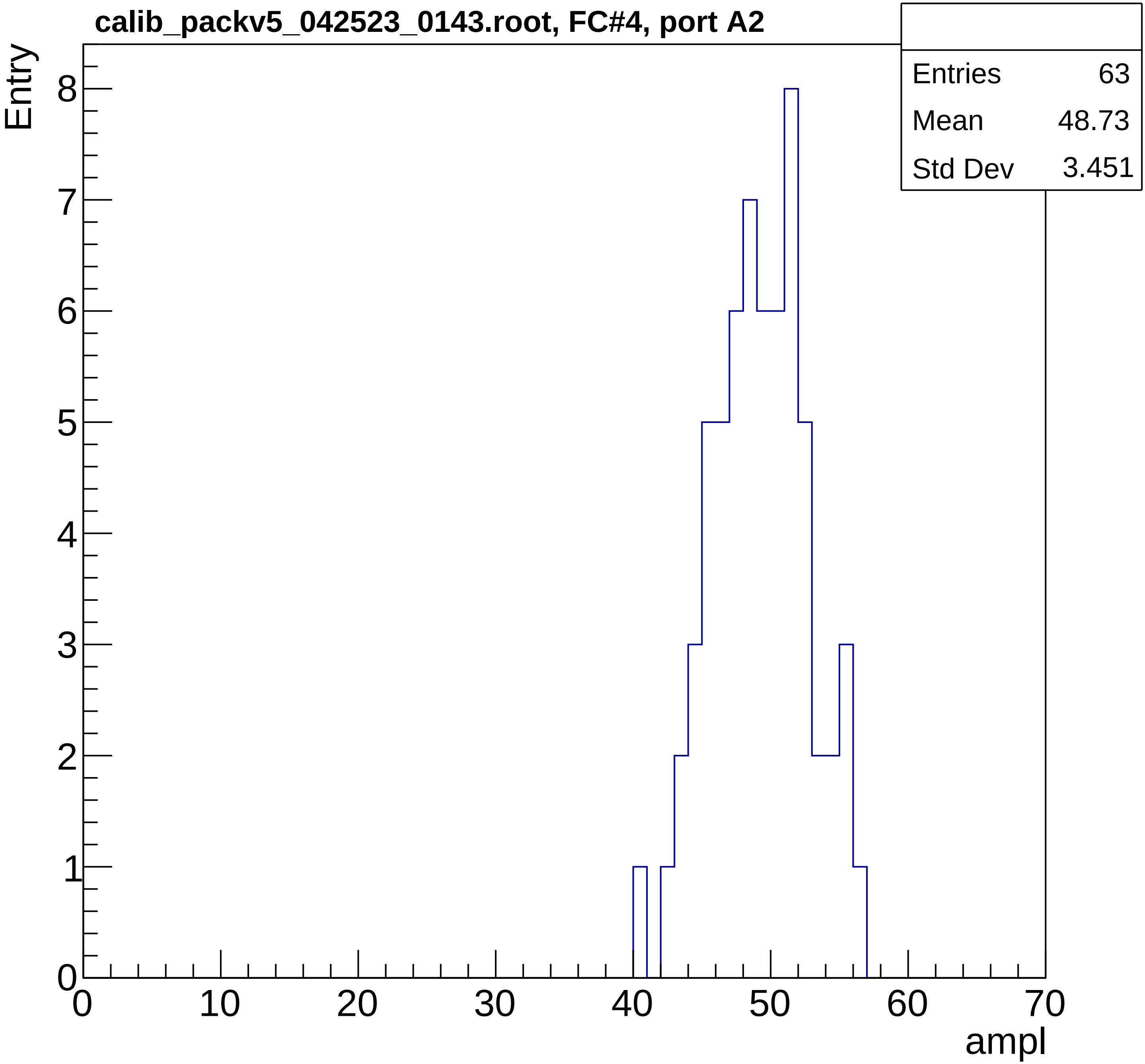

B1L100S, U21-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.07
Std Dev 4.751
Gaus mean : 28.8953
Gaus Width: 3.7328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

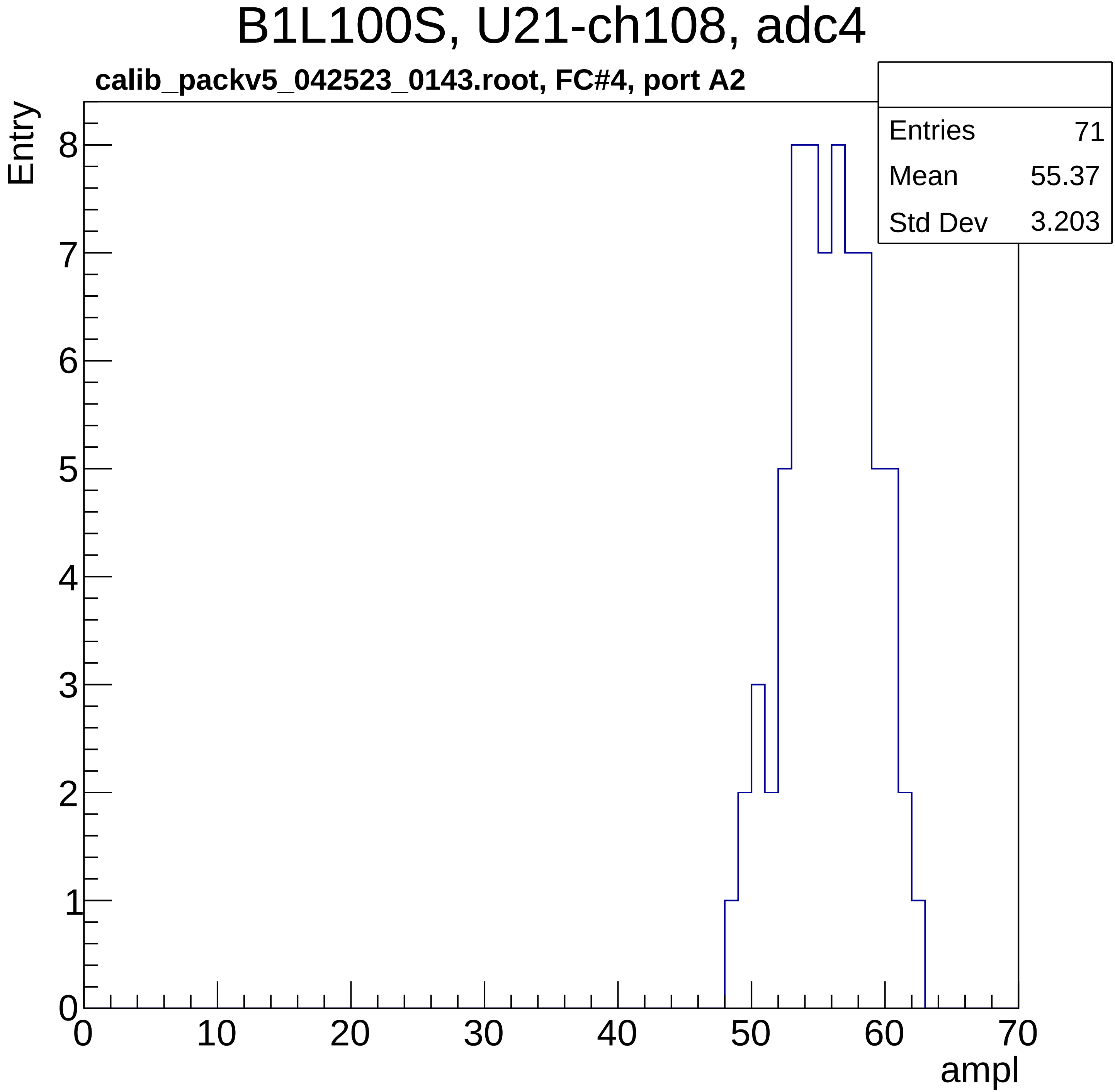

B1L100S, U21-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 71
Mean 35.54
Std Dev 3.673
Gaus mean : 36.8929
Gaus Width: 4.0607
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

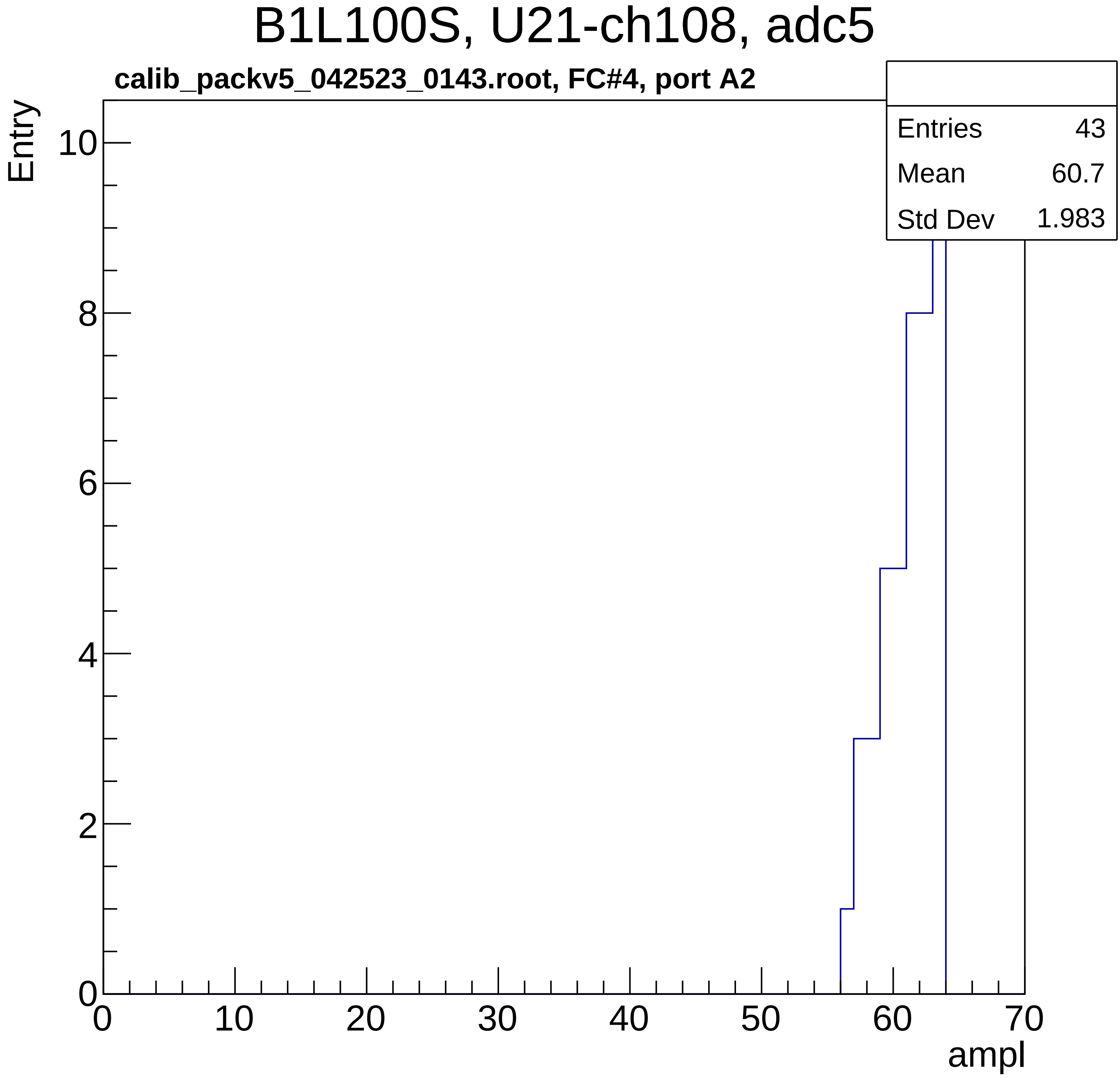

B1L100S, U21-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 42.24
Std Dev 3.821
Gaus mean : 42.9819
Gaus Width: 4.1854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

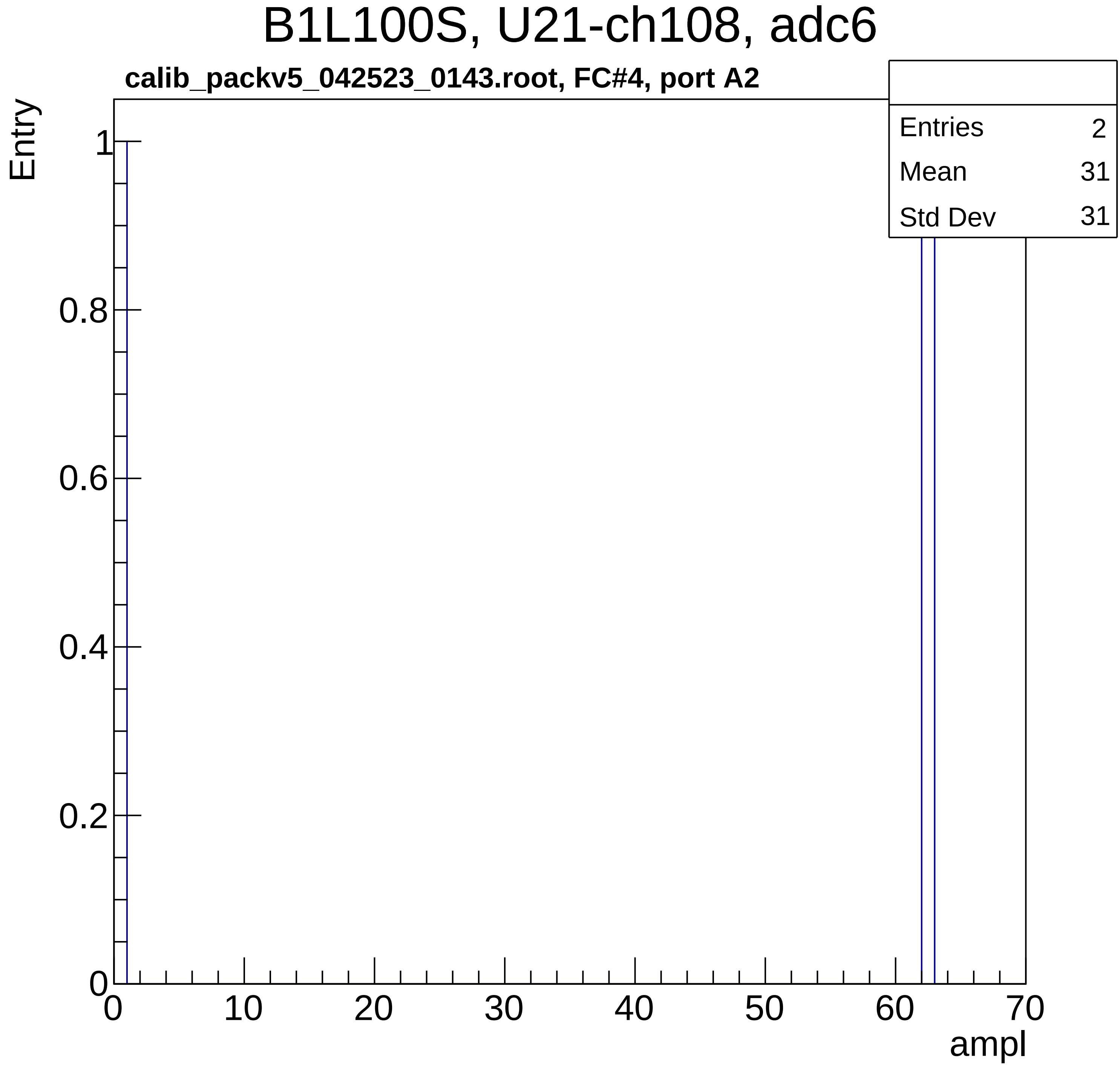
B1L100S, U21-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2



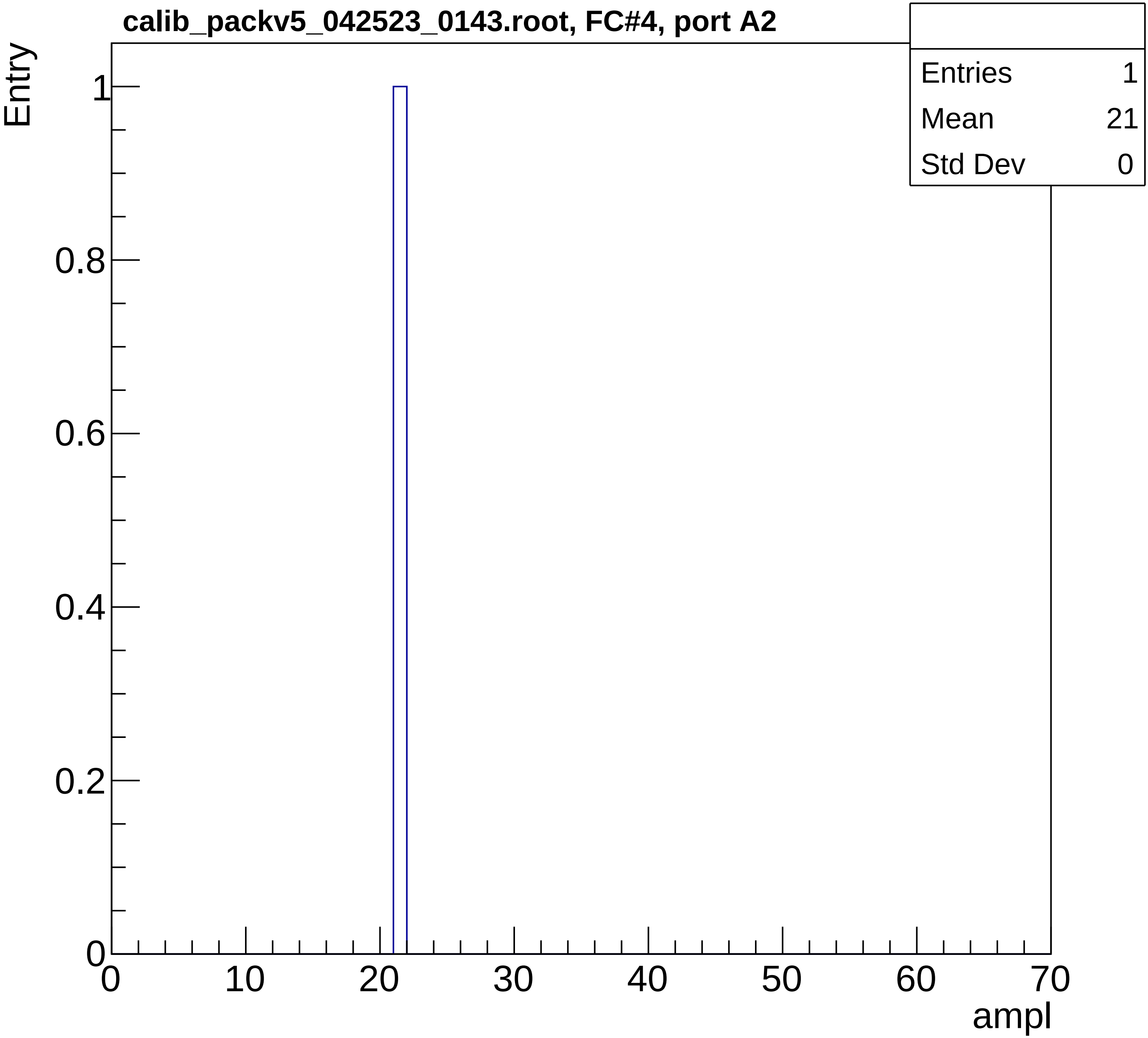

B1L100S, U21-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 55.37
Std Dev 3.203
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

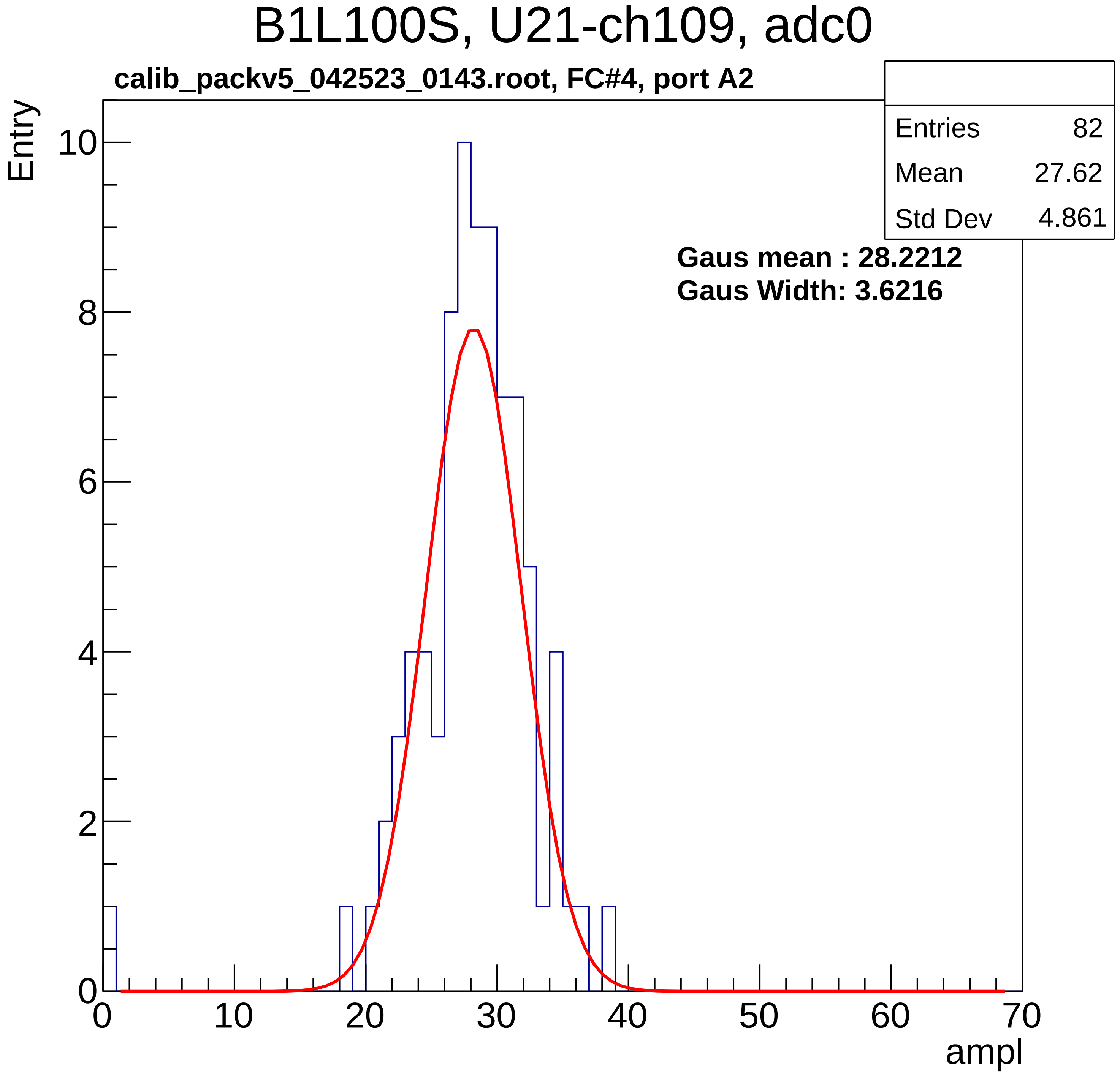

B1L100S, U21-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 60.7
Std Dev 1.983
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

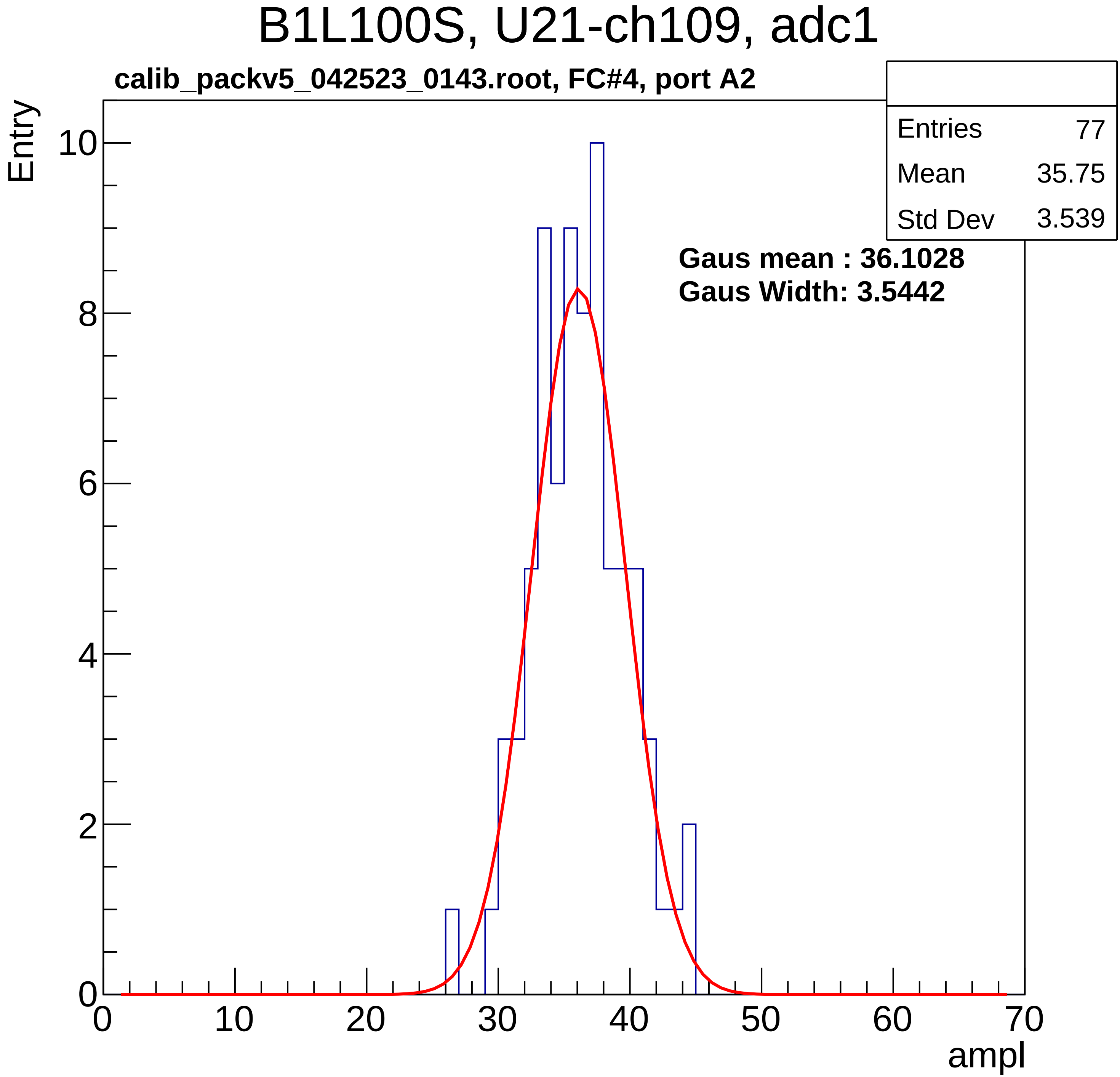

B1L100S, U21-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

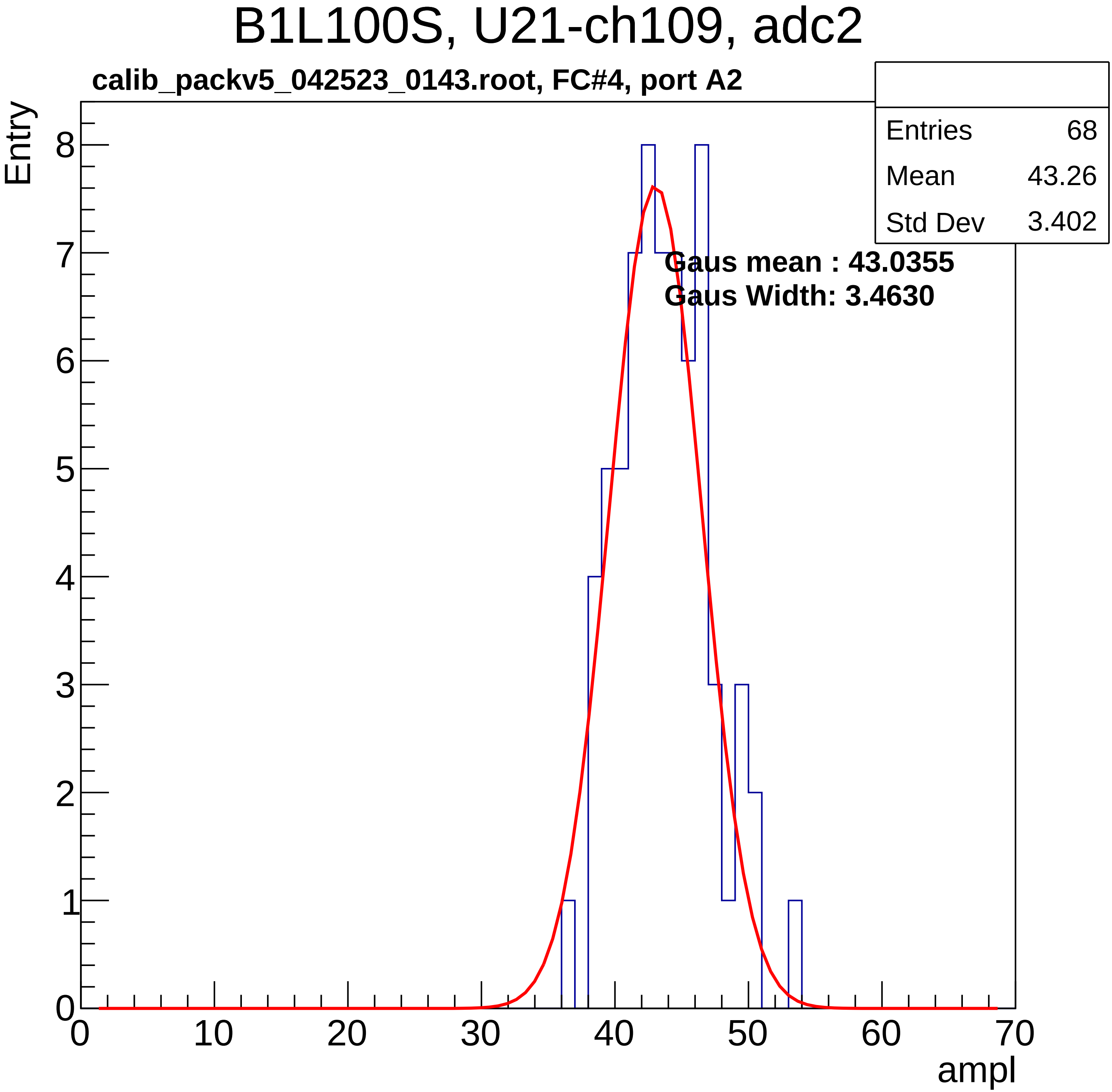
B1L100S, U21-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2



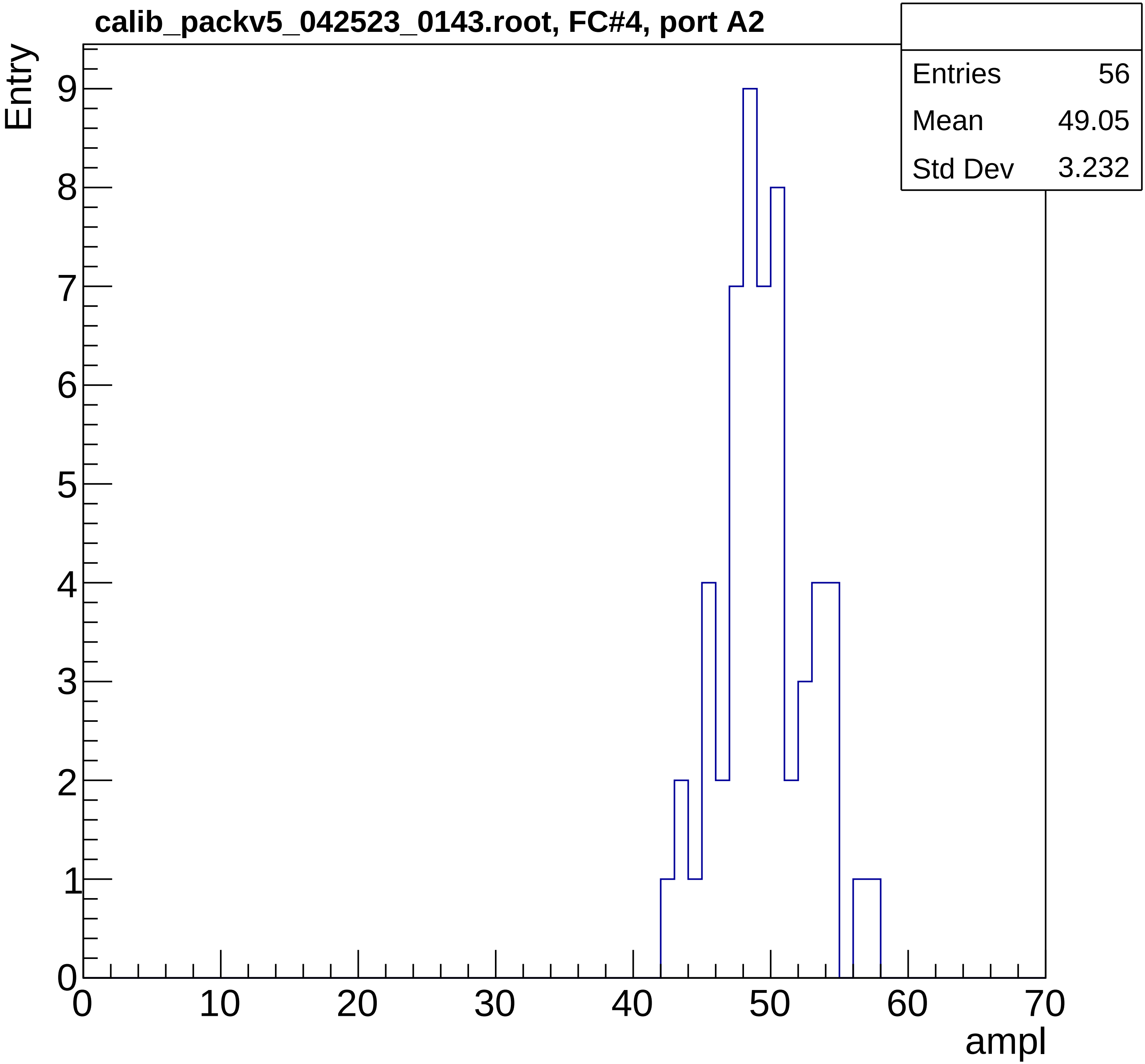

B1L100S, U21-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 27.62
Std Dev 4.861
Gaus mean : 28.2212
Gaus Width: 3.6216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 35.75
Std Dev 3.539
Gaus mean : 36.1028
Gaus Width: 3.5442
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
Entries 68
Mean 43.26
Std Dev 3.402
Gaus mean : 43.0355
Gaus Width: 3.4630

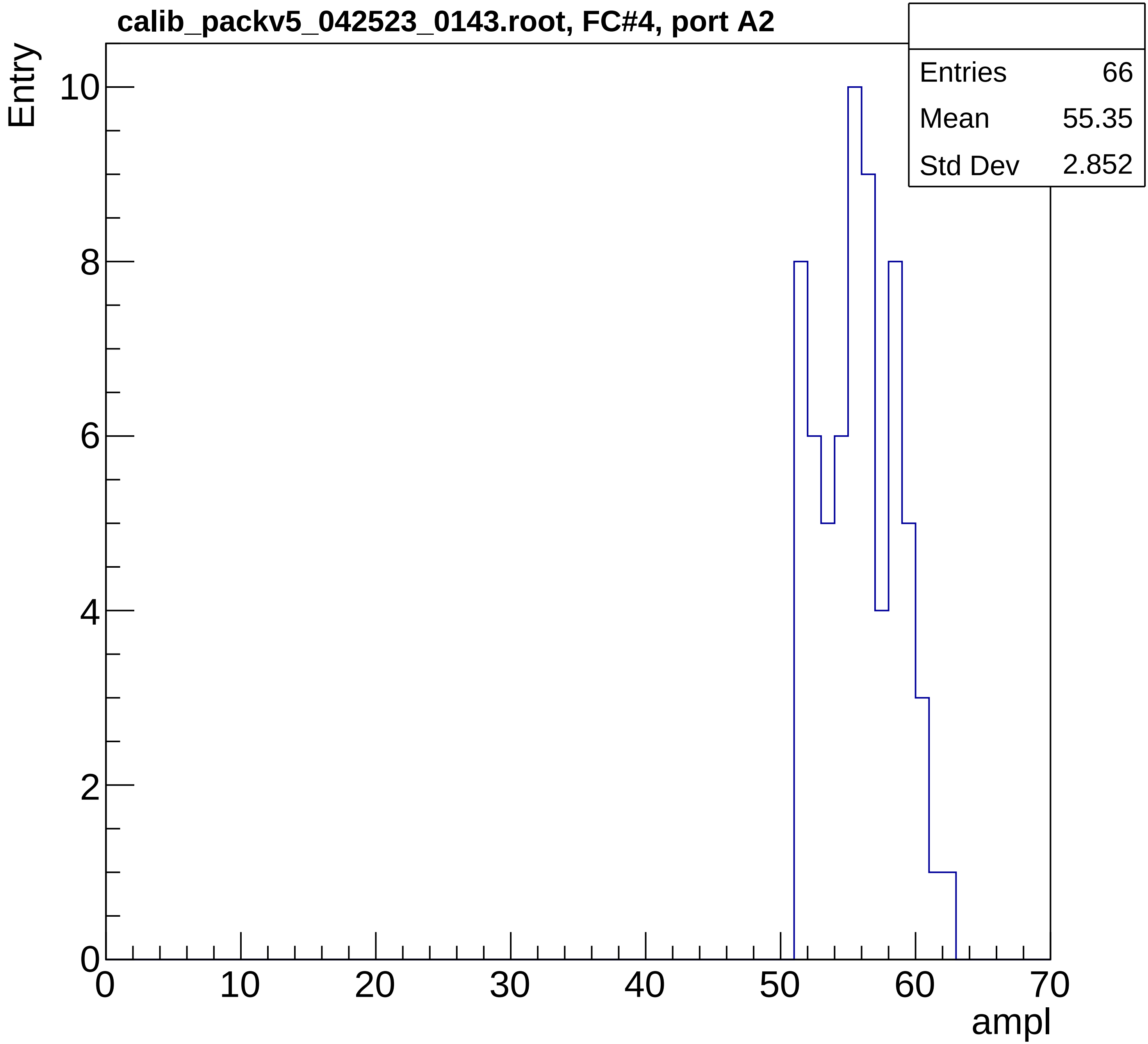
B1L100S, U21-ch109, adc3

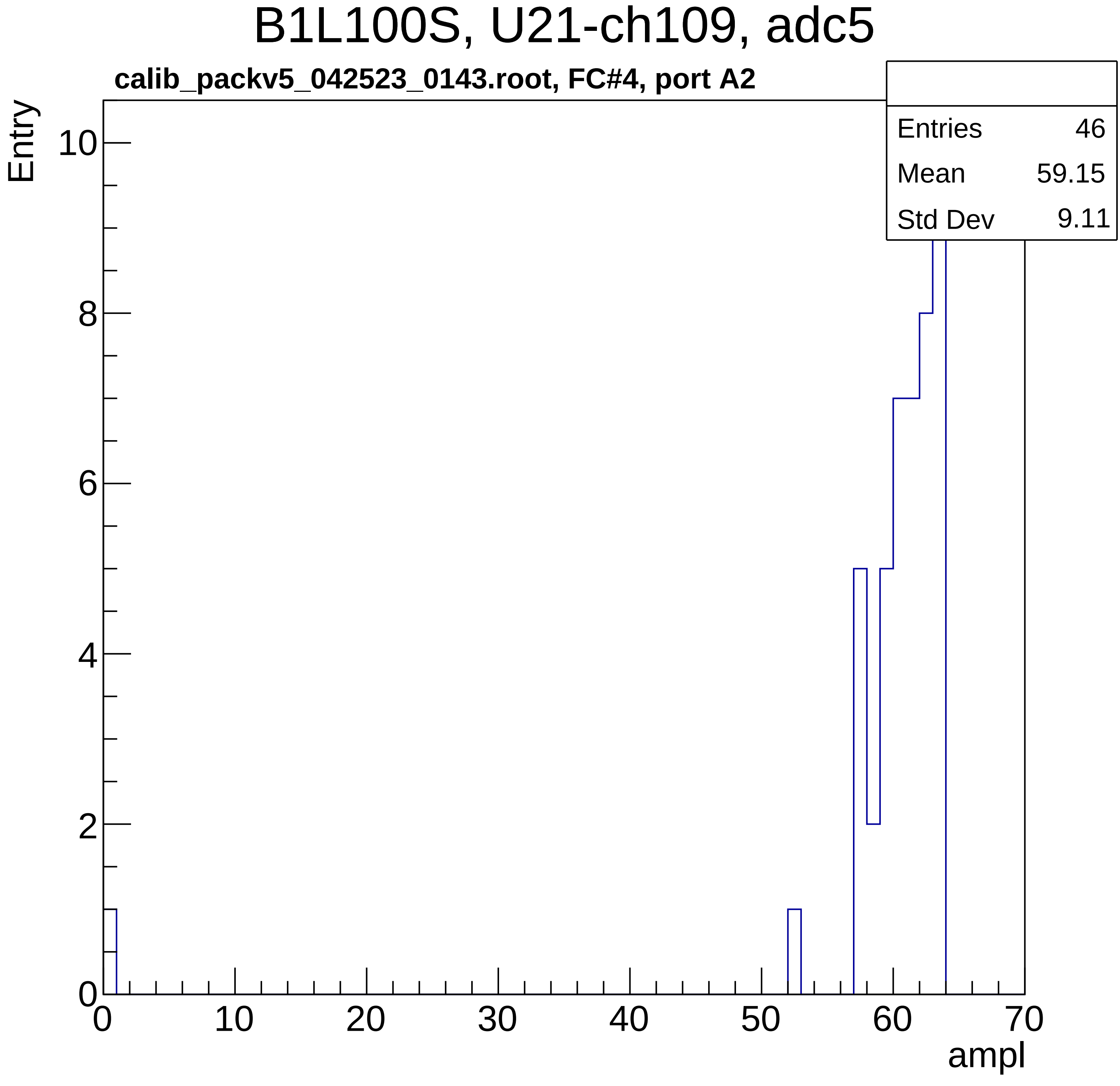
calib_packv5_042523_0143.root, FC#4, port A2



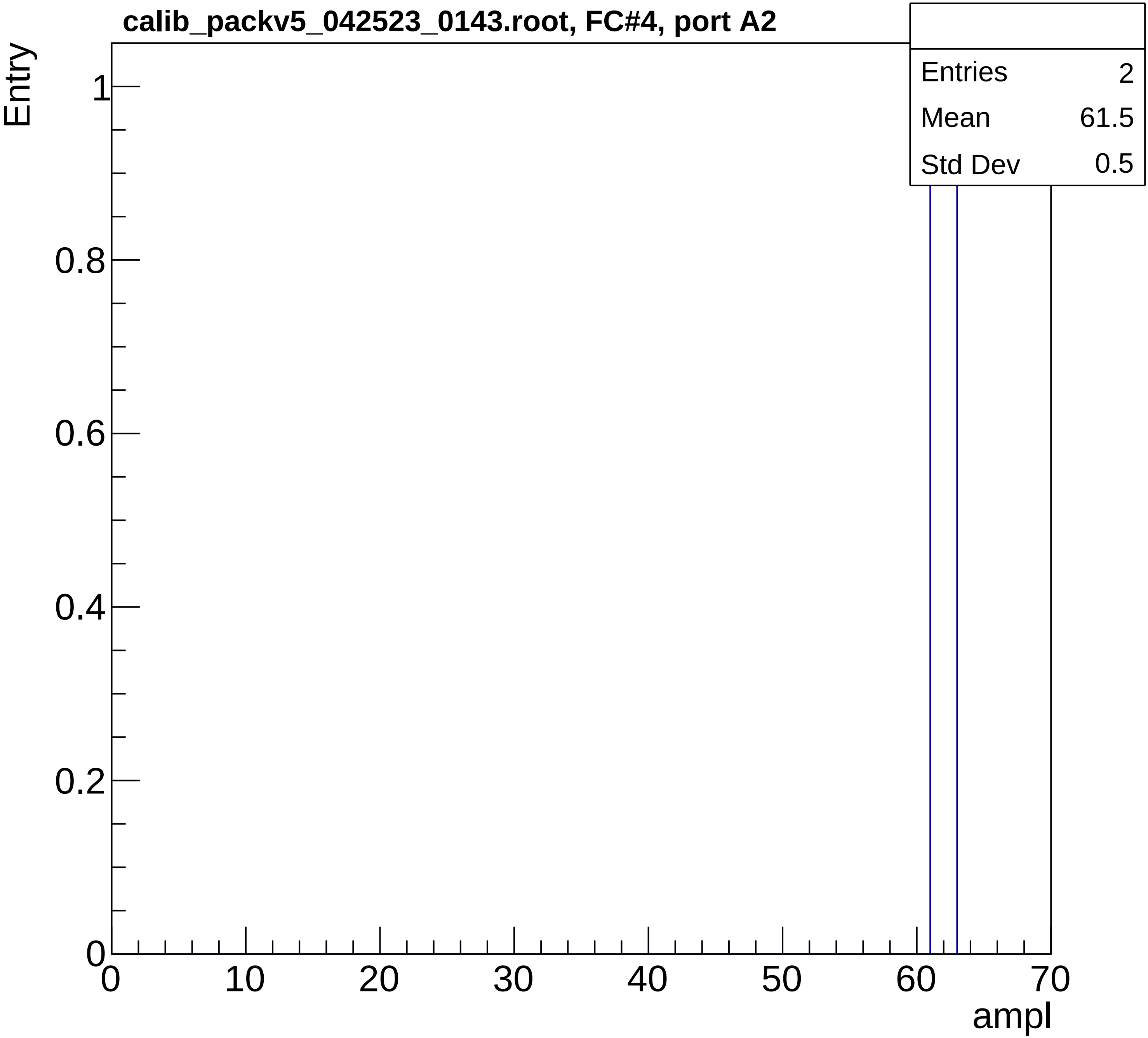
B1L100S, U21-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2



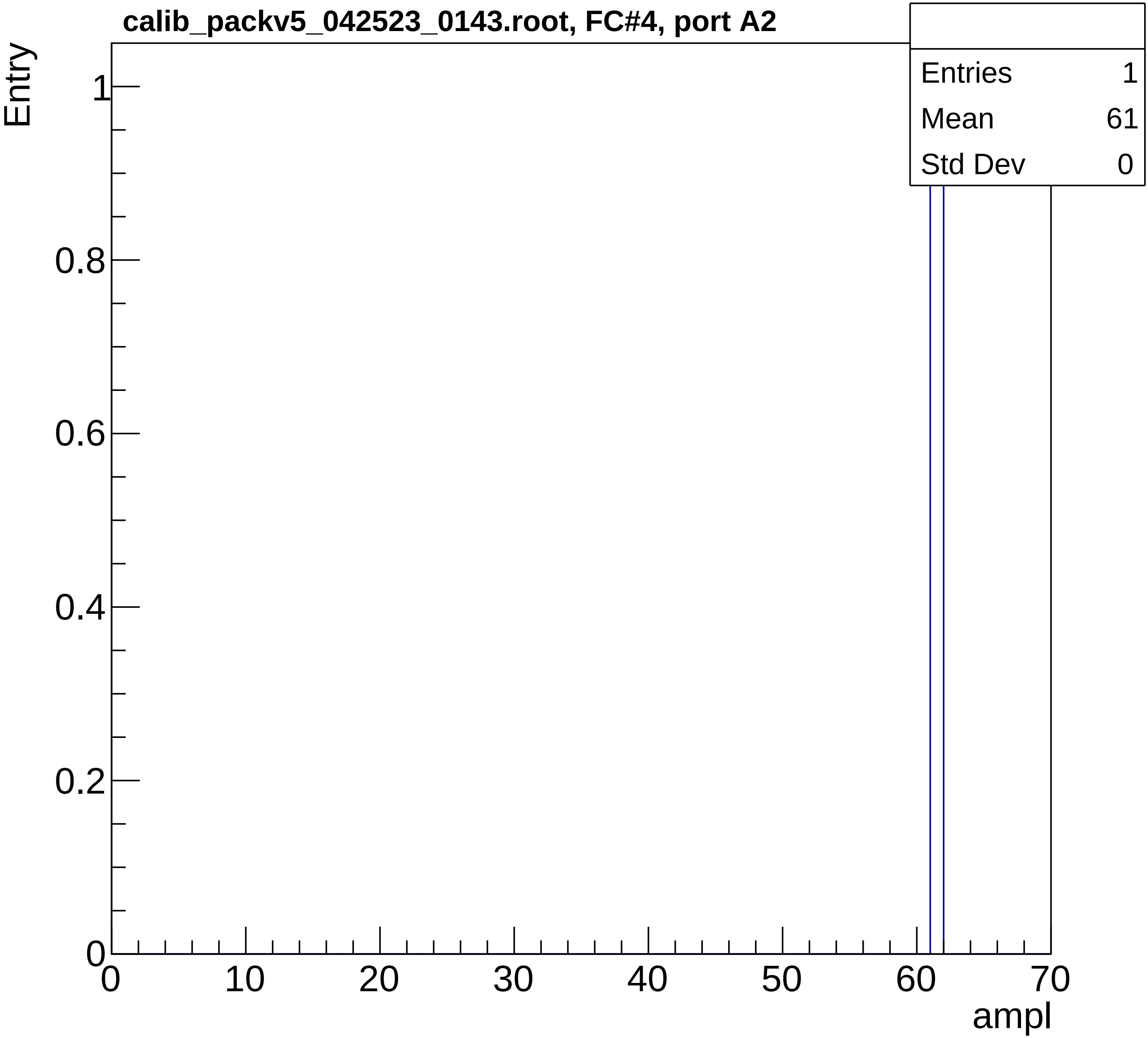

B1L100S, U21-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 59.15
Std Dev 9.11
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

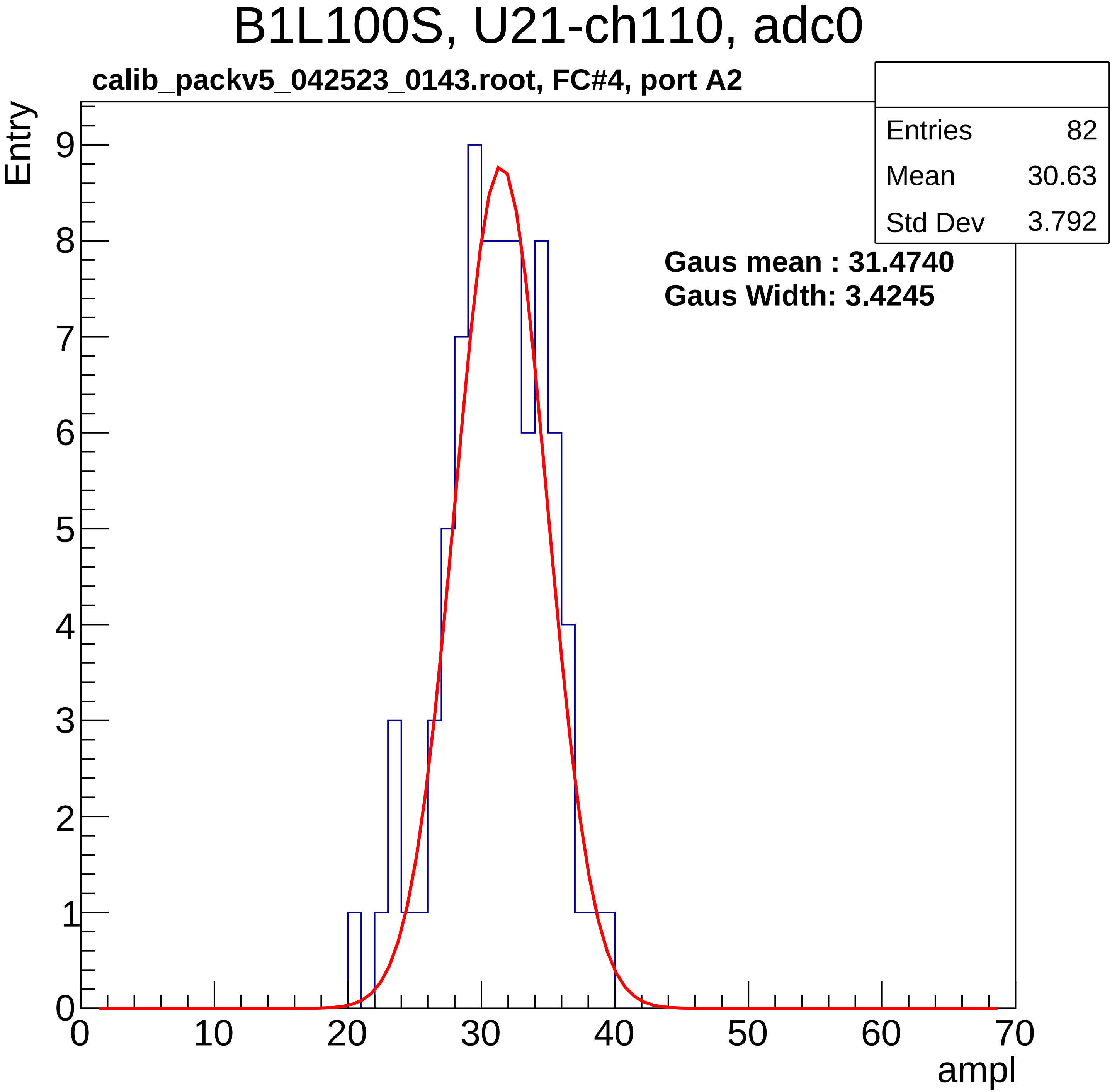
B1L100S, U21-ch109, adc6

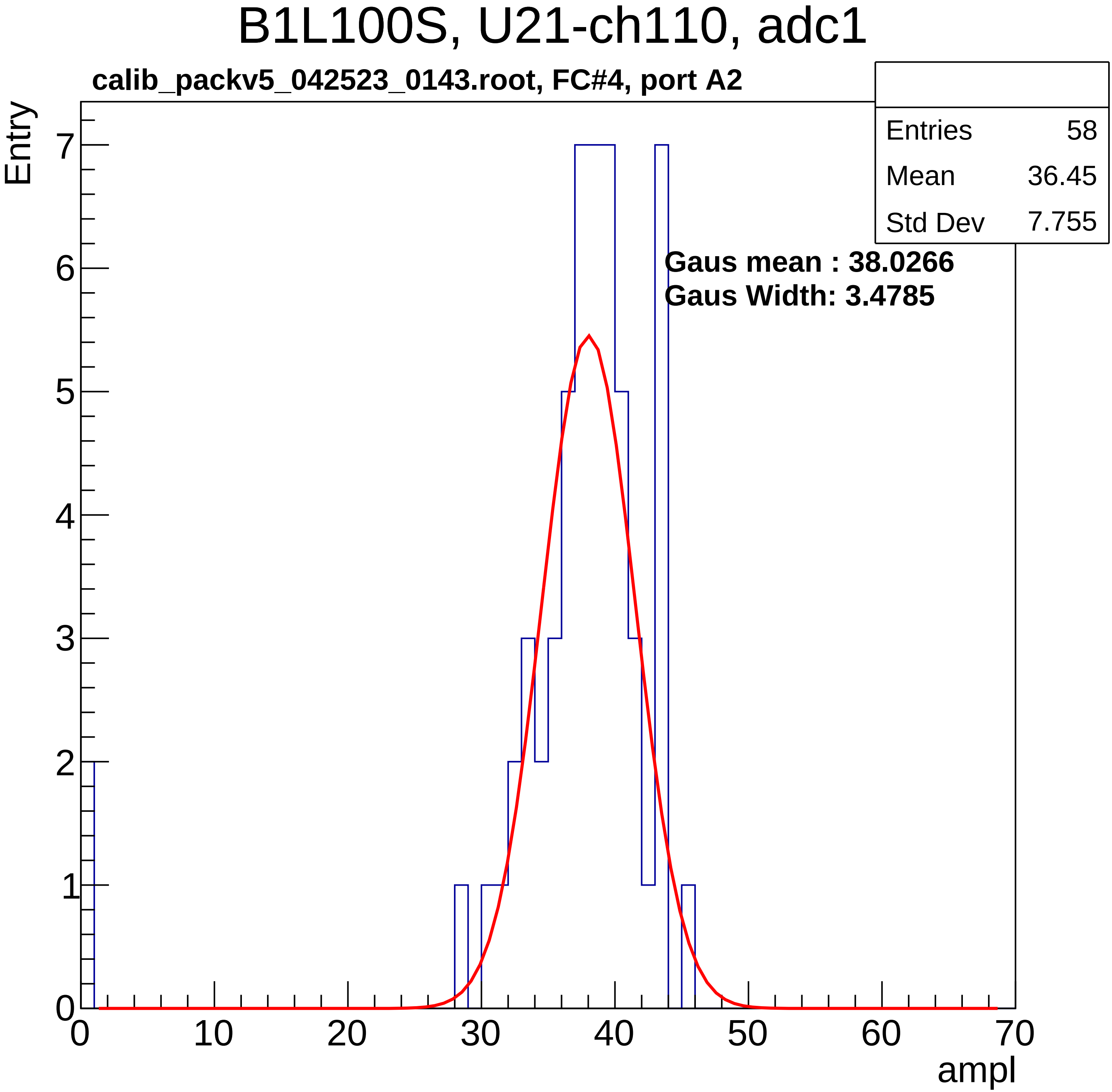


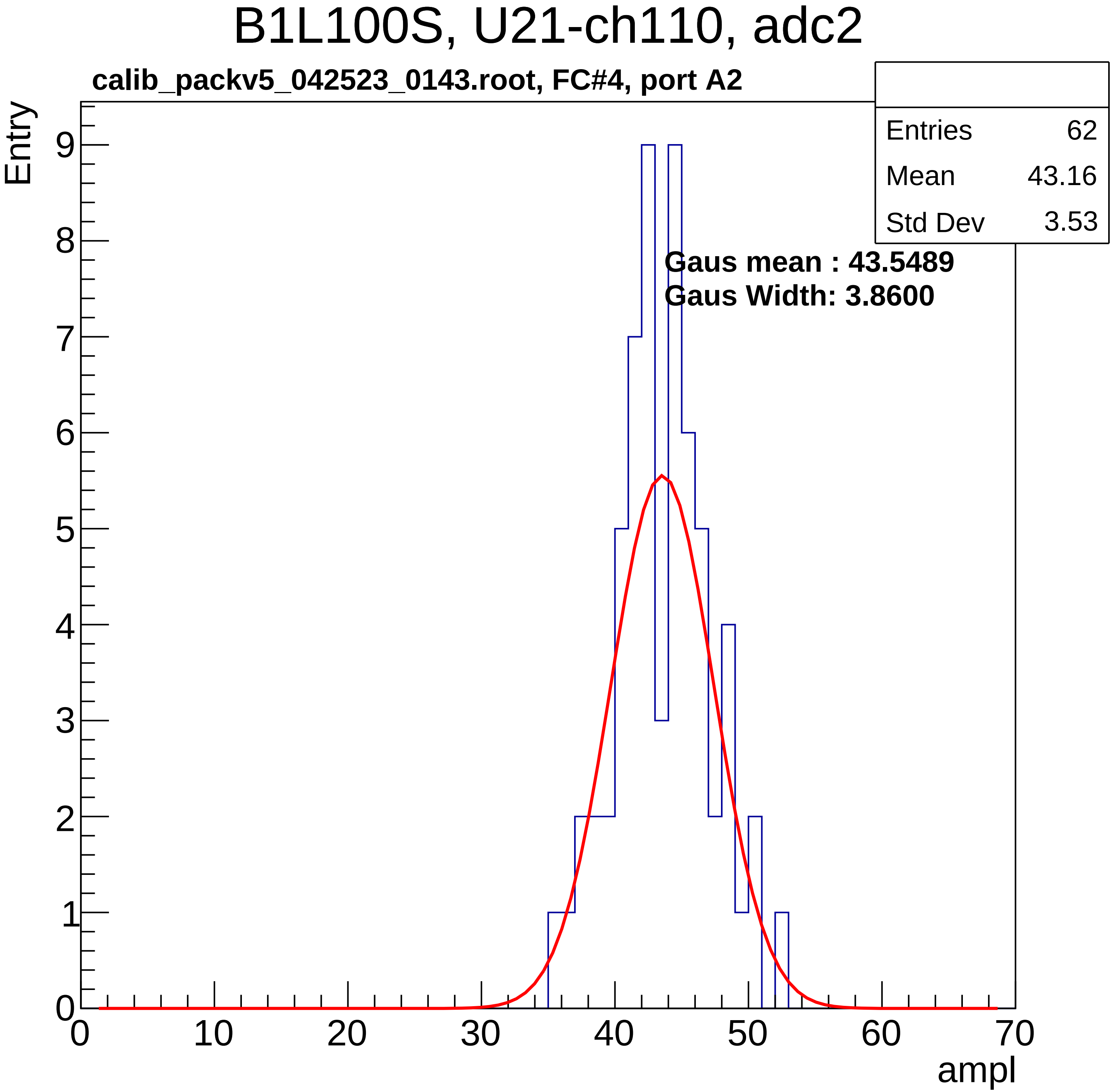
B1L100S, U21-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2



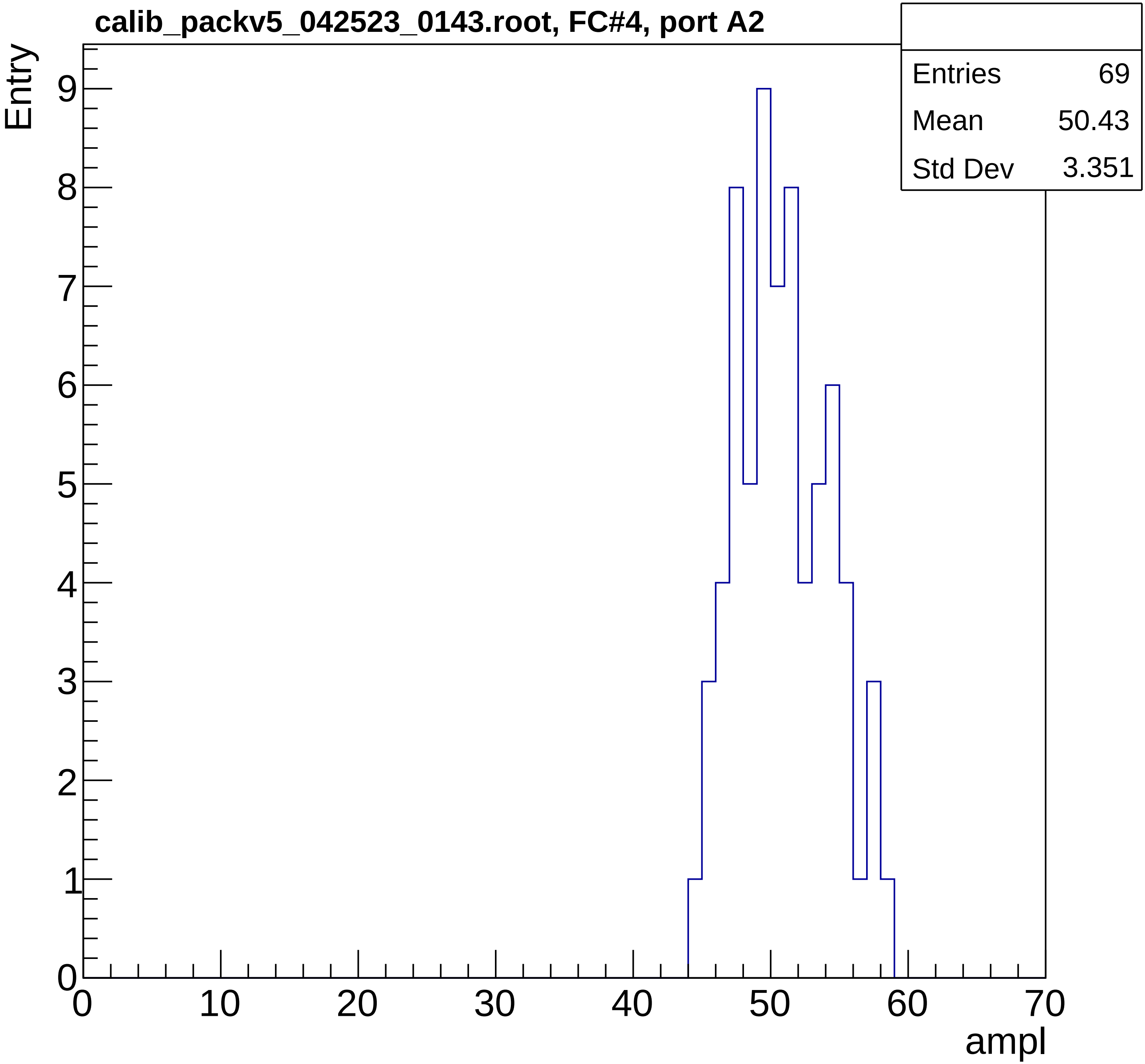

B1L100S, U21-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 30.63
Std Dev 3.792
Gaus mean : 31.4740
Gaus Width: 3.4245
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

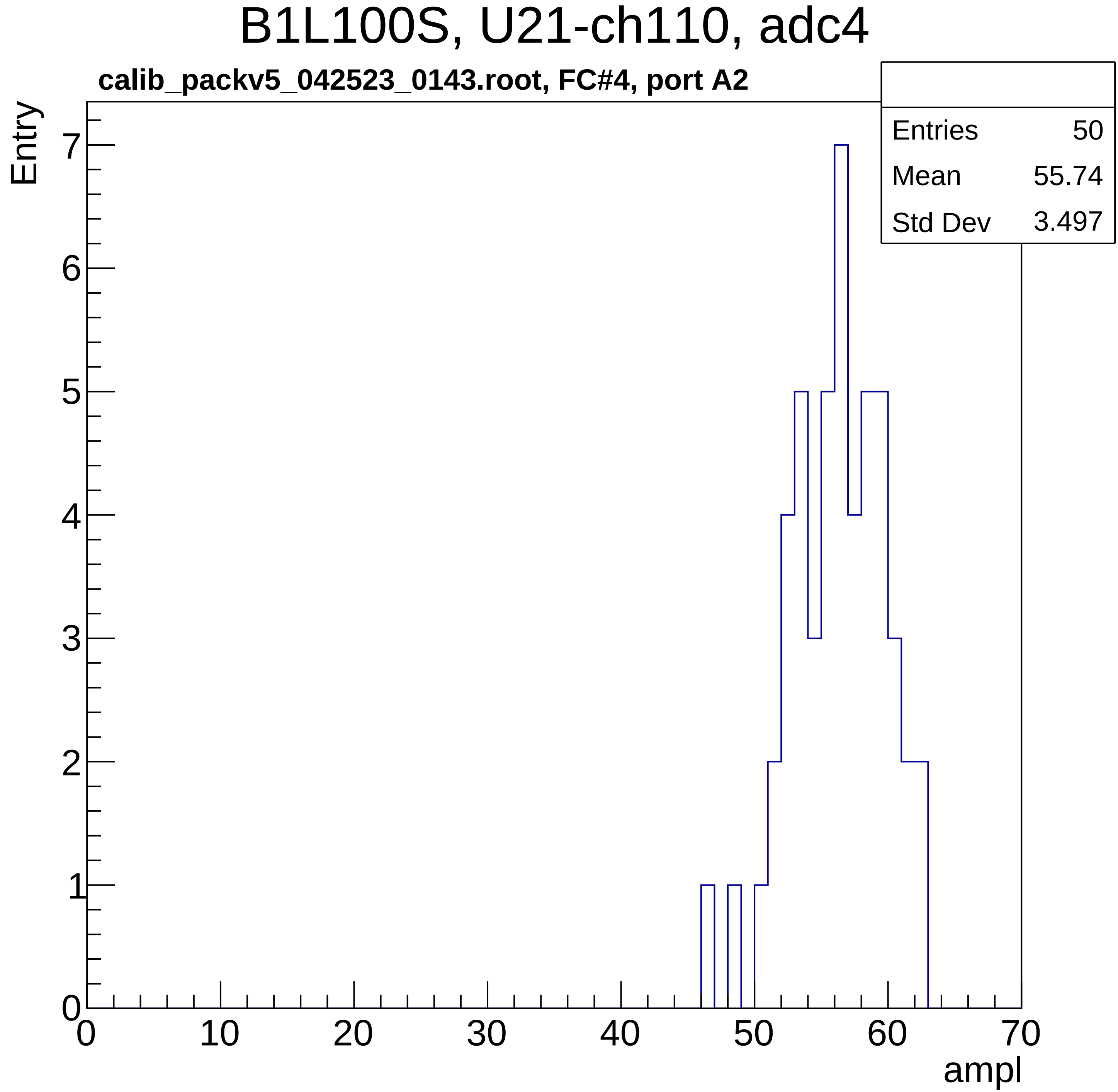

B1L100S, U21-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 36.45
Std Dev 7.755
Gaus mean : 38.0266
Gaus Width: 3.4785
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

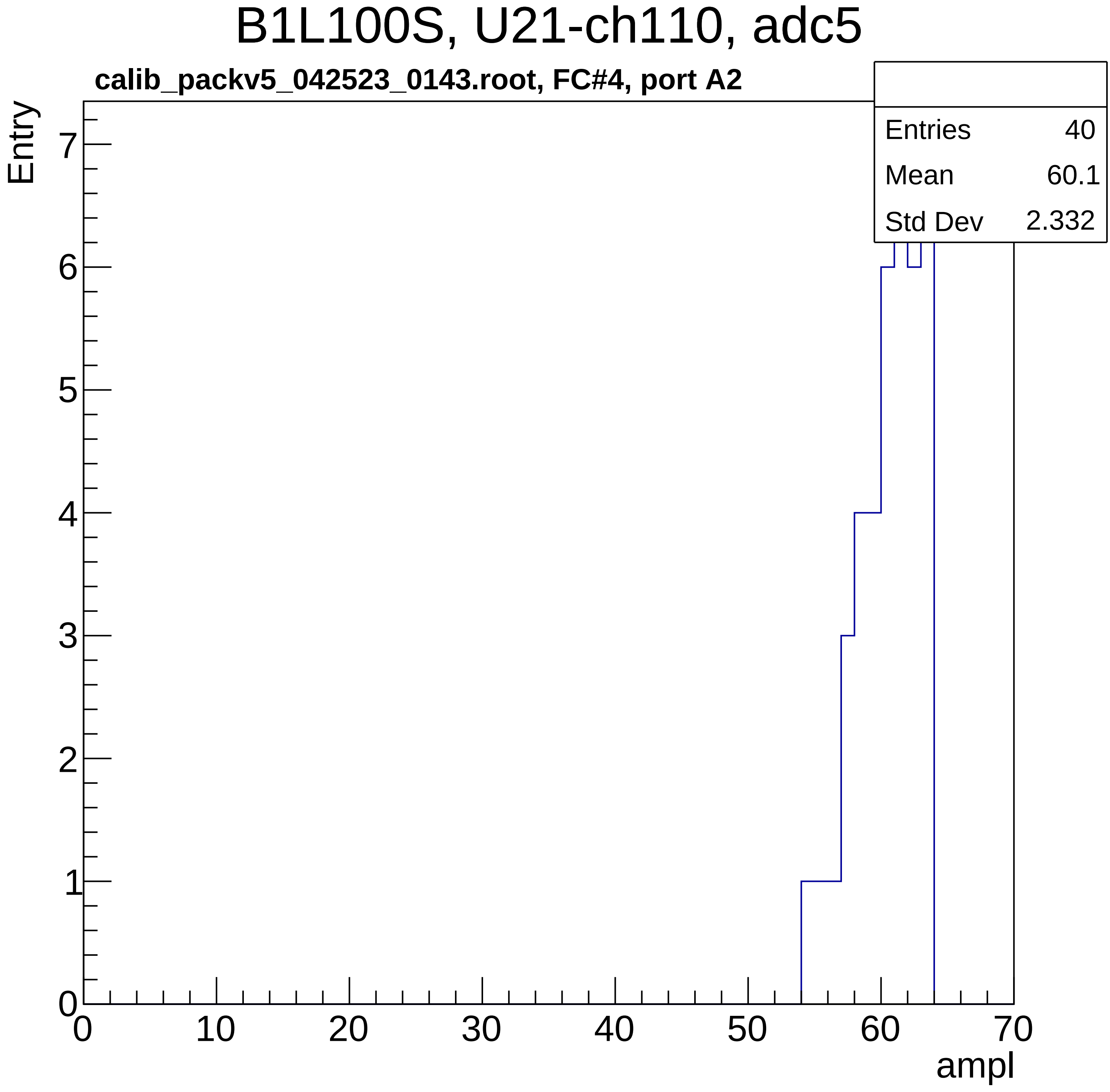

B1L100S, U21-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.16
Std Dev 3.53
Gaus mean : 43.5489
Gaus Width: 3.8600
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

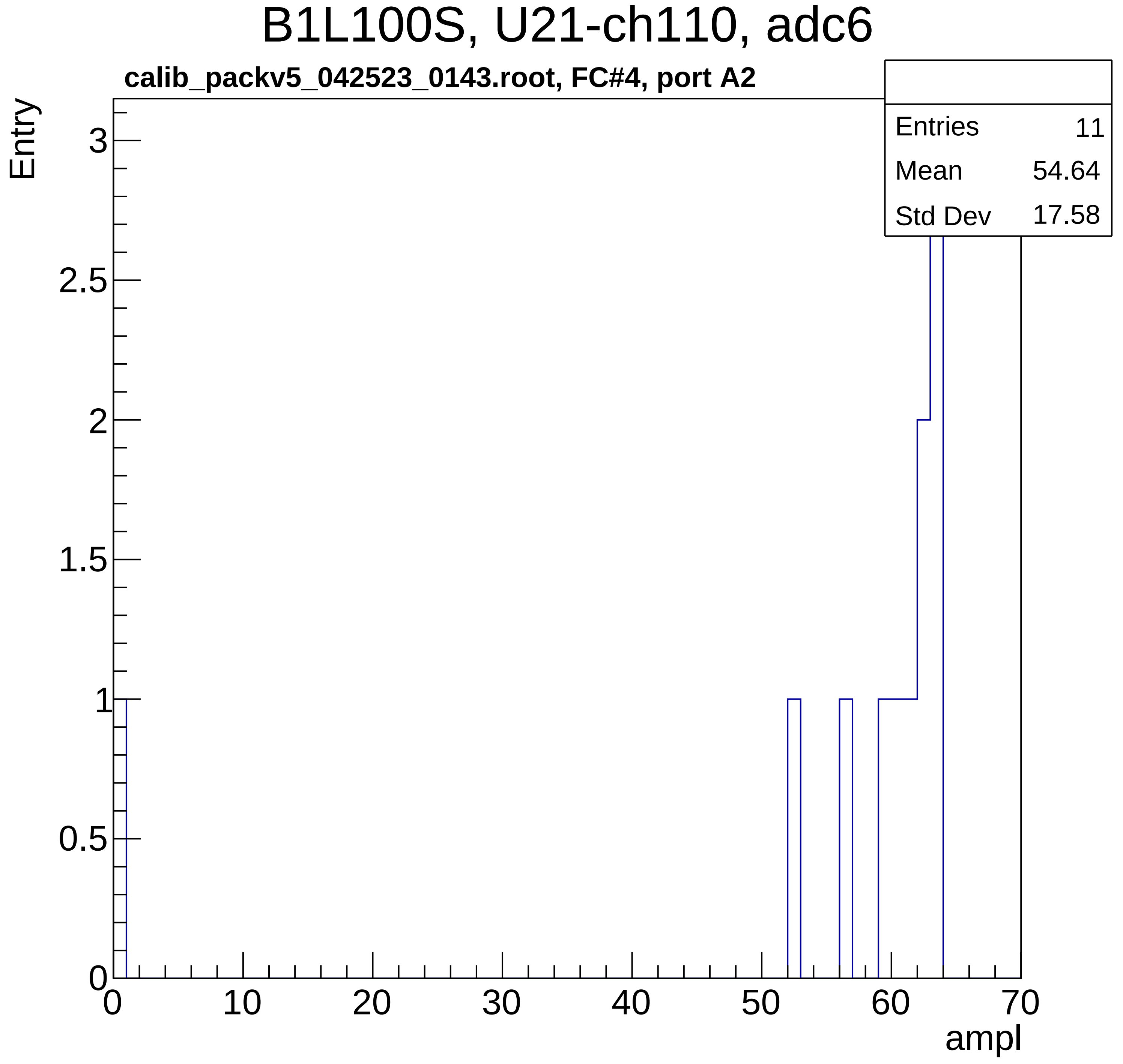
B1L100S, U21-ch110, adc3

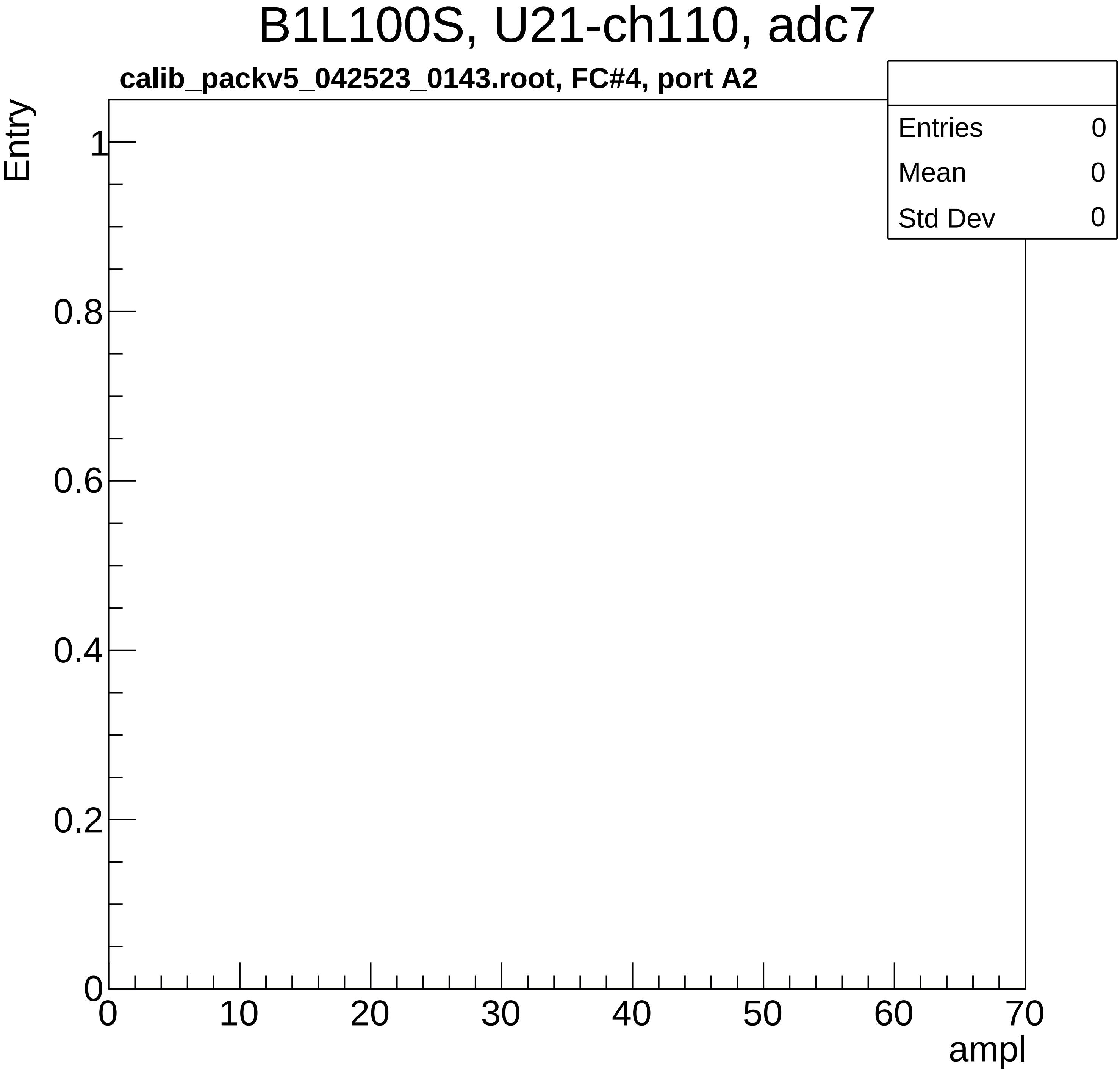
calib_packv5_042523_0143.root, FC#4, port A2



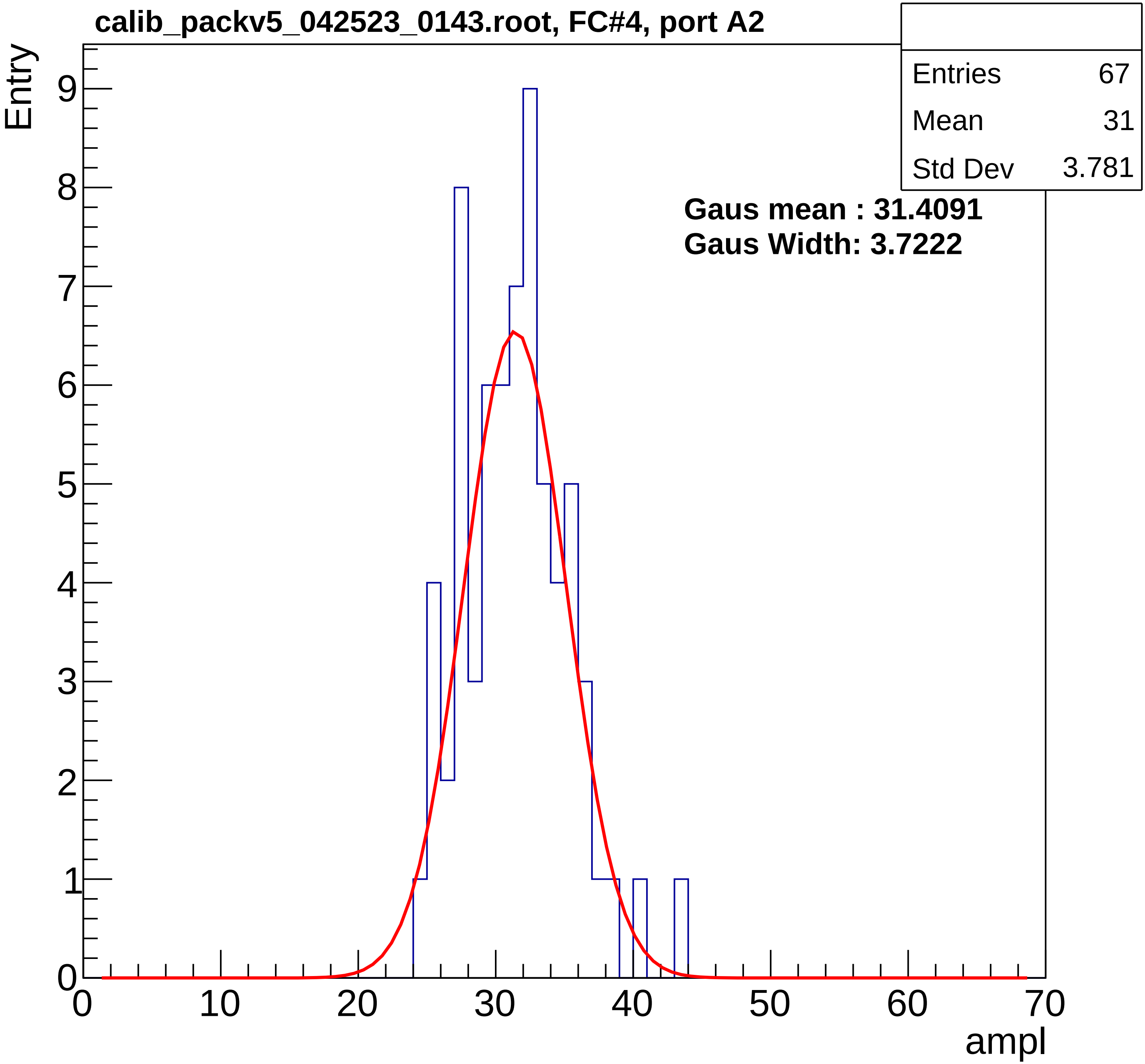

B1L100S, U21-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.74
Std Dev 3.497
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

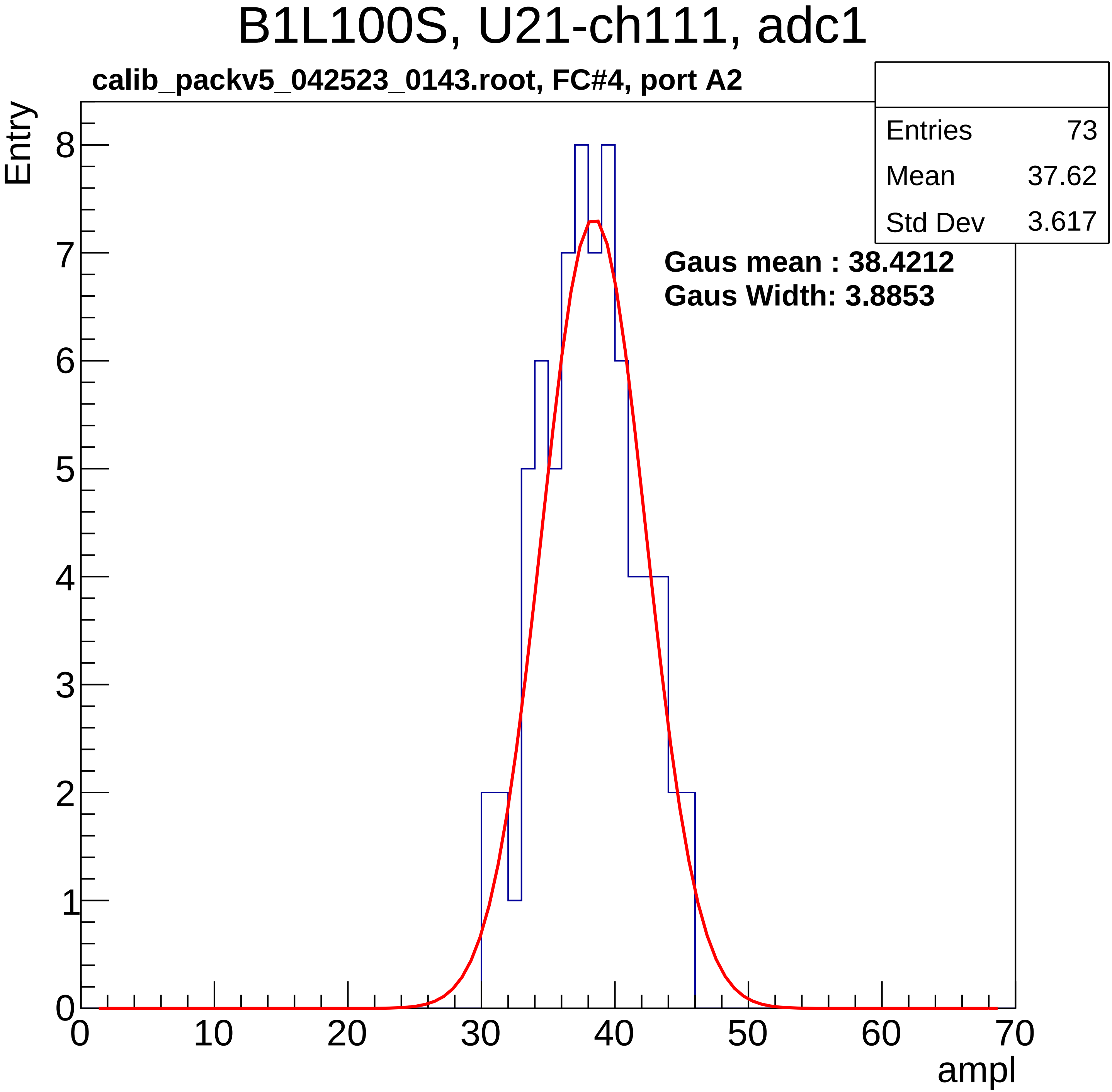

B1L100S, U21-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 60.1
Std Dev 2.332
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

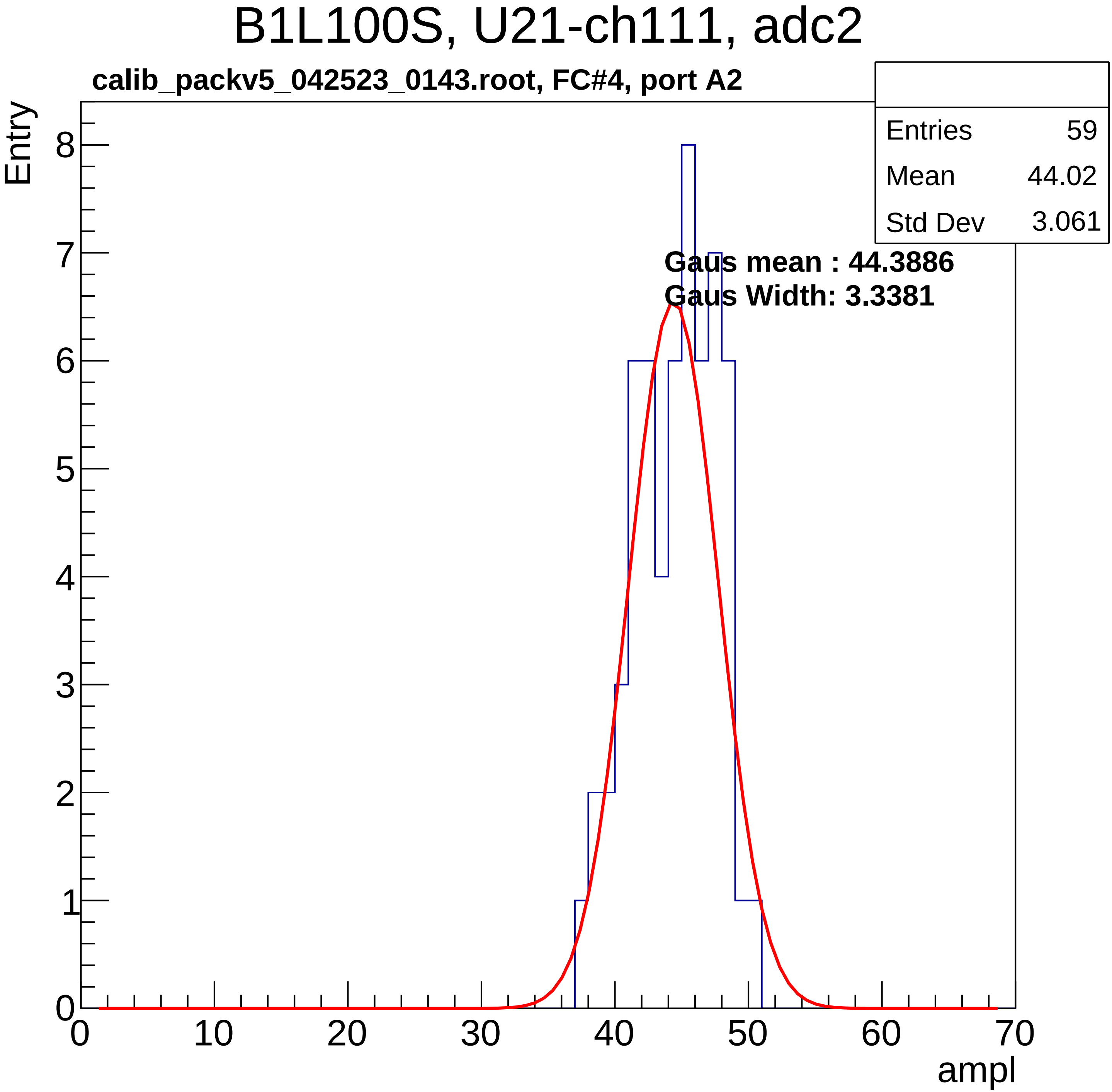

B1L100S, U21-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl
Entries 11
Mean 54.64
Std Dev 17.58

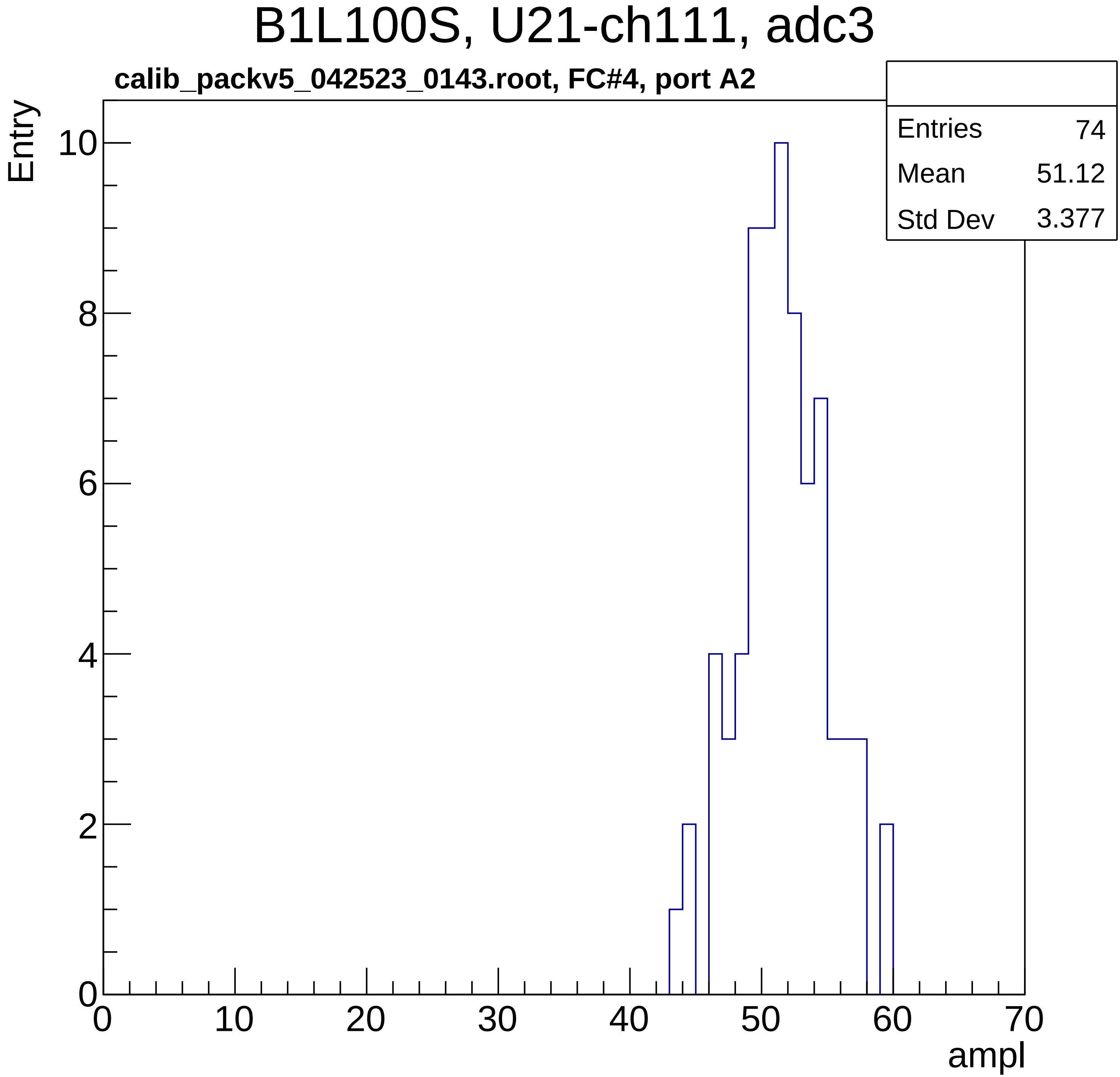

B1L100S, U21-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 0
Mean 0
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

B1L100S, U21-ch111, adc0



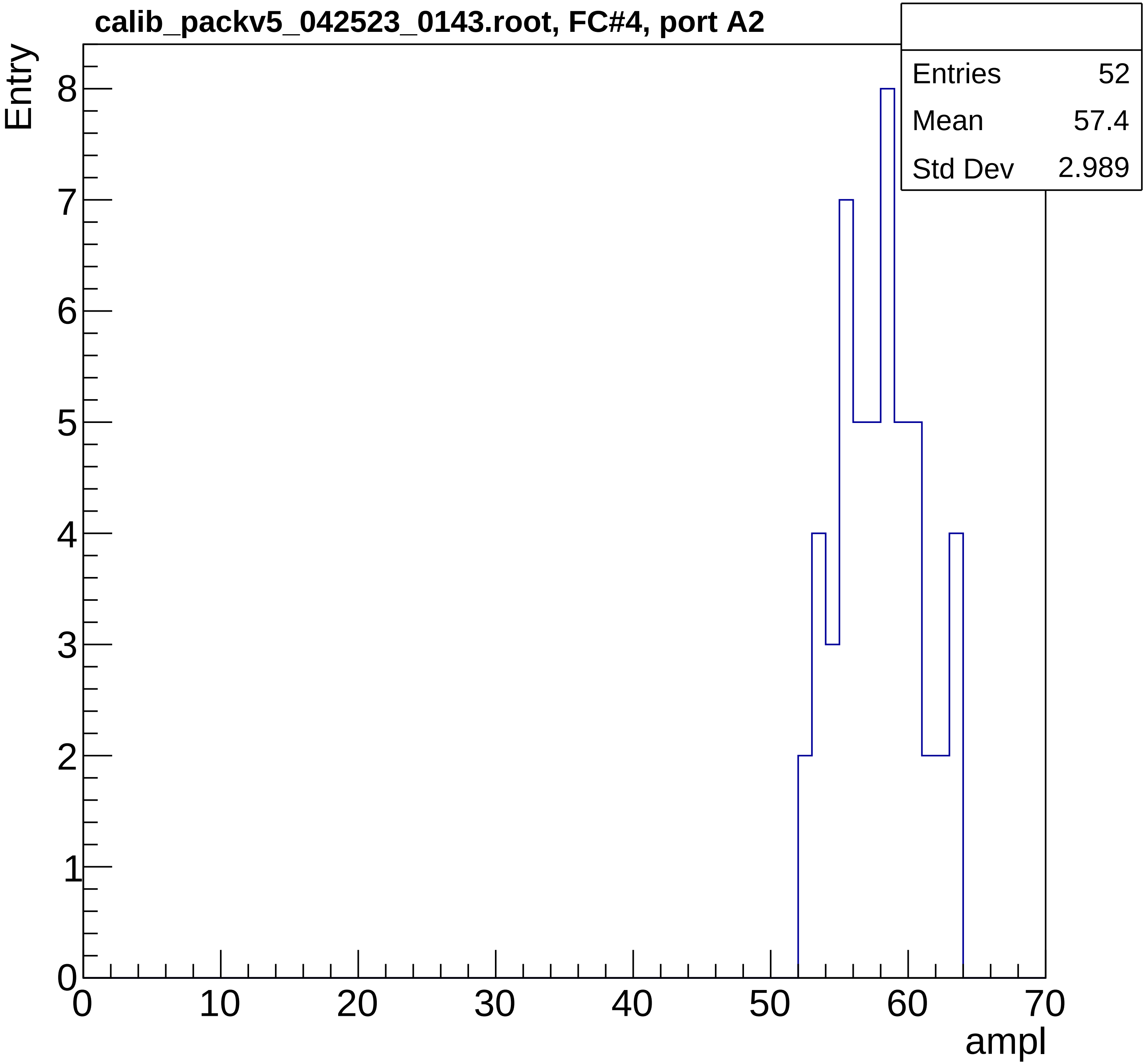

B1L100S, U21-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.62
Std Dev 3.617
Gaus mean : 38.4212
Gaus Width: 3.8853
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

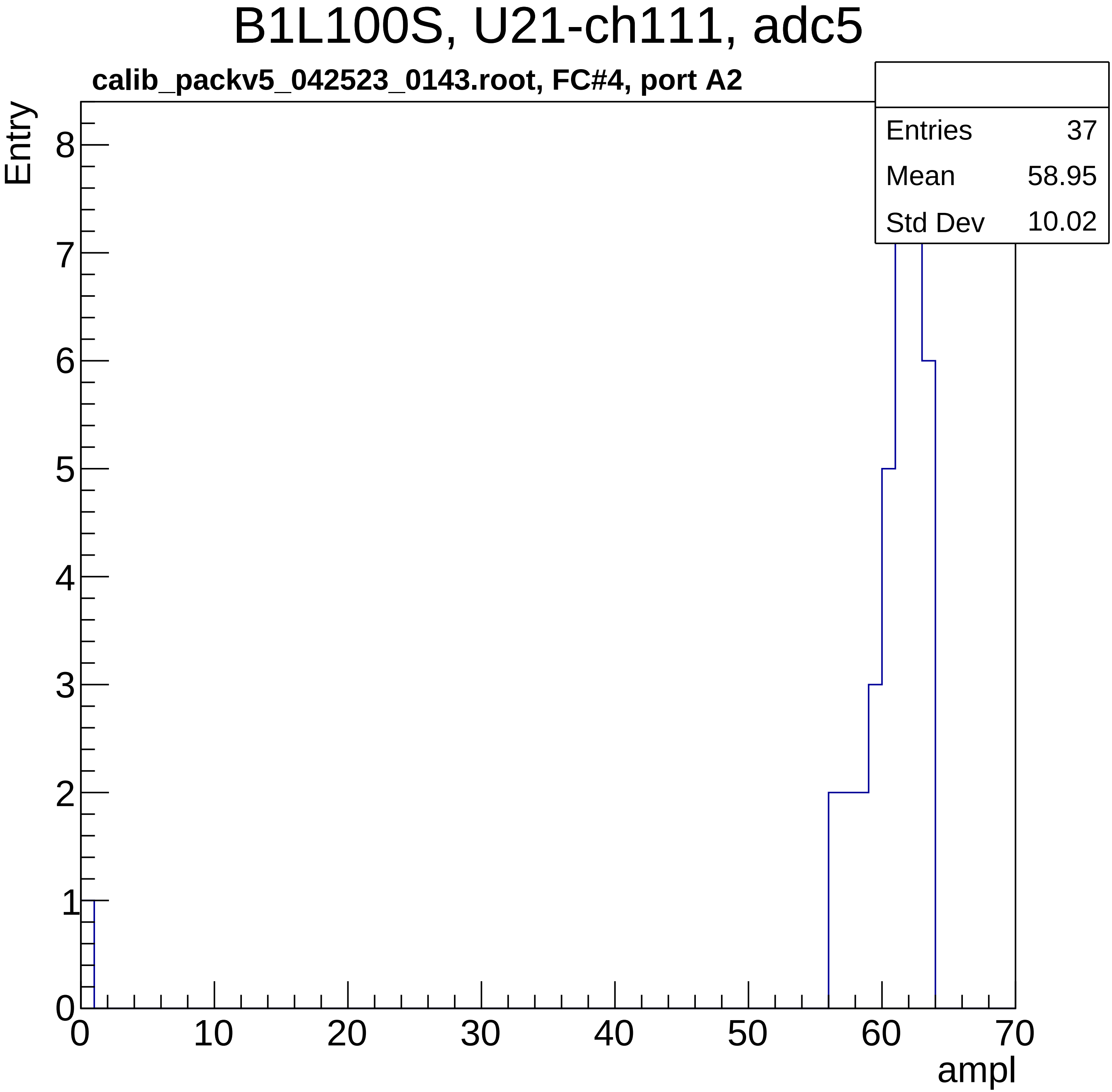

B1L100S, U21-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 59
Mean 44.02
Std Dev 3.061
Gaus mean : 44.3886
Gaus Width: 3.3381
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

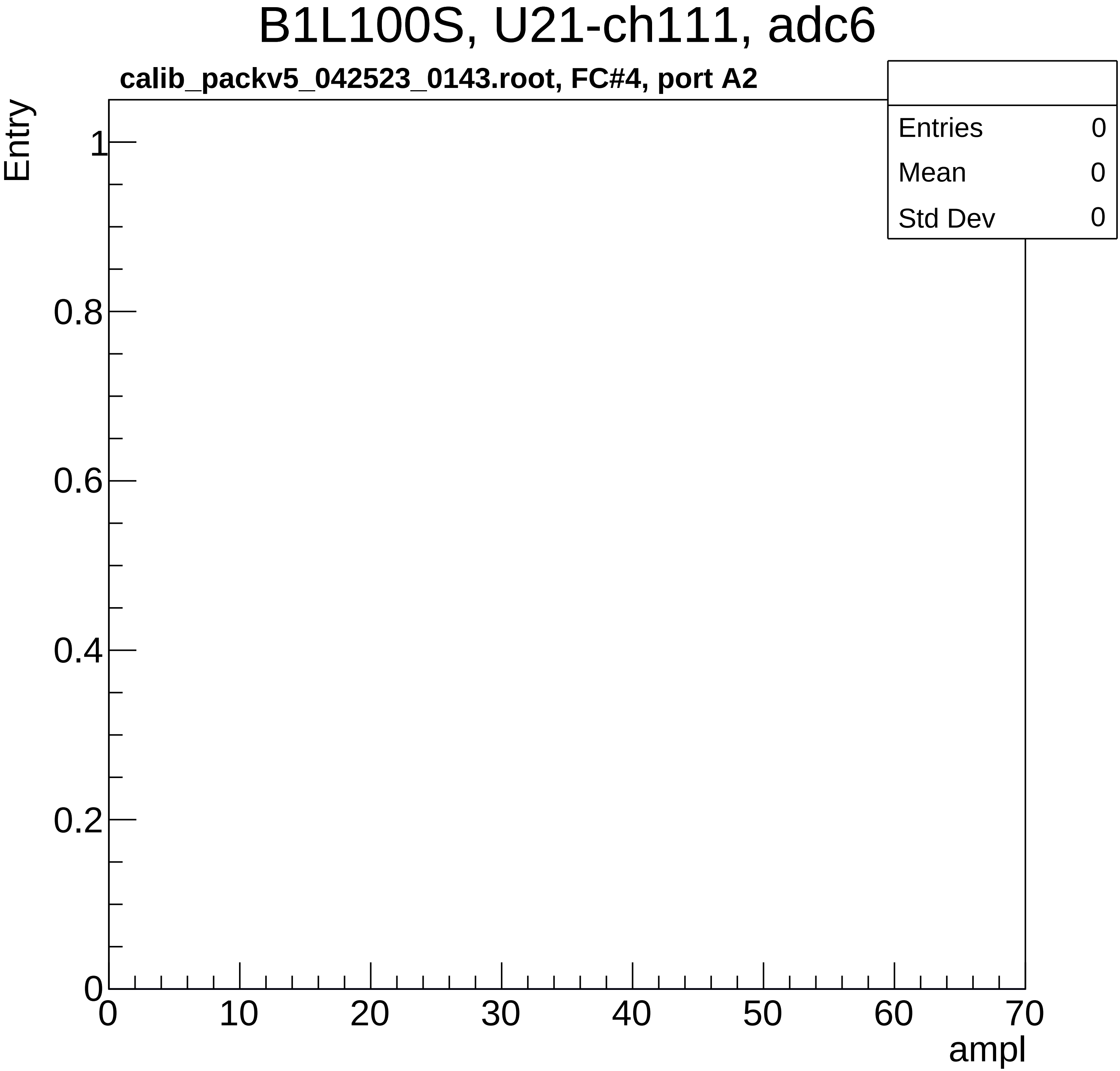

B1L100S, U21-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.12
Std Dev 3.377
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch111, adc4

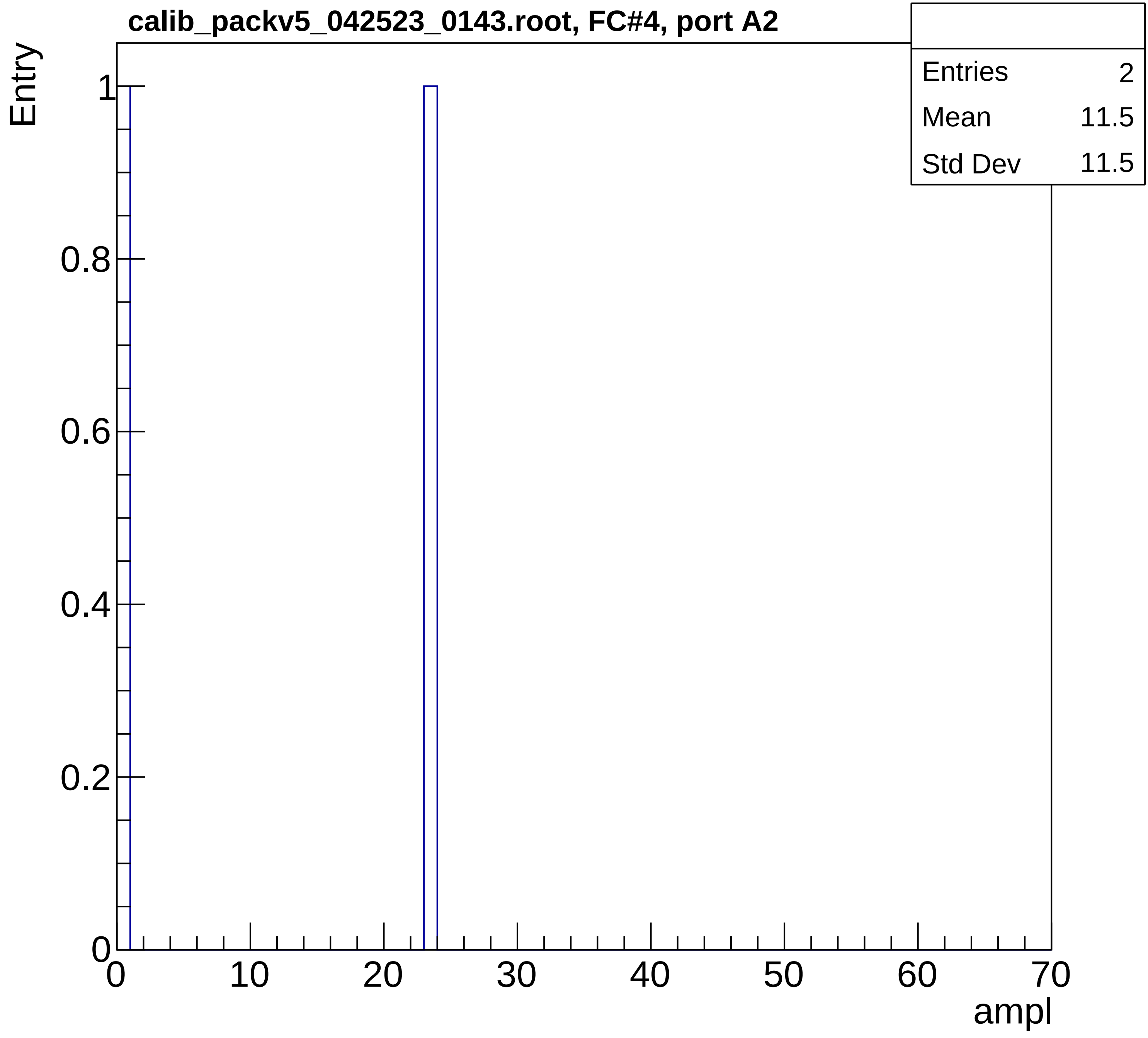
calib_packv5_042523_0143.root, FC#4, port A2

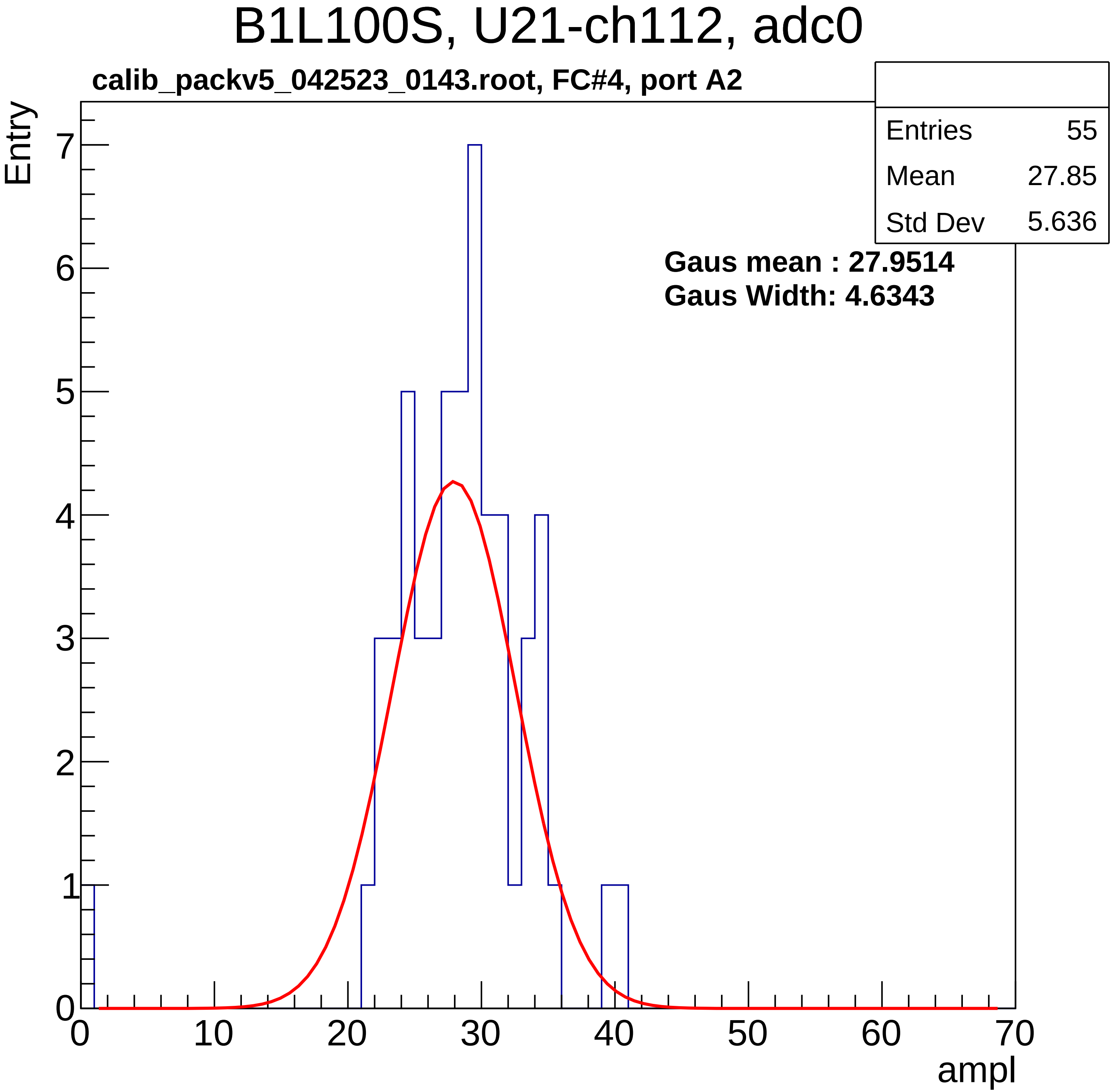


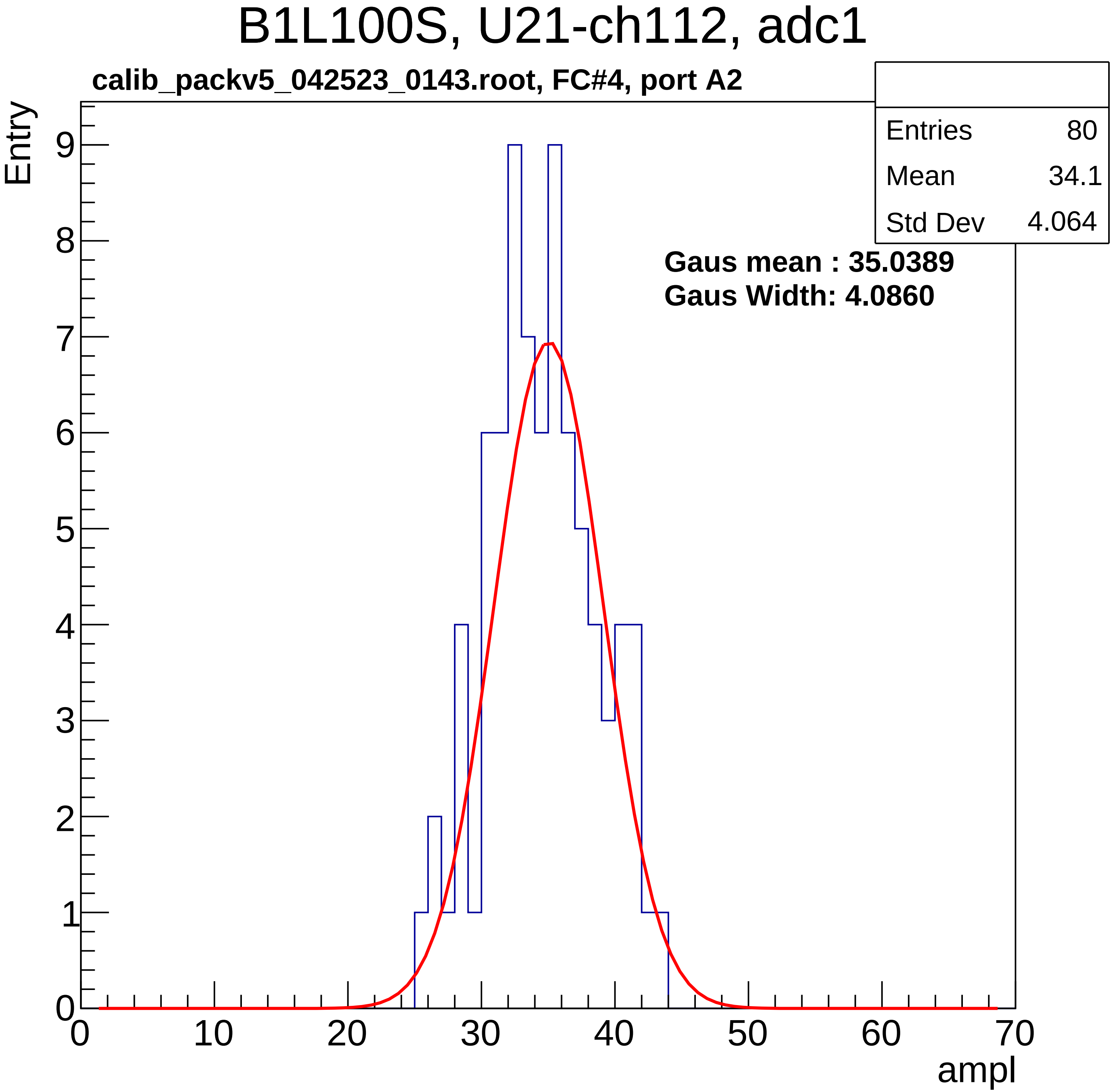

B1L100S, U21-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.95
Std Dev 10.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

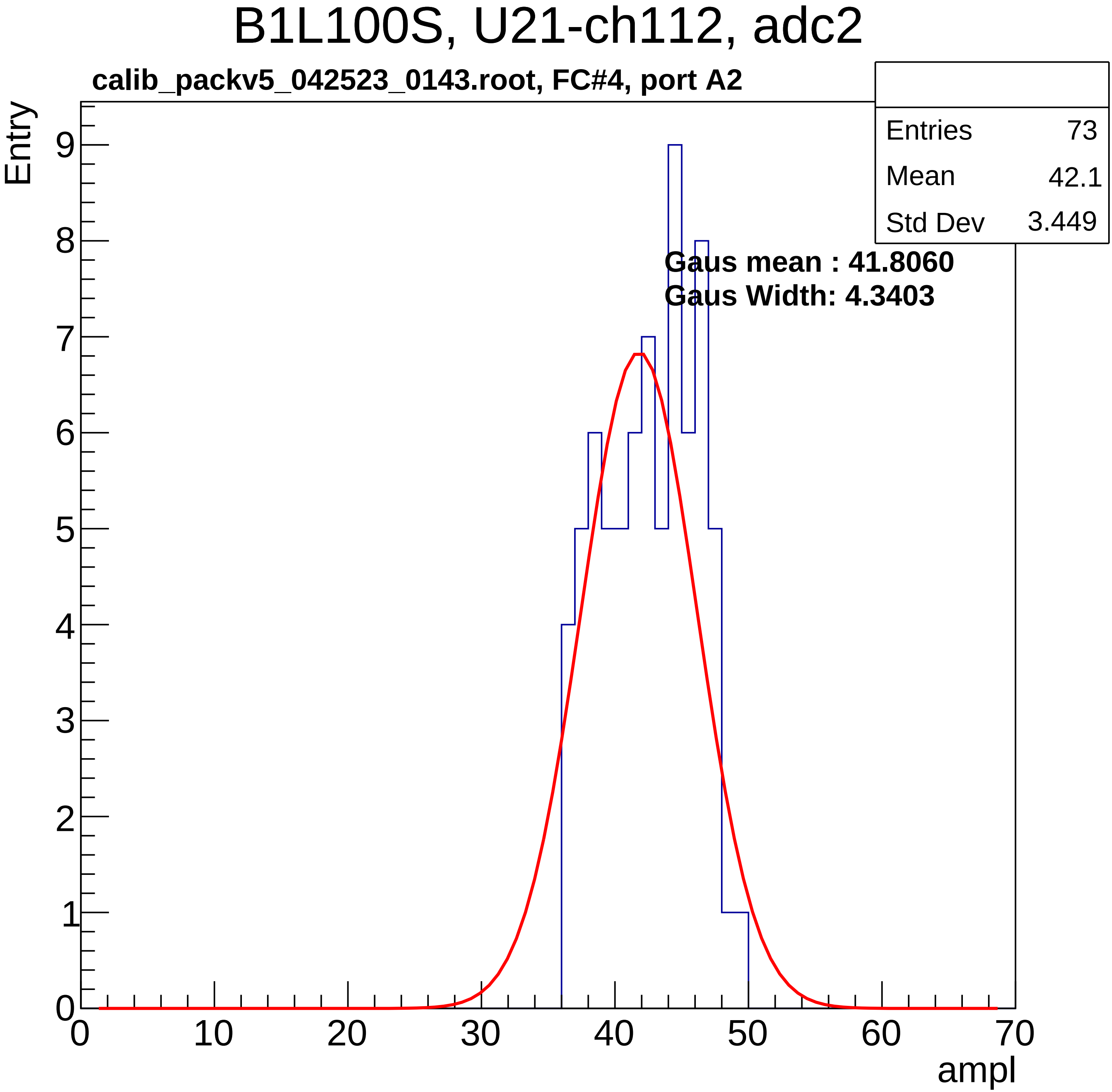

B1L100S, U21-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 0
Mean 0
Std Dev 0
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

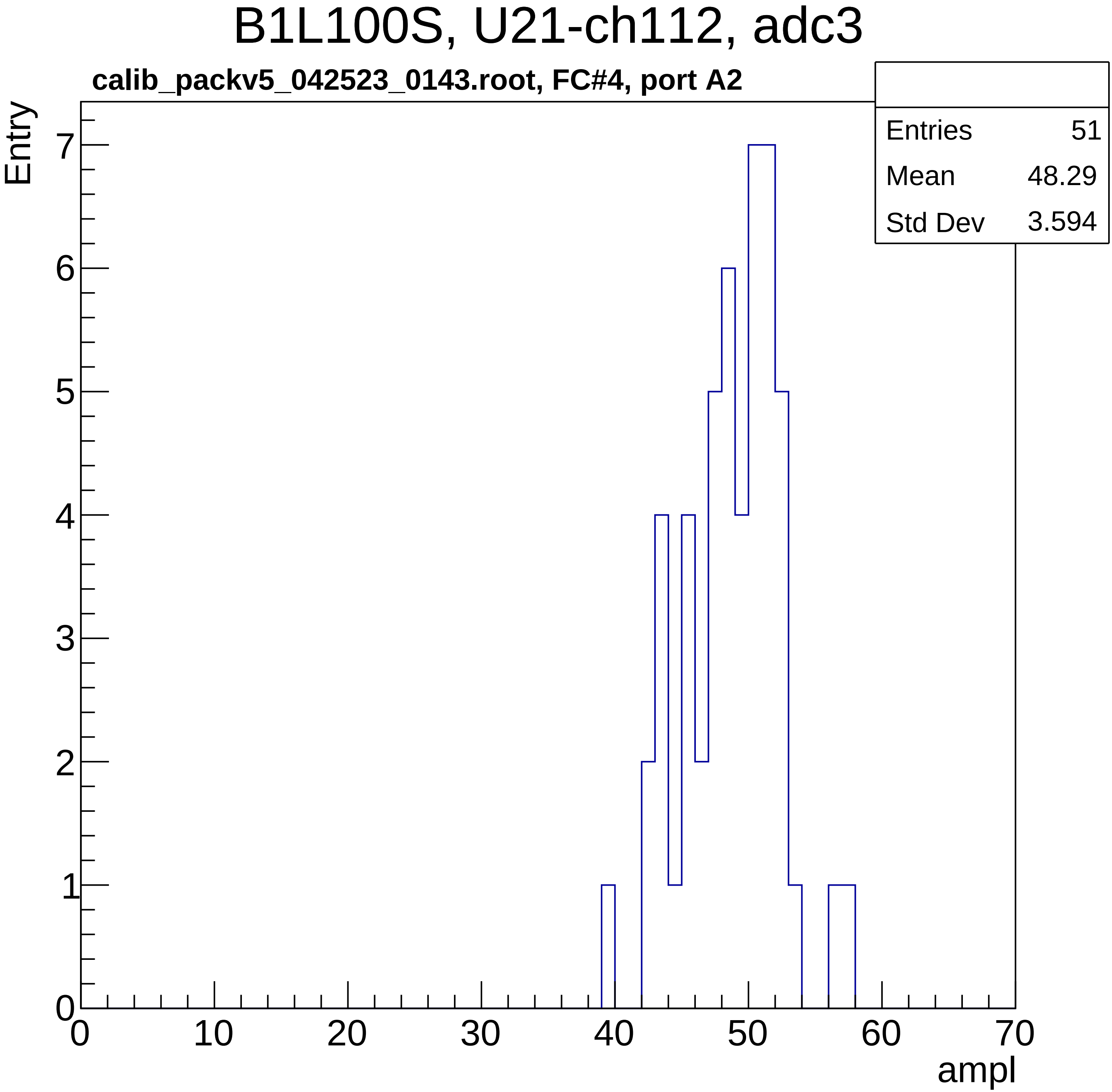
B1L100S, U21-ch111, adc7

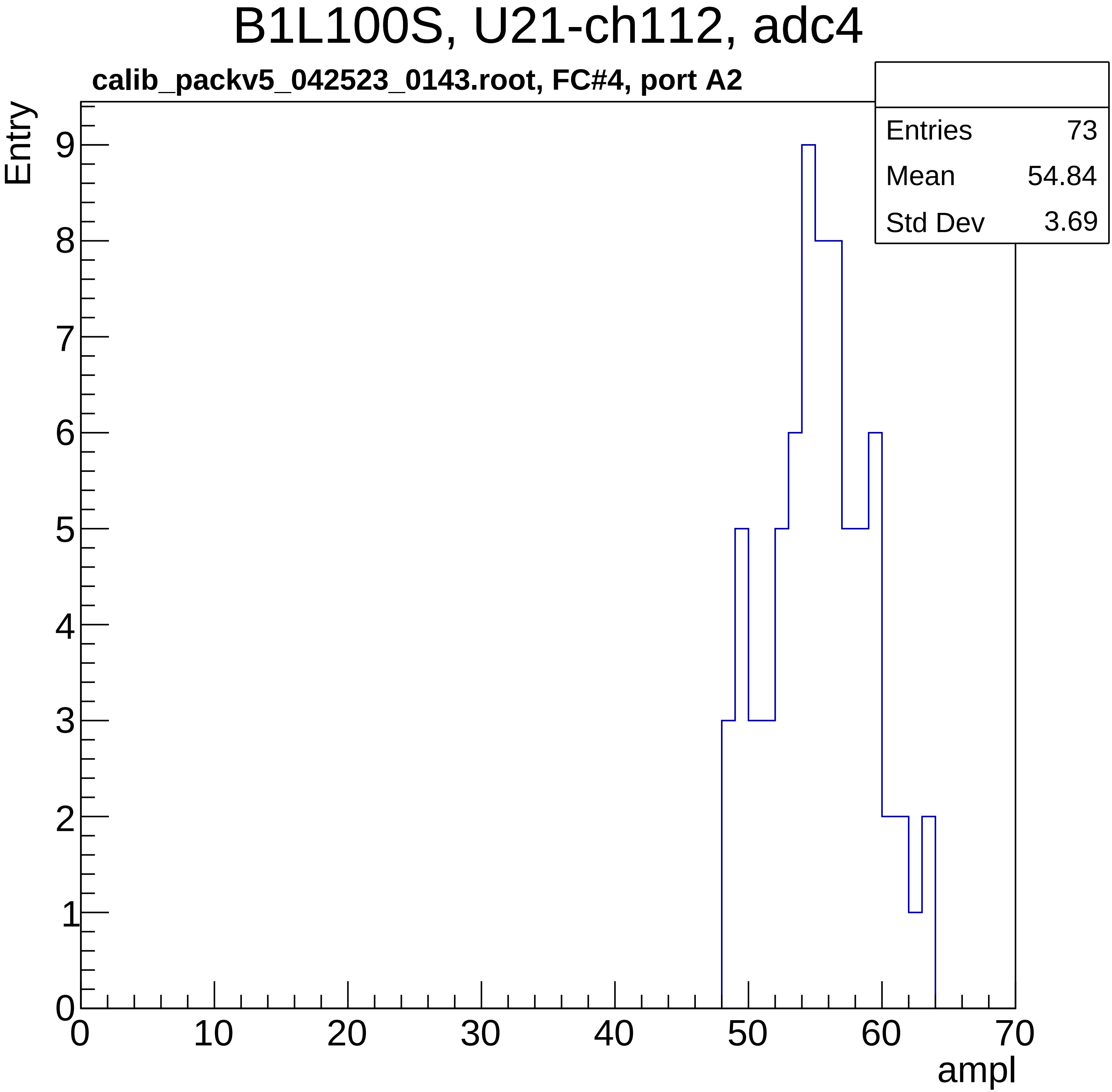



B1L100S, U21-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 27.85
Std Dev 5.636
Gaus mean : 27.9514
Gaus Width: 4.6343
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 34.1
Std Dev 4.064
Gaus mean : 35.0389
Gaus Width: 4.0860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

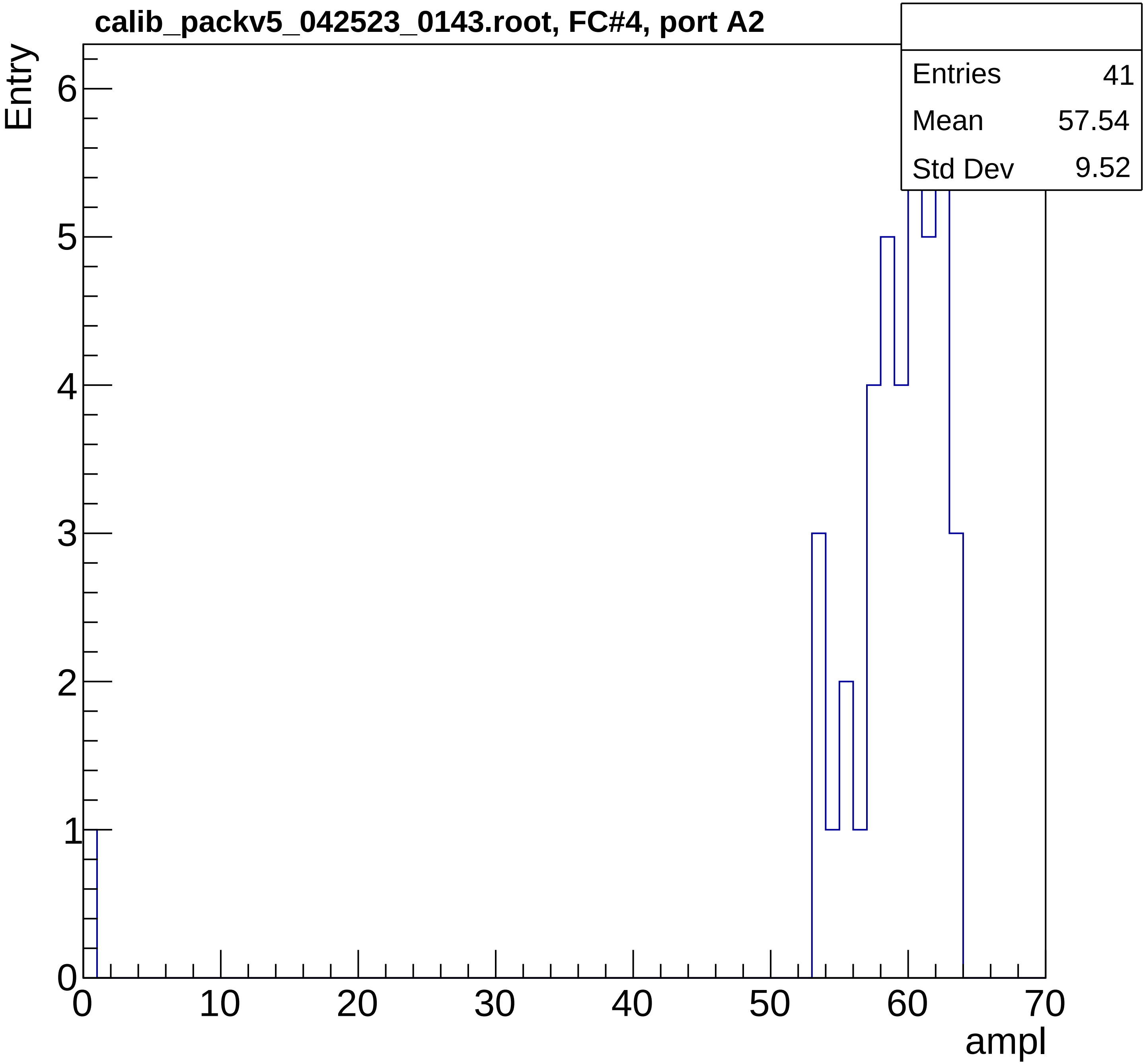

B1L100S, U21-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 42.1
Std Dev 3.449
Gaus mean : 41.8060
Gaus Width: 4.3403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 48.29
Std Dev 3.594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 54.84
Std Dev 3.69
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

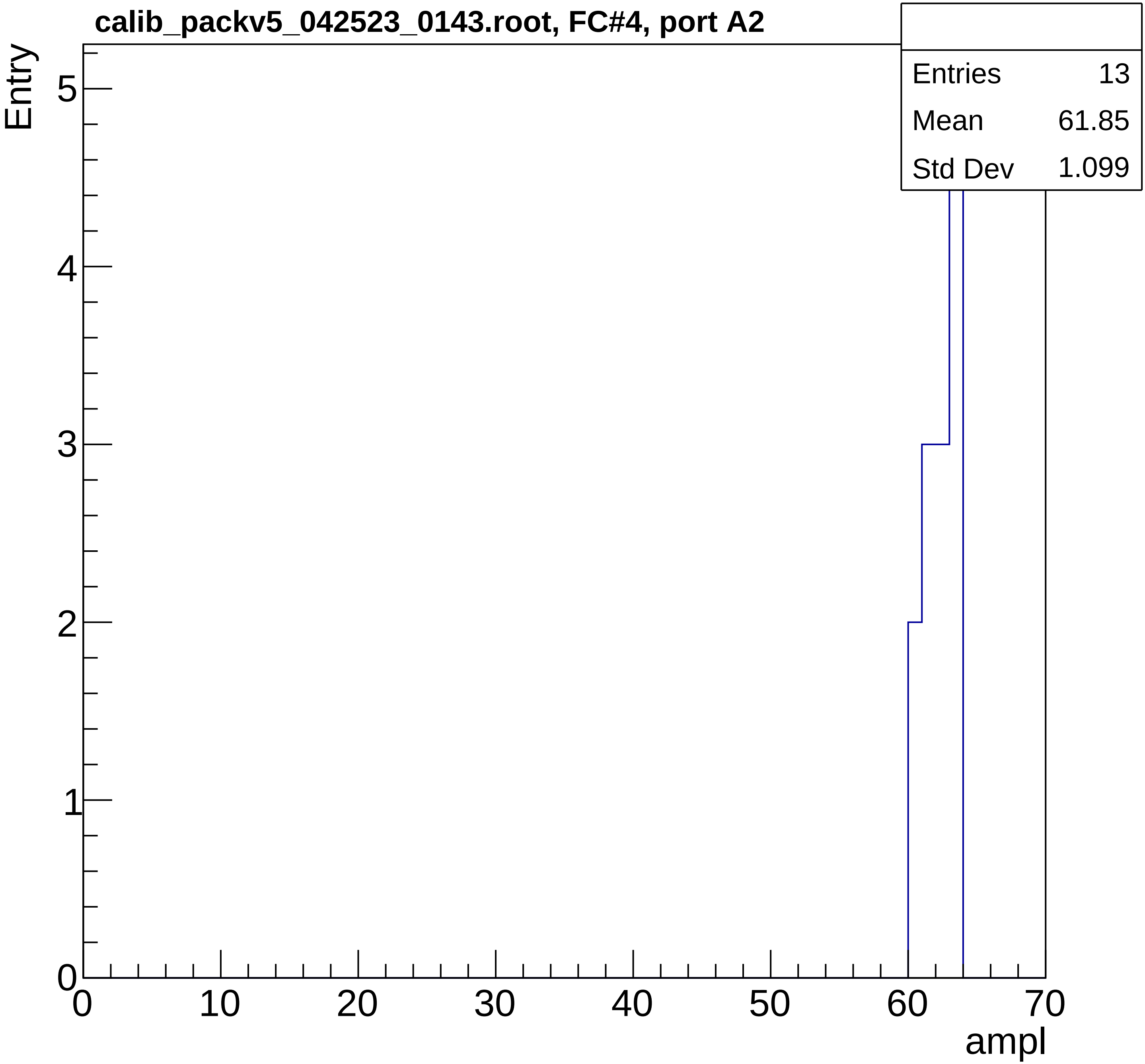
B1L100S, U21-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2



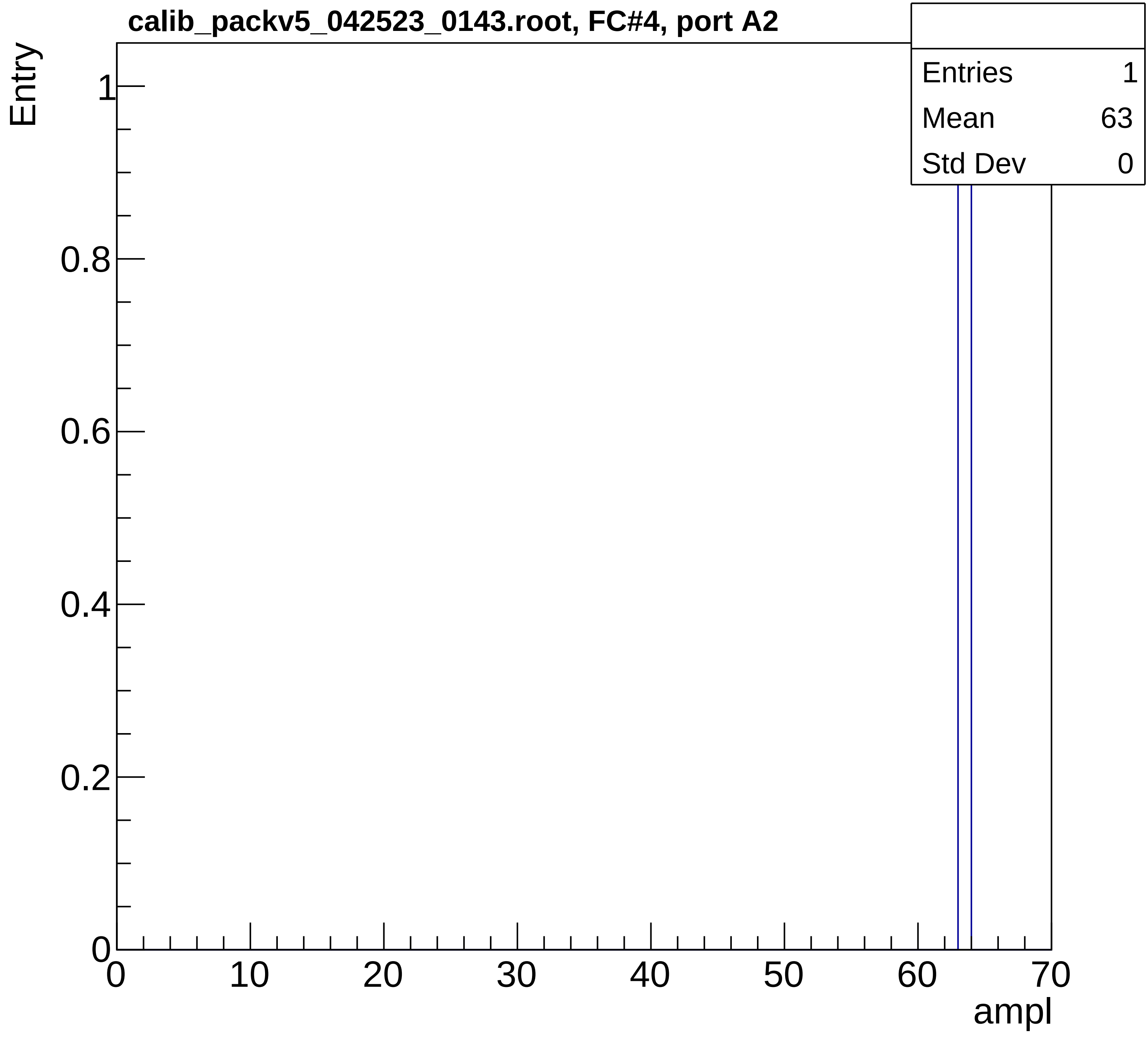
B1L100S, U21-ch112, adc6

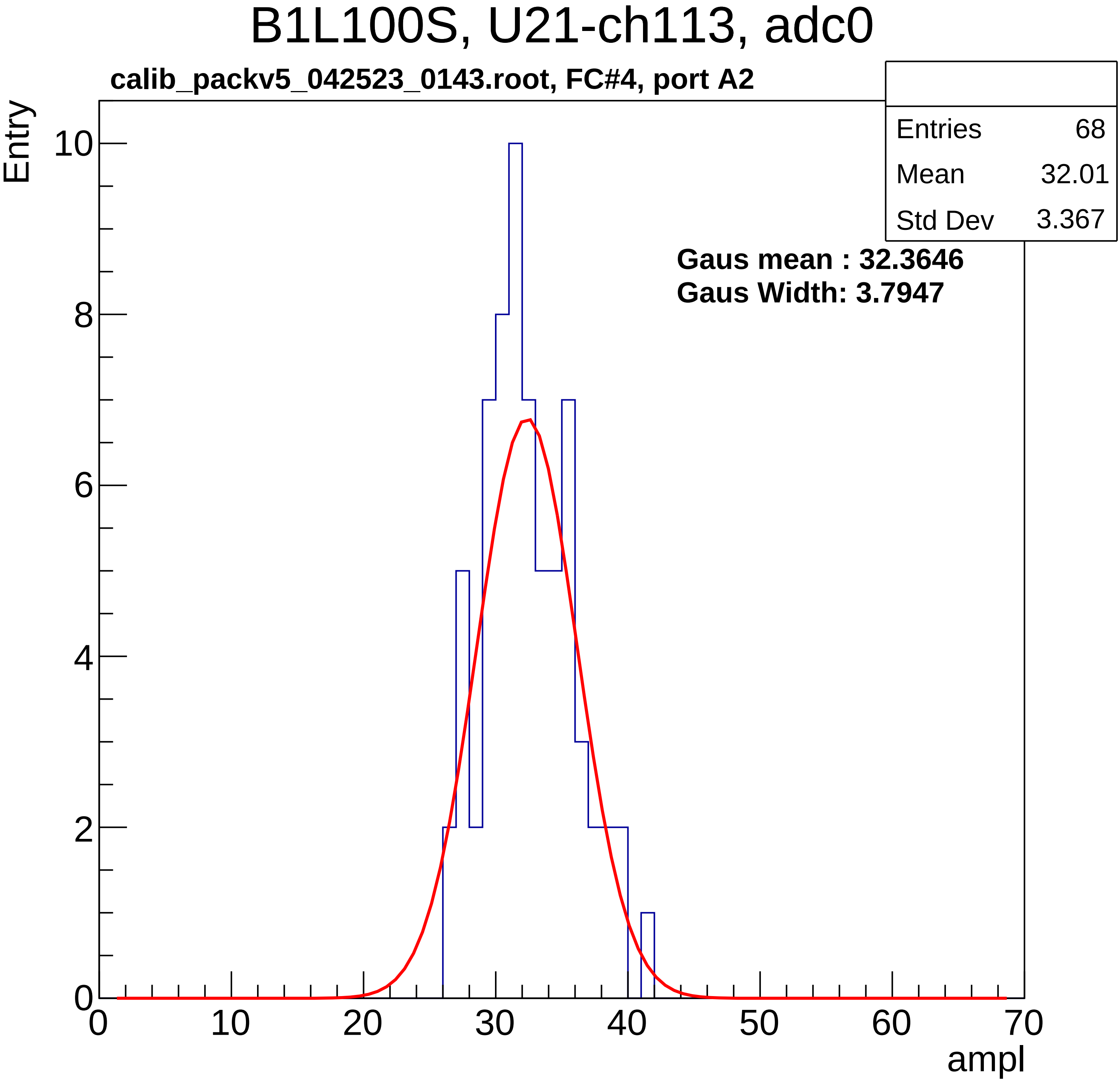
calib_packv5_042523_0143.root, FC#4, port A2

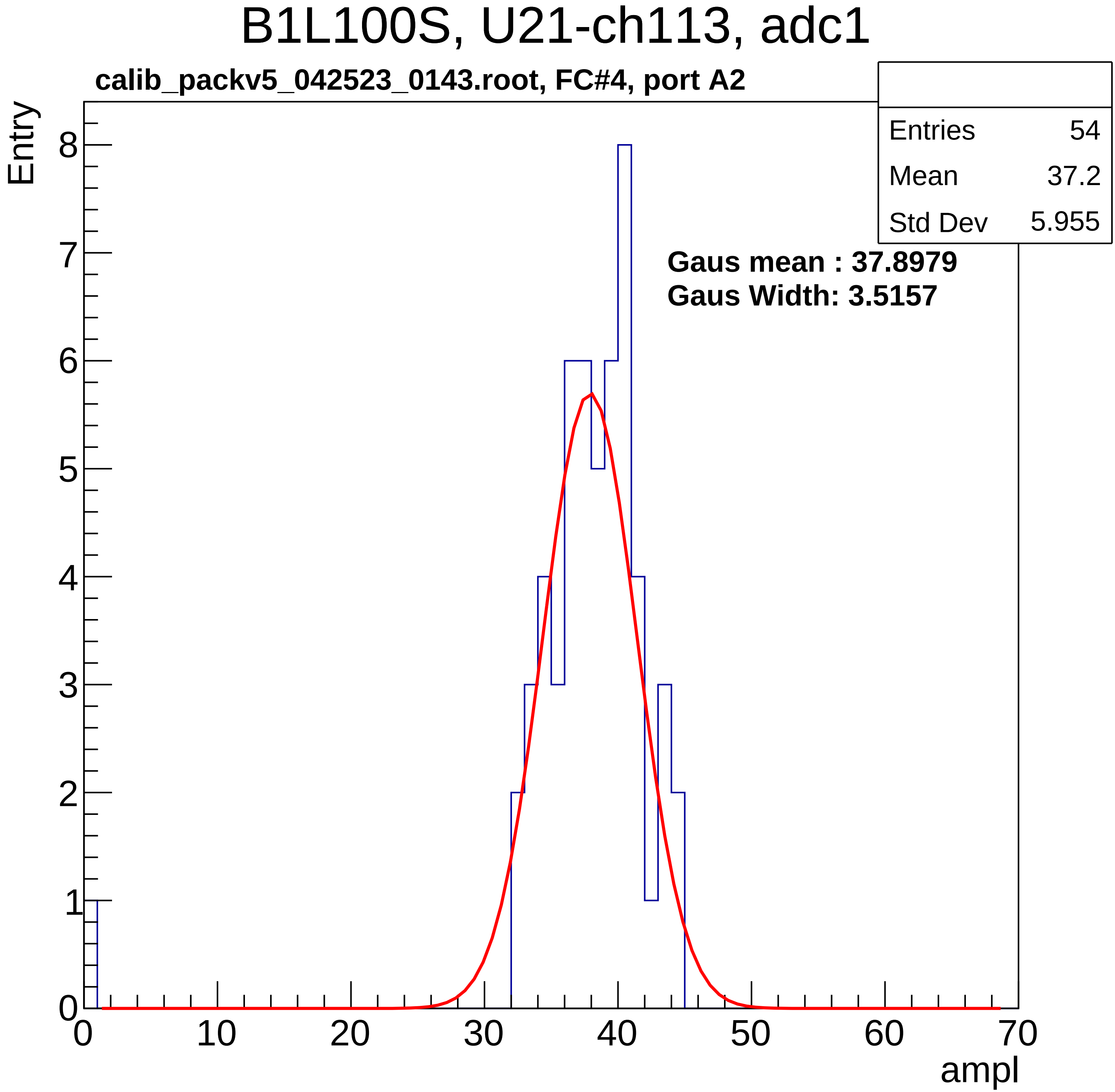


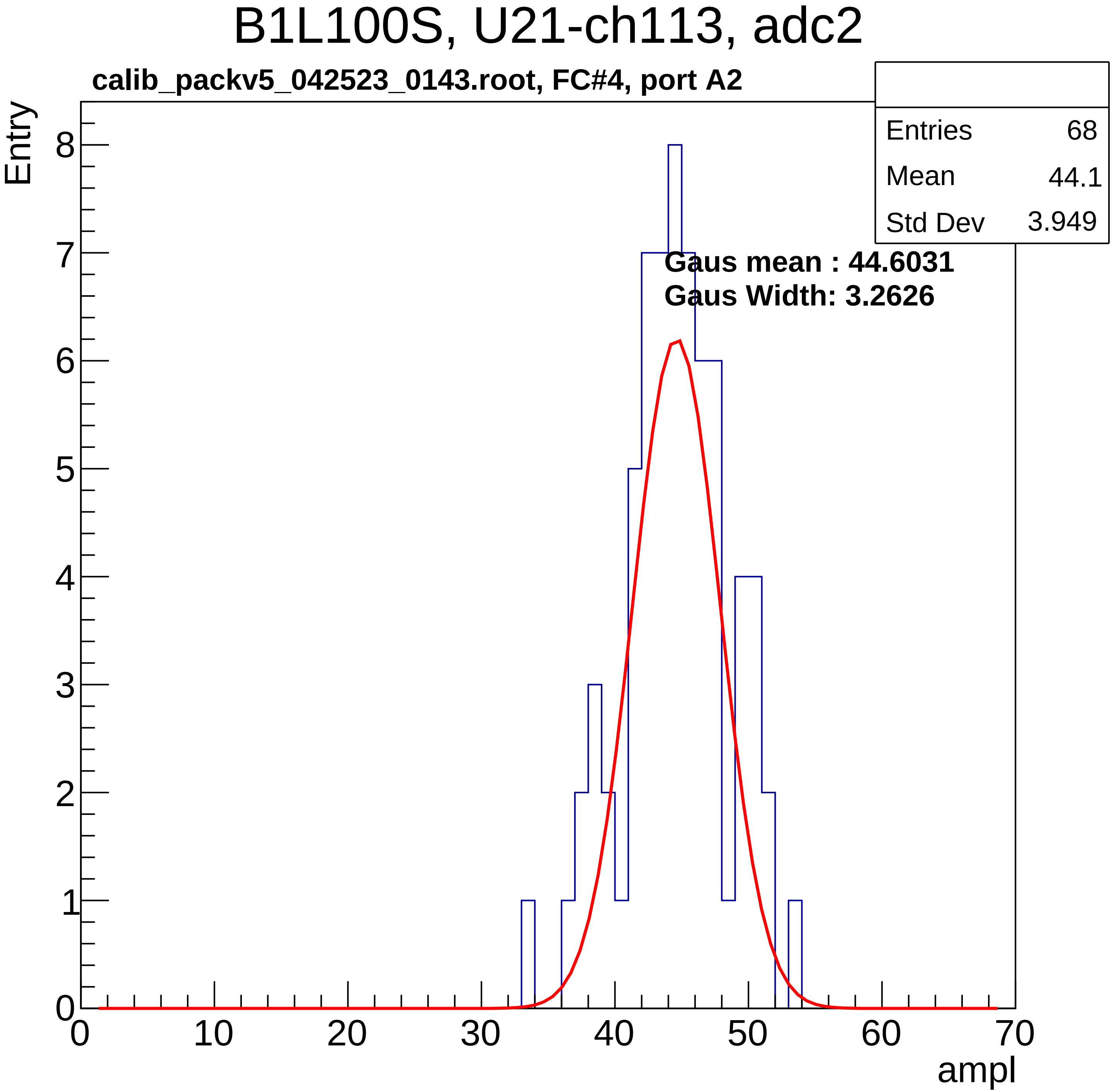
B1L100S, U21-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

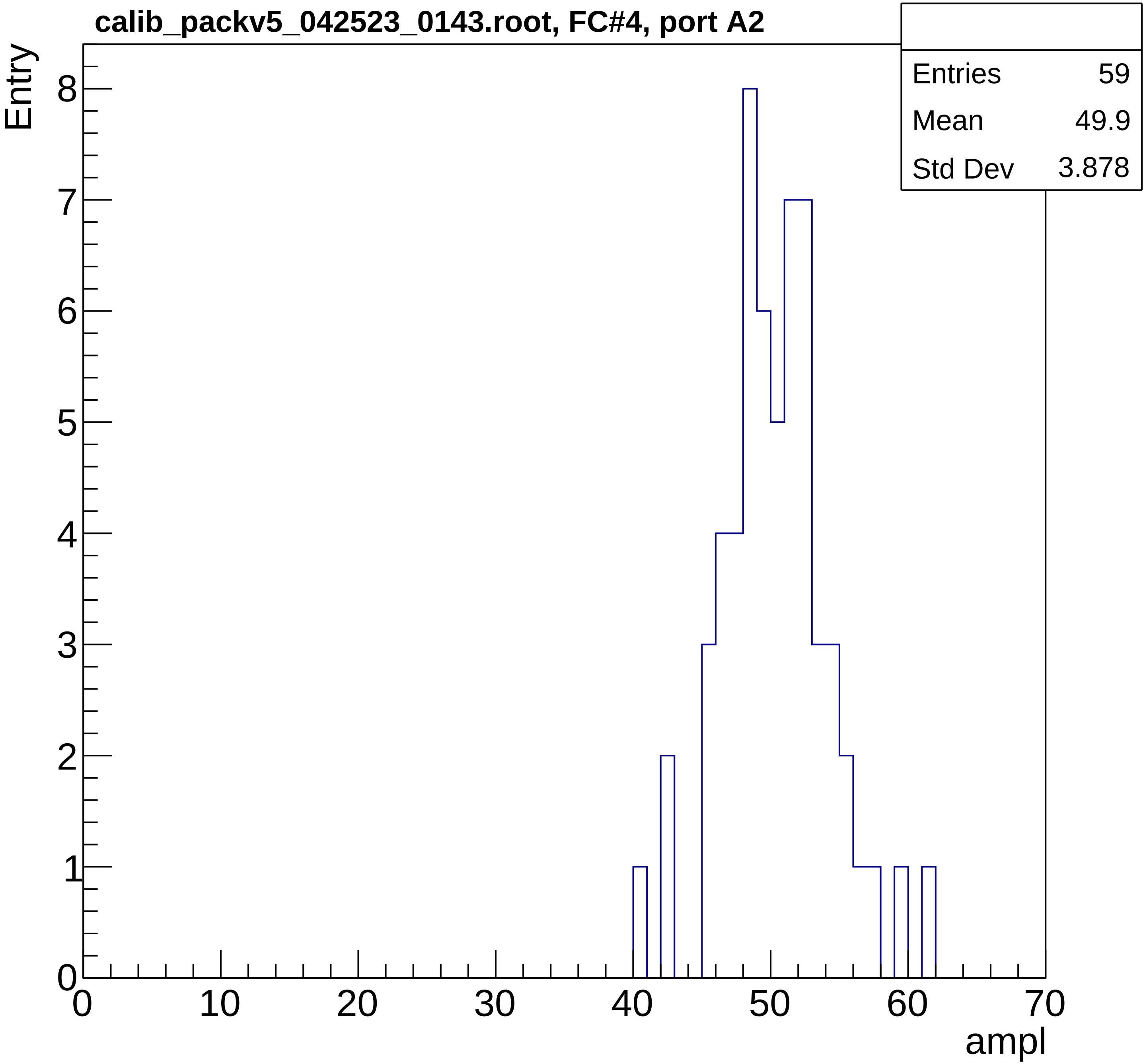


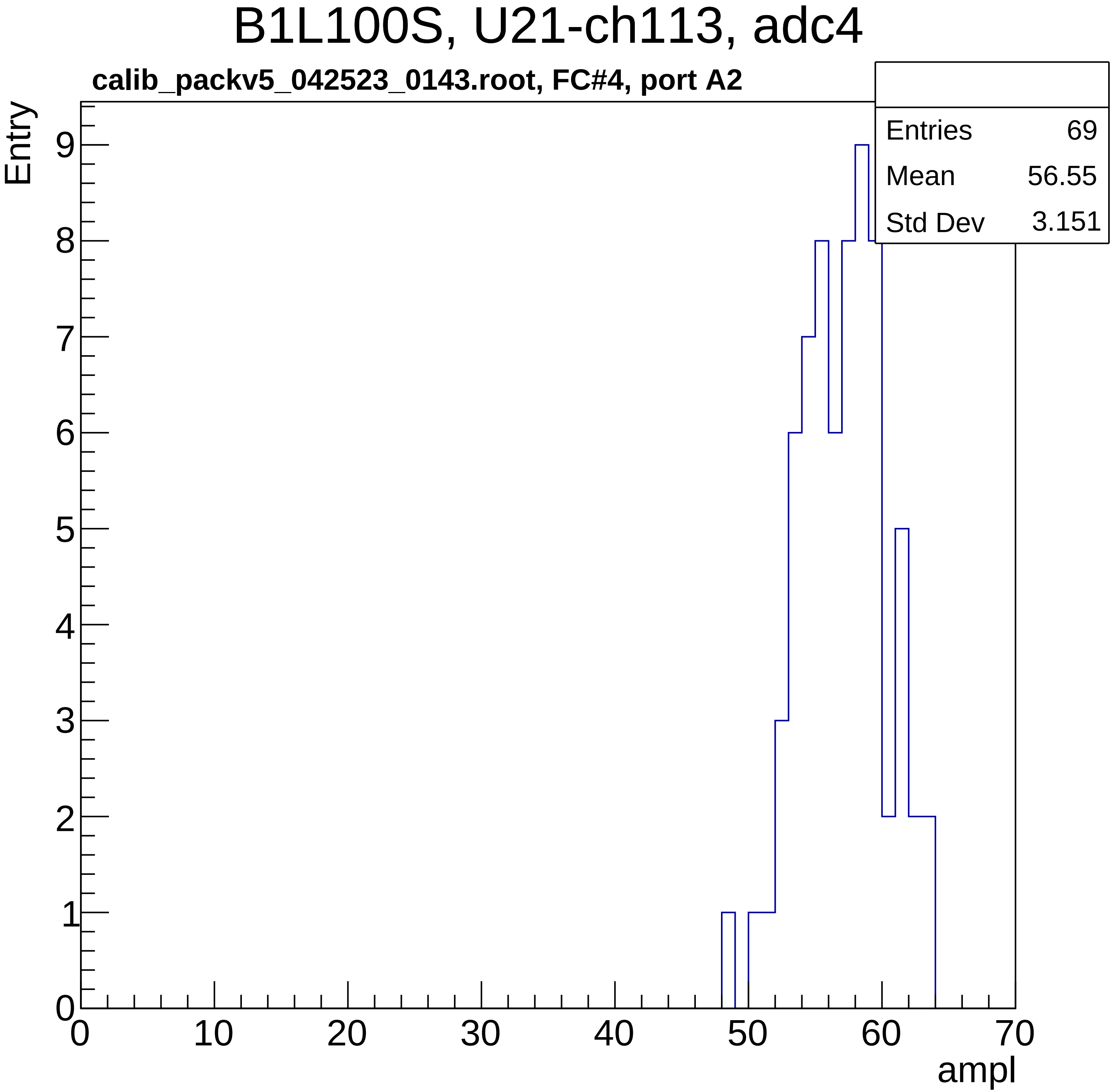

B1L100S, U21-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 32.01
Std Dev 3.367
Gaus mean : 32.3646
Gaus Width: 3.7947
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

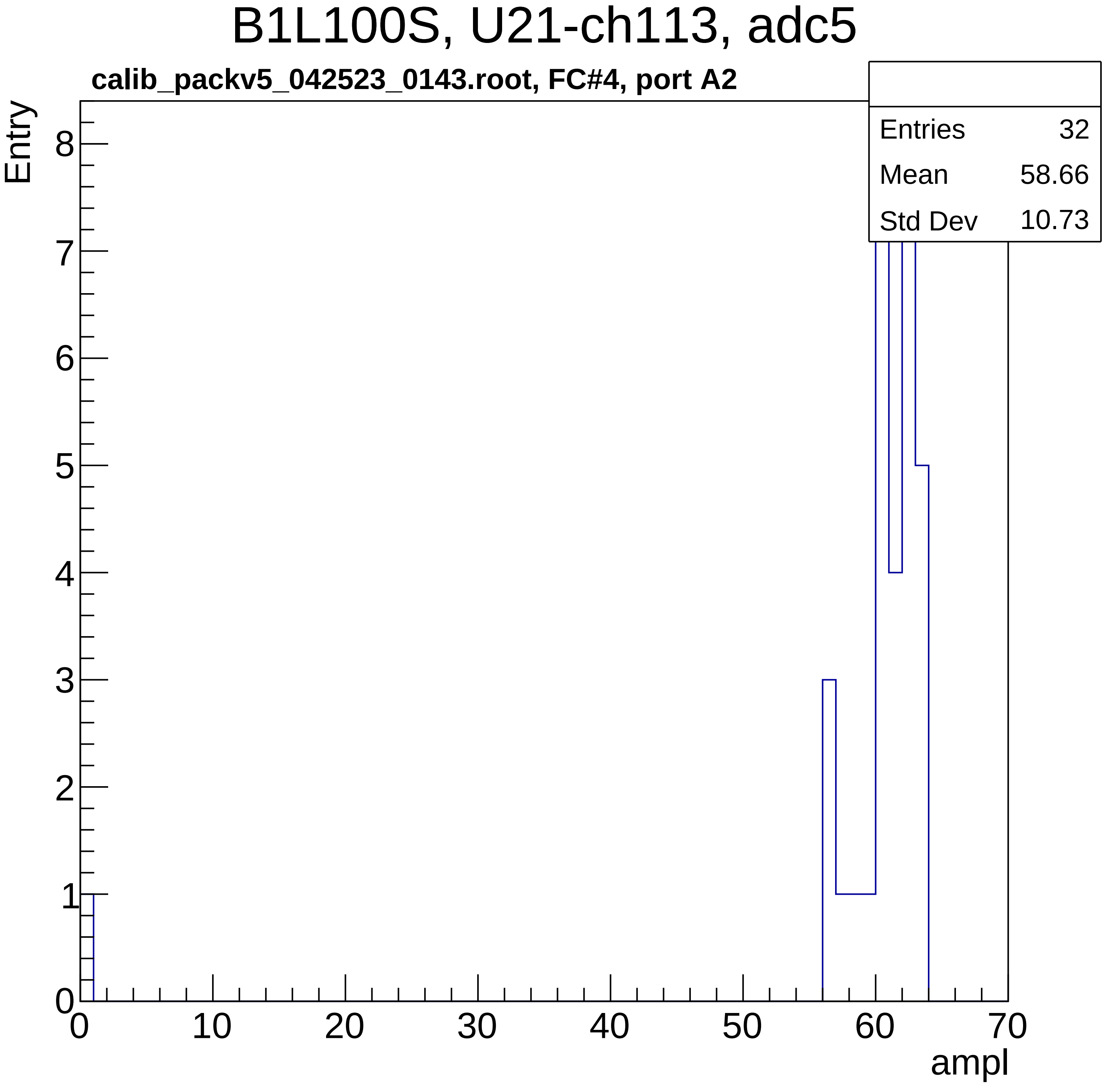

B1L100S, U21-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 37.2
Std Dev 5.955
Gaus mean : 37.8979
Gaus Width: 3.5157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

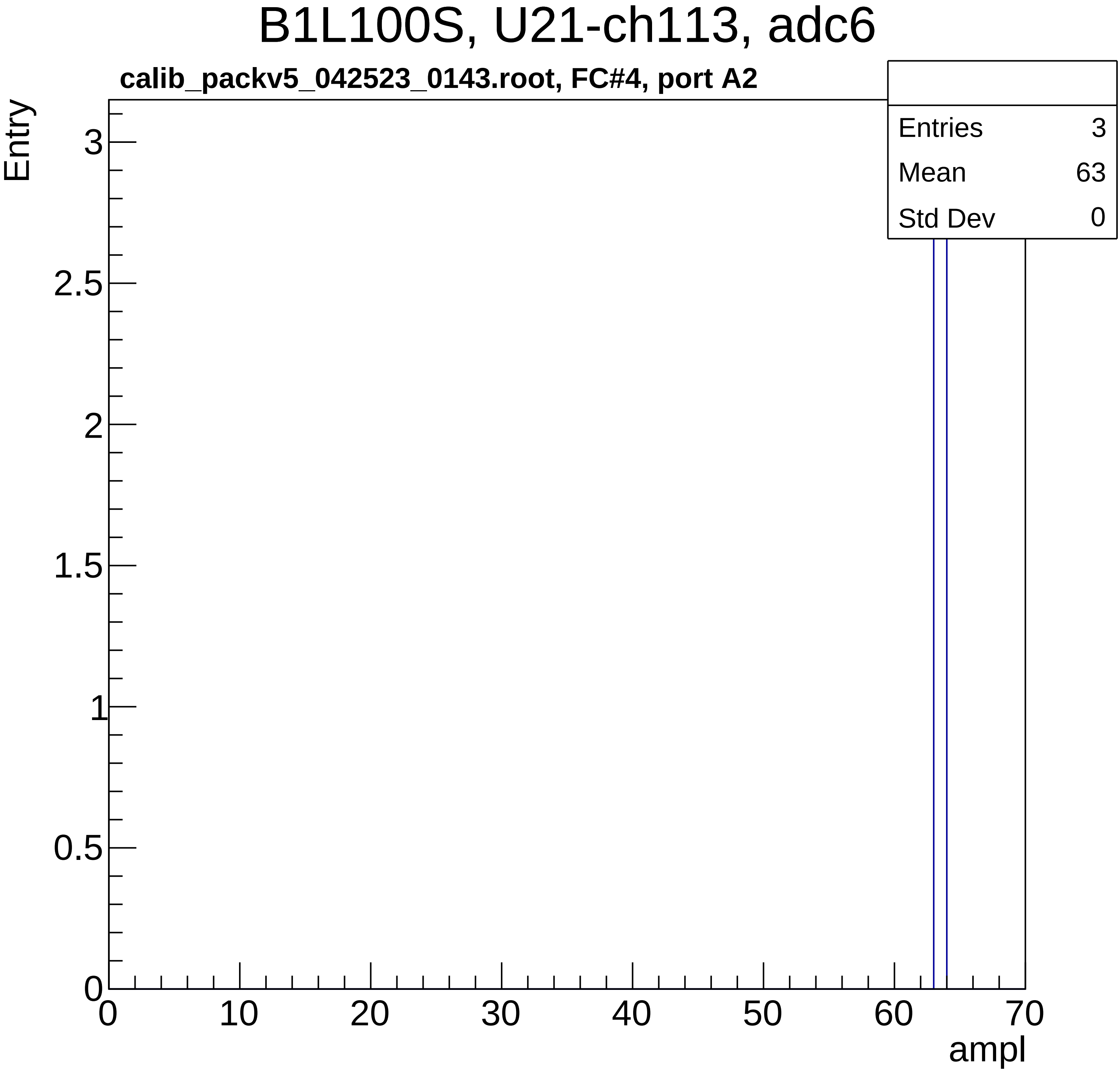

B1L100S, U21-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 68
Mean 44.1
Std Dev 3.949
Gaus mean : 44.6031
Gaus Width: 3.2626
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

B1L100S, U21-ch113, adc3



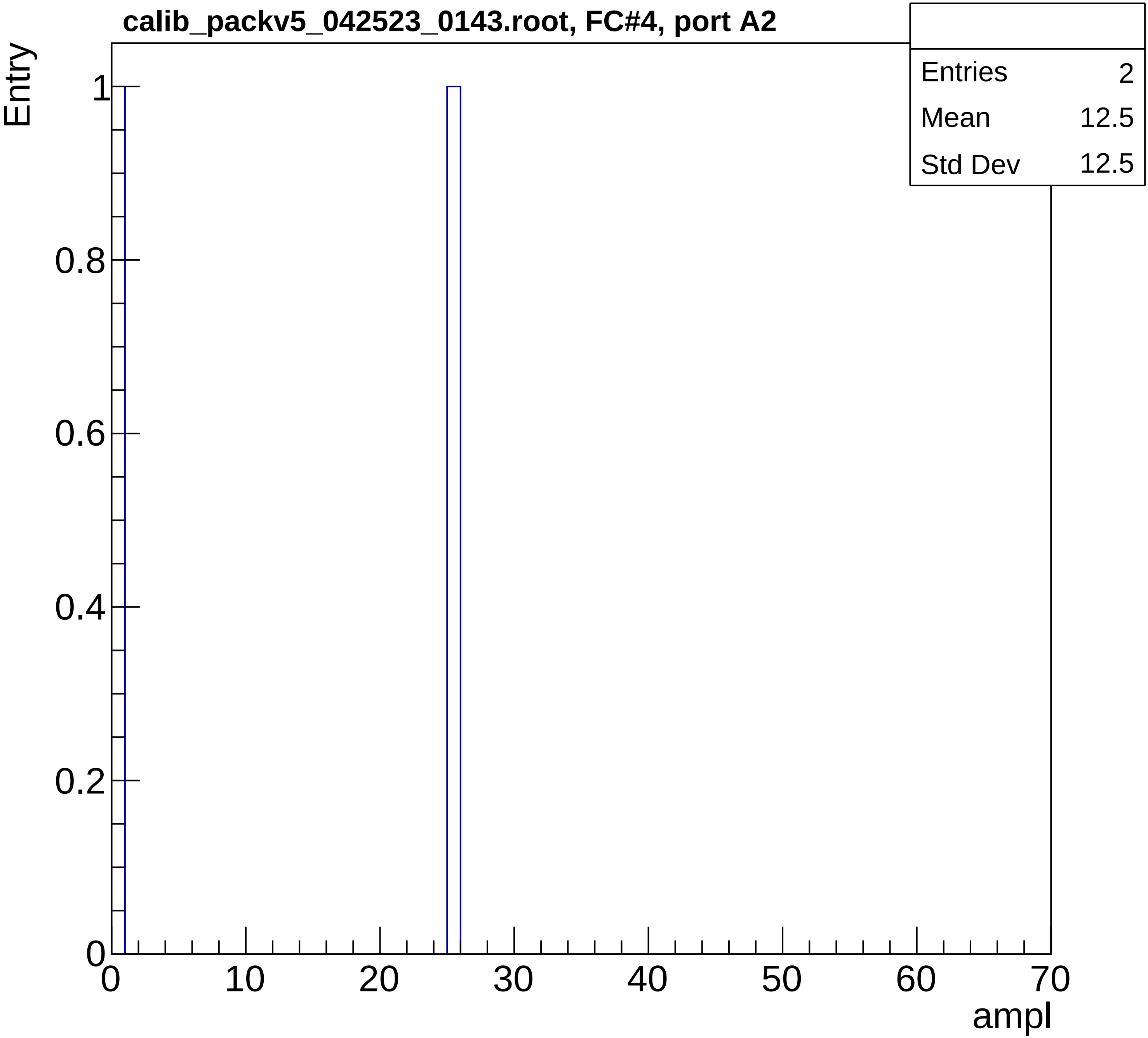

B1L100S, U21-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.55
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.66
Std Dev 10.73
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

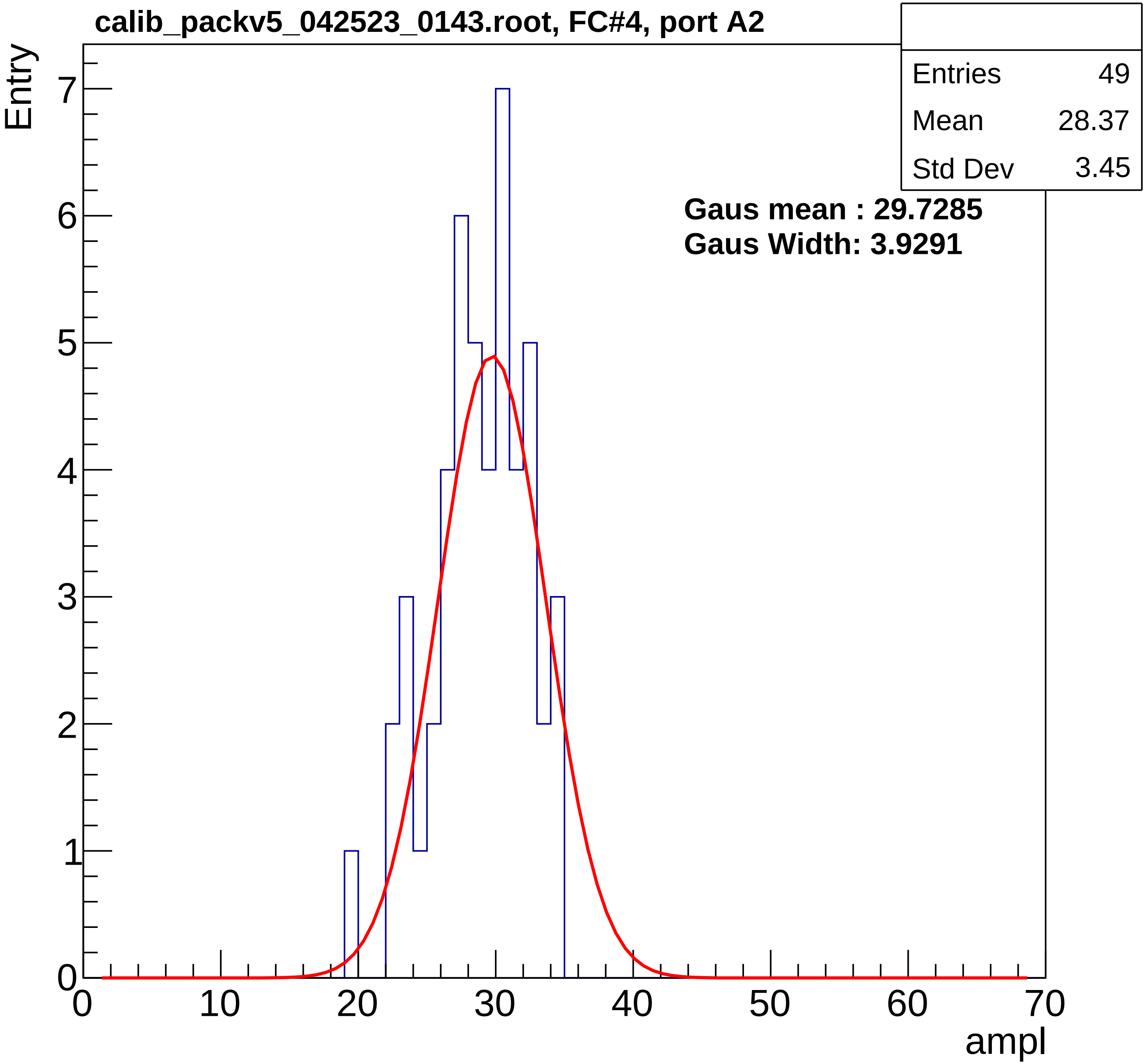

B1L100S, U21-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 3
Mean 63
Std Dev 0
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

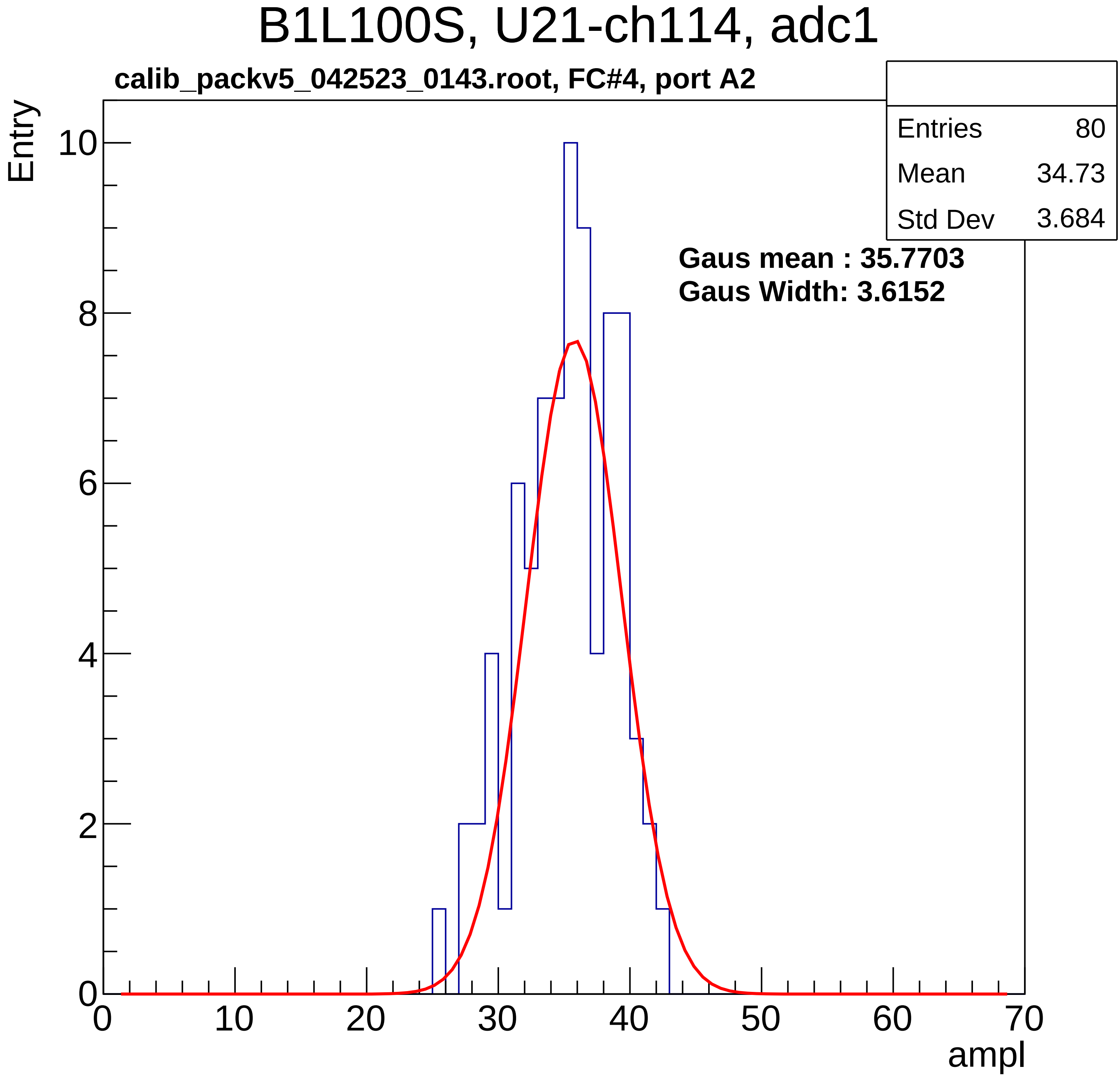
B1L100S, U21-ch113, adc7

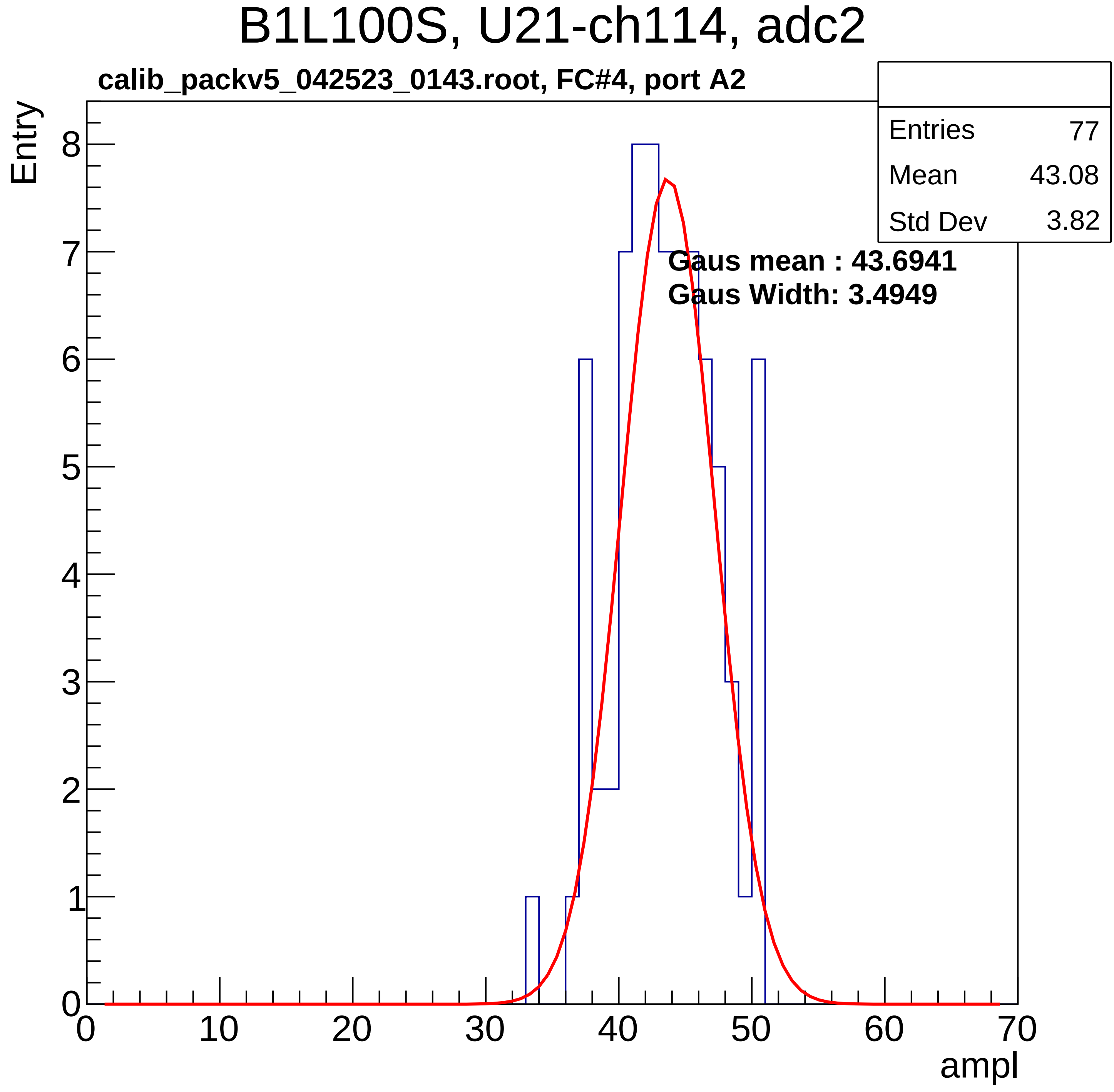
calib_packv5_042523_0143.root, FC#4, port A2

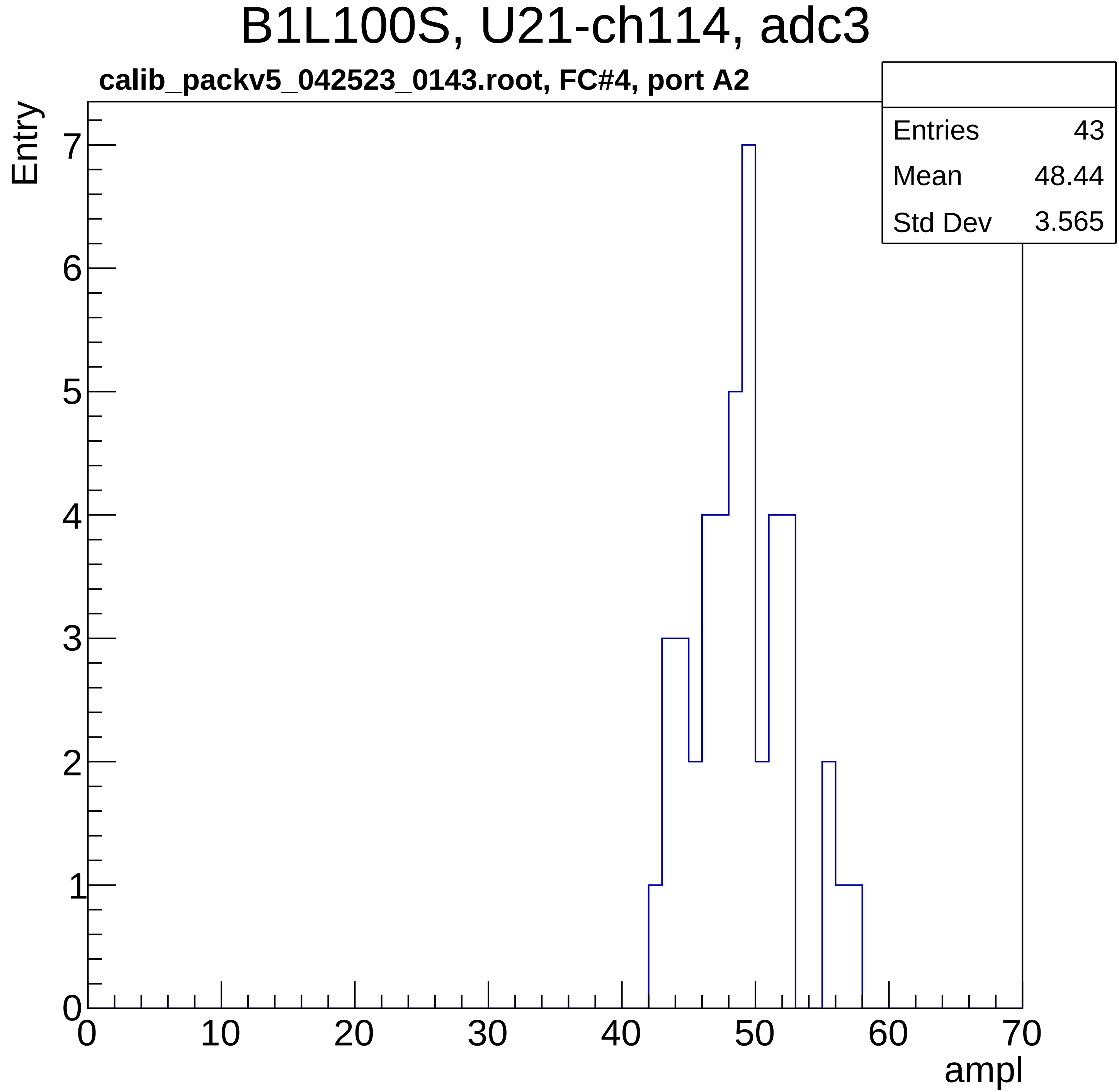


B1L100S, U21-ch114, adc0

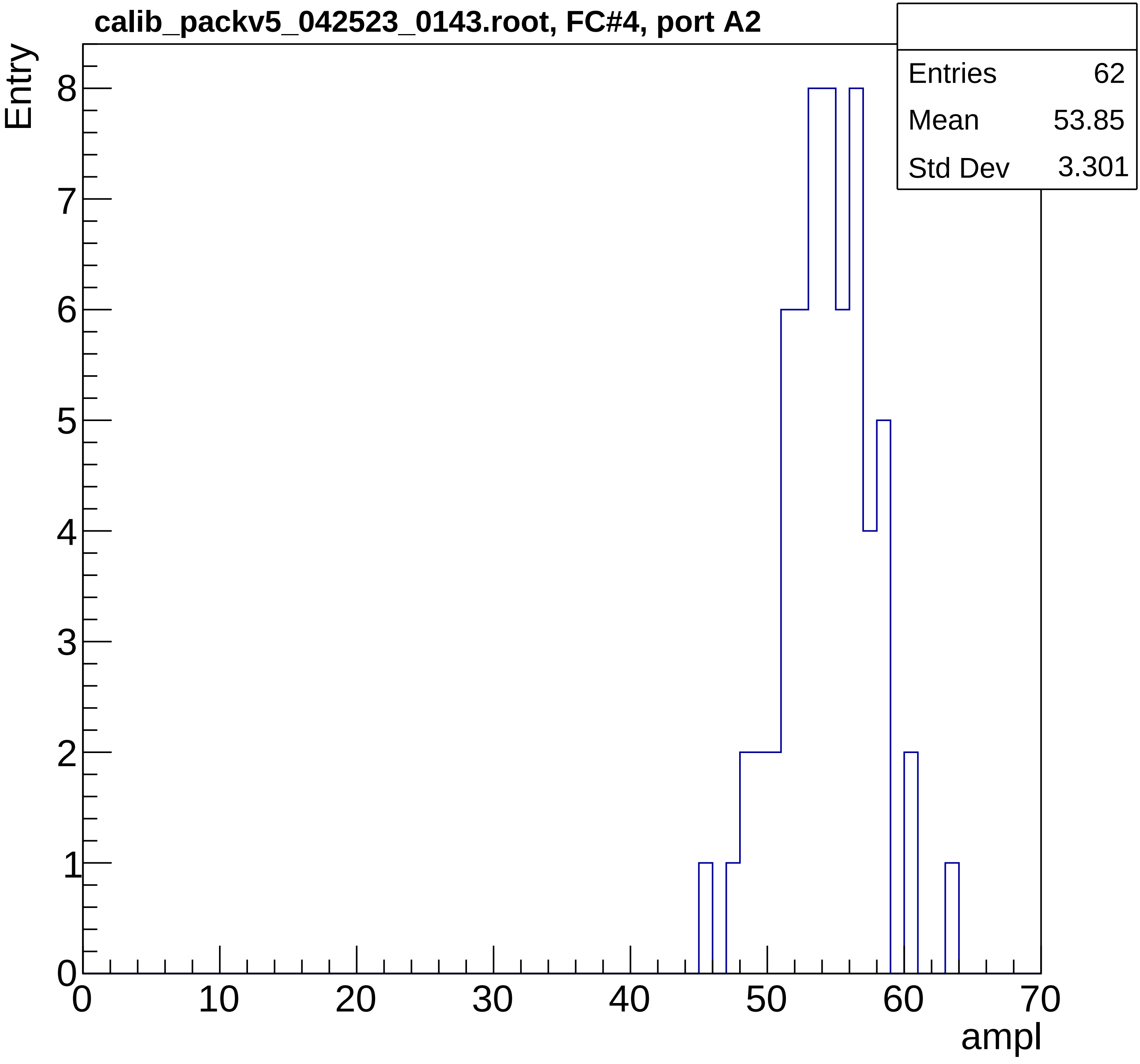



B1L100S, U21-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 34.73
Std Dev 3.684
Gaus mean : 35.7703
Gaus Width: 3.6152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 77
Mean 43.08
Std Dev 3.82
Gaus mean : 43.6941
Gaus Width: 3.4949
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

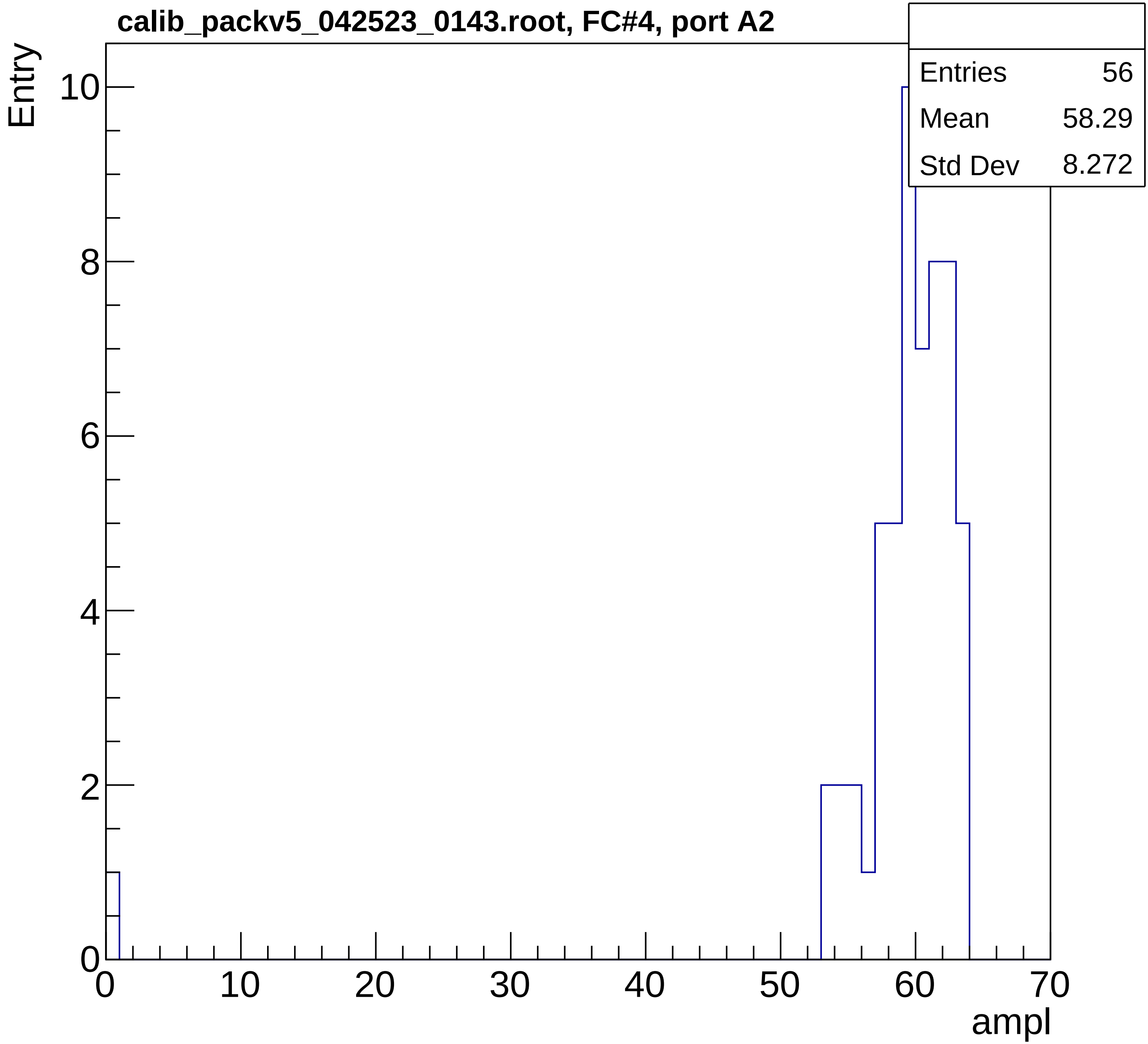

B1L100S, U21-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 48.44
Std Dev 3.565
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

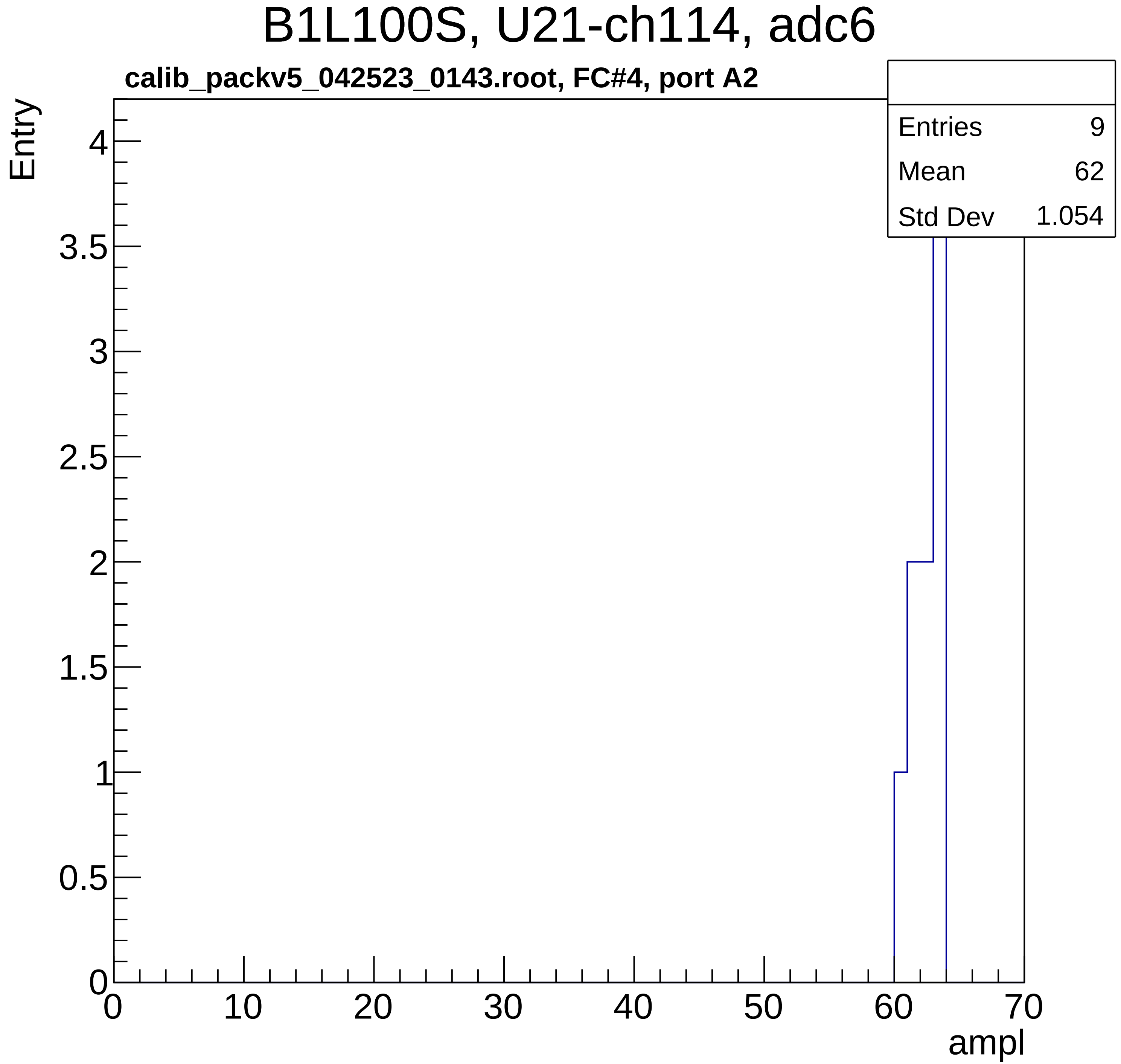
B1L100S, U21-ch114, adc4

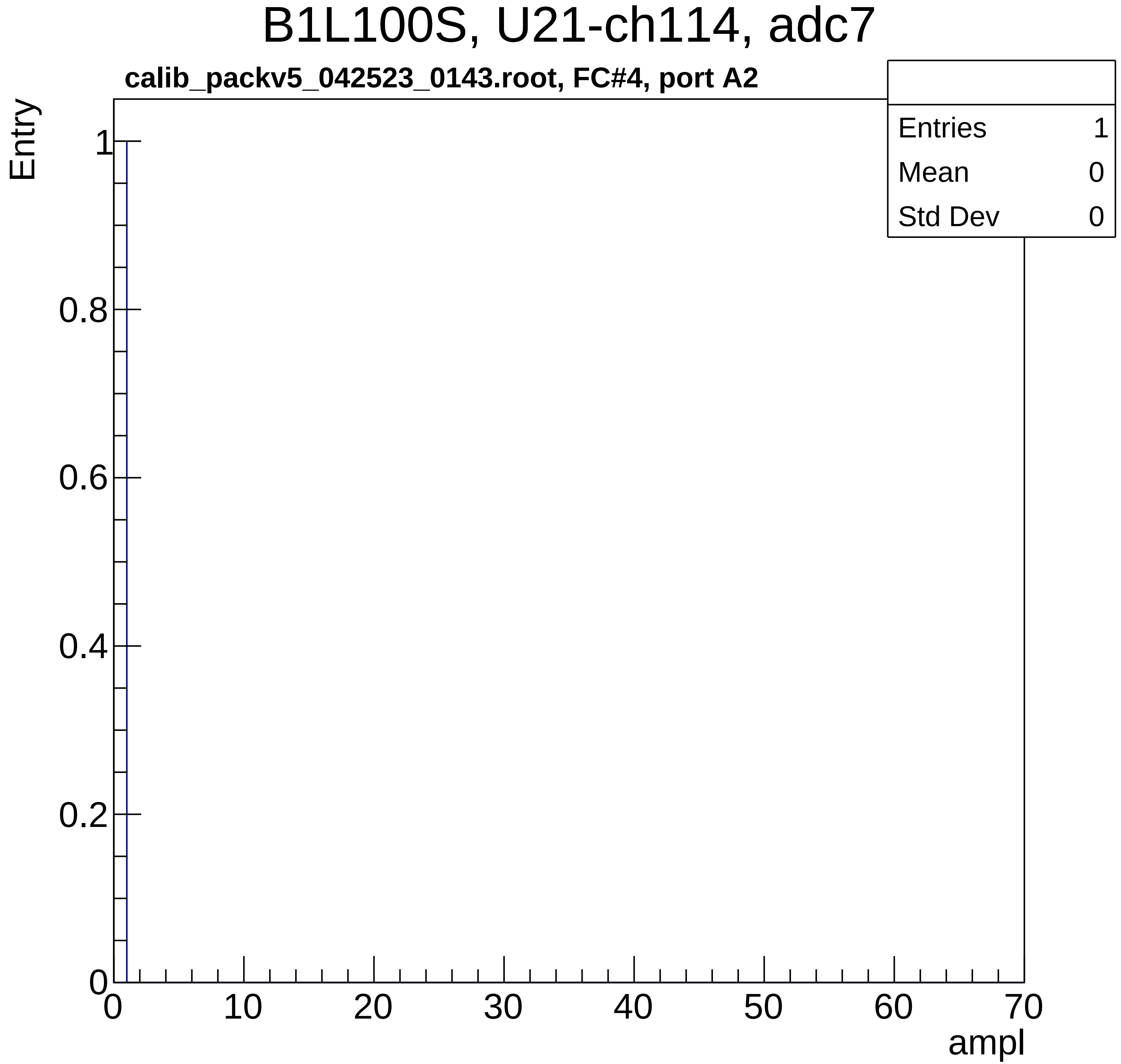


B1L100S, U21-ch114, adc5

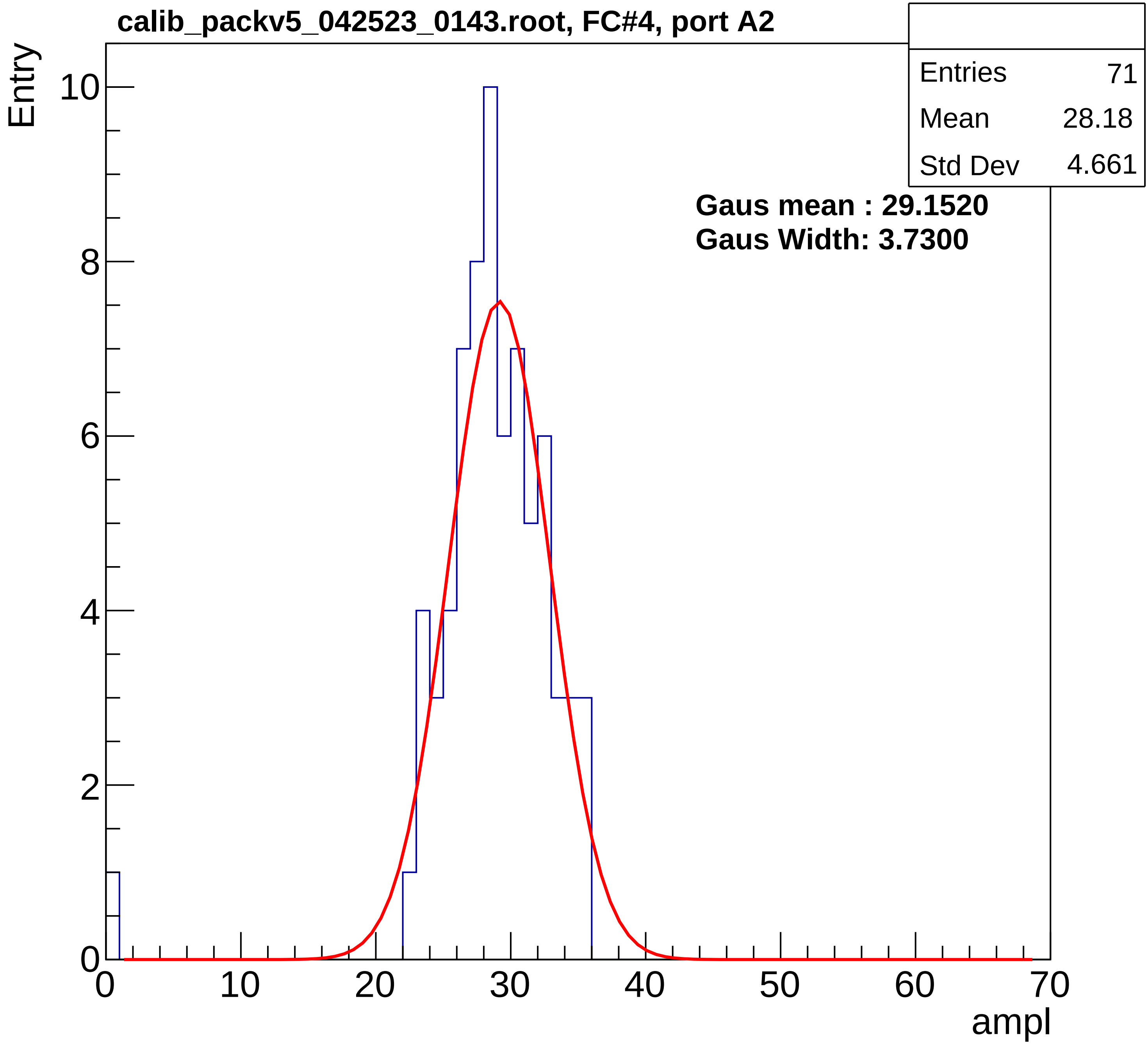
calib_packv5_042523_0143.root, FC#4, port A2

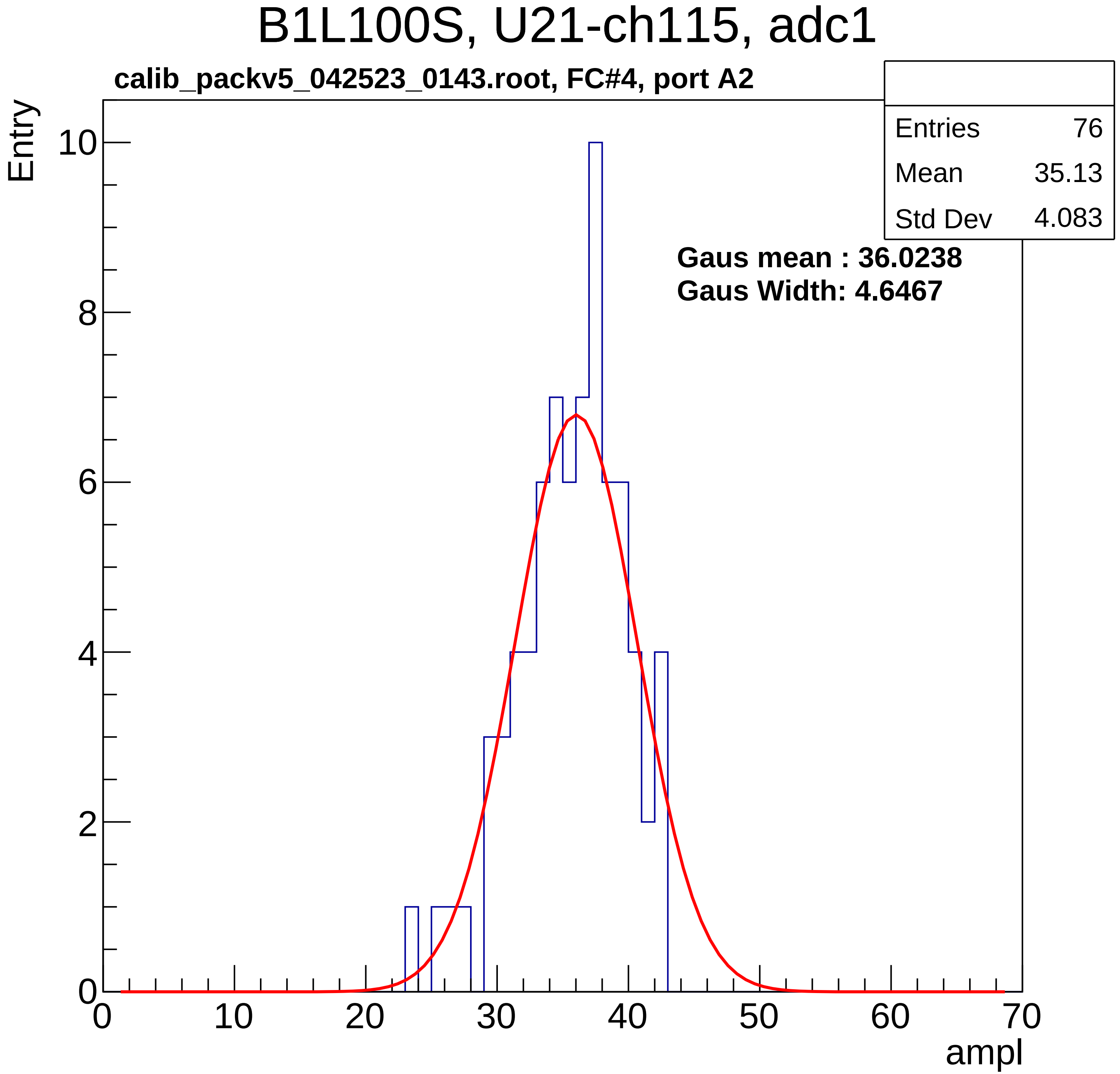


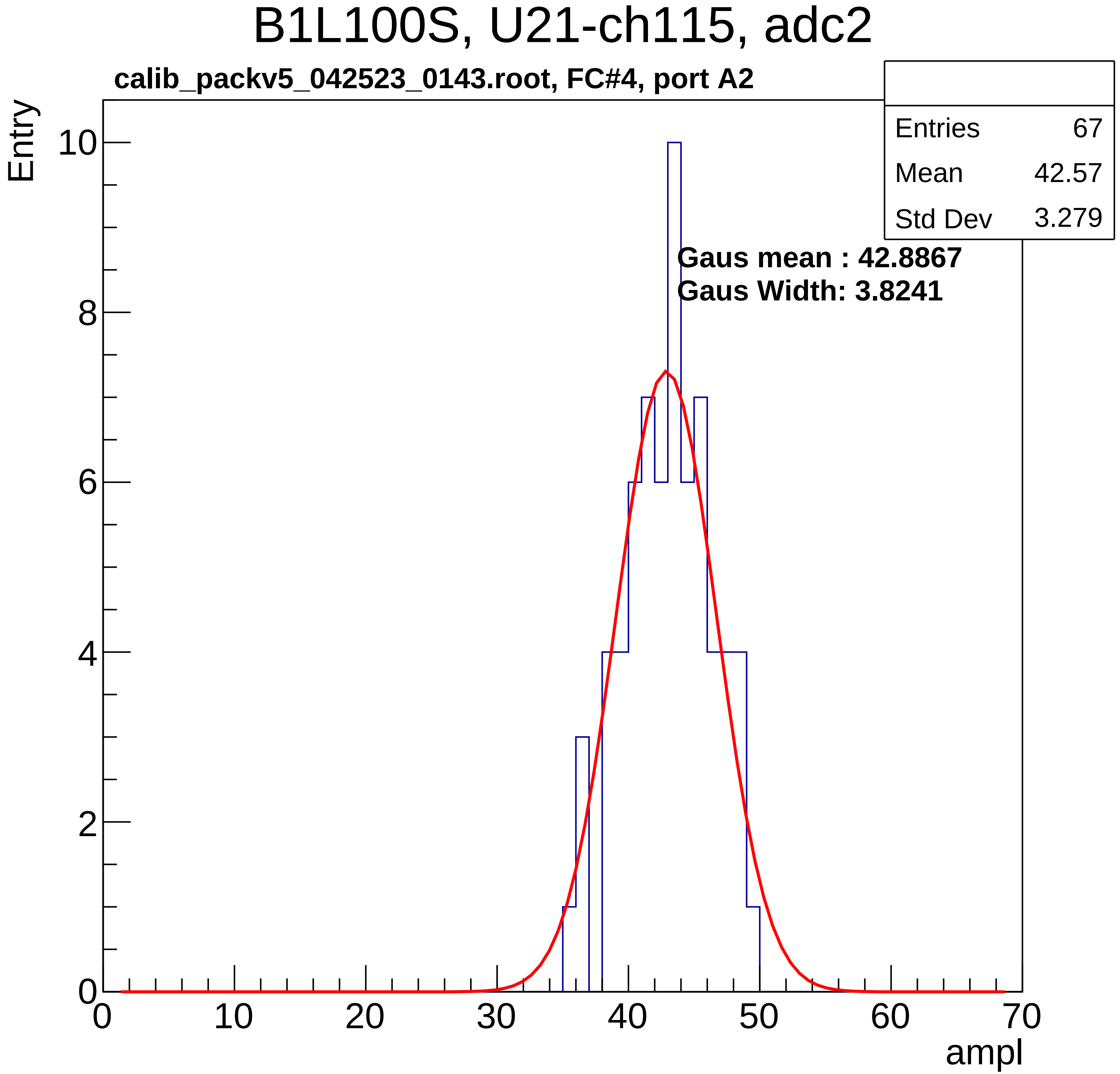

B1L100S, U21-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 9
Mean 62
Std Dev 1.054
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

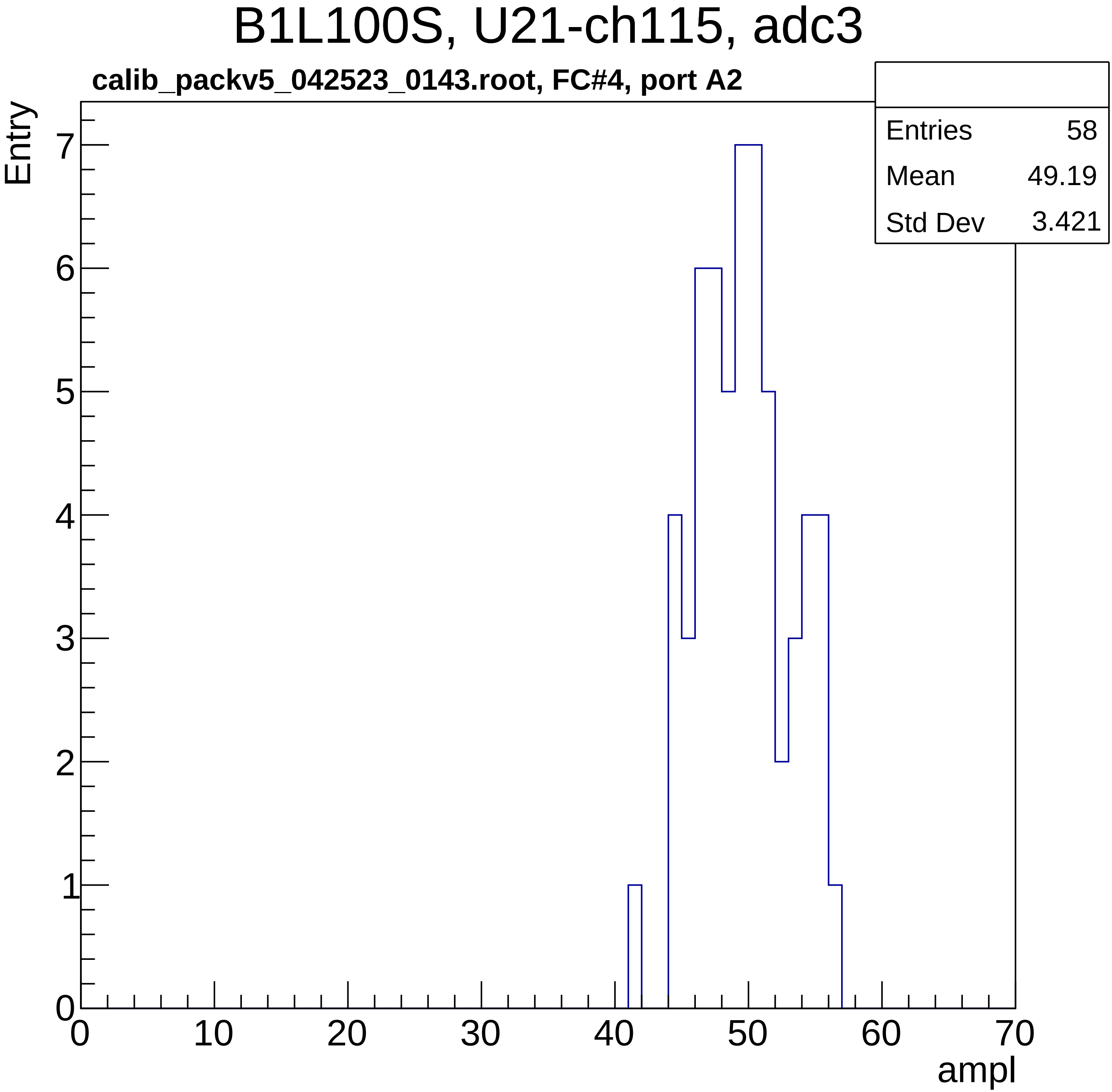

B1L100S, U21-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch115, adc0

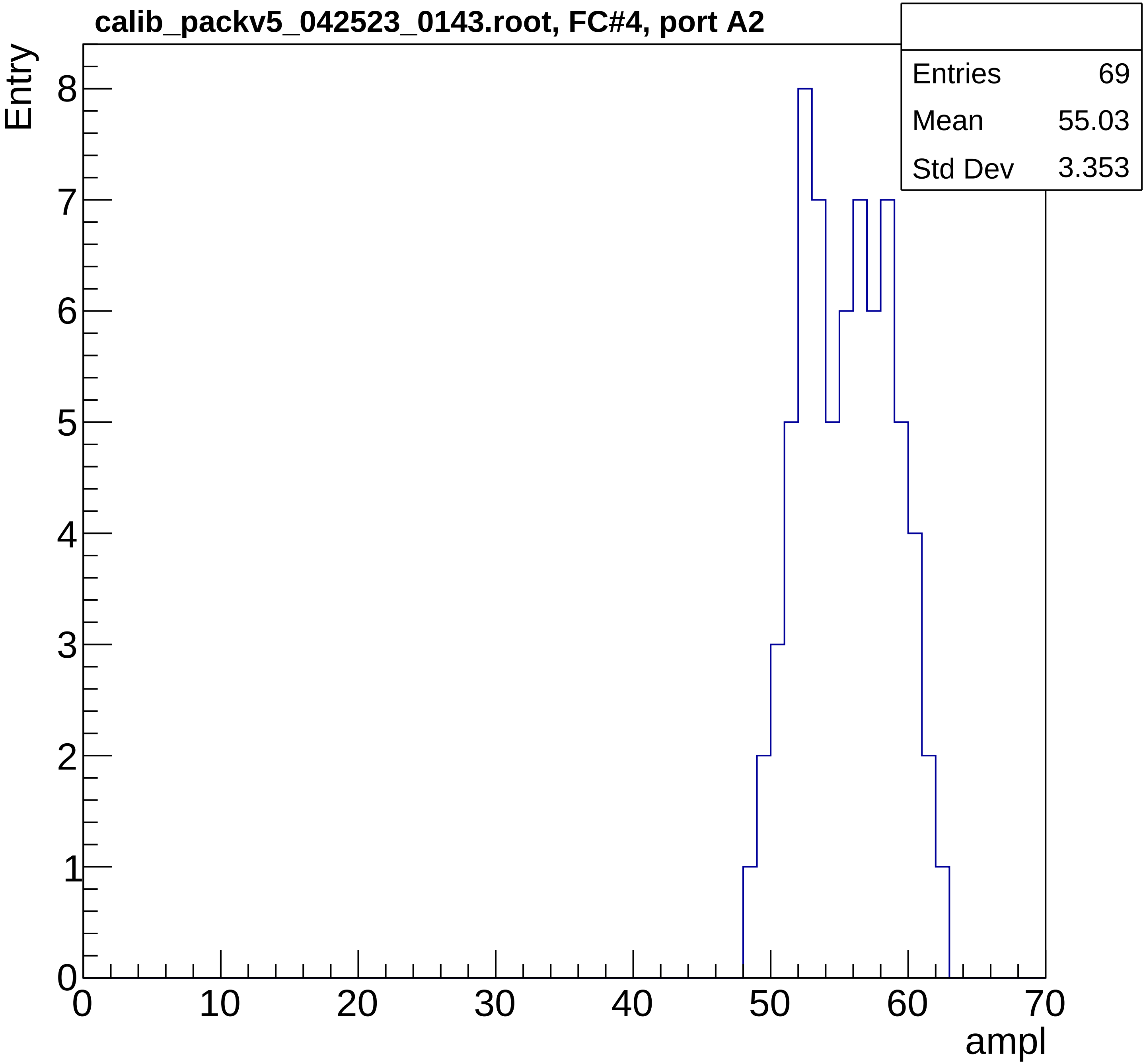


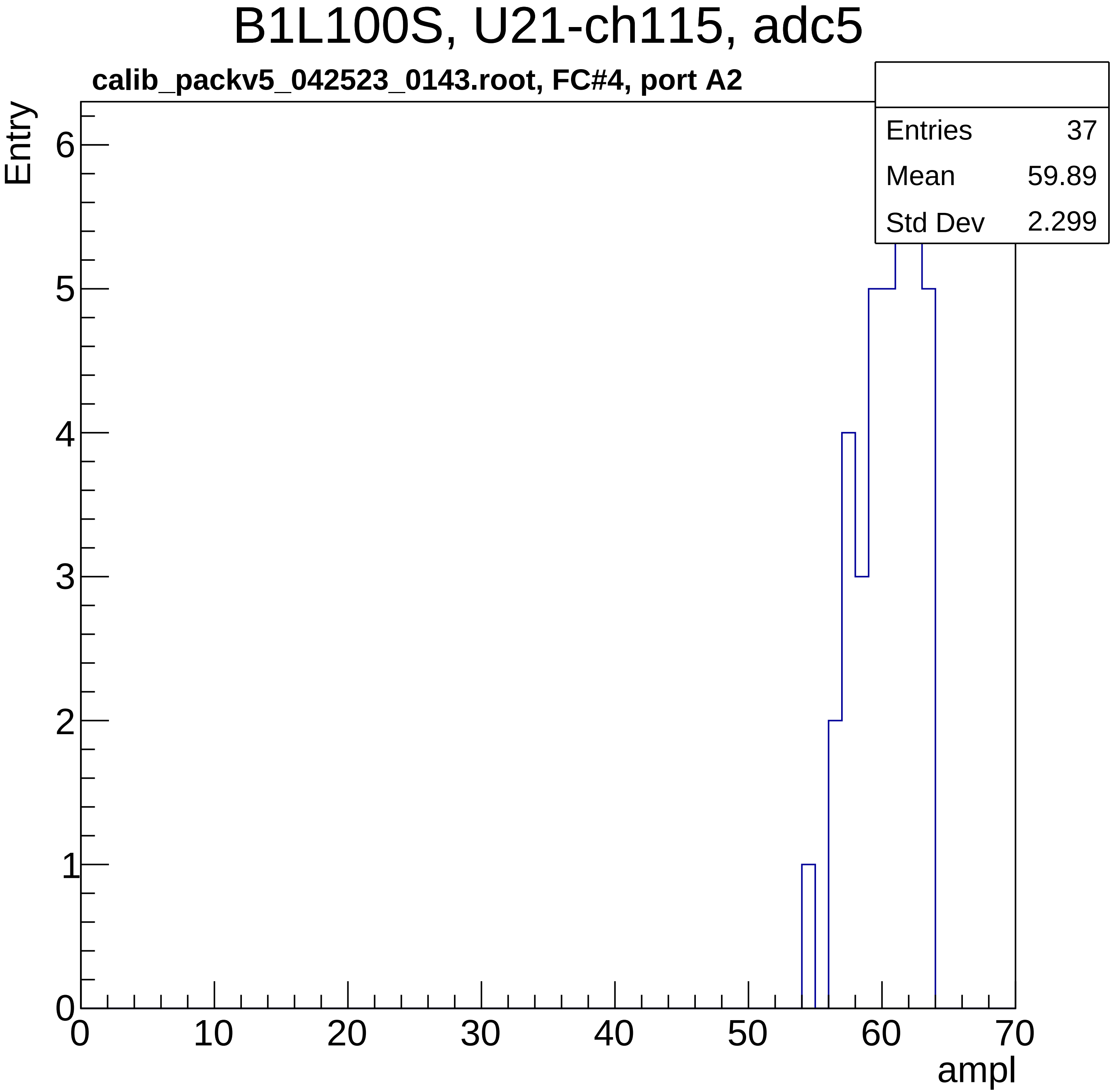

B1L100S, U21-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.13
Std Dev 4.083
Gaus mean : 36.0238
Gaus Width: 4.6467
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

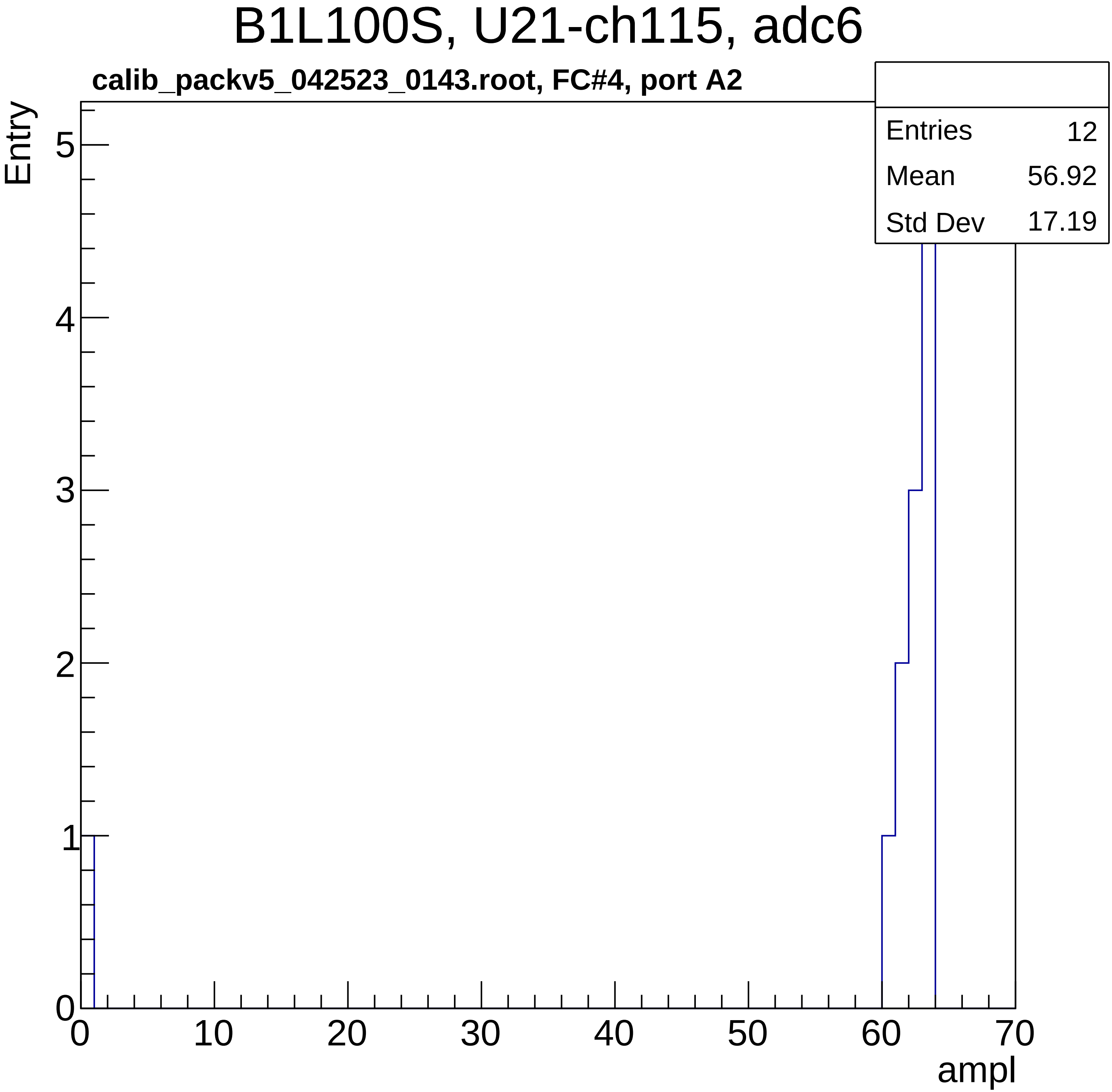

B1L100S, U21-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 42.57
Std Dev 3.279
Gaus mean : 42.8867
Gaus Width: 3.8241
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

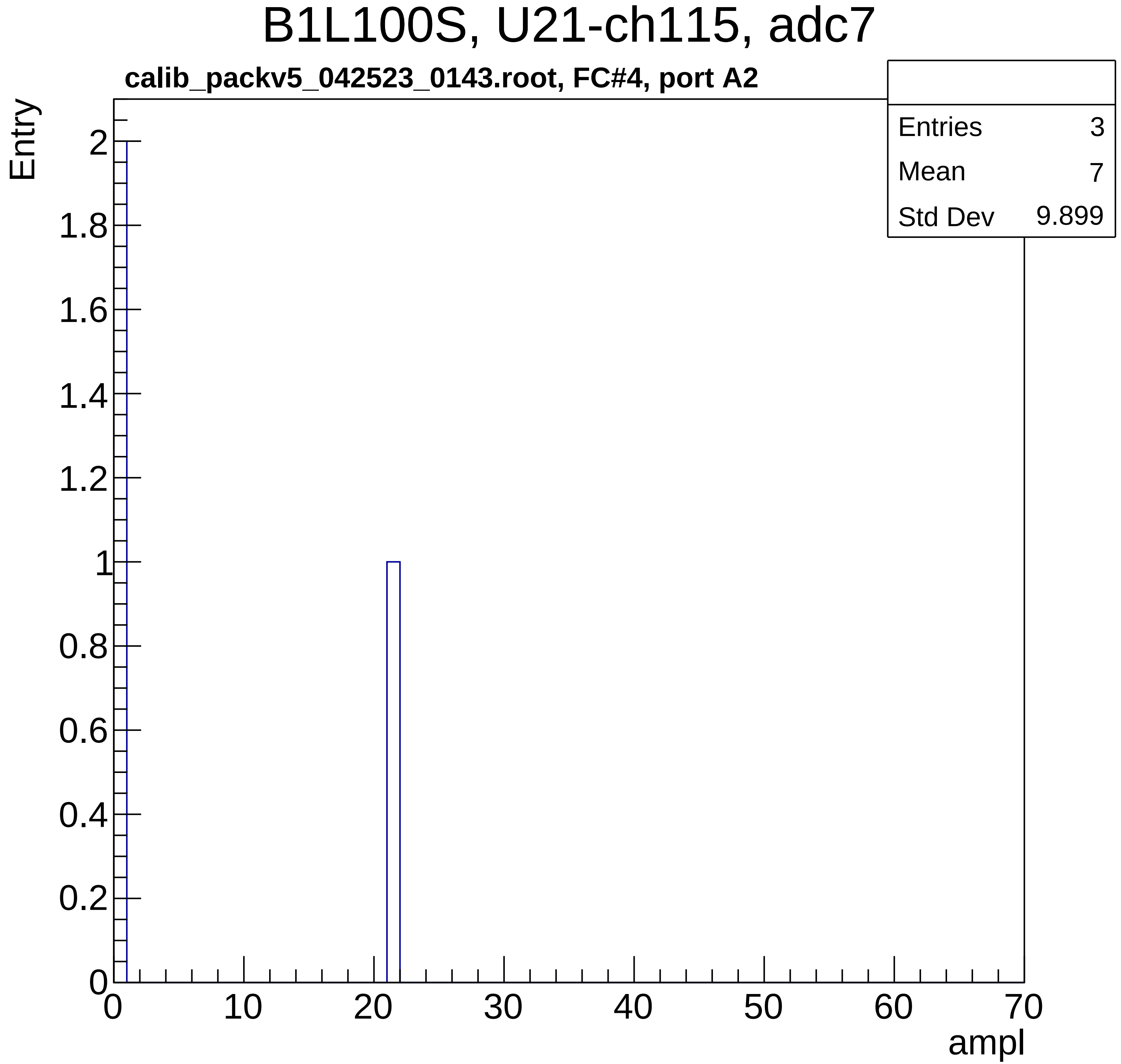

B1L100S, U21-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 58
Mean 49.19
Std Dev 3.421
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

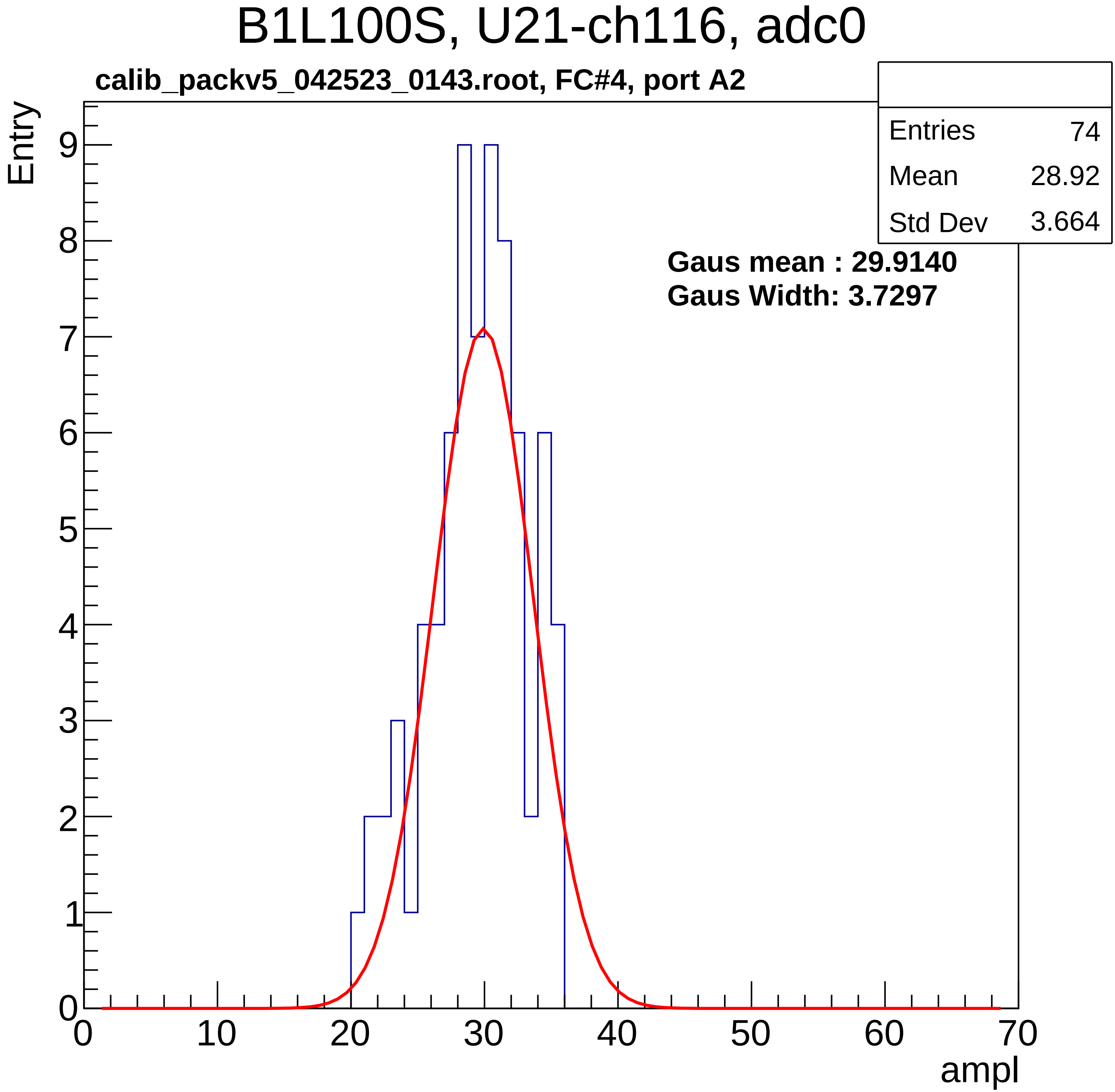
B1L100S, U21-ch115, adc4

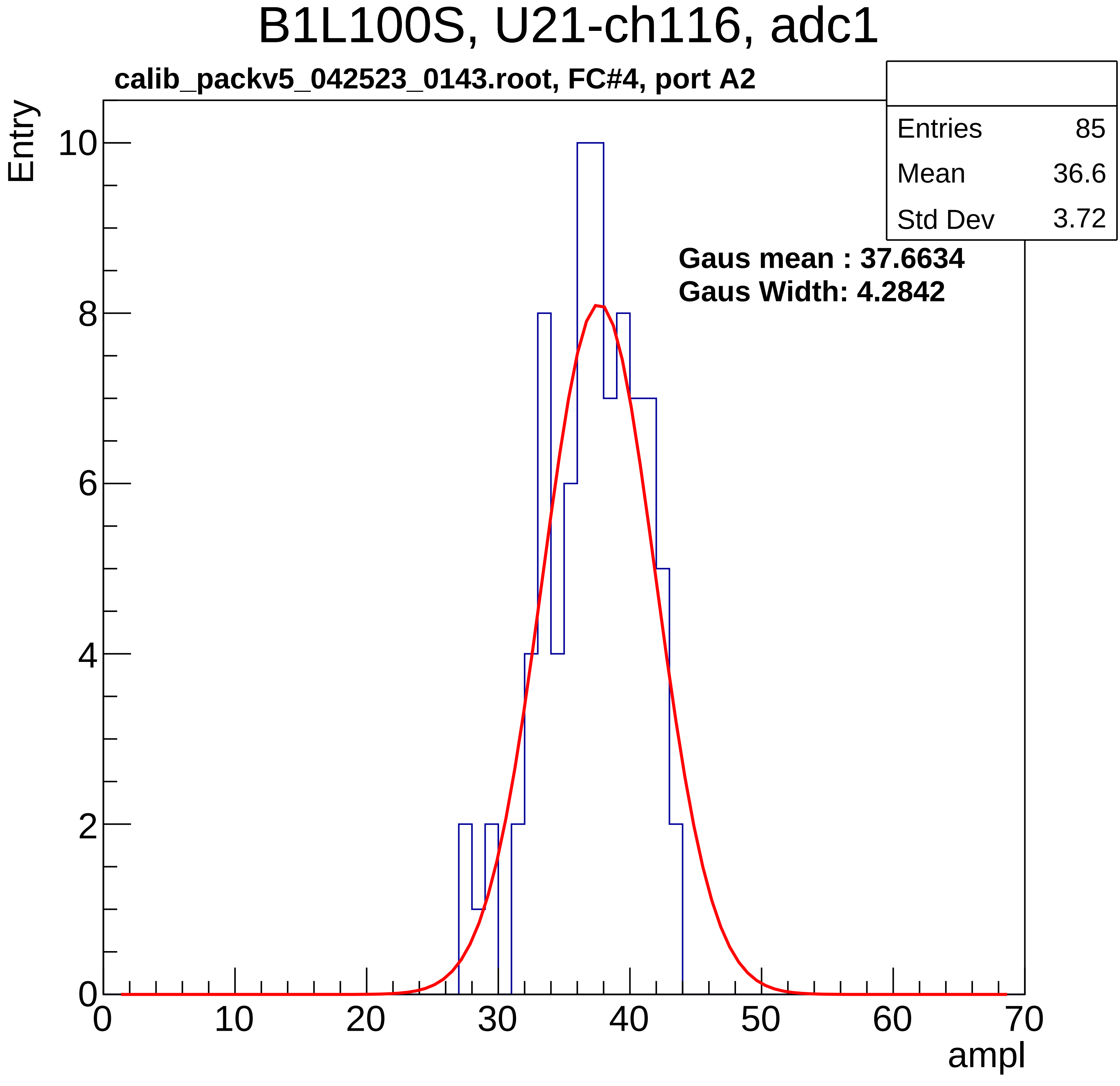


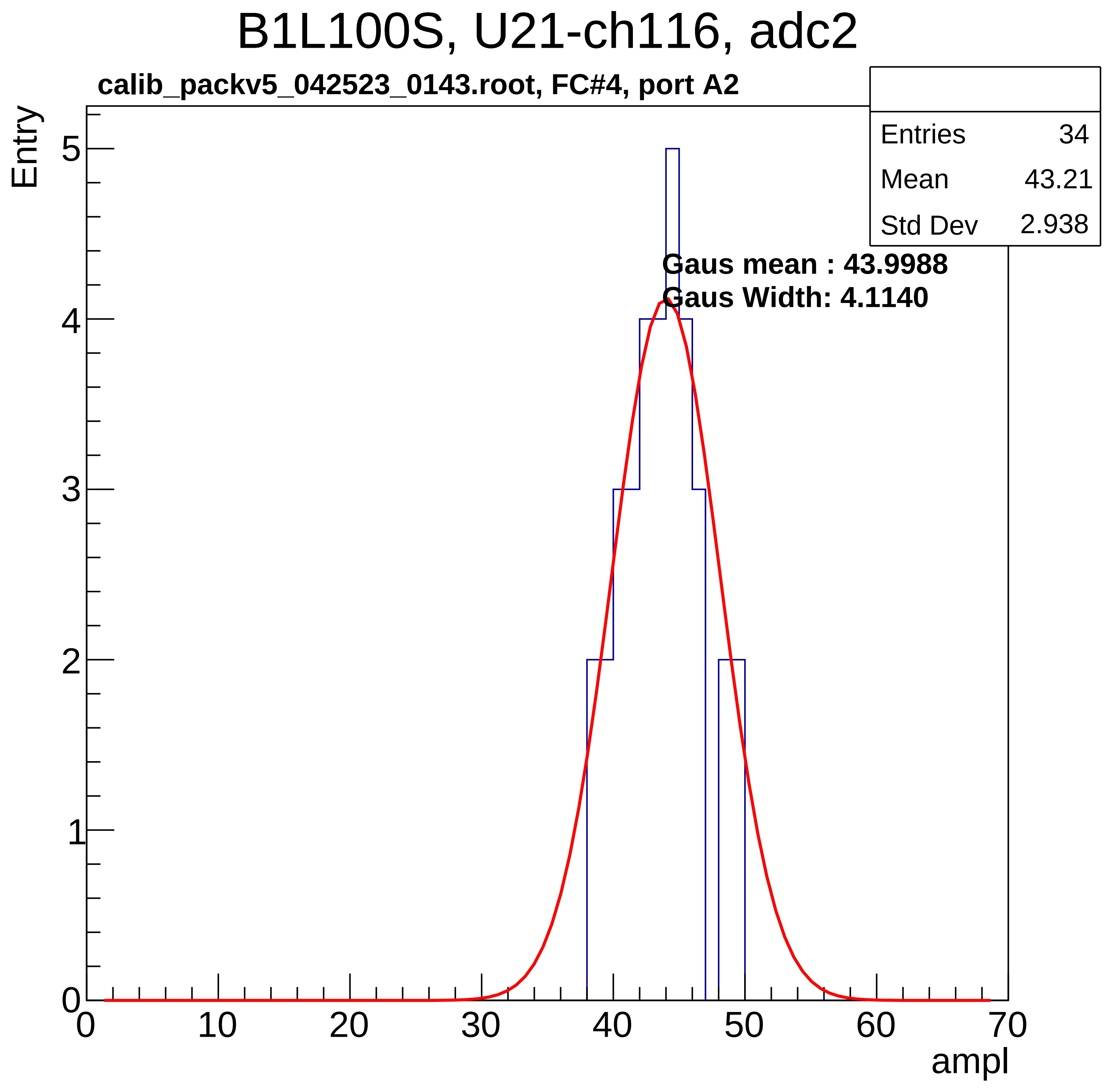

B1L100S, U21-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.89
Std Dev 2.299
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

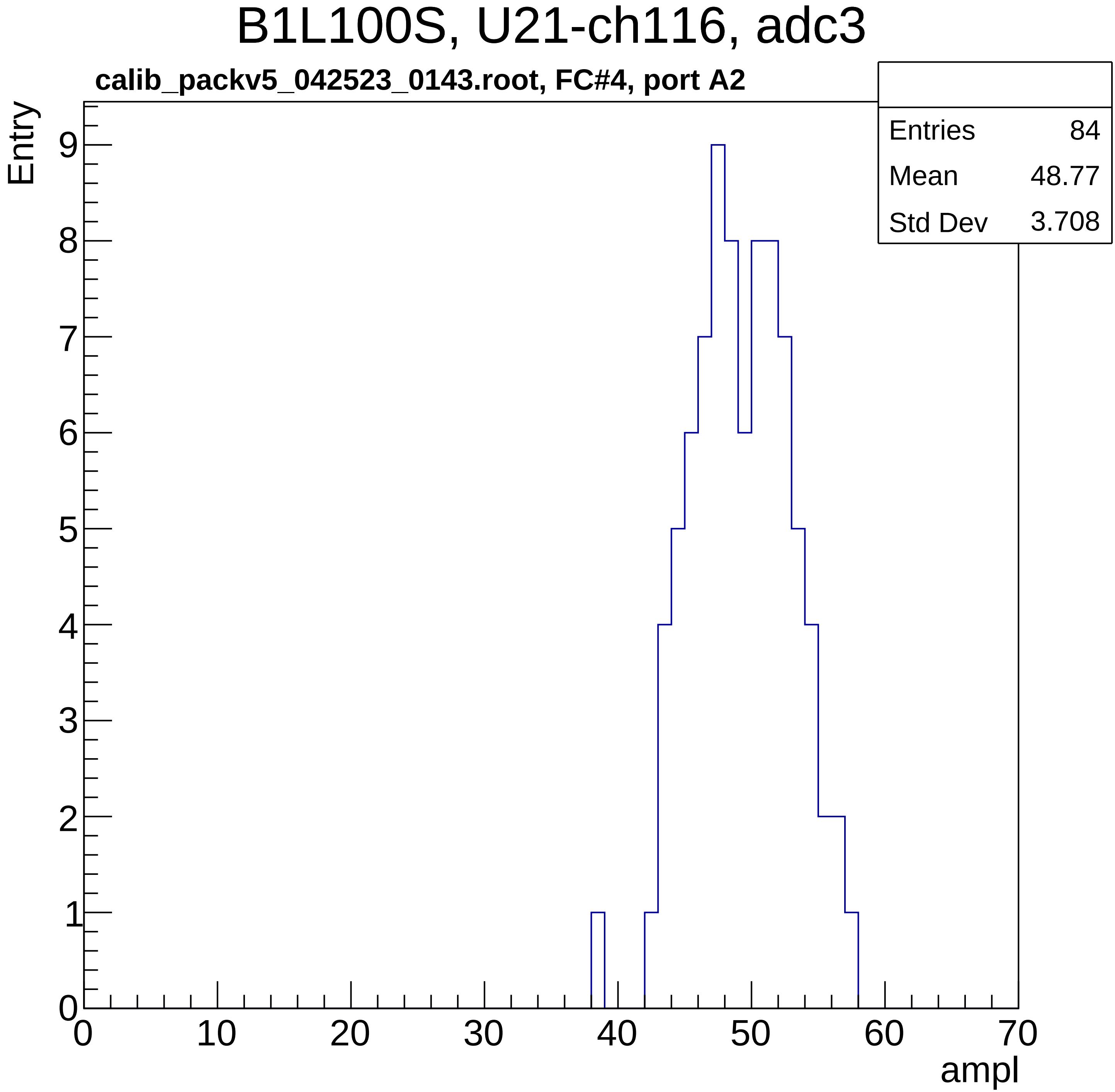

B1L100S, U21-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.92
Std Dev 17.19
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

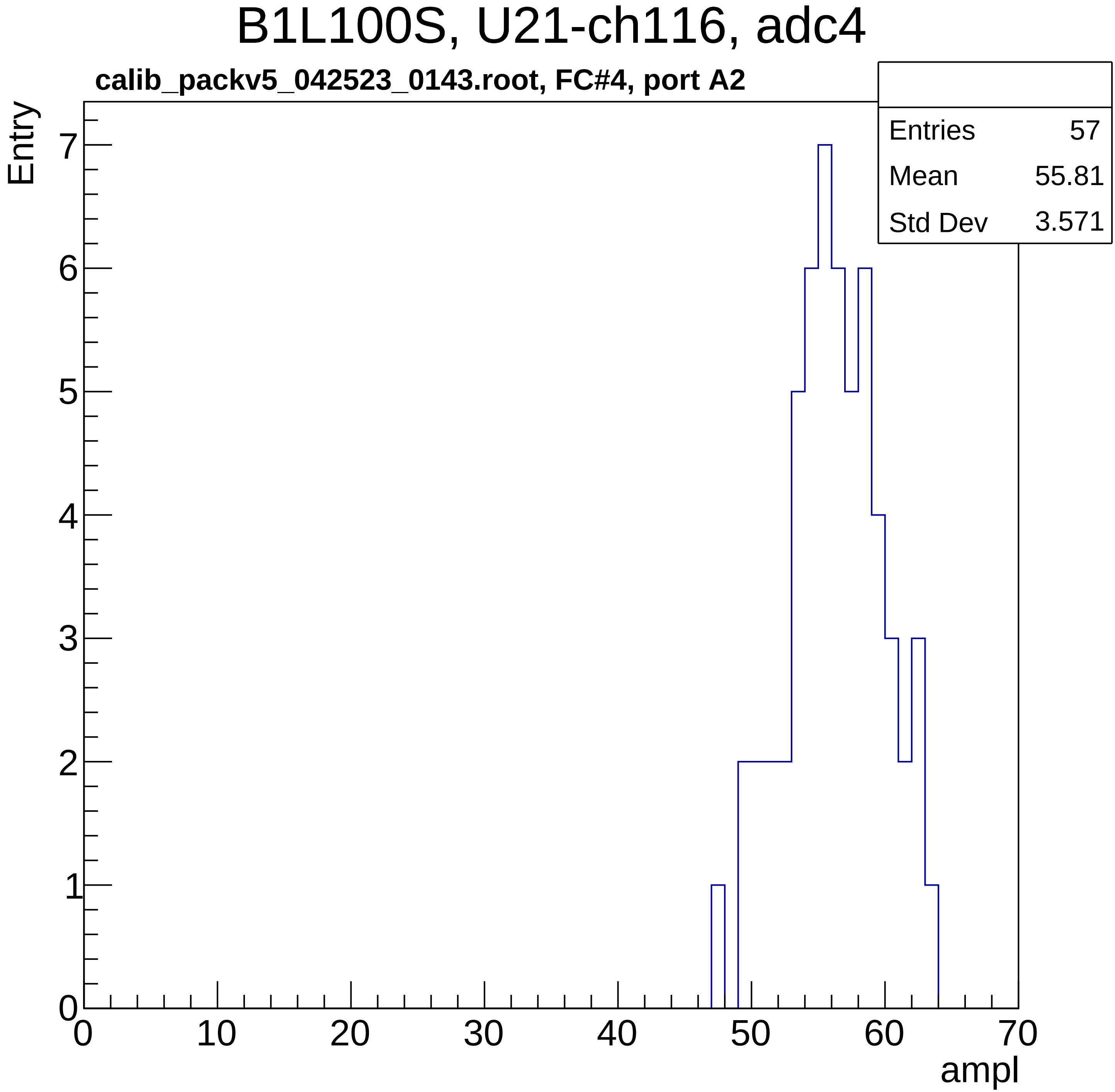

B1L100S, U21-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7
Std Dev 9.899
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.92
Std Dev 3.664
Gaus mean : 29.9140
Gaus Width: 3.7297
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

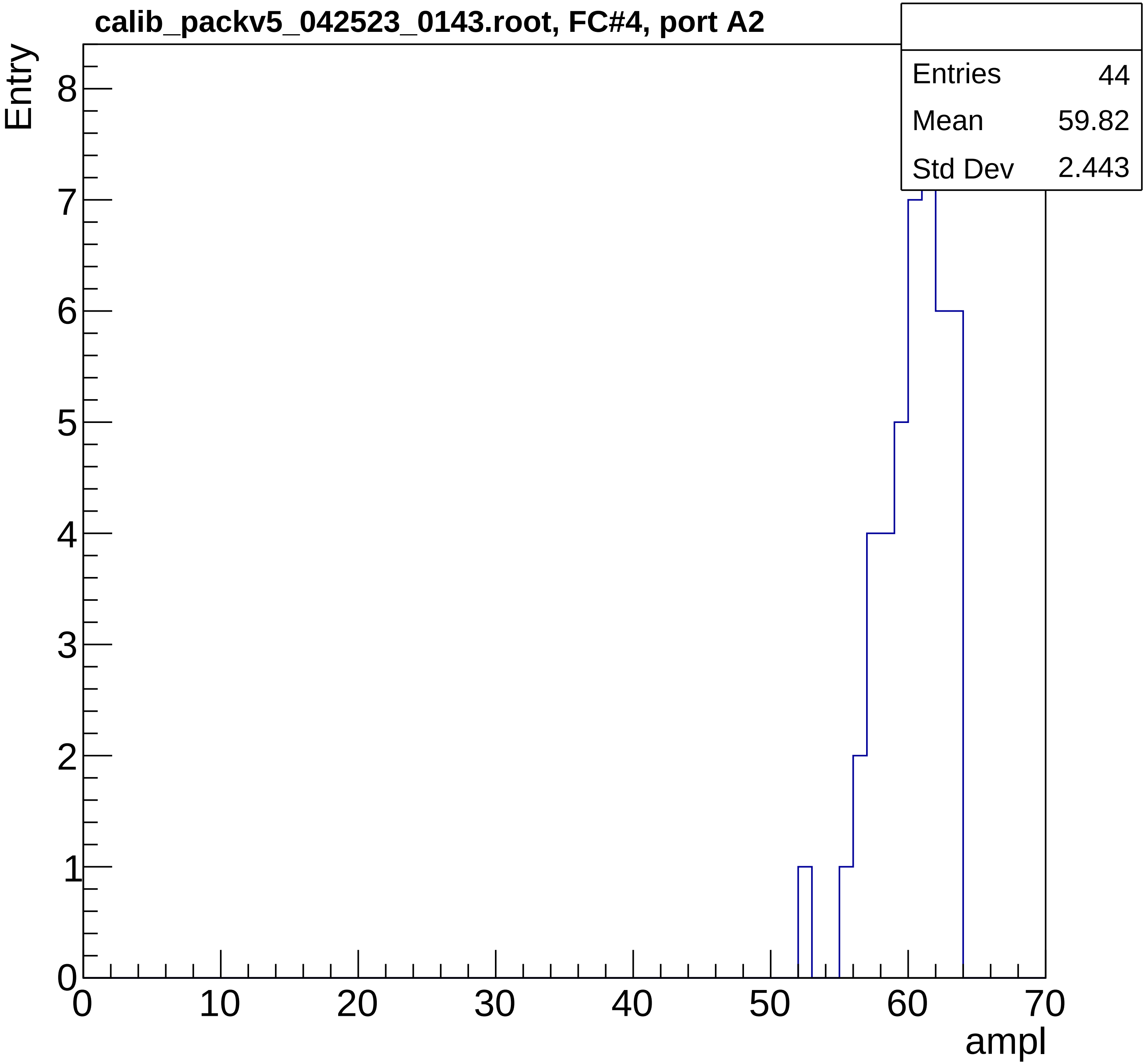

B1L100S, U21-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 36.6
Std Dev 3.72
Gaus mean : 37.6634
Gaus Width: 4.2842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U21-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 43.21
Std Dev 2.938
Gaus mean : 43.9988
Gaus Width: 4.1140
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

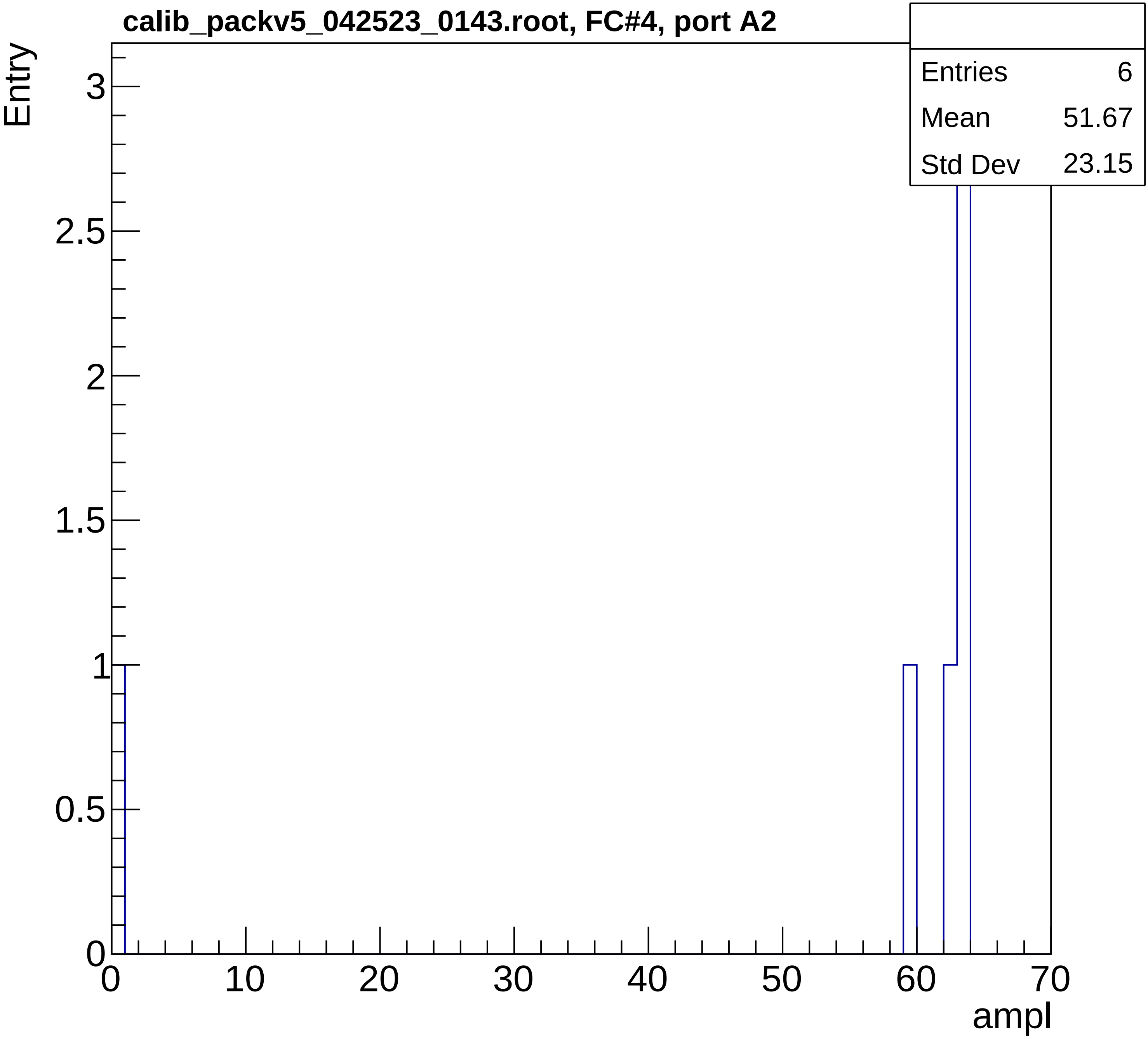

B1L100S, U21-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 48.77
Std Dev 3.708
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

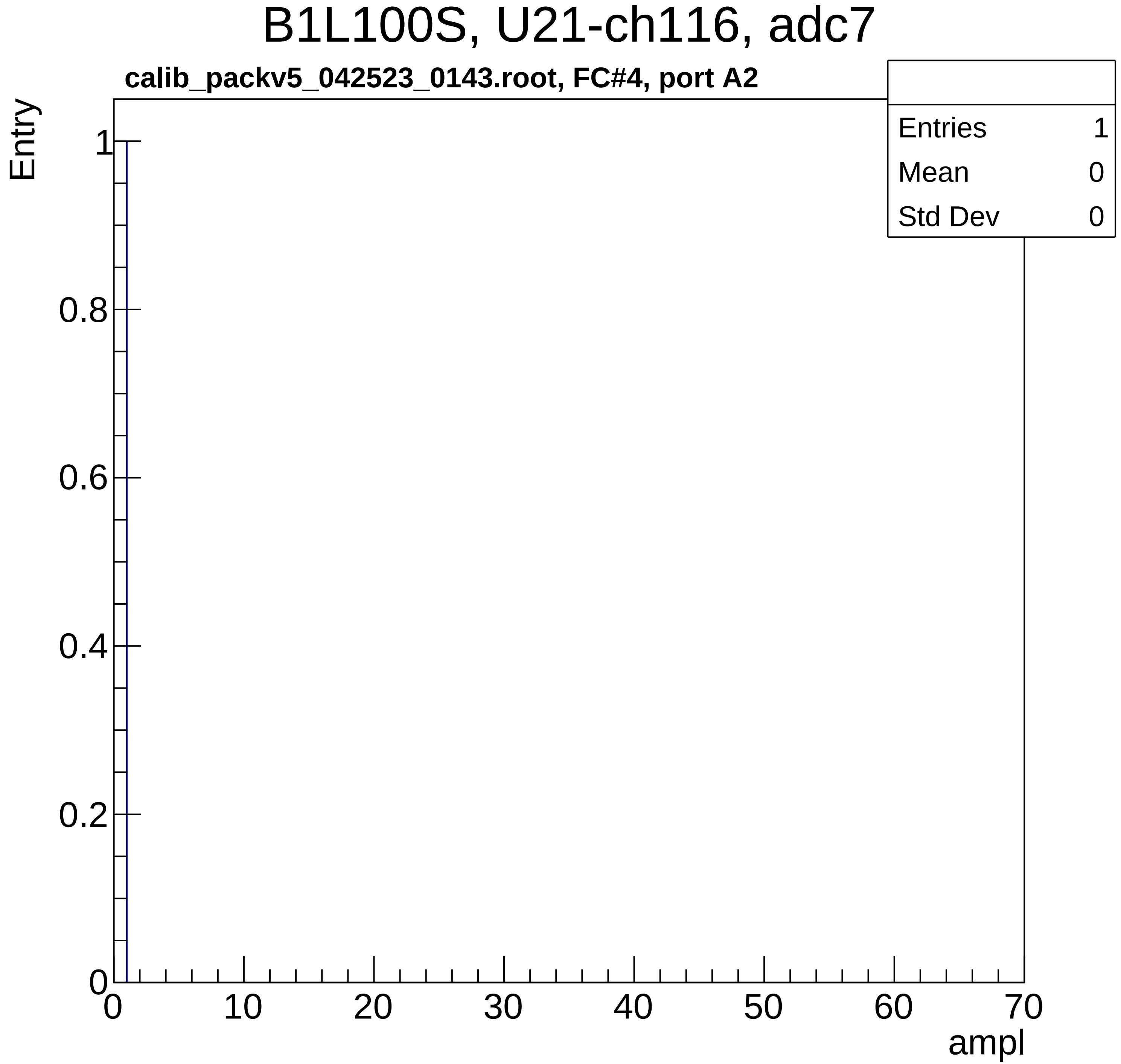

B1L100S, U21-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 55.81
Std Dev 3.571
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

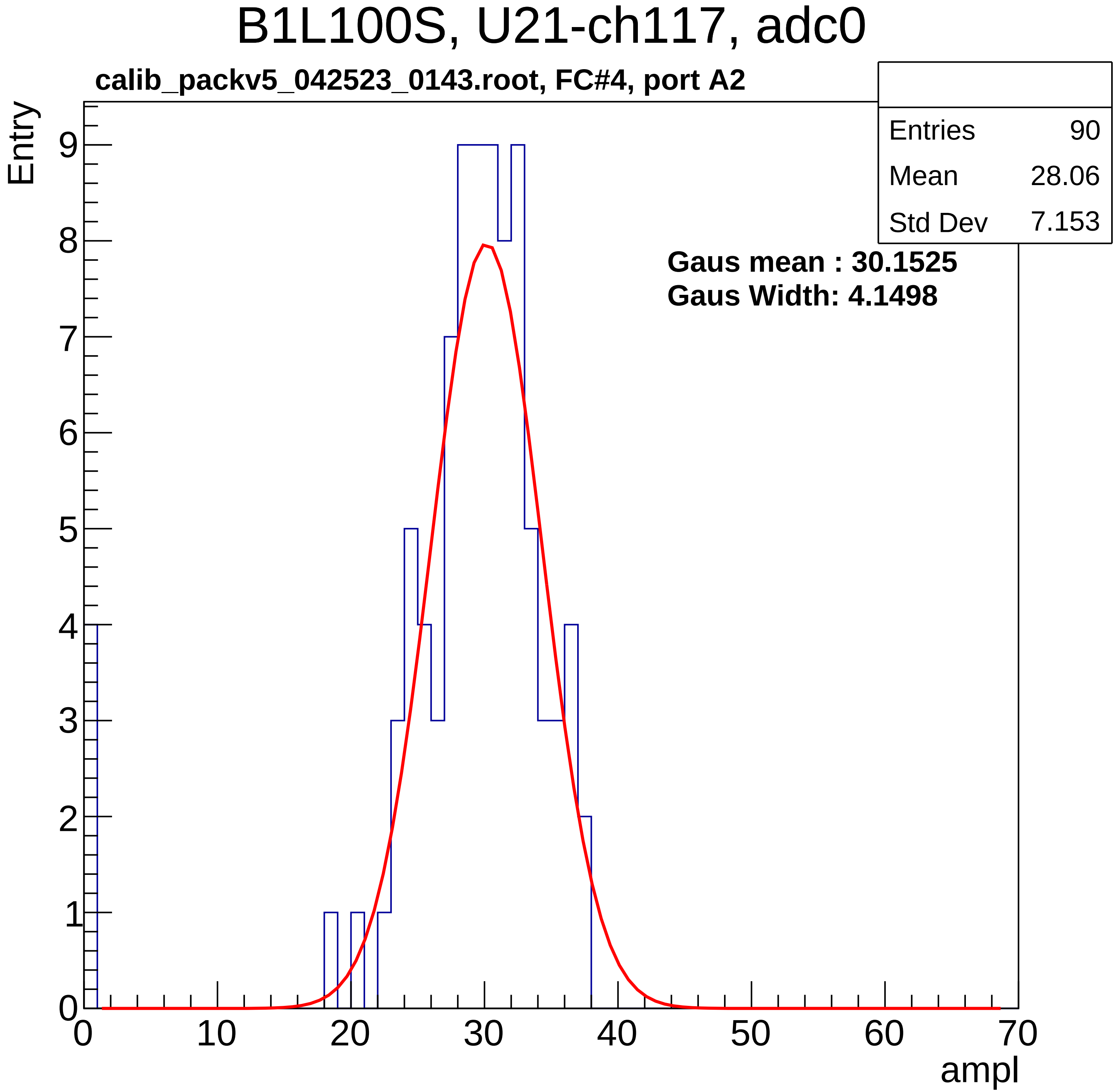
B1L100S, U21-ch116, adc5

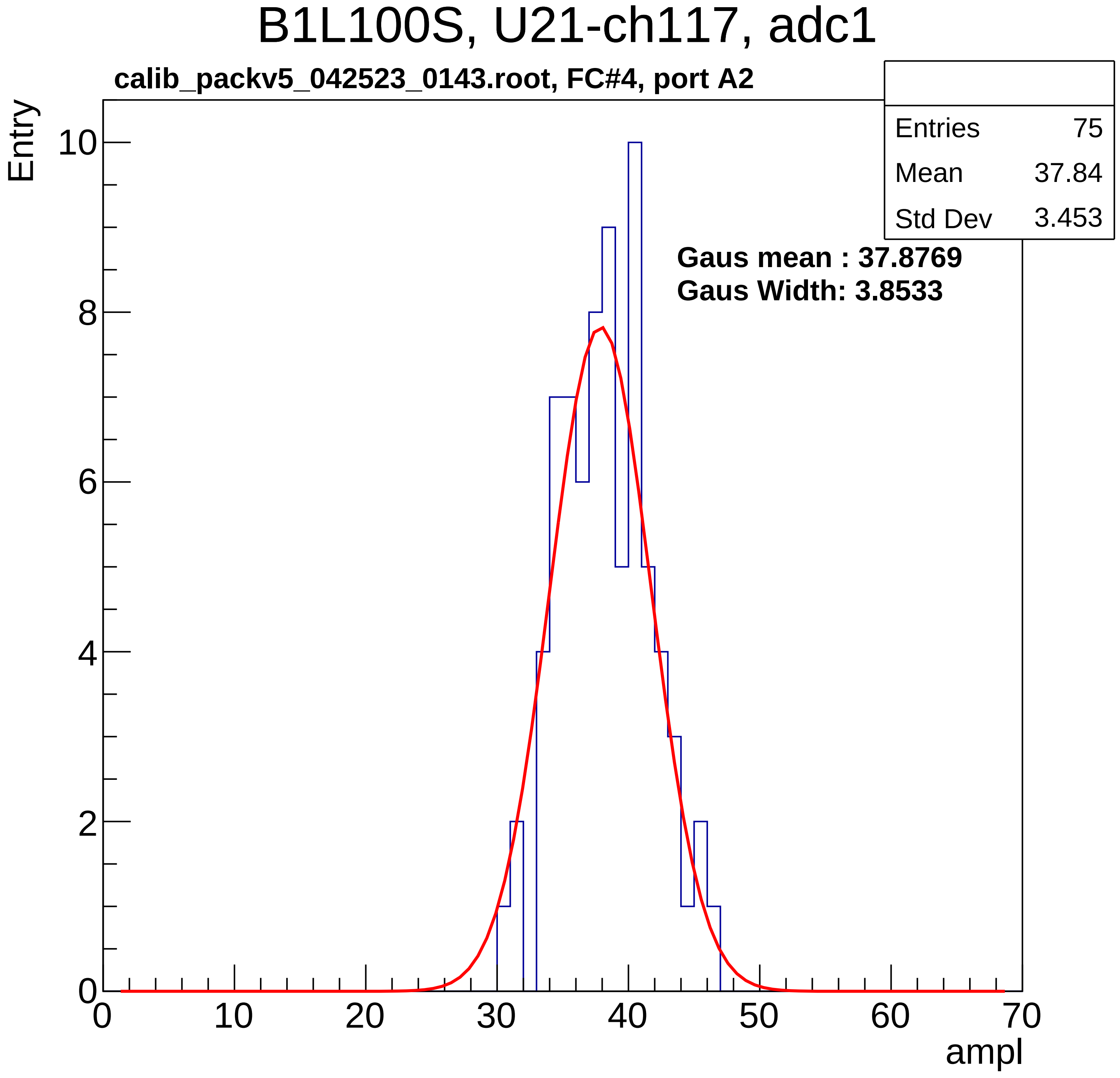


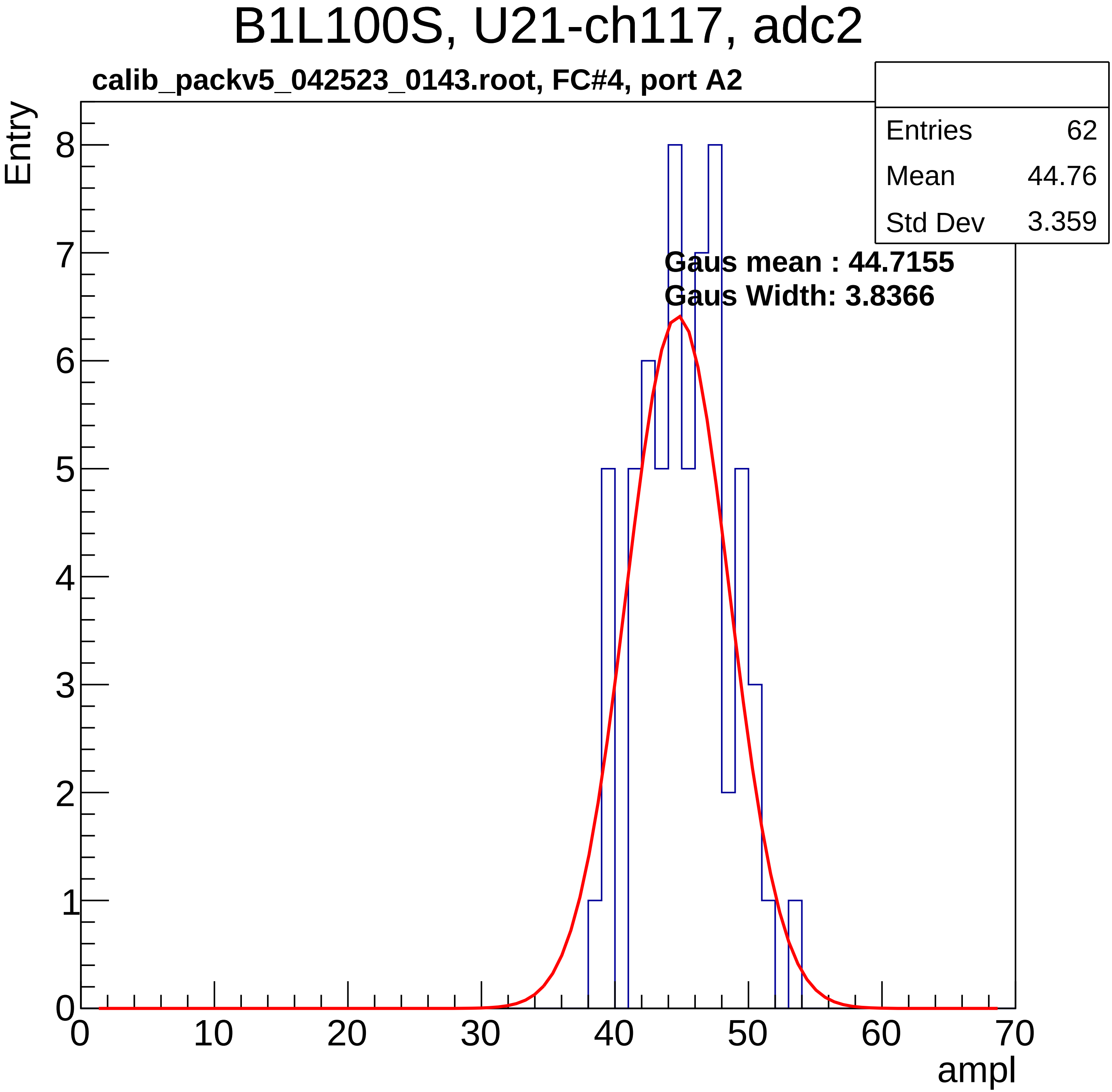
B1L100S, U21-ch116, adc6

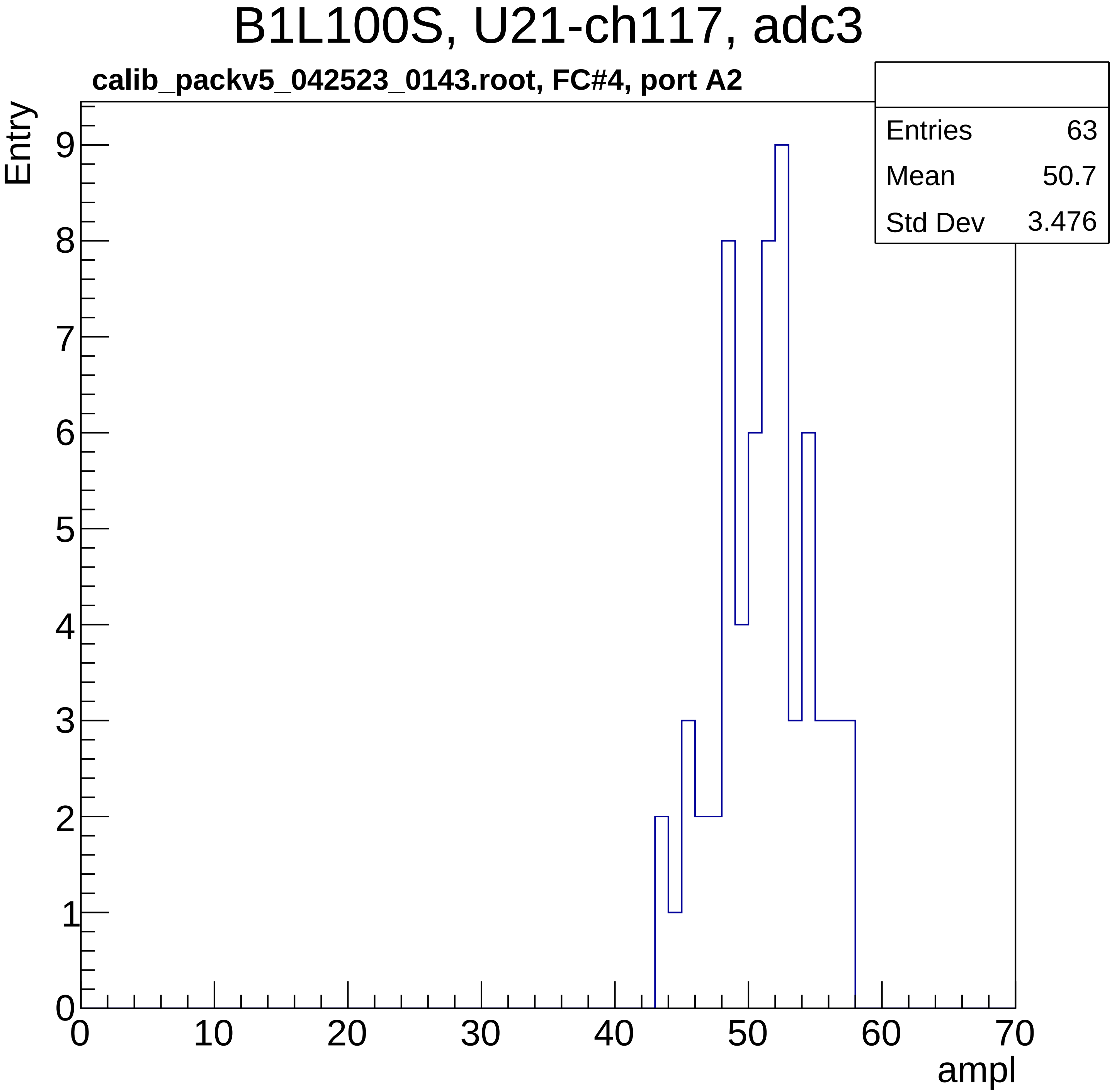


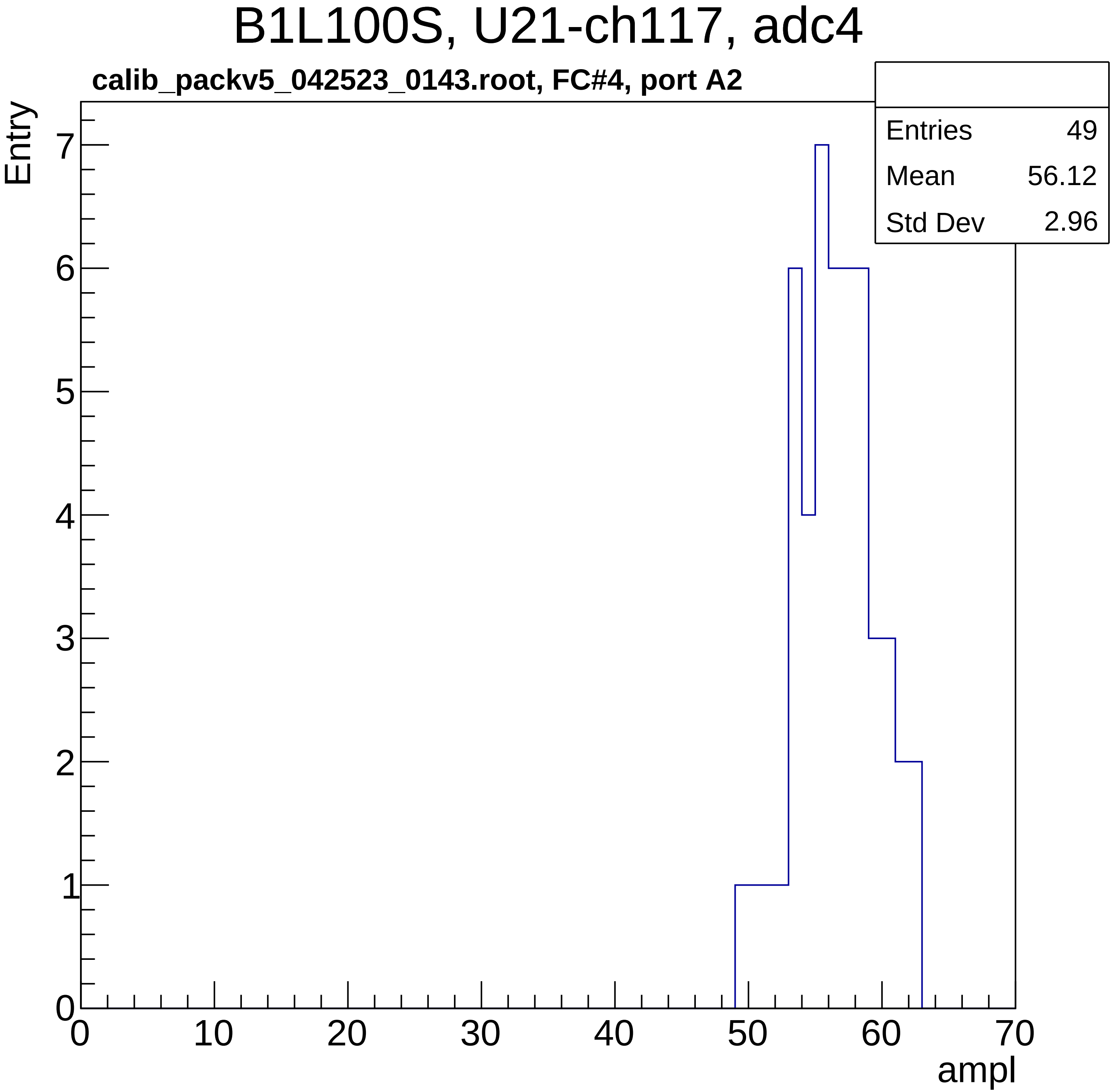

B1L100S, U21-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 28.06
Std Dev 7.153
Gaus mean : 30.1525
Gaus Width: 4.1498
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 75
Mean 37.84
Std Dev 3.453
Gaus mean : 37.8769
Gaus Width: 3.8533
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

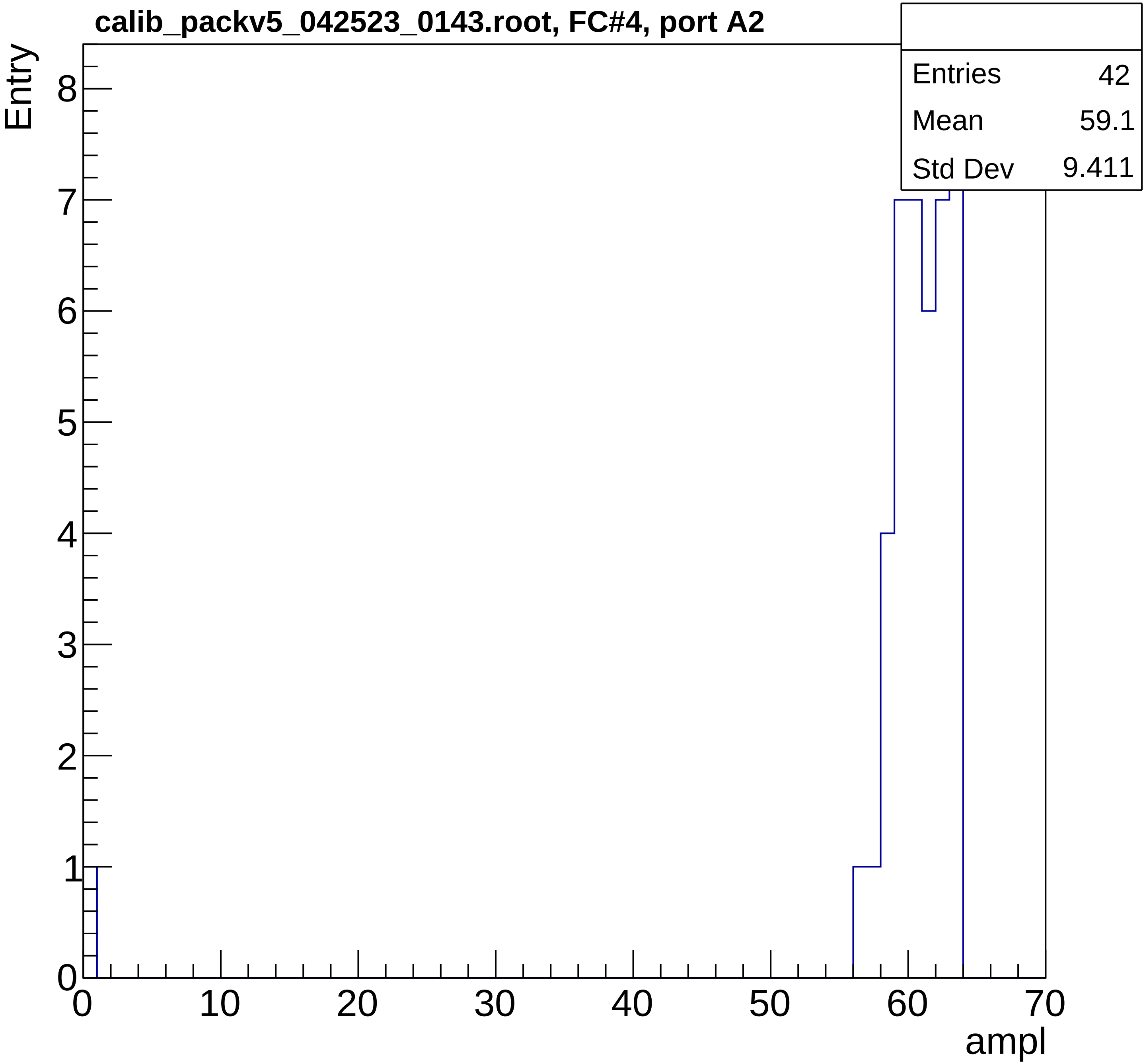

B1L100S, U21-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44.76
Std Dev 3.359
Gaus mean : 44.7155
Gaus Width: 3.8366
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.7
Std Dev 3.476
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

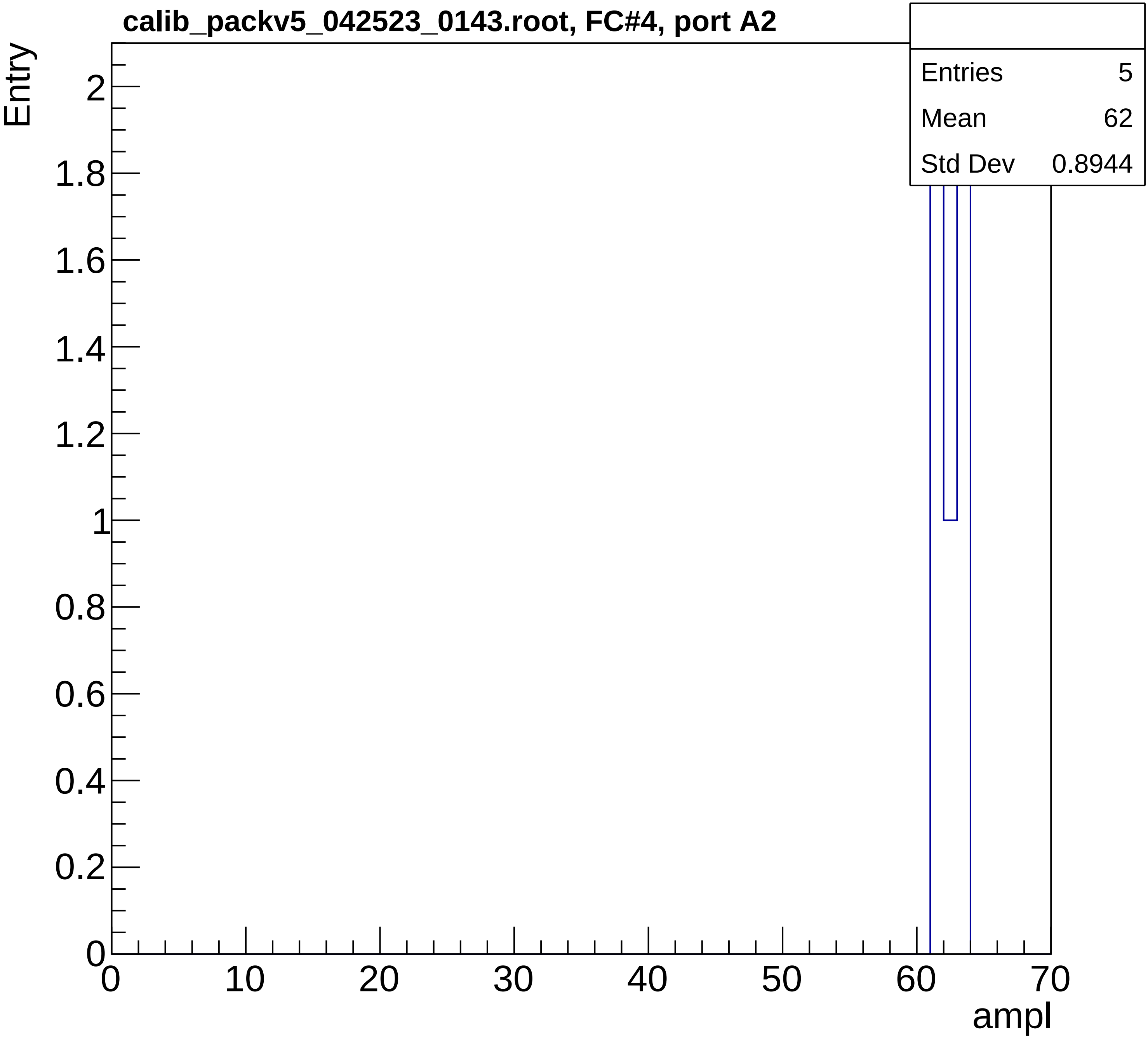

B1L100S, U21-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.12
Std Dev 2.96
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

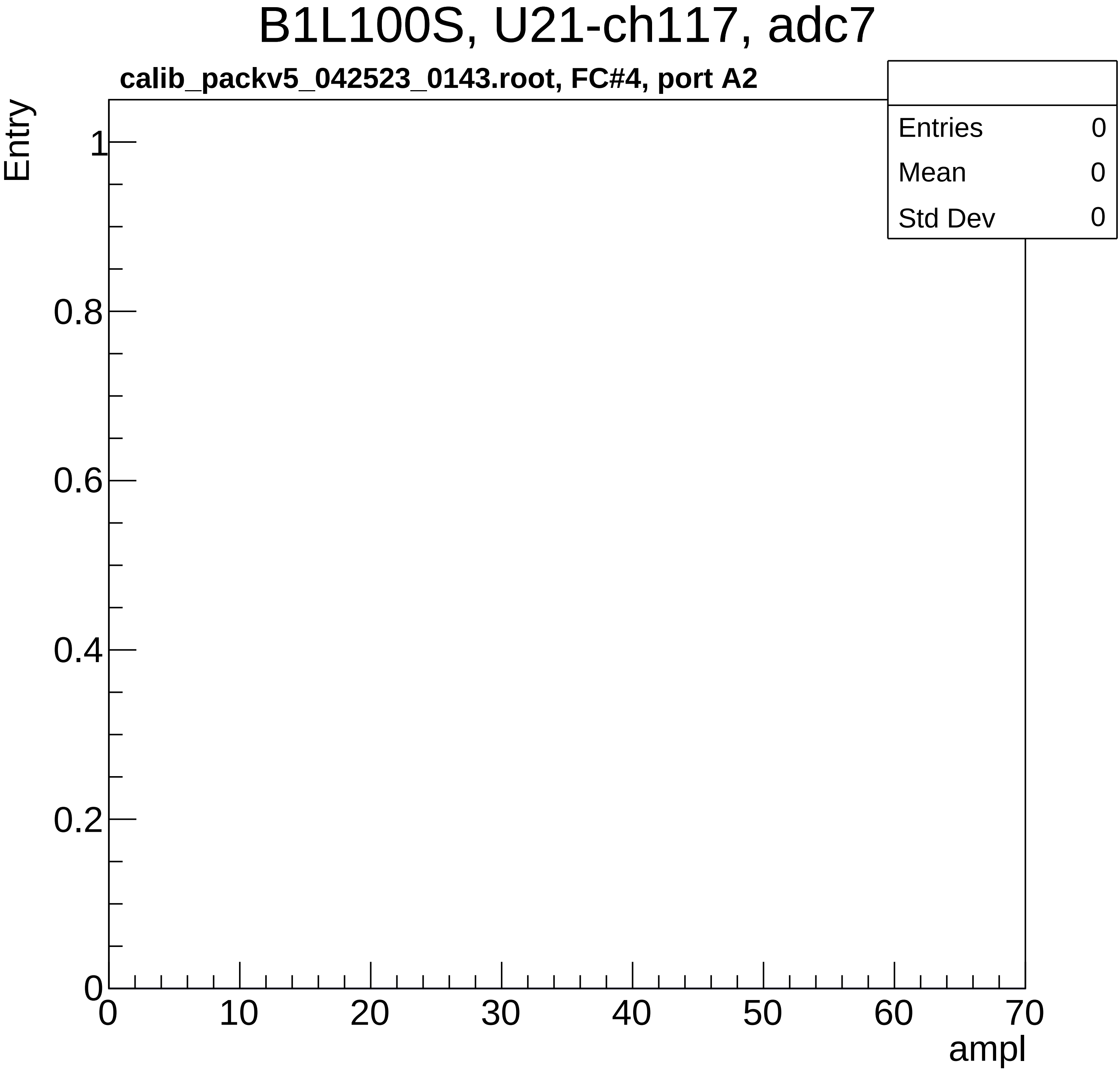
B1L100S, U21-ch117, adc5

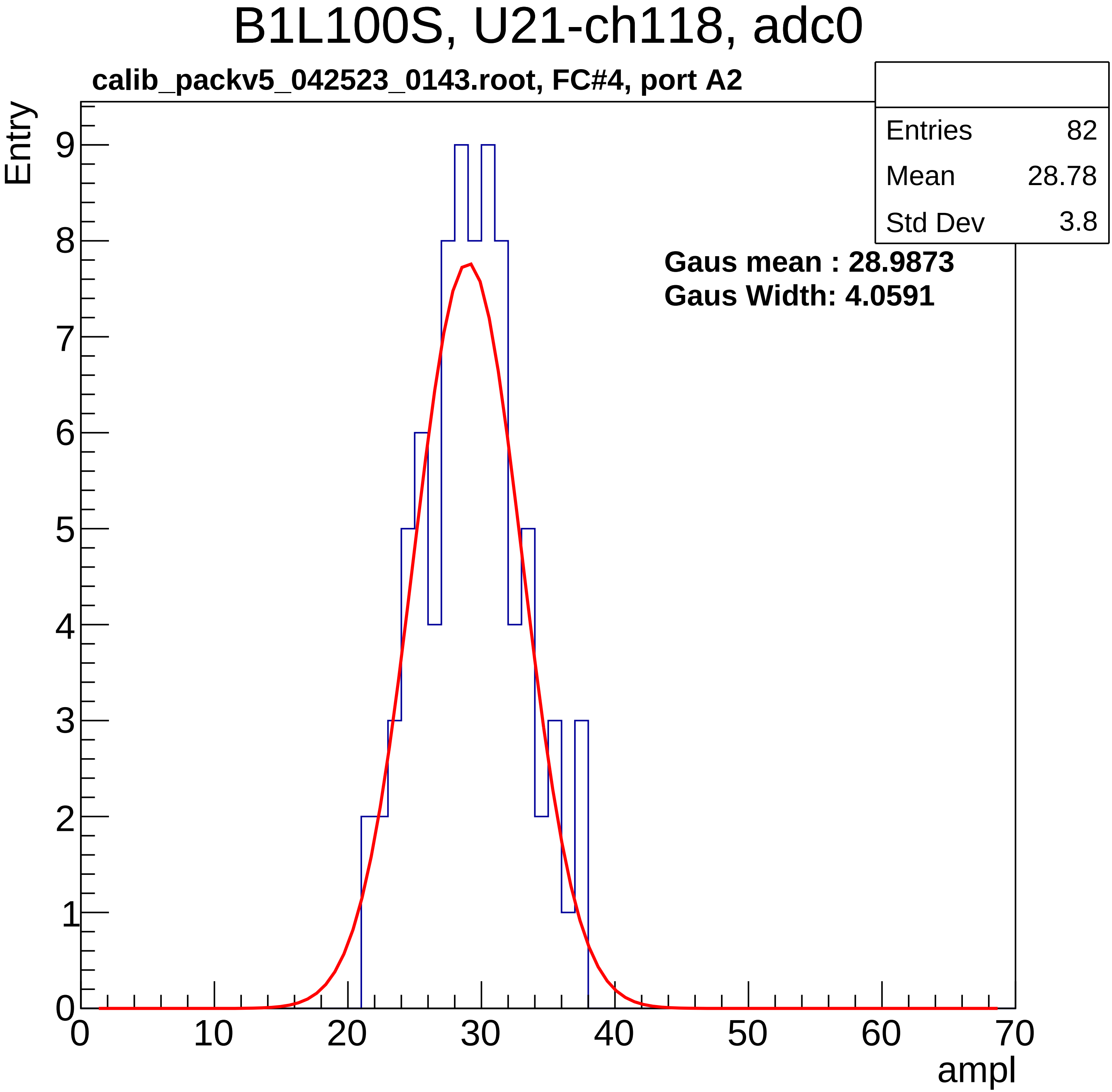
calib_packv5_042523_0143.root, FC#4, port A2

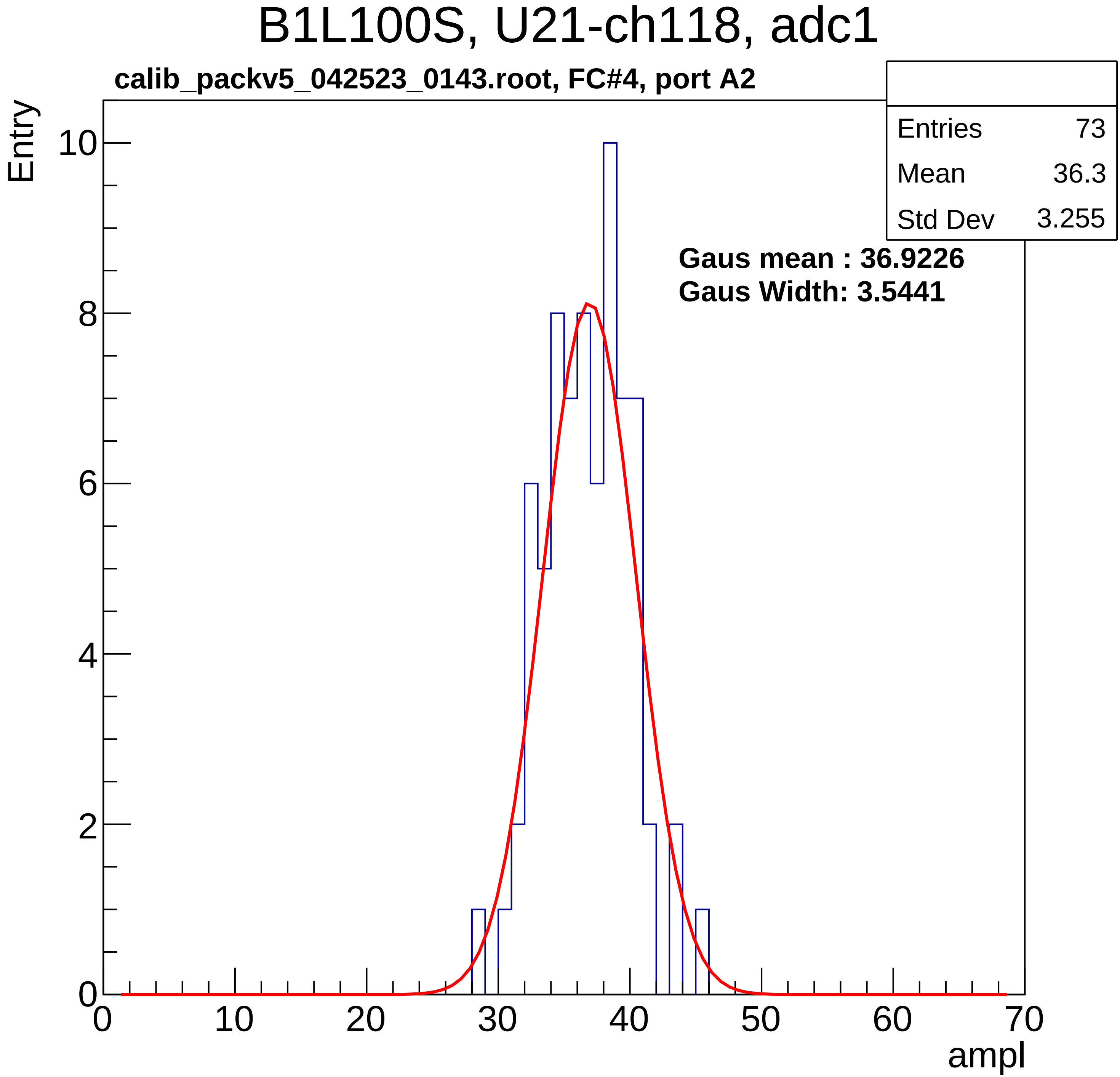


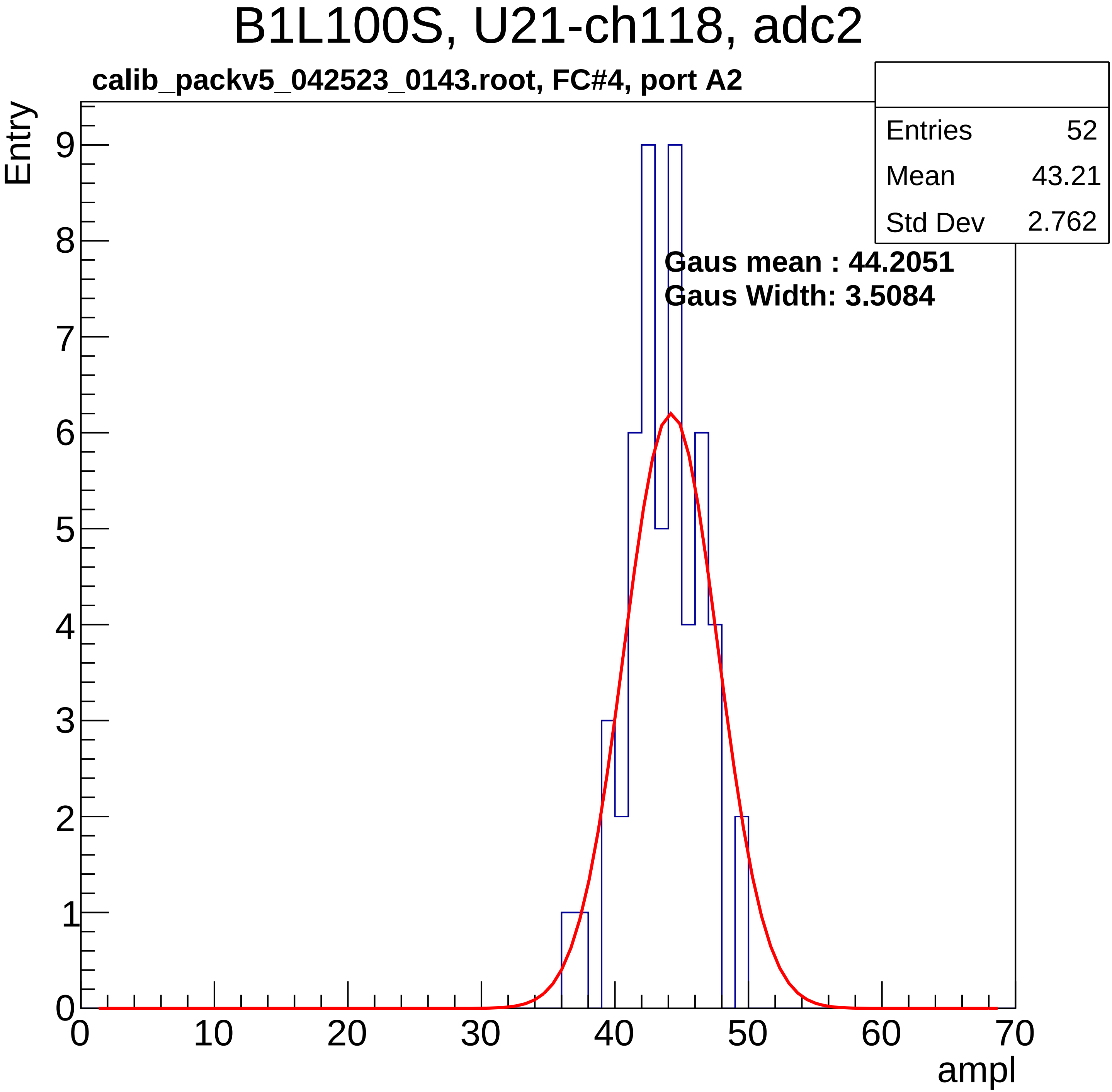
B1L100S, U21-ch117, adc6

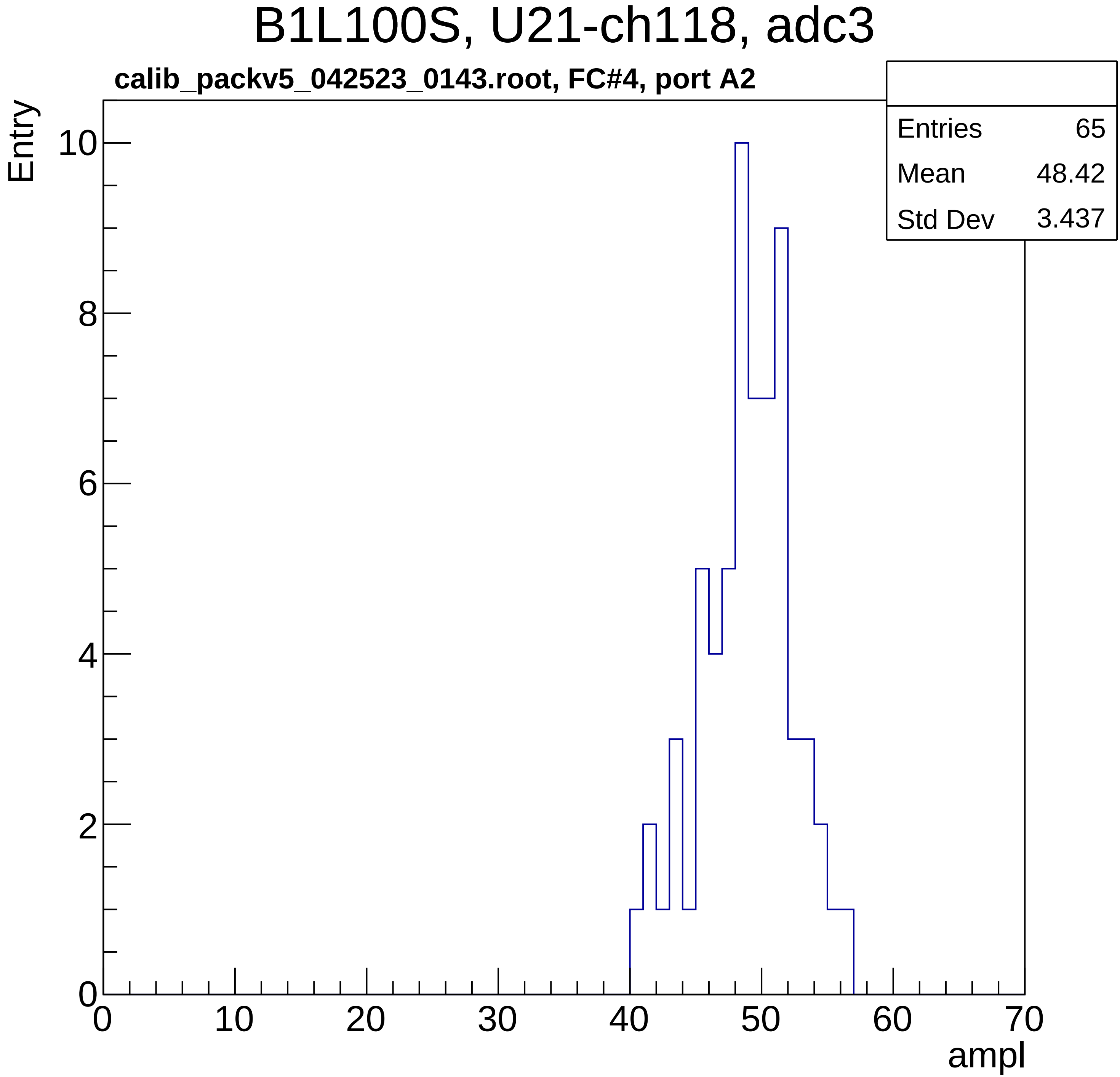


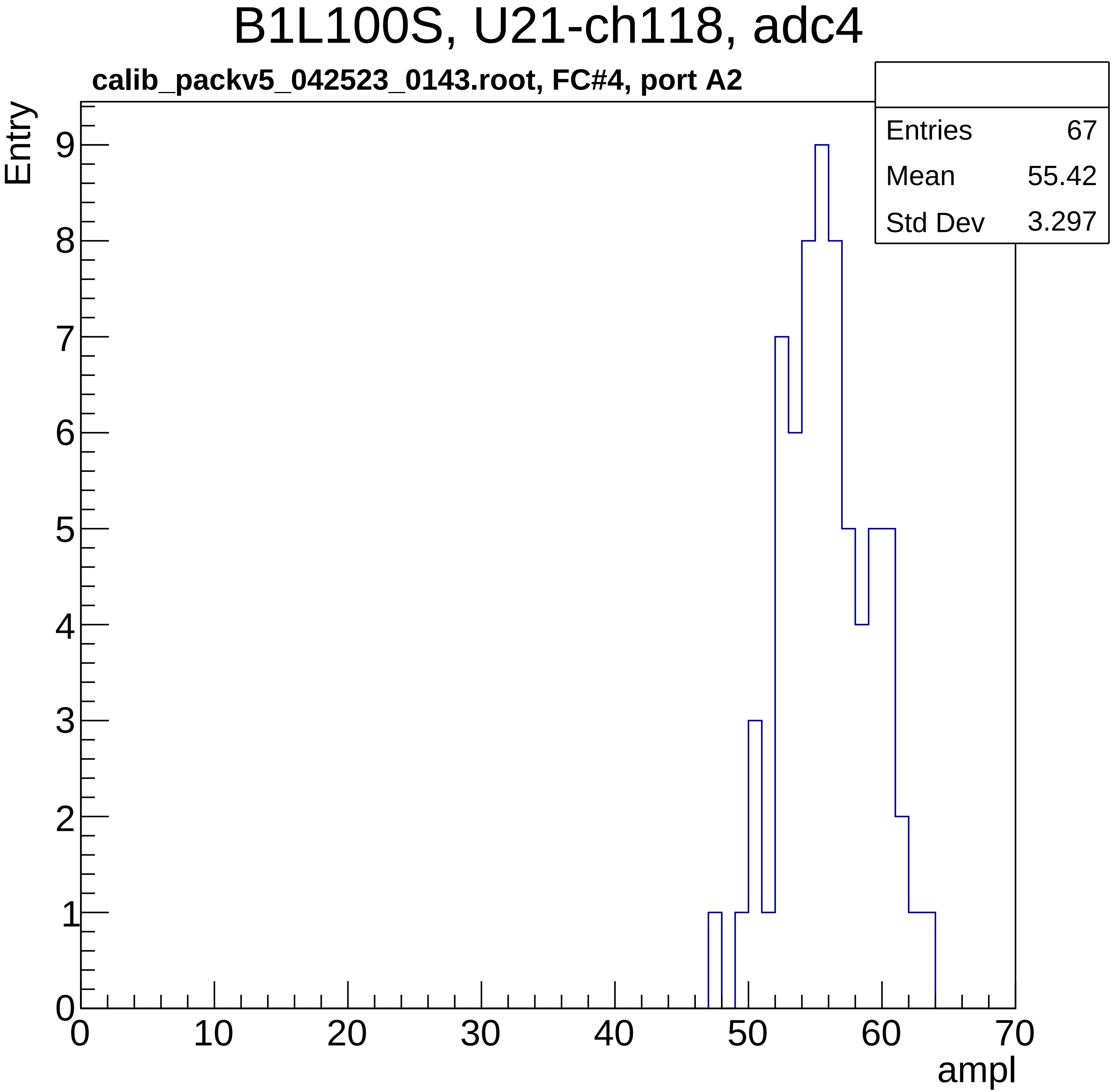

B1L100S, U21-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

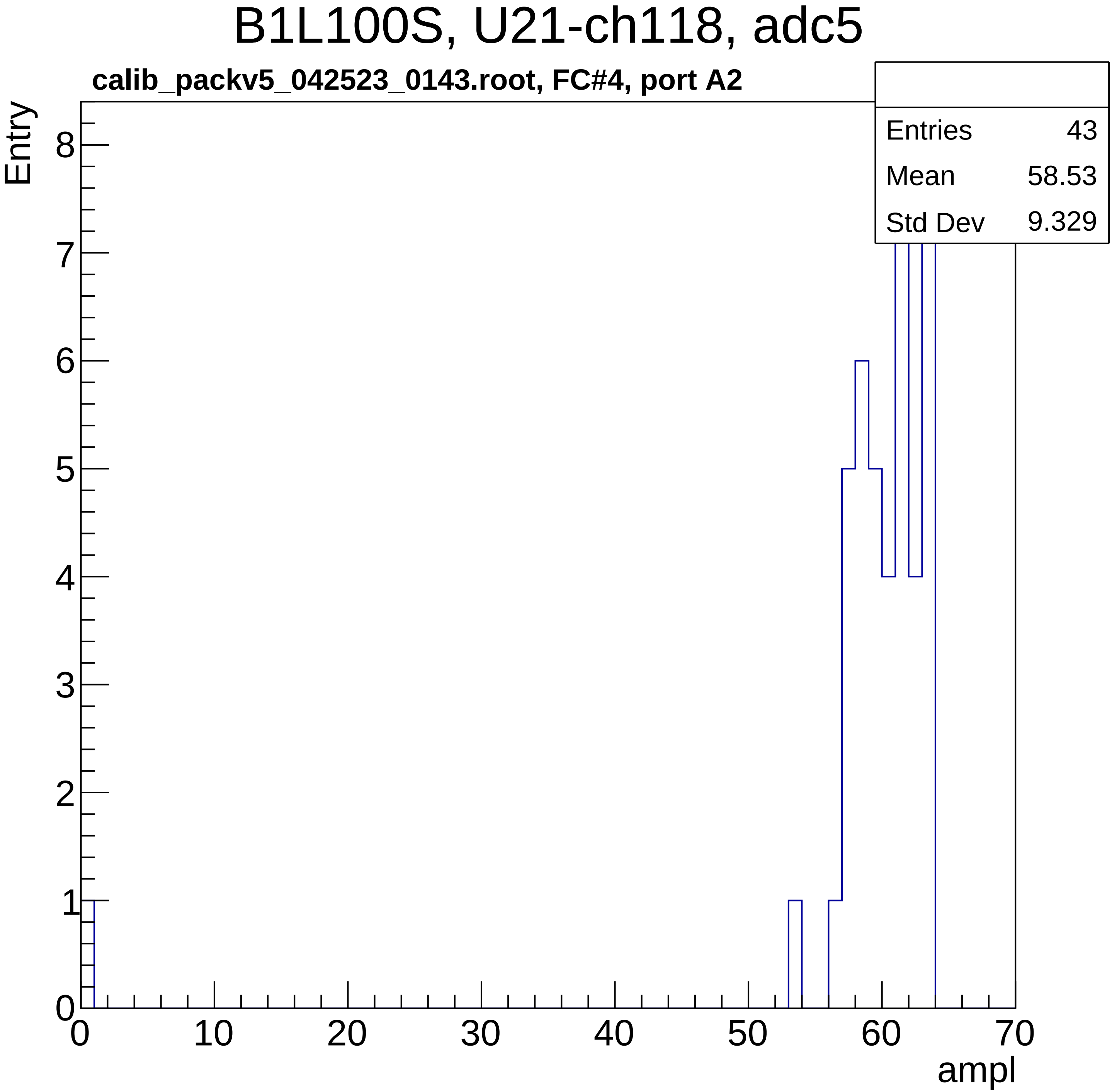

B1L100S, U21-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 82
Mean 28.78
Std Dev 3.8
Gaus mean : 28.9873
Gaus Width: 4.0591
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.3
Std Dev 3.255
Gaus mean : 36.9226
Gaus Width: 3.5441
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

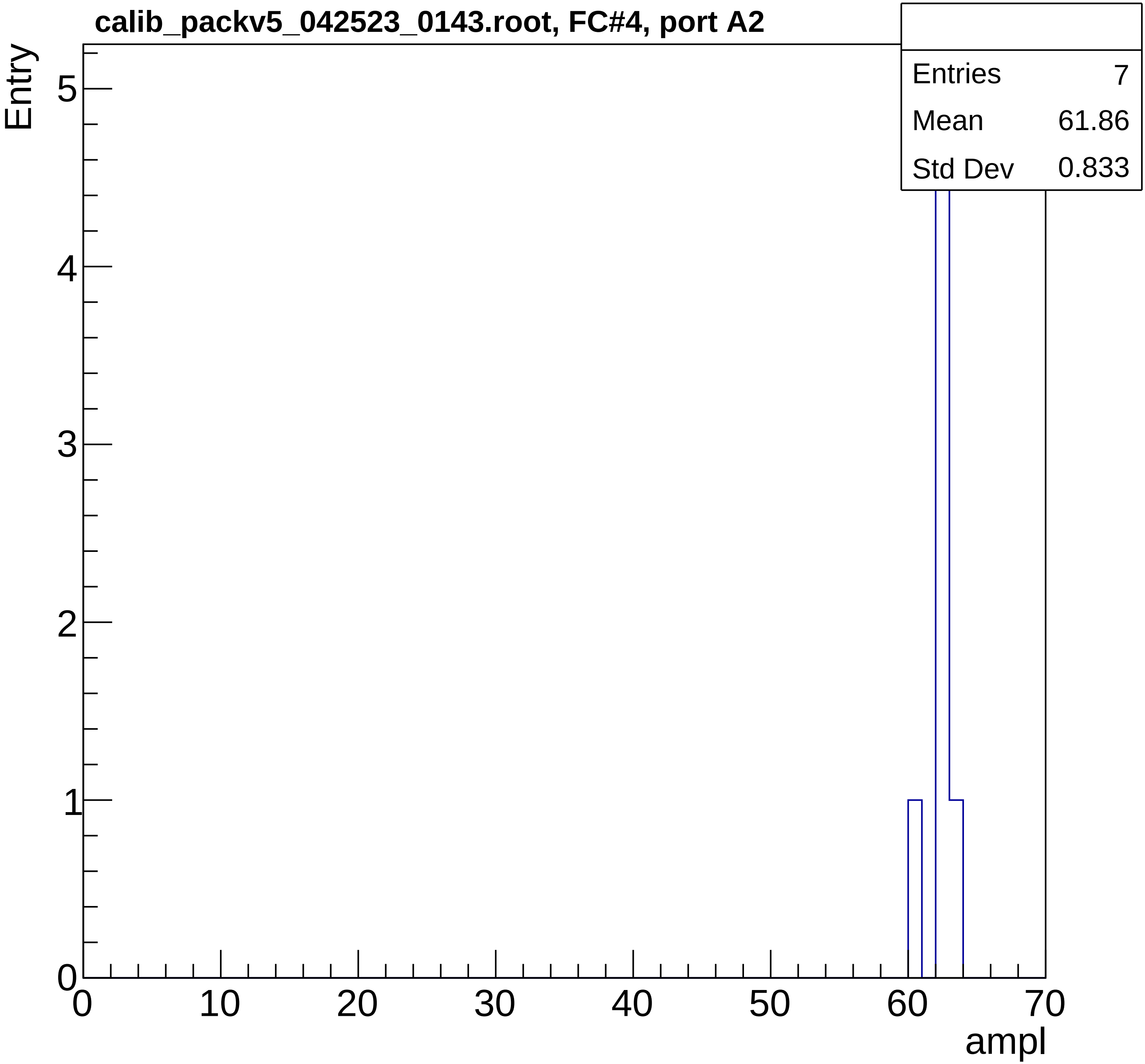

B1L100S, U21-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.21
Std Dev 2.762
Gaus mean : 44.2051
Gaus Width: 3.5084
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U21-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 48.42
Std Dev 3.437
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

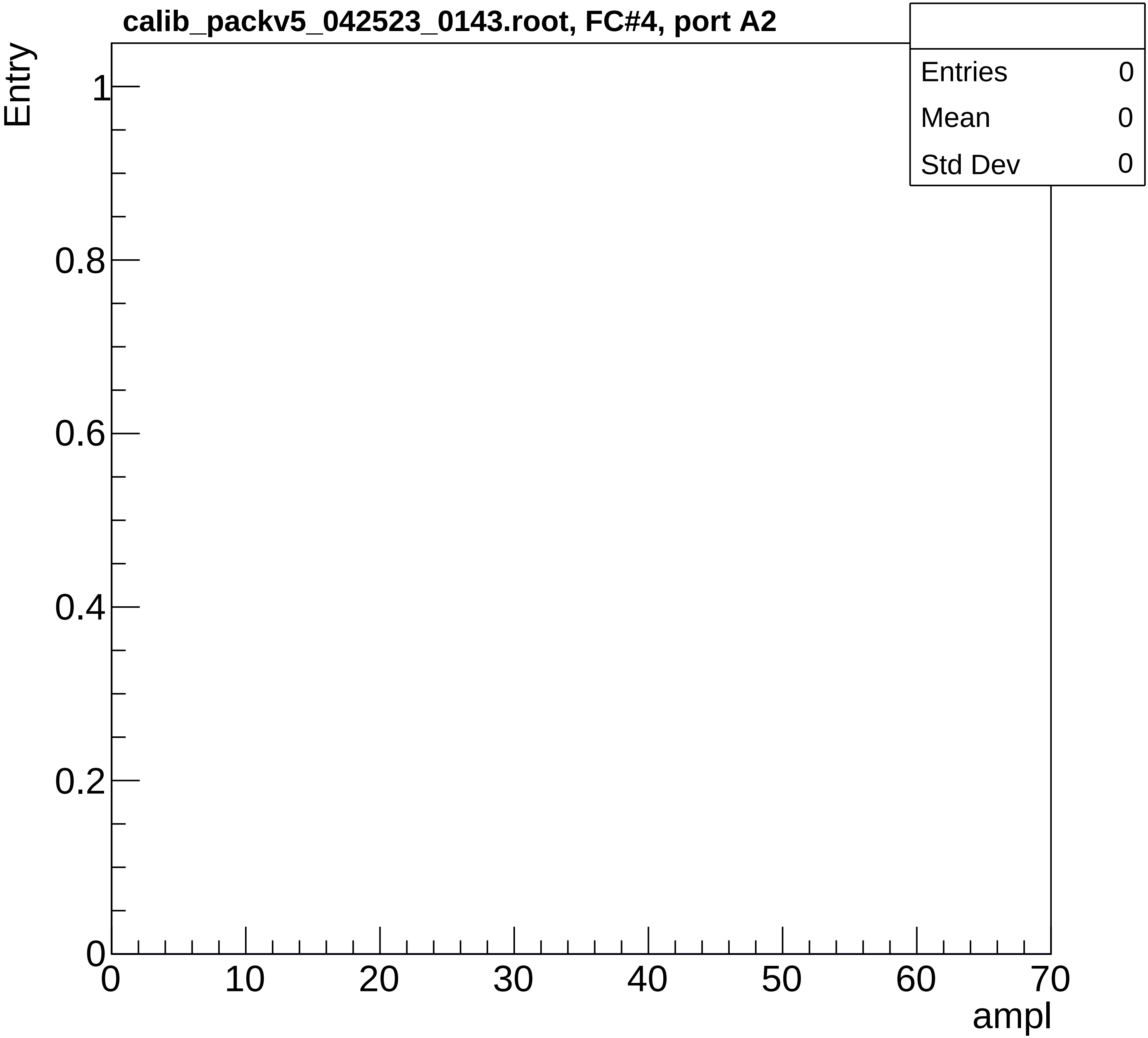

B1L100S, U21-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.42
Std Dev 3.297
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

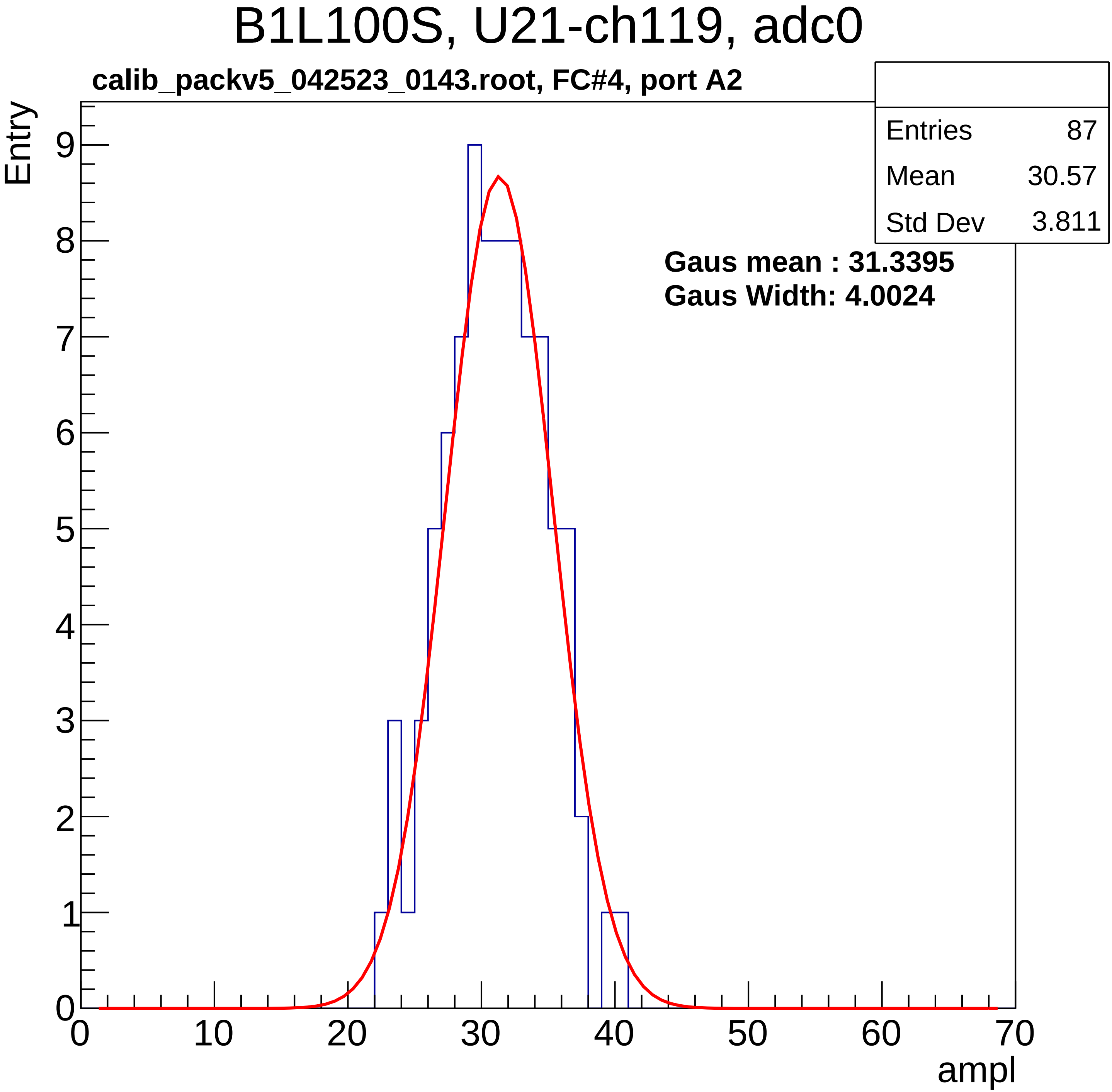

B1L100S, U21-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.53
Std Dev 9.329
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

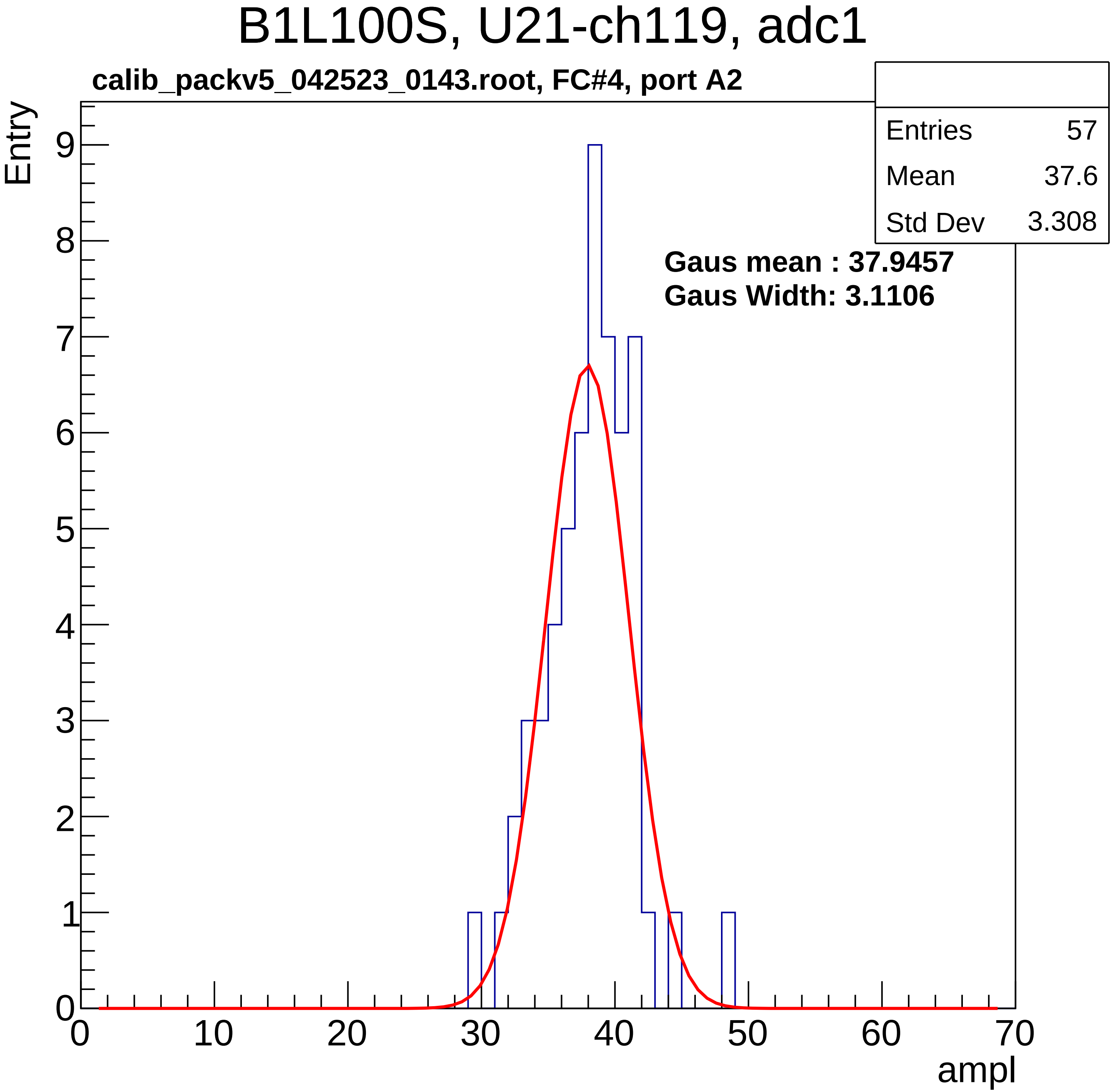
B1L100S, U21-ch118, adc6

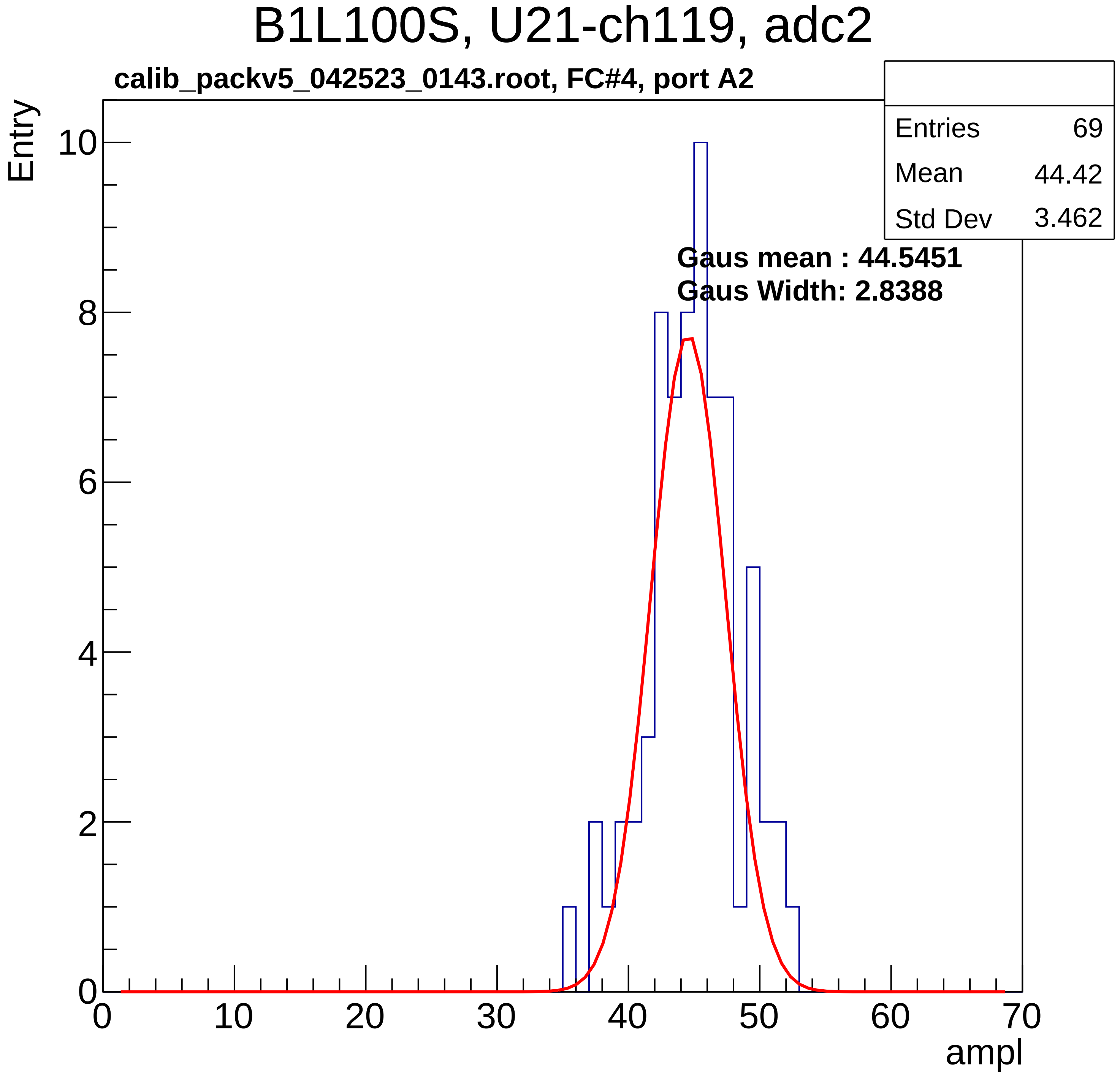


B1L100S, U21-ch118, adc7

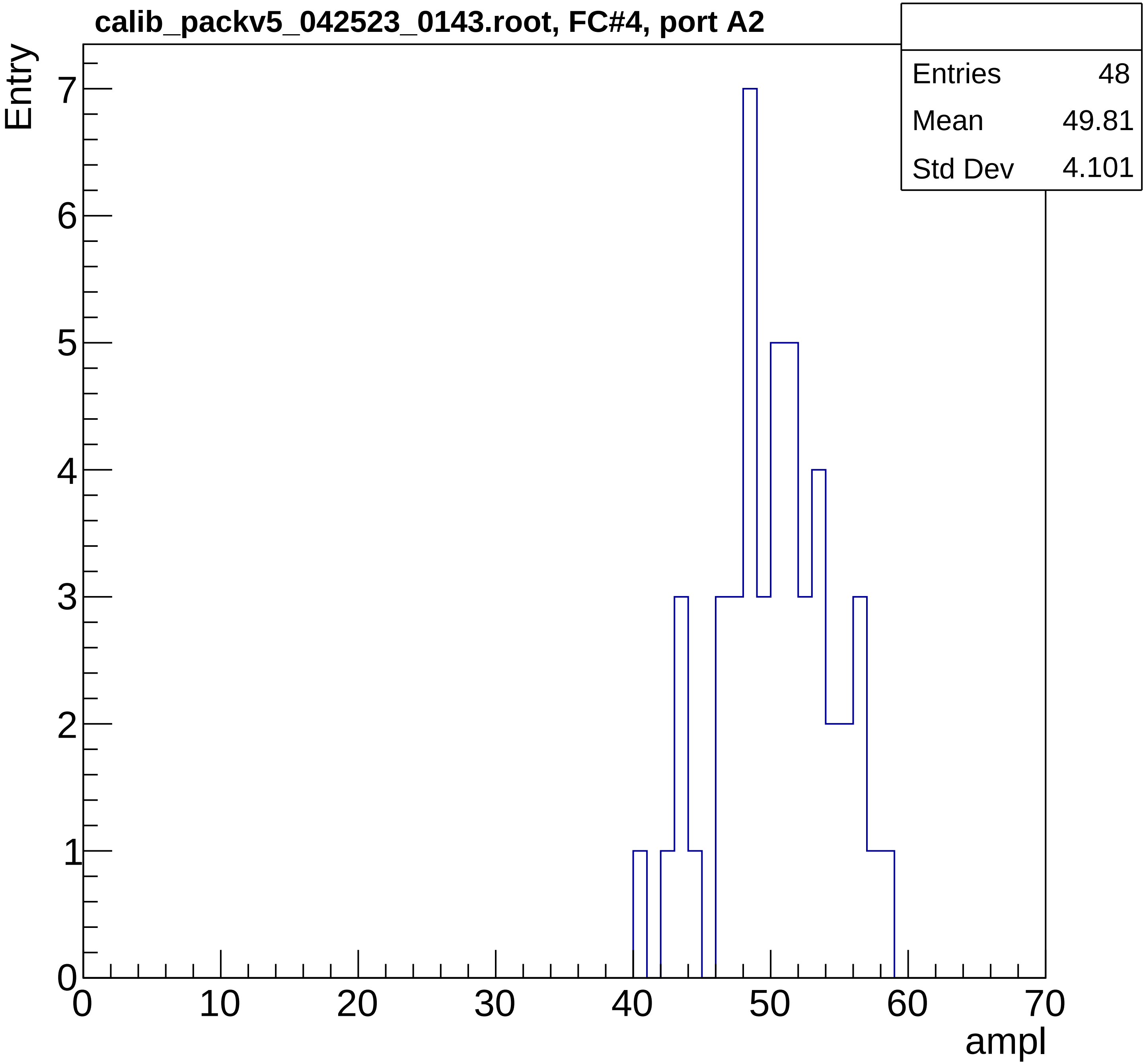


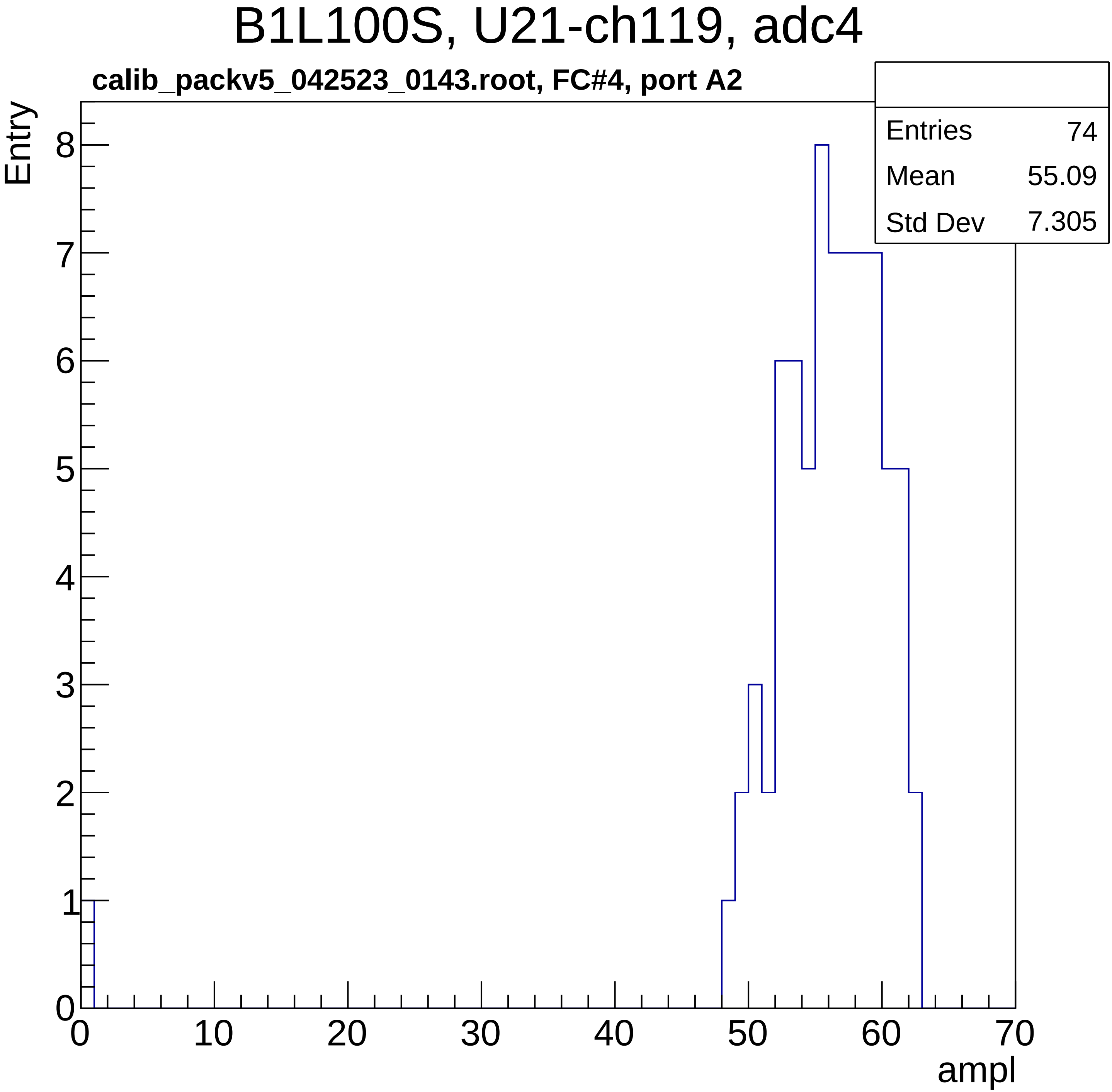

B1L100S, U21-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 30.57
Std Dev 3.811
Gaus mean : 31.3395
Gaus Width: 4.0024
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

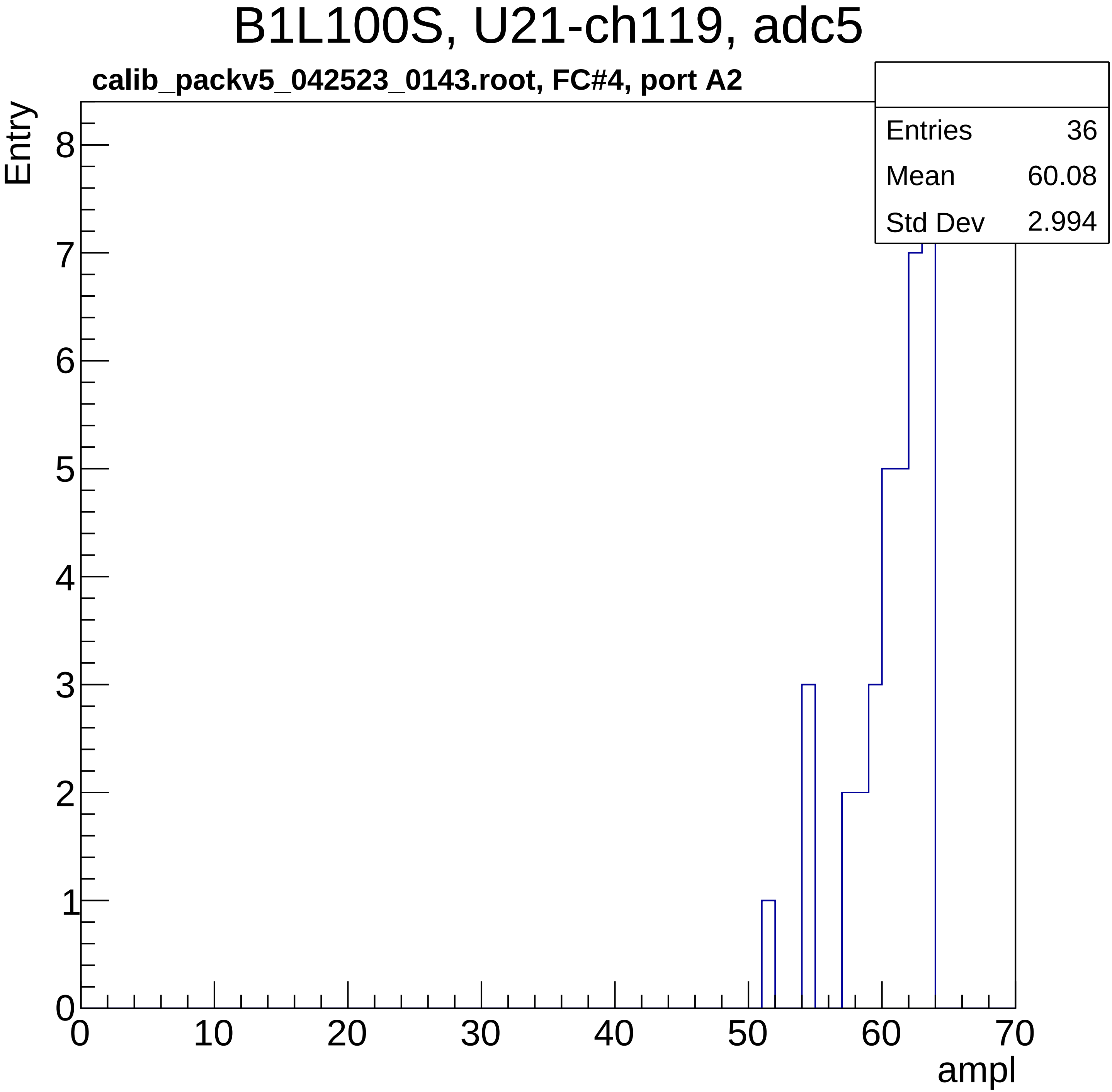

B1L100S, U21-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 57
Mean 37.6
Std Dev 3.308
Gaus mean : 37.9457
Gaus Width: 3.1106
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

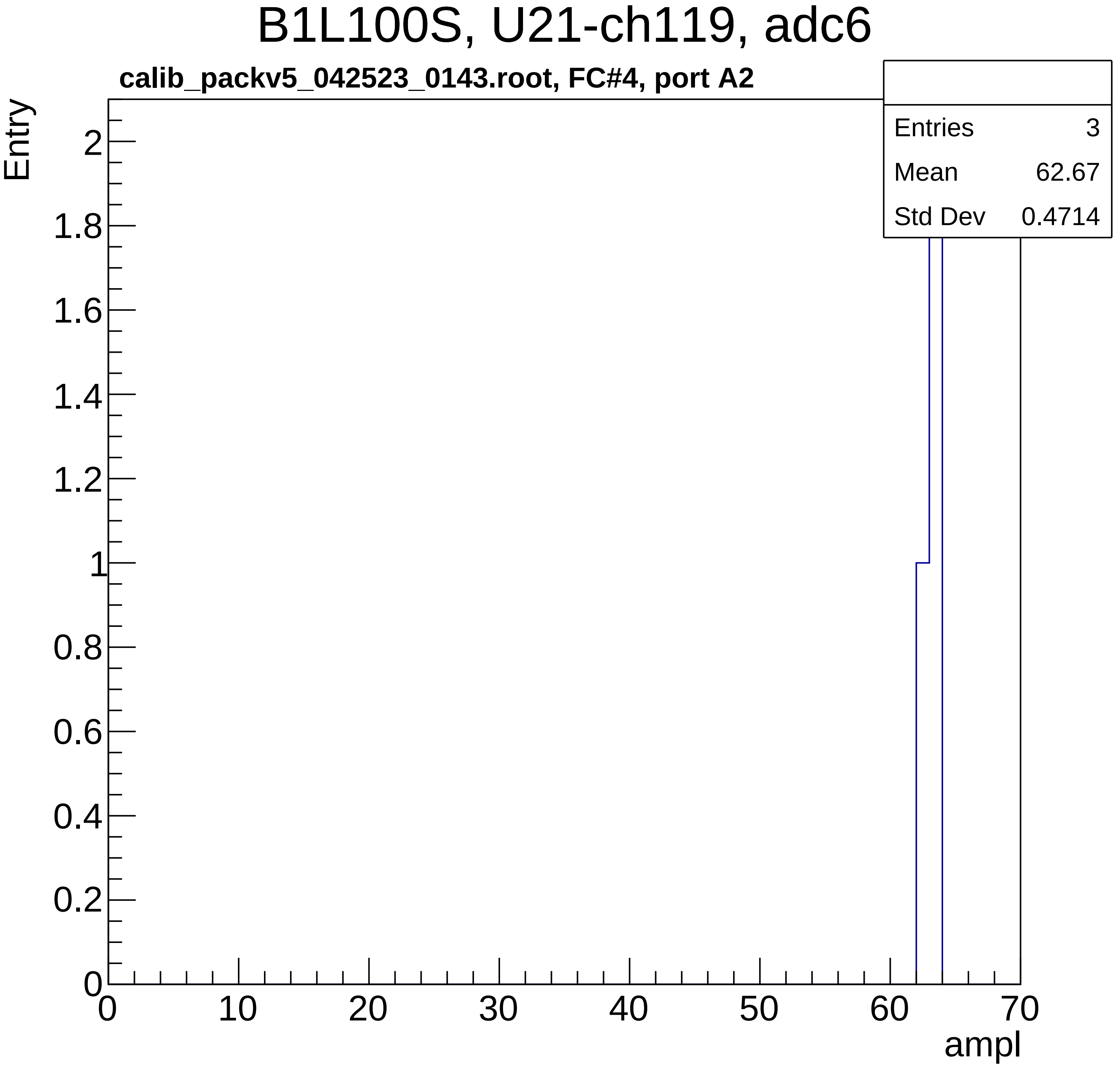

B1L100S, U21-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.42
Std Dev 3.462
Gaus mean : 44.5451
Gaus Width: 2.8388
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

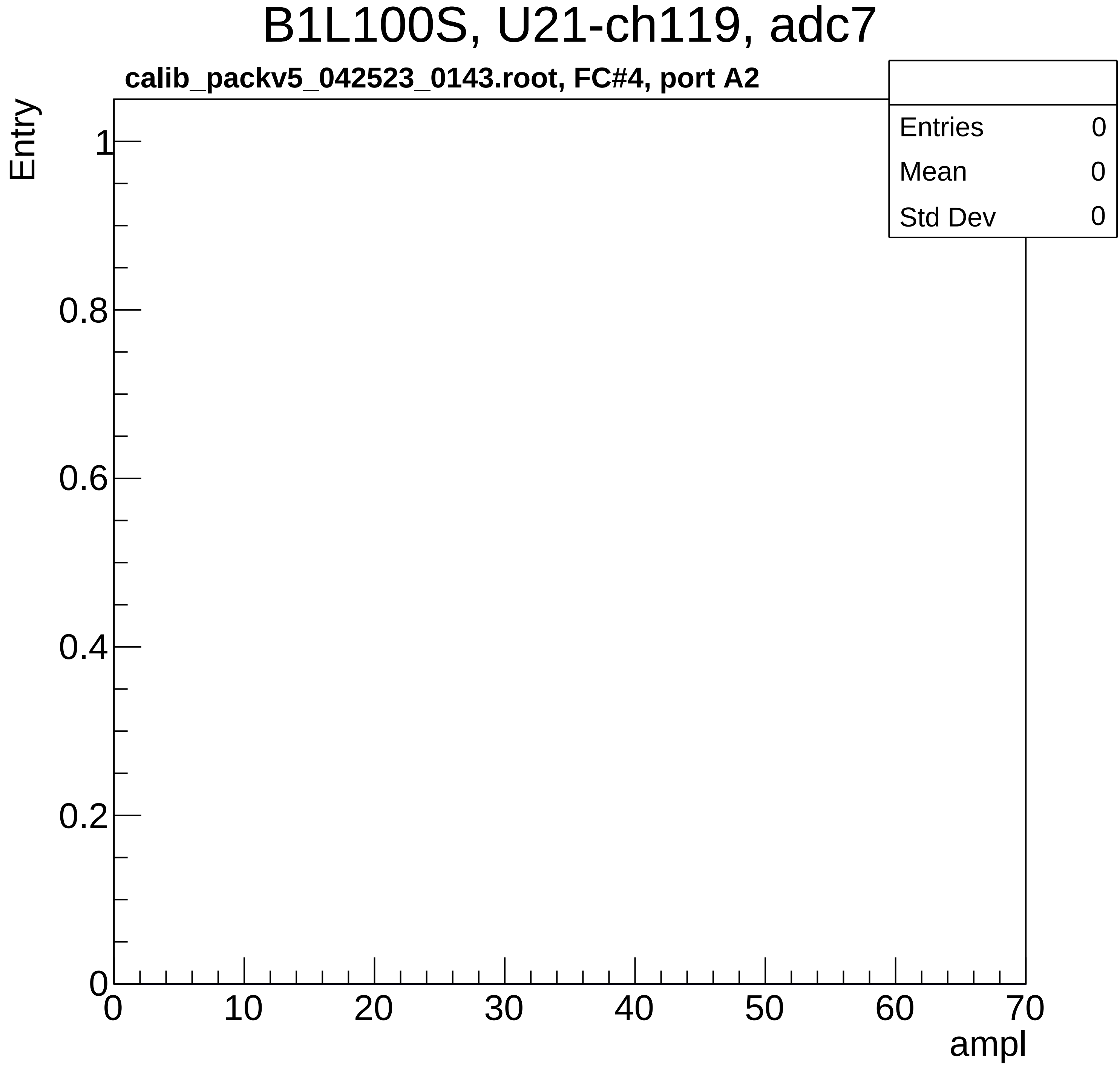
B1L100S, U21-ch119, adc3

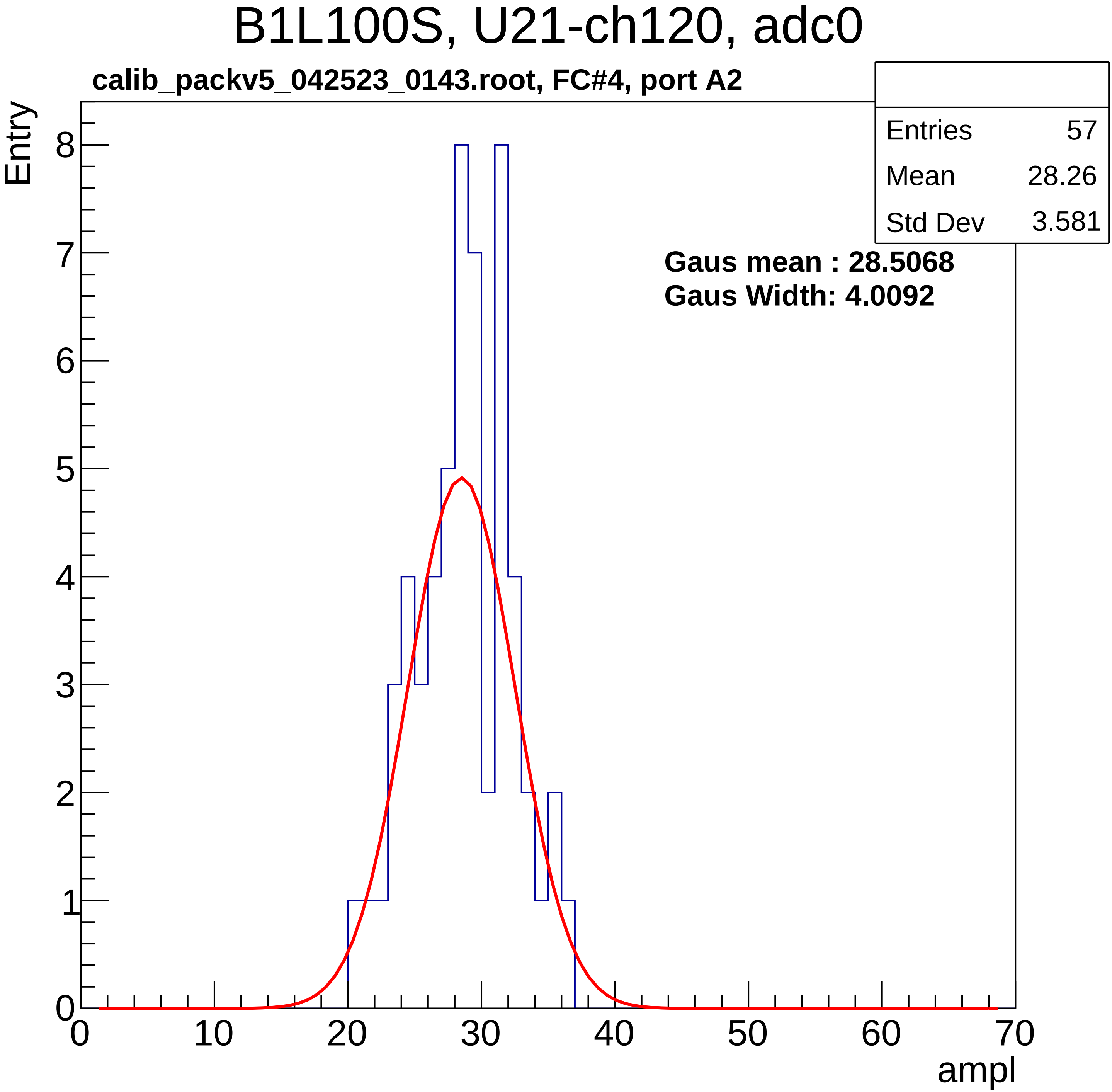


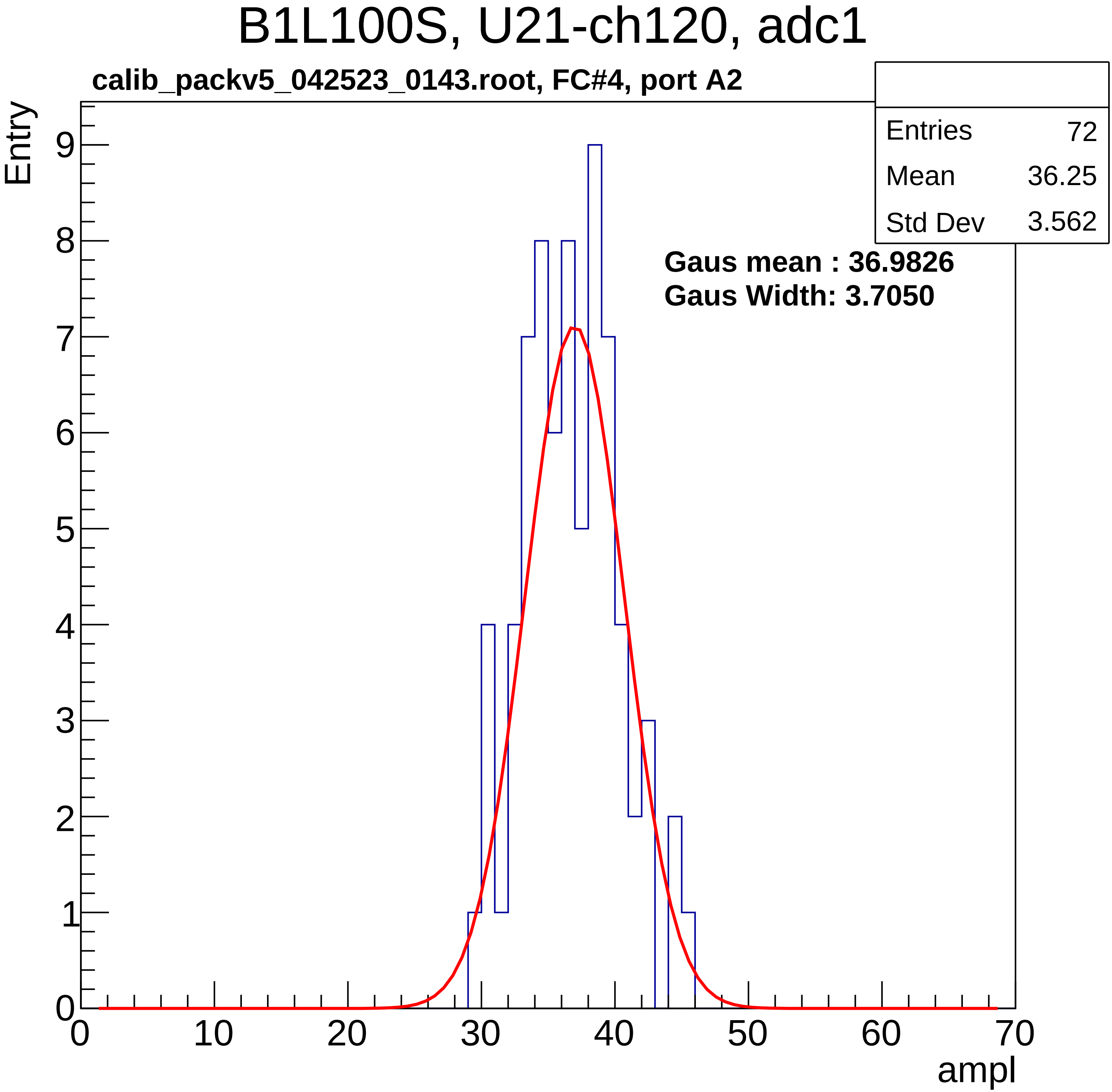

B1L100S, U21-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 55.09
Std Dev 7.305
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.08
Std Dev 2.994
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

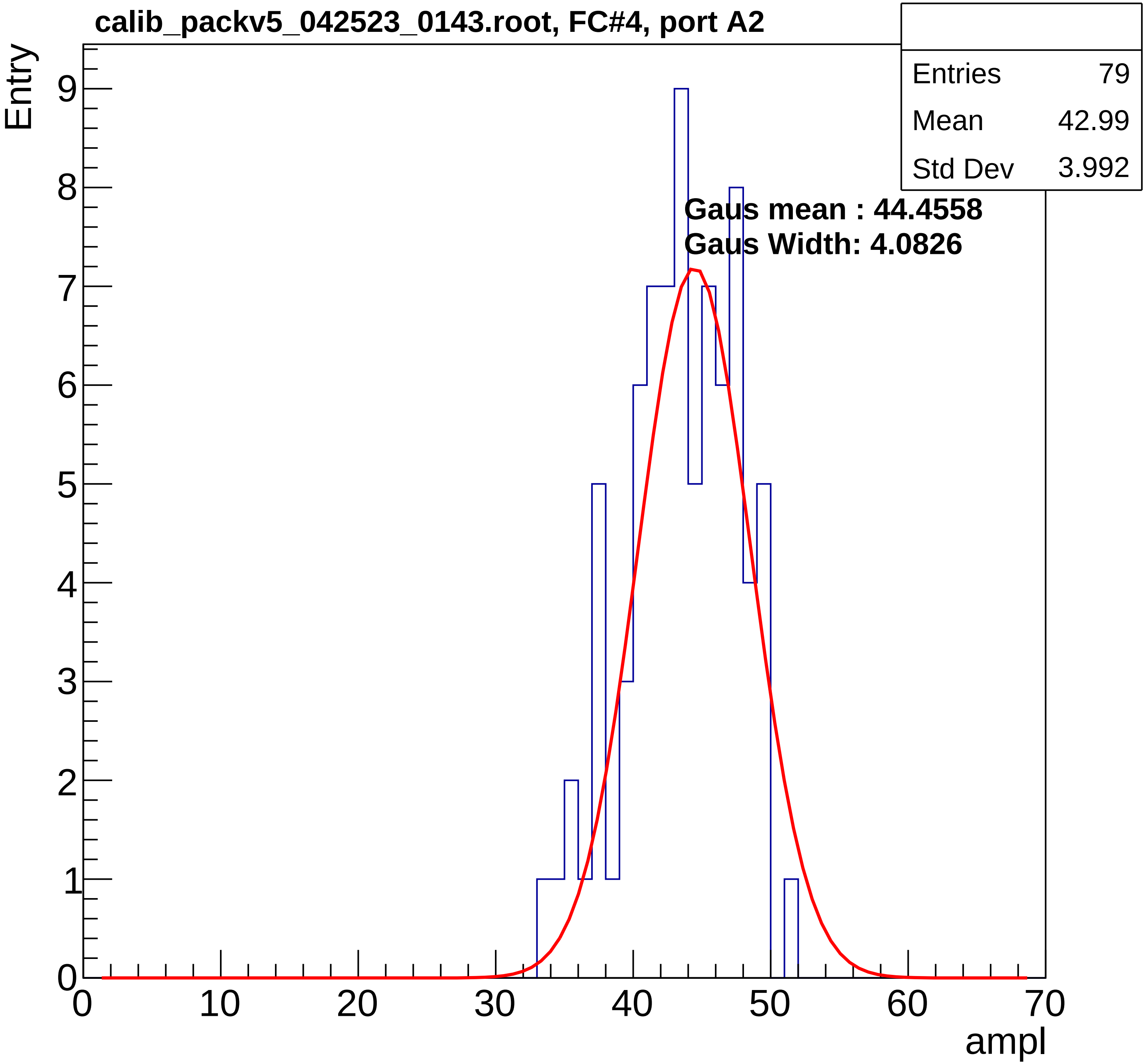

B1L100S, U21-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

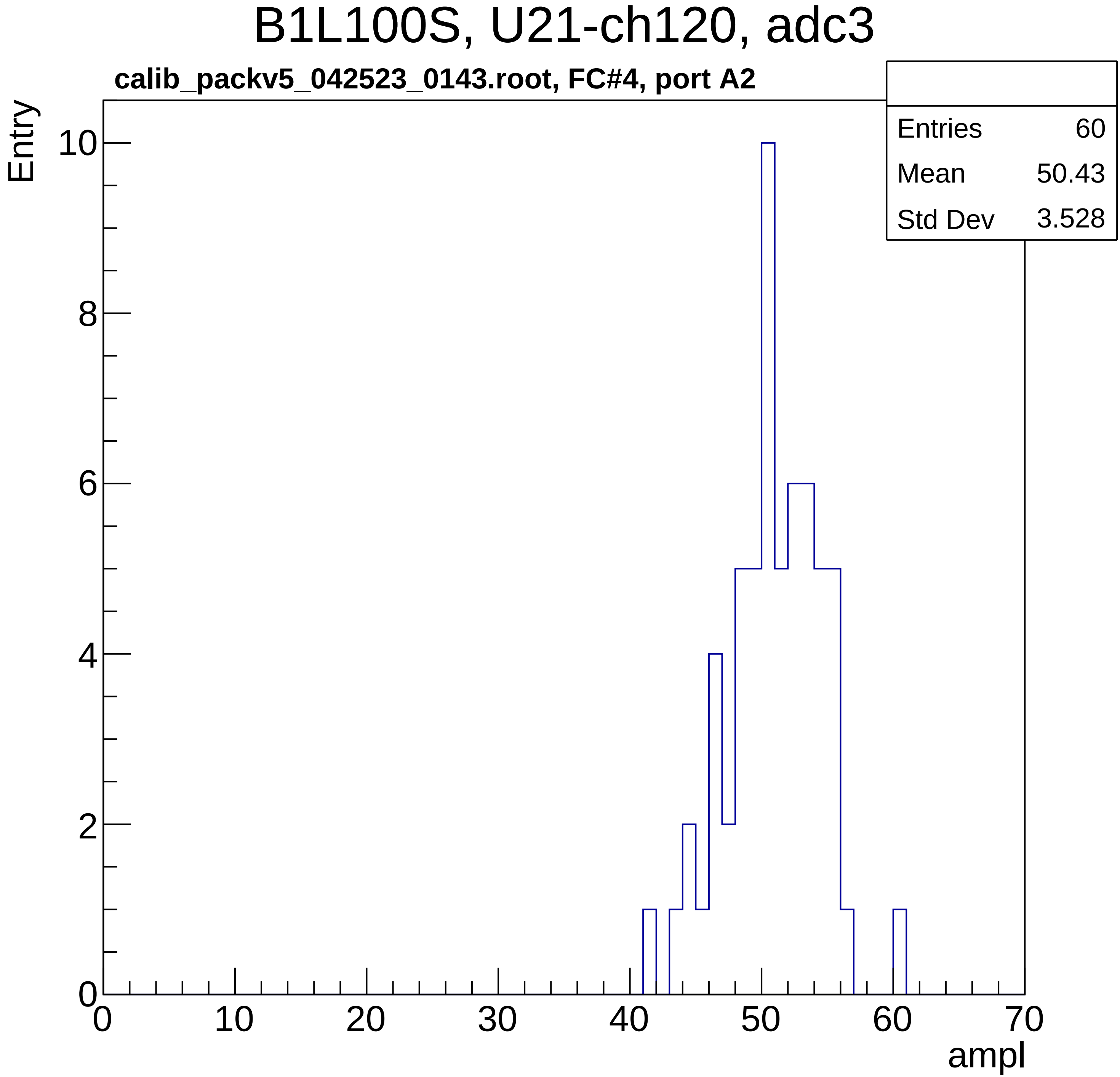

B1L100S, U21-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

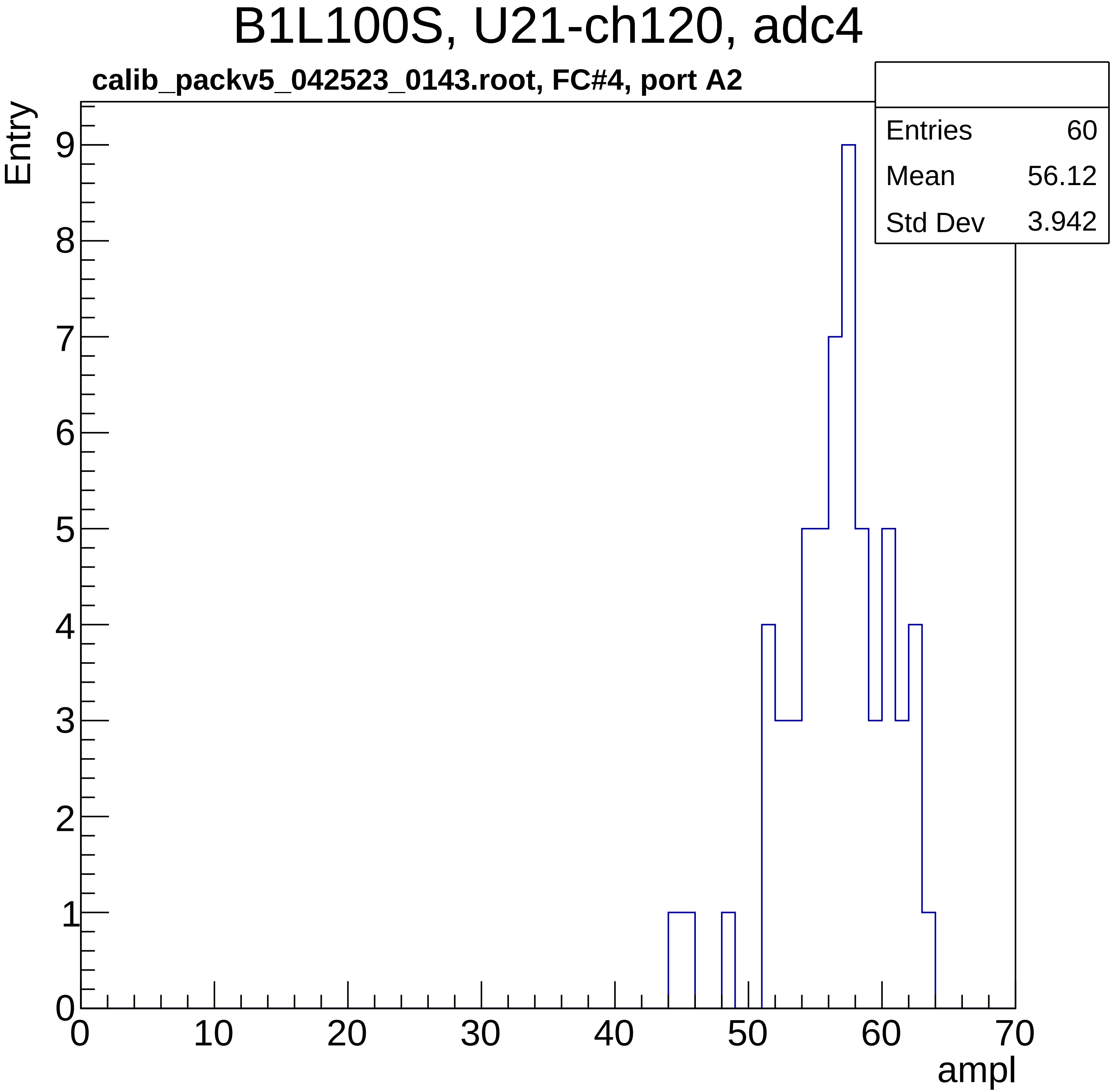

B1L100S, U21-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 57
Mean 28.26
Std Dev 3.581
Gaus mean : 28.5068
Gaus Width: 4.0092
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

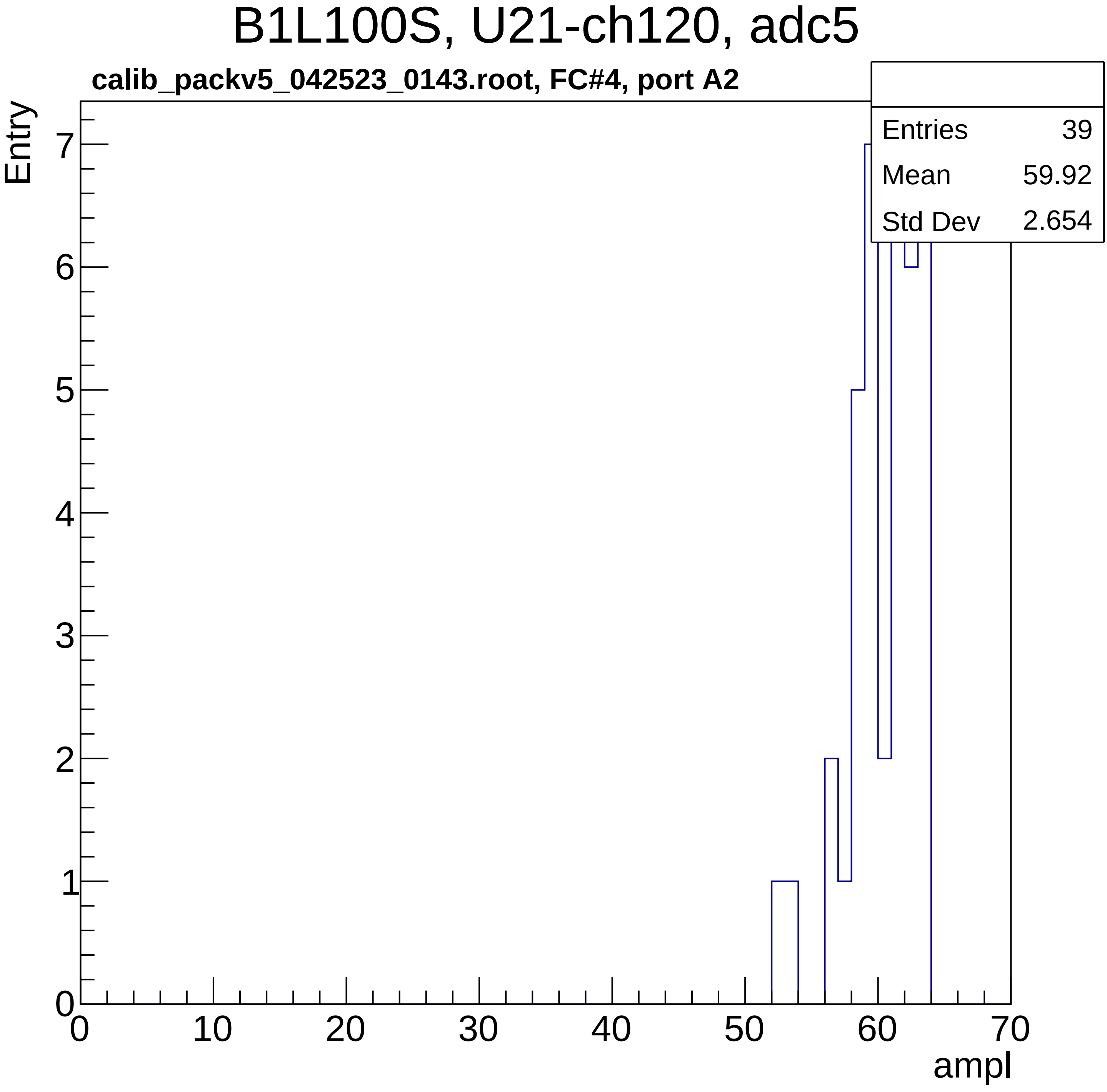

B1L100S, U21-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.25
Std Dev 3.562
Gaus mean : 36.9826
Gaus Width: 3.7050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

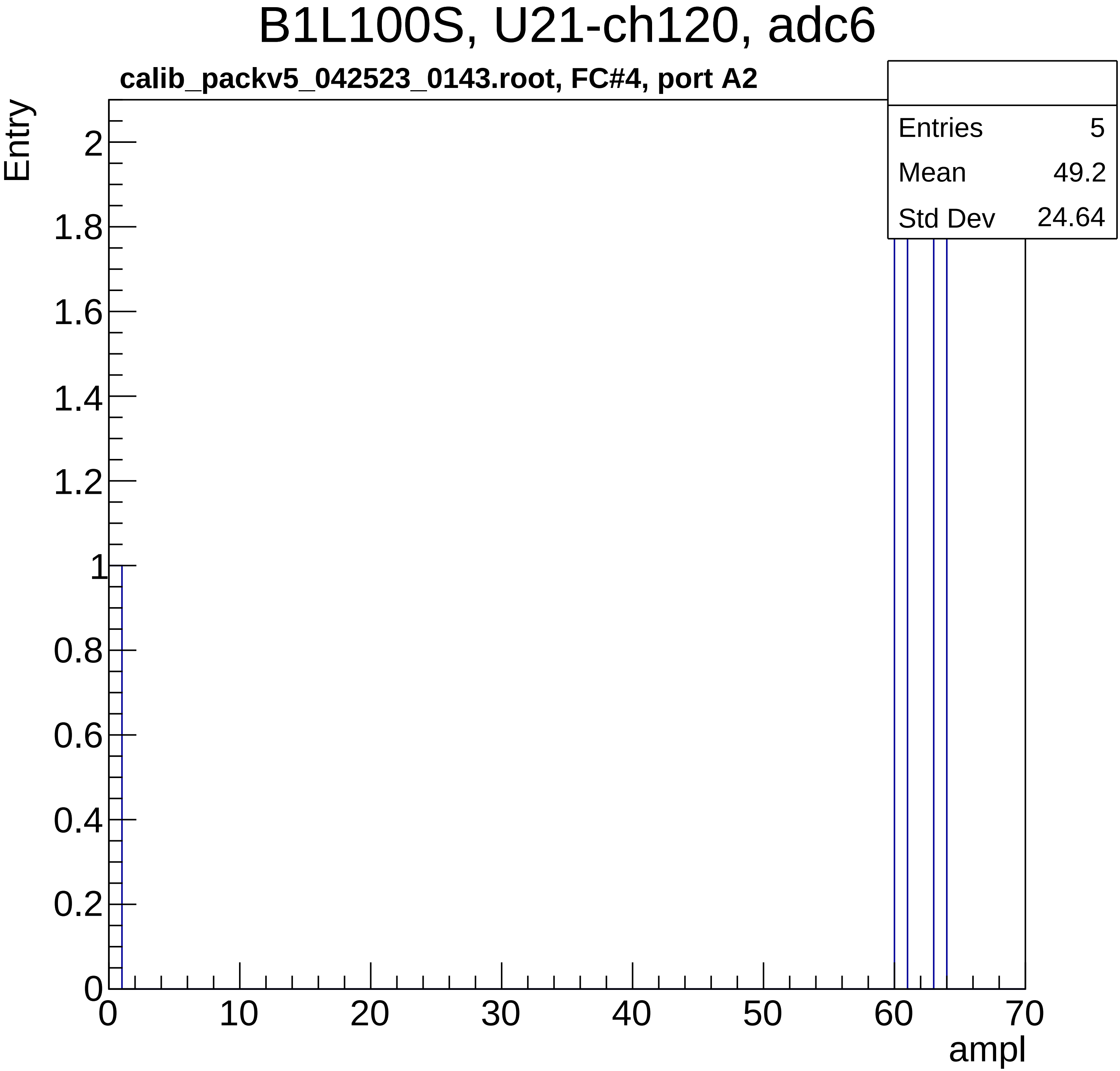
B1L100S, U21-ch120, adc2

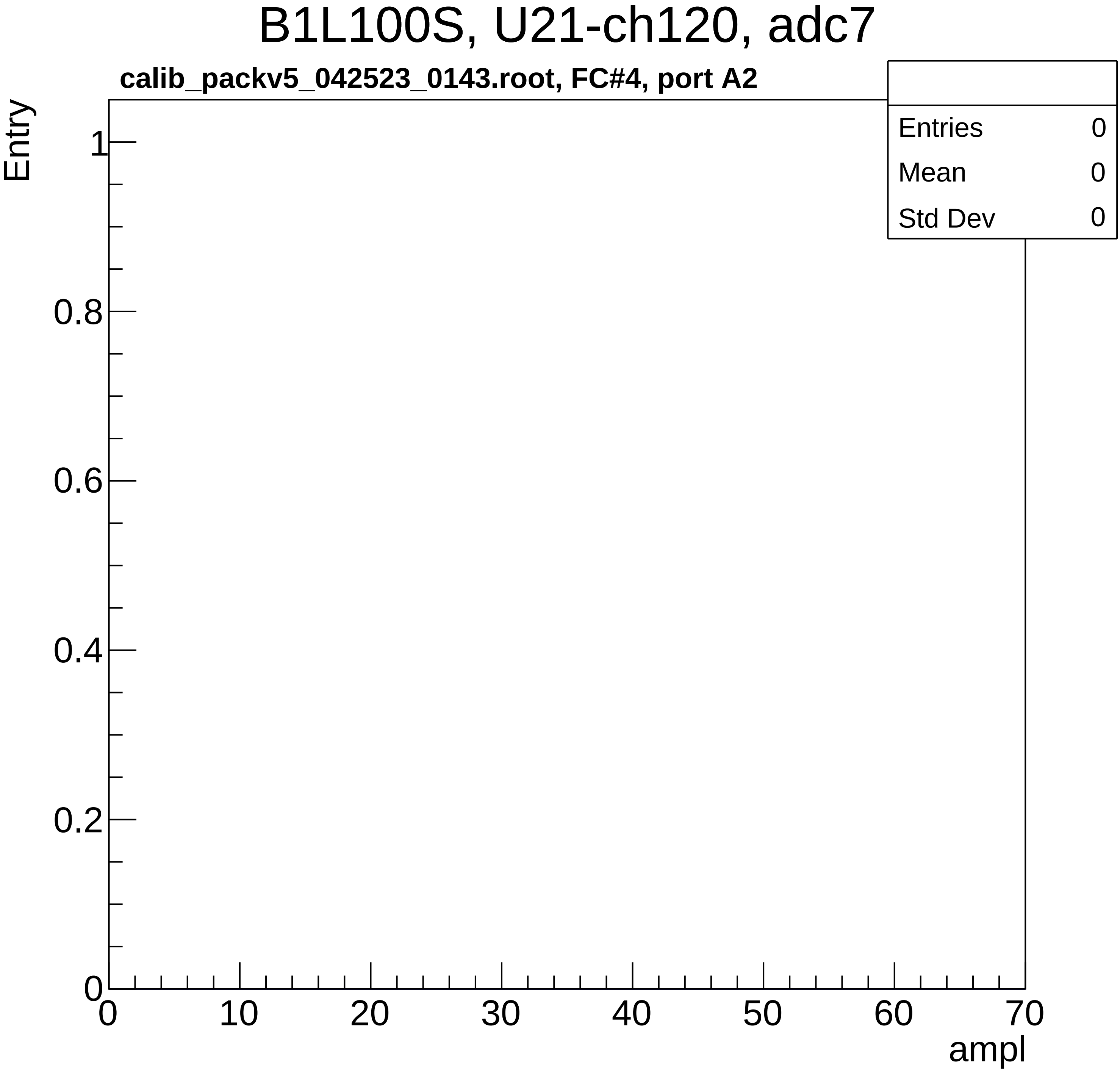


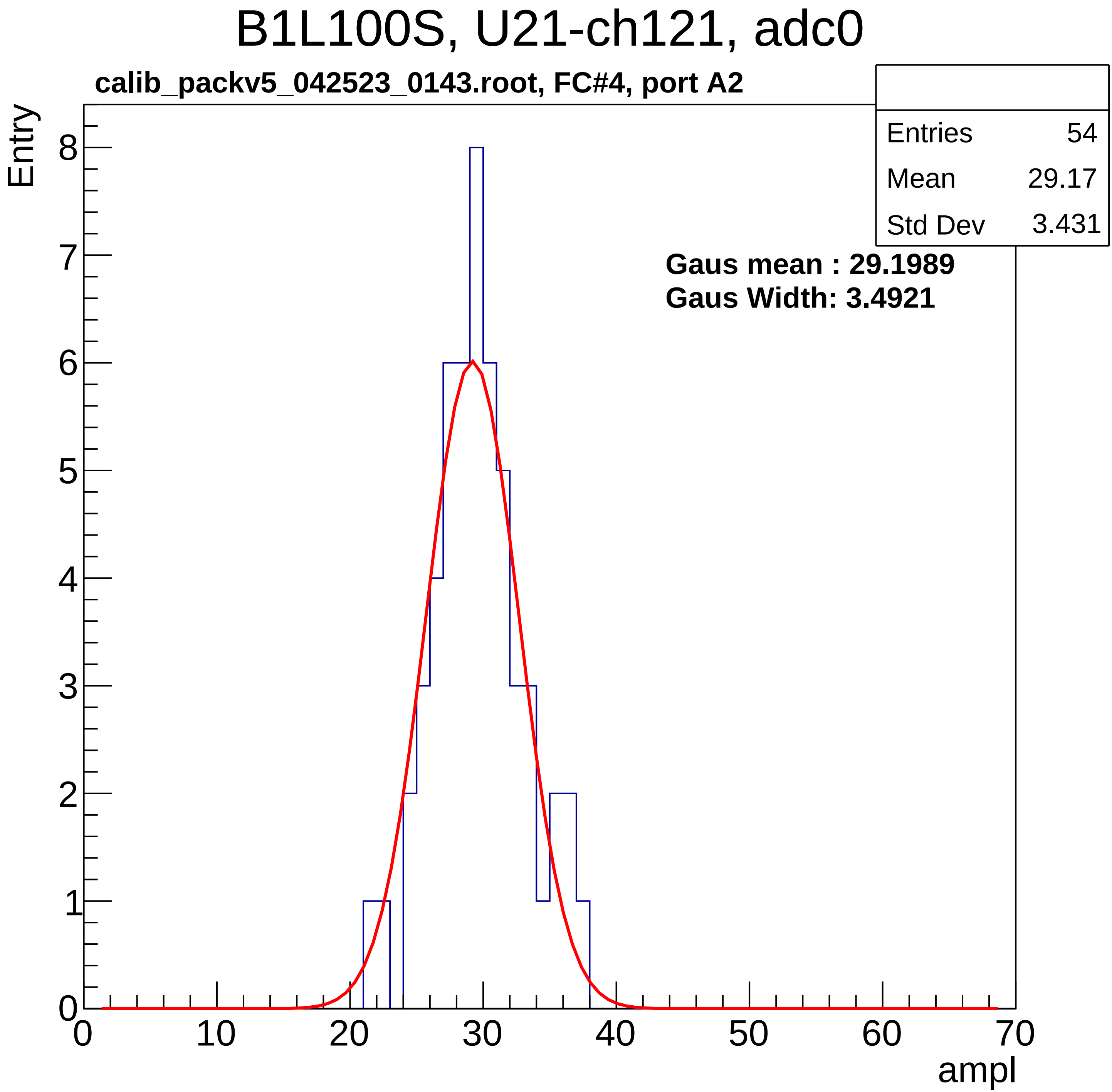

B1L100S, U21-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 50.43
Std Dev 3.528
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

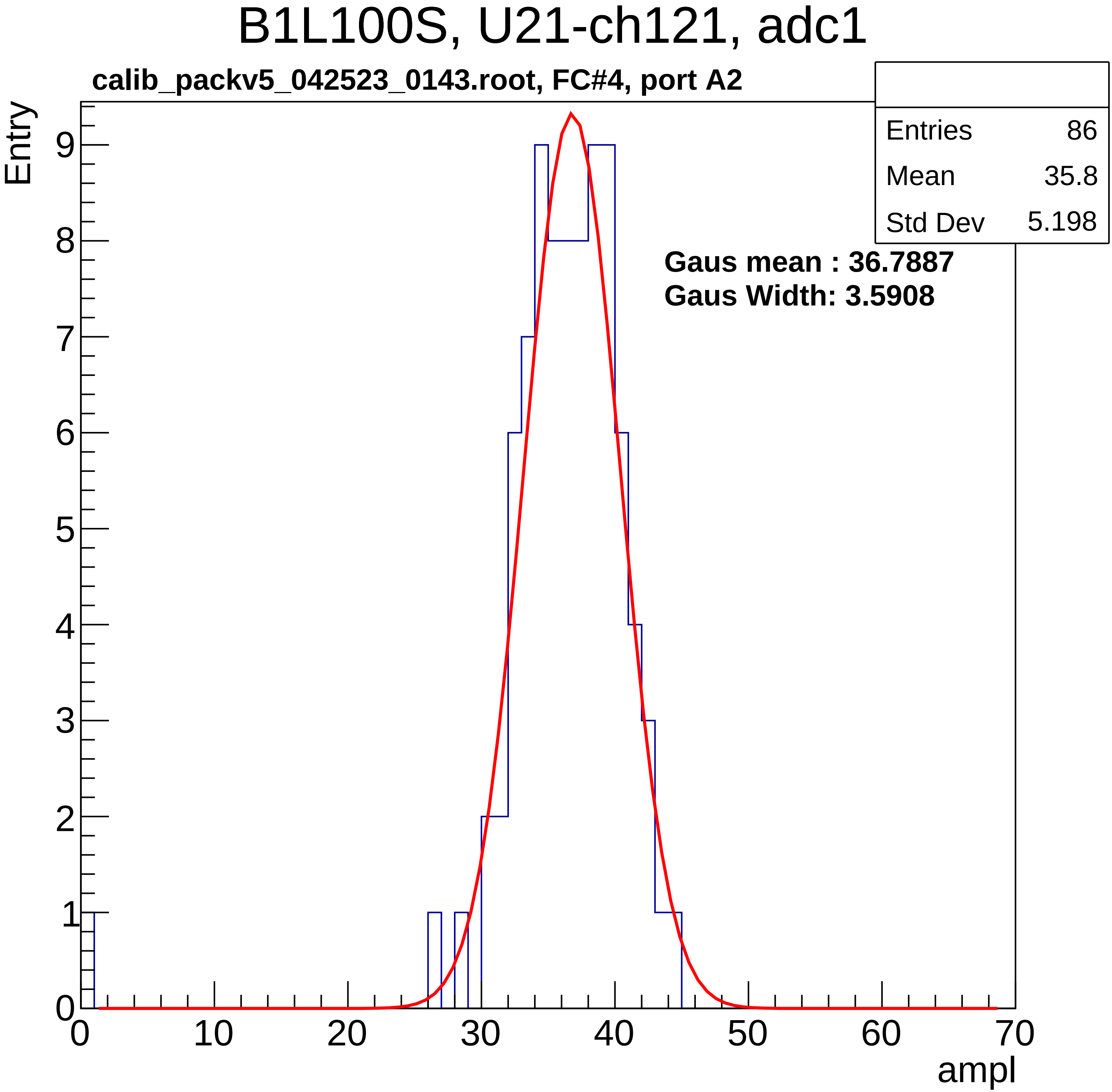

B1L100S, U21-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.12
Std Dev 3.942
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.92
Std Dev 2.654
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

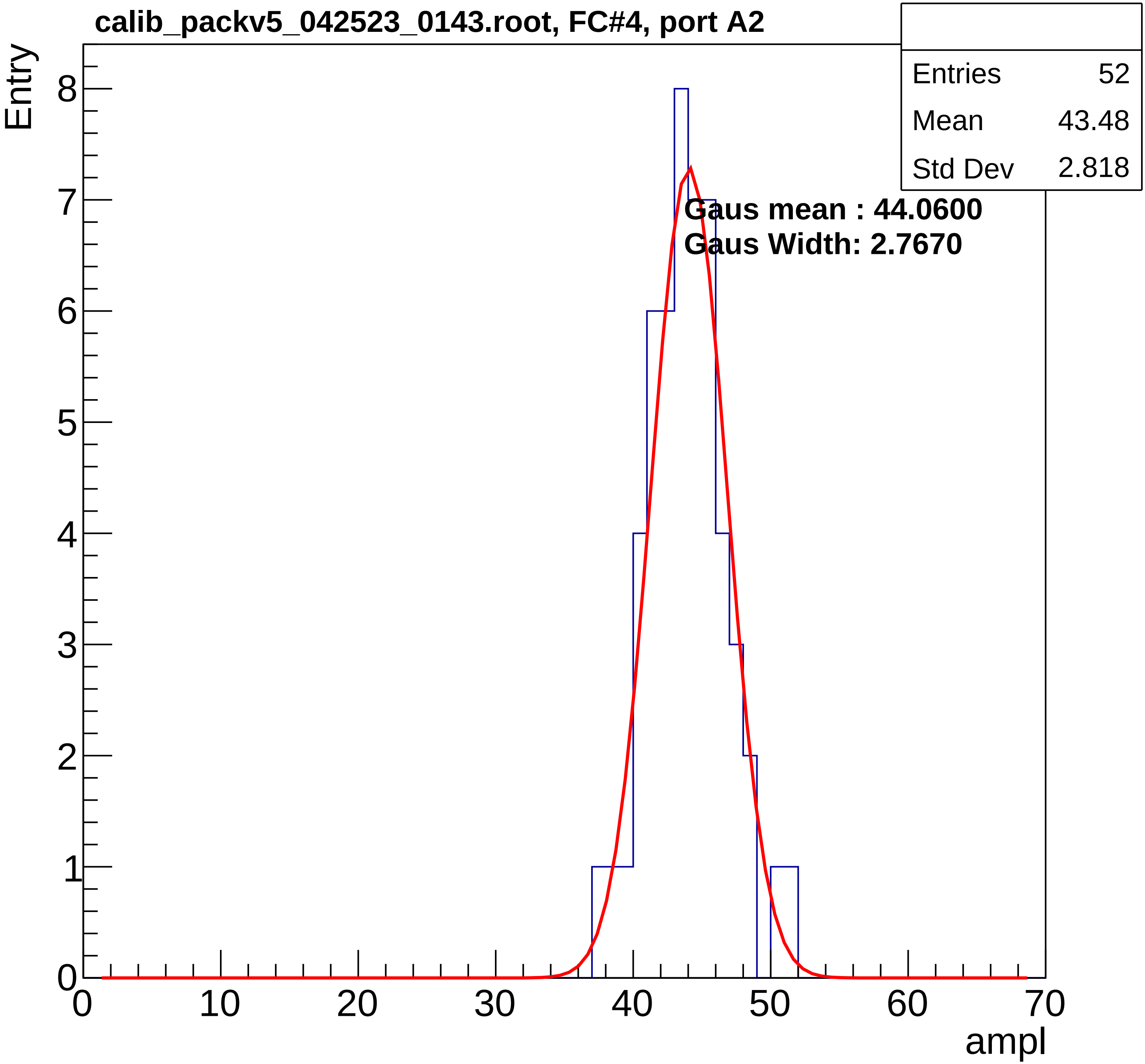

B1L100S, U21-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 5
Mean 49.2
Std Dev 24.64
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

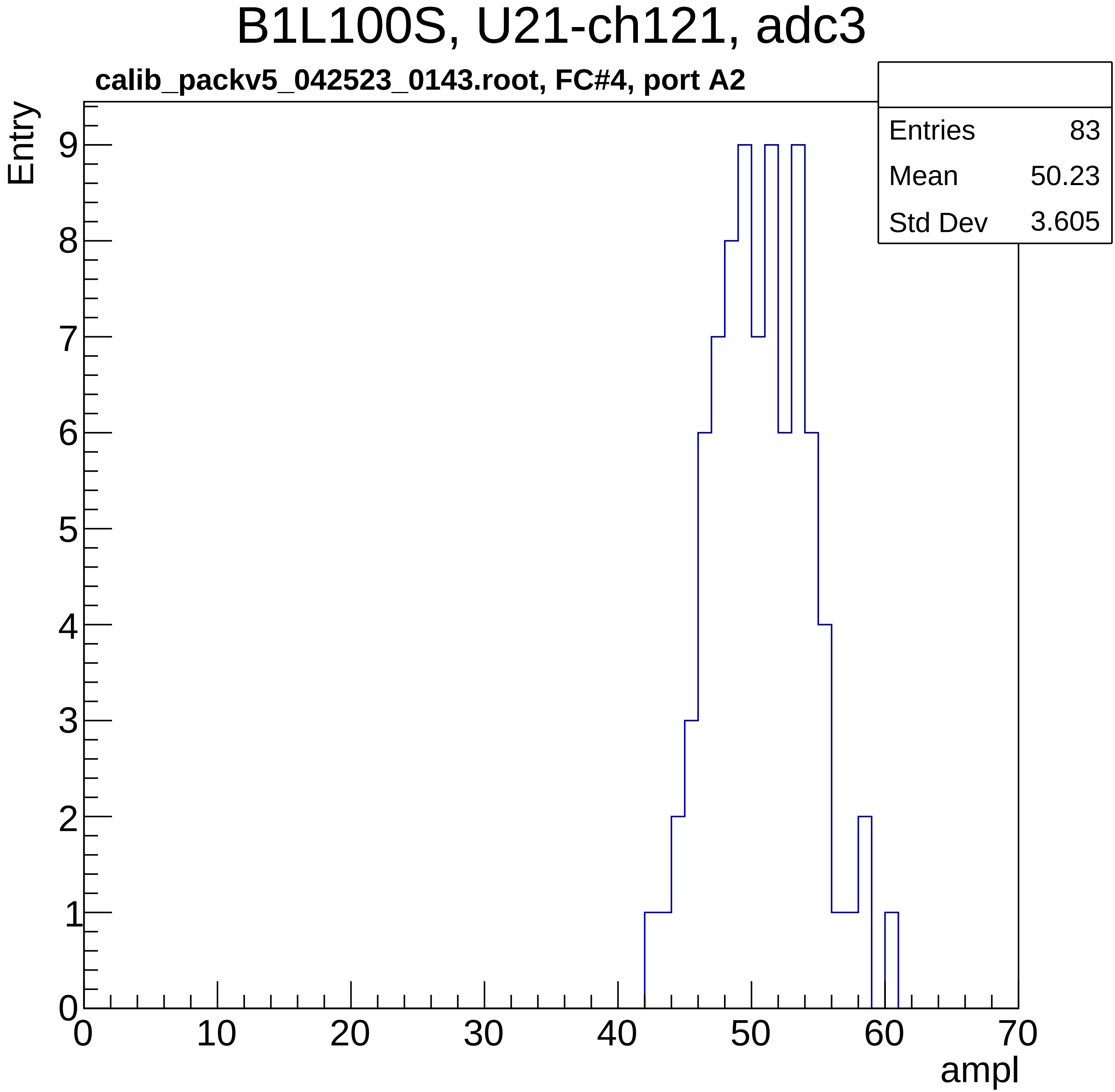

B1L100S, U21-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

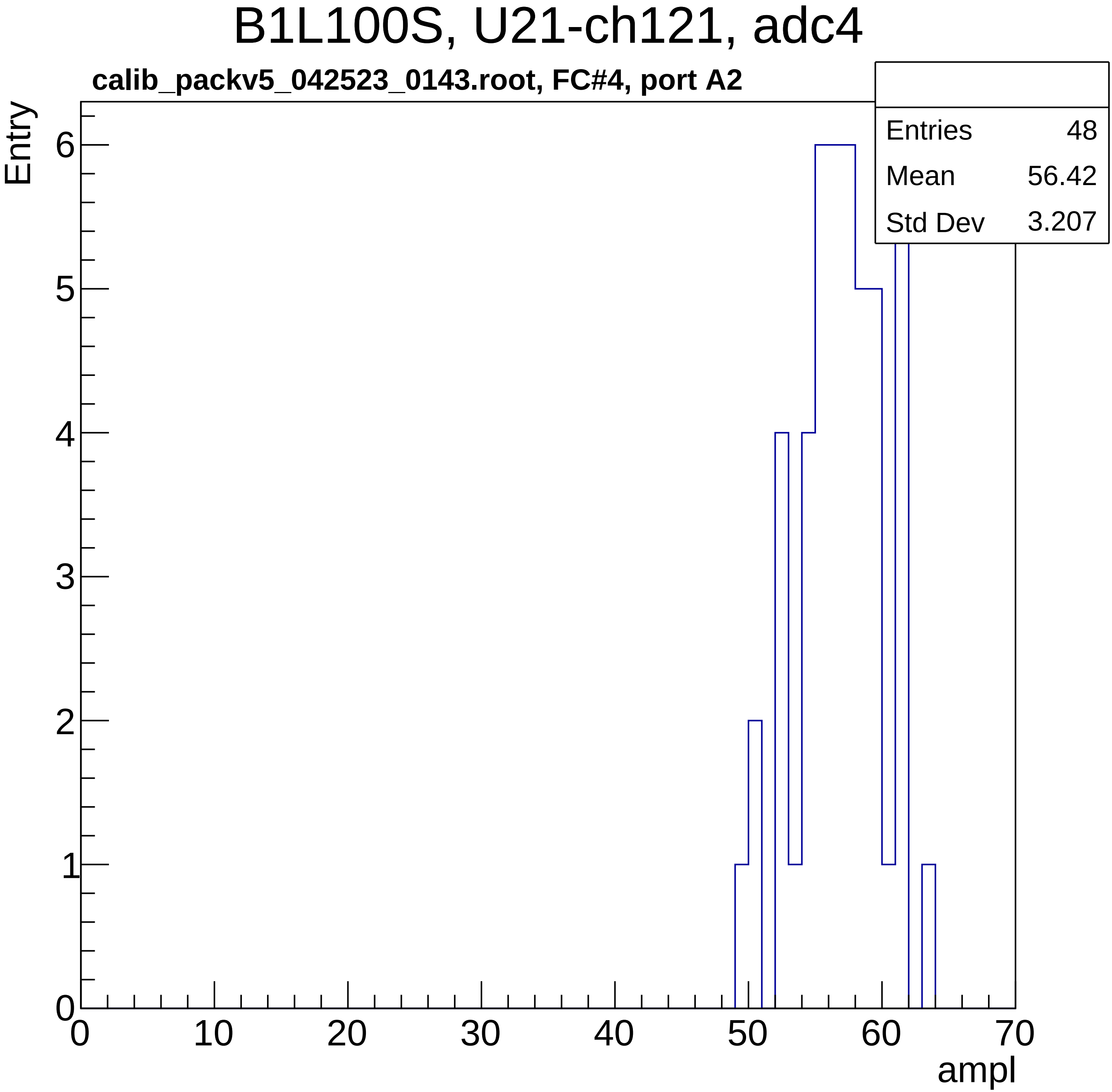

B1L100S, U21-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 29.17
Std Dev 3.431
Gaus mean : 29.1989
Gaus Width: 3.4921
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 86
Mean 35.8
Std Dev 5.198
Gaus mean : 36.7887
Gaus Width: 3.5908
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

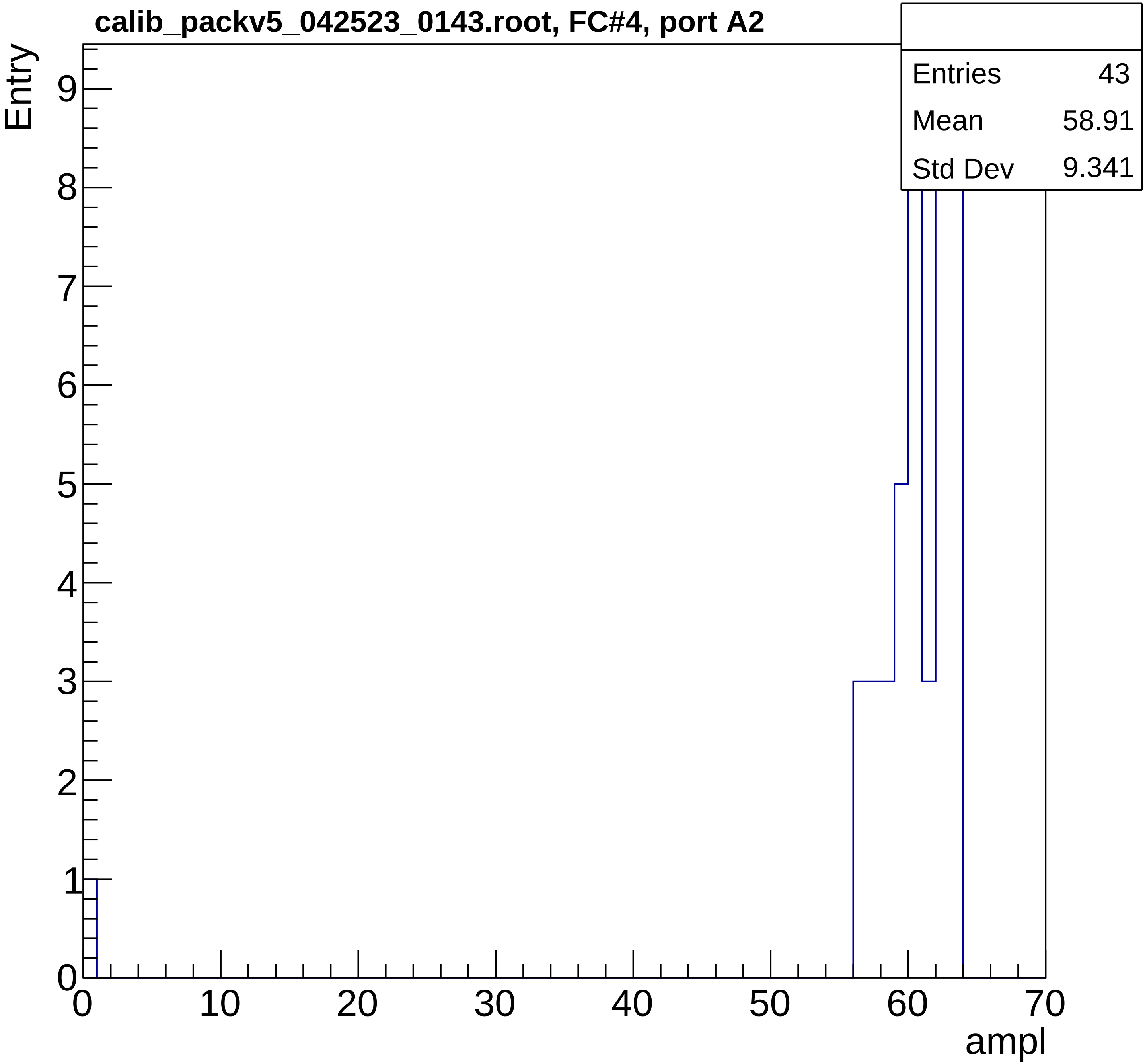
B1L100S, U21-ch121, adc2

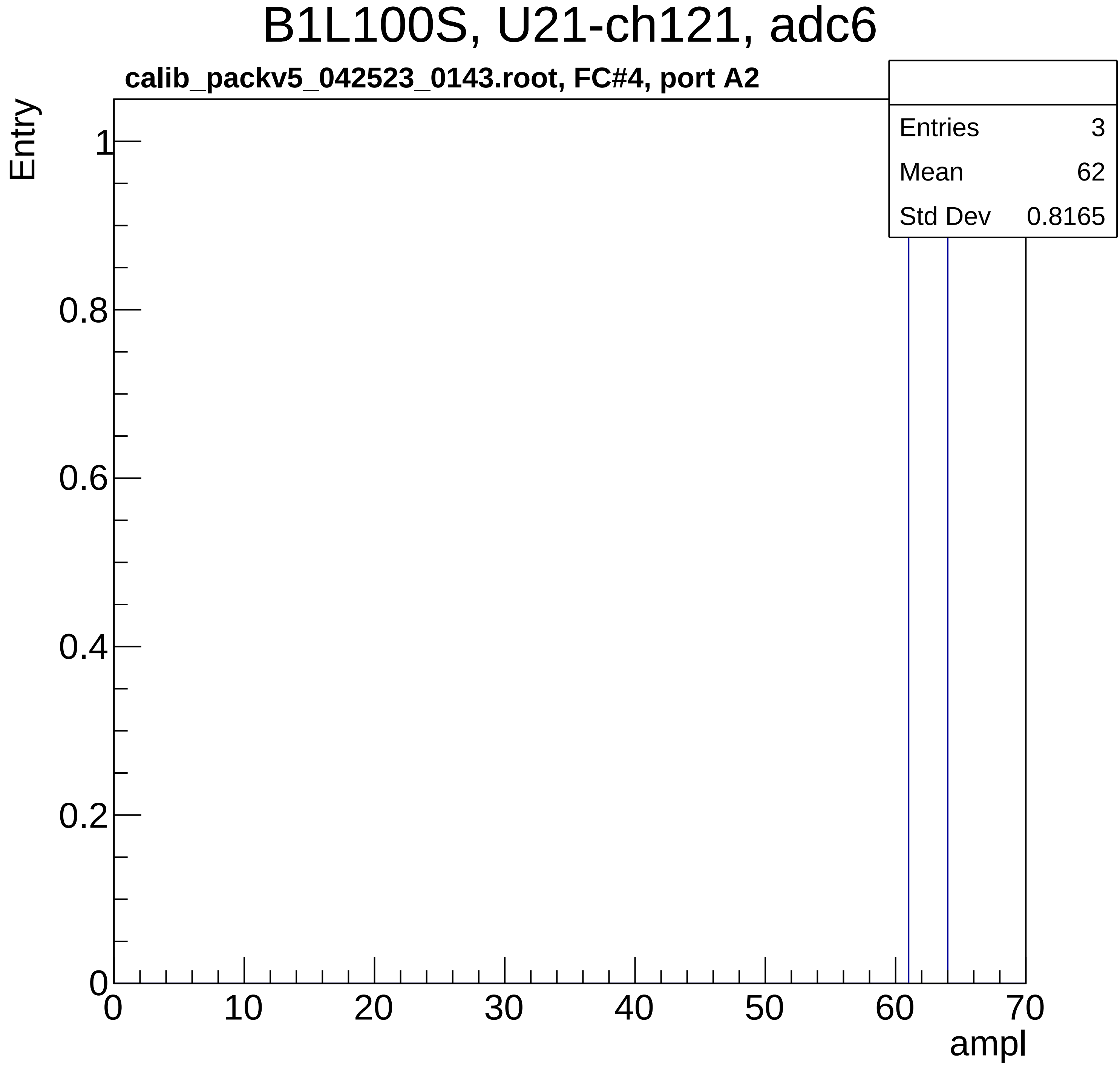


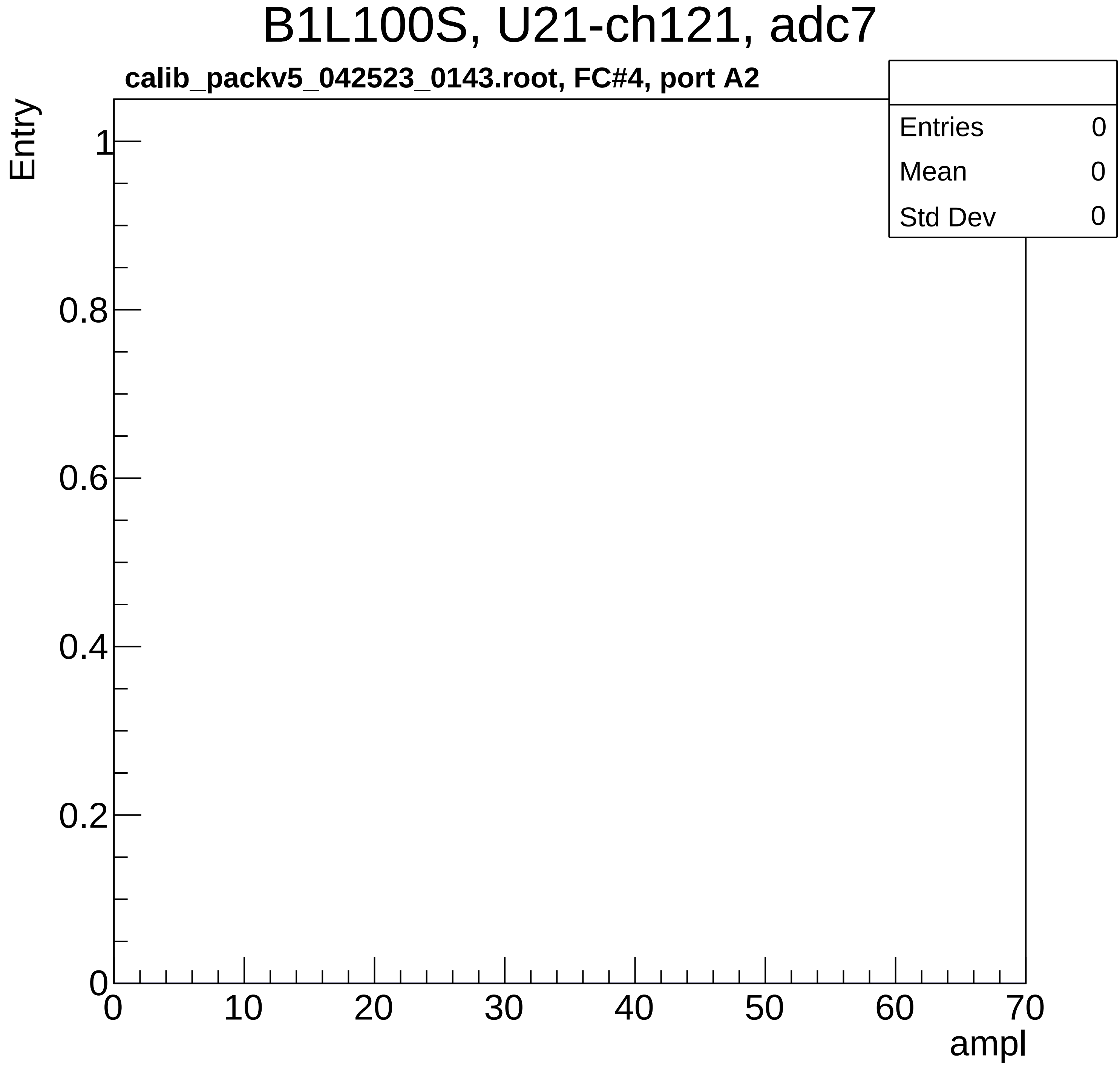

B1L100S, U21-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 83
Mean 50.23
Std Dev 3.605
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

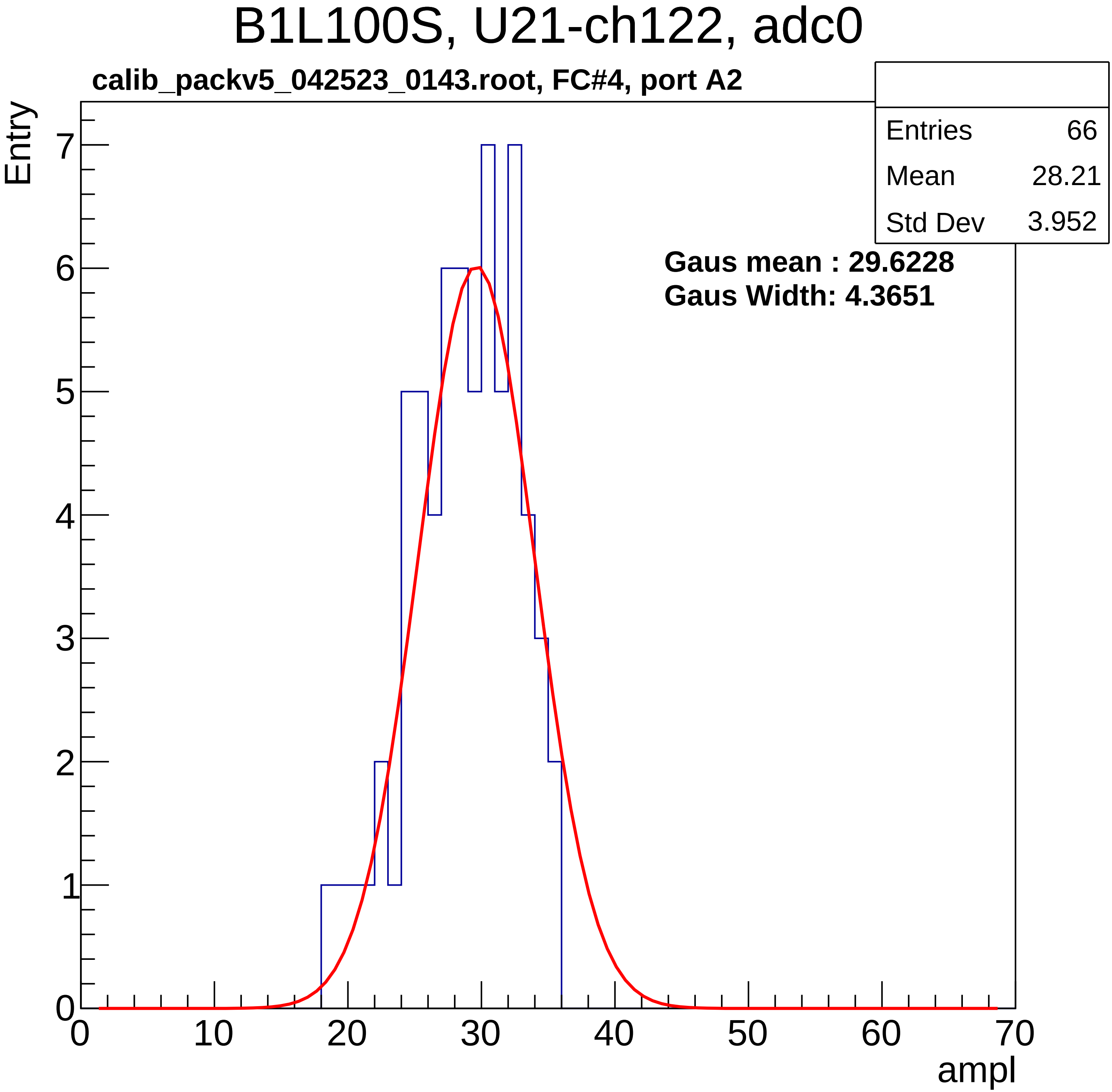

B1L100S, U21-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.42
Std Dev 3.207
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

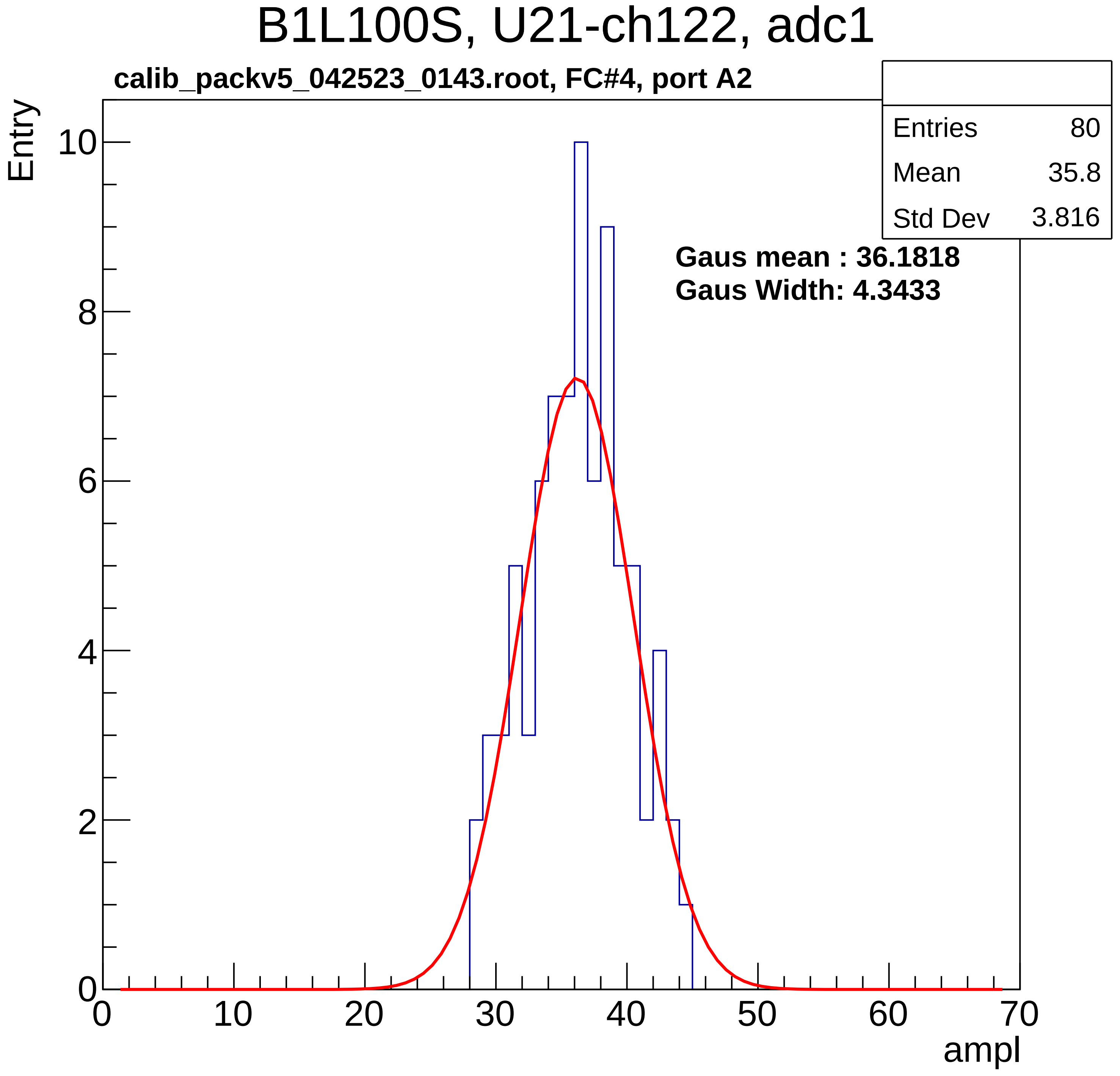
B1L100S, U21-ch121, adc5

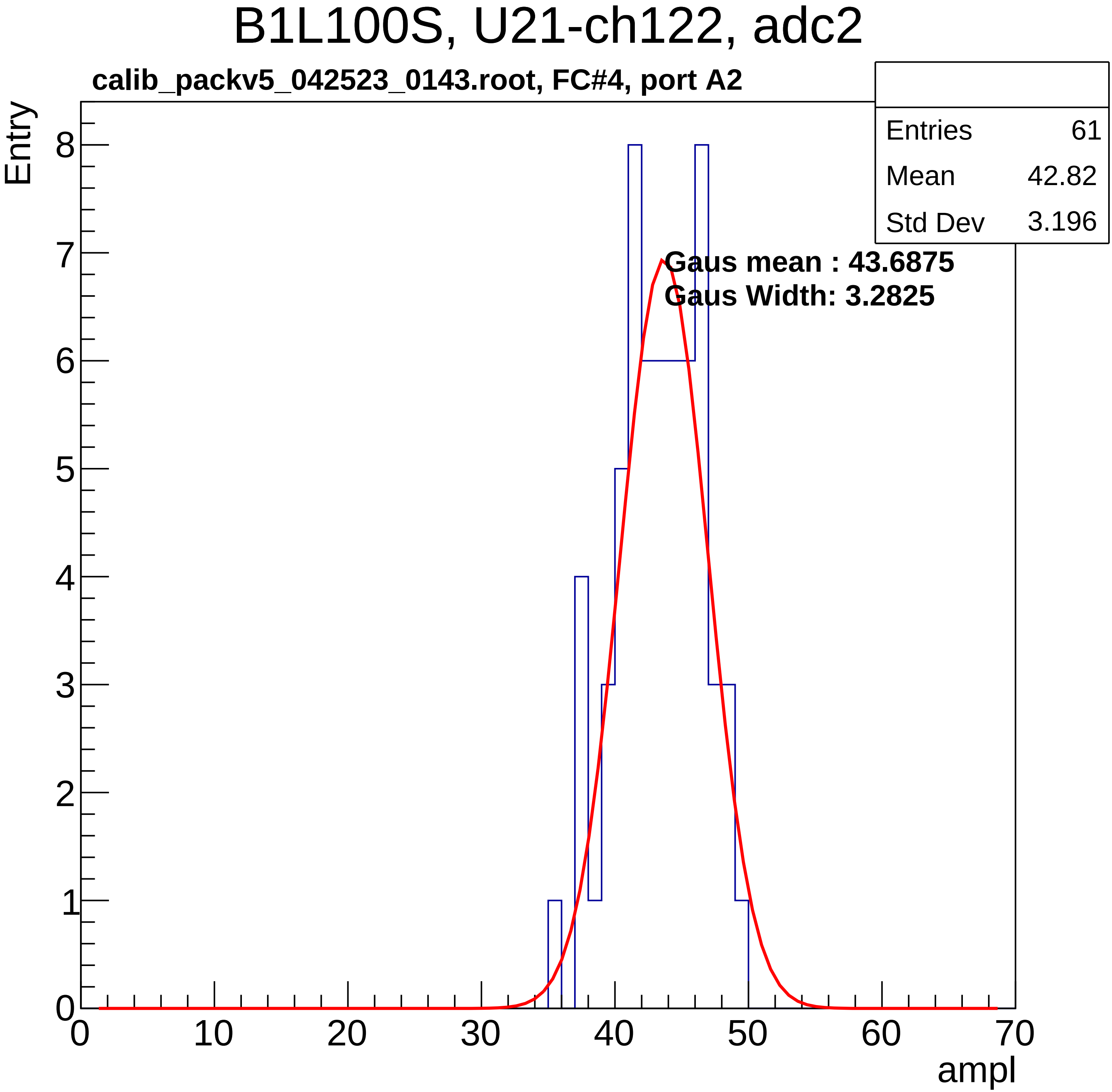


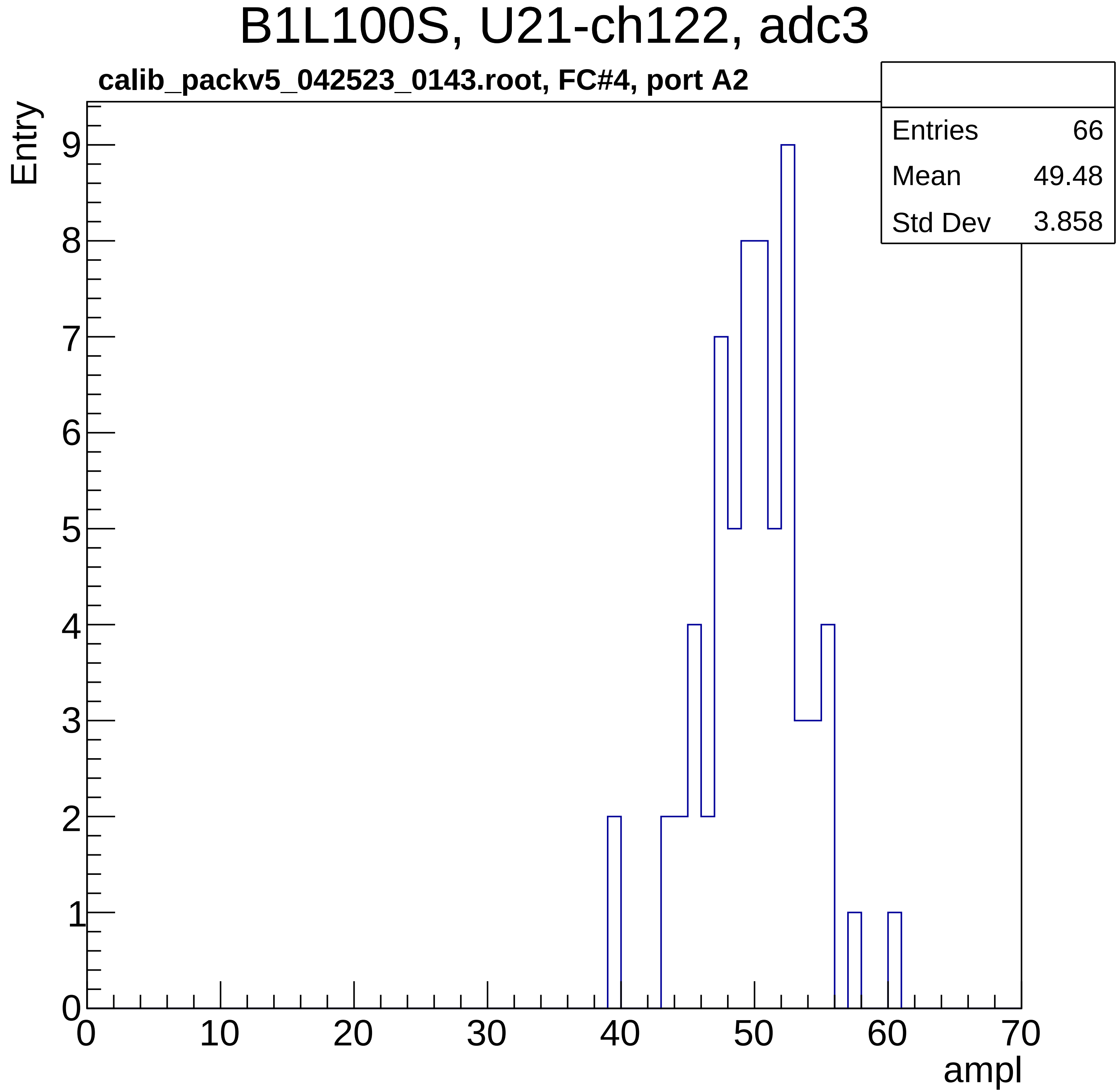

B1L100S, U21-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

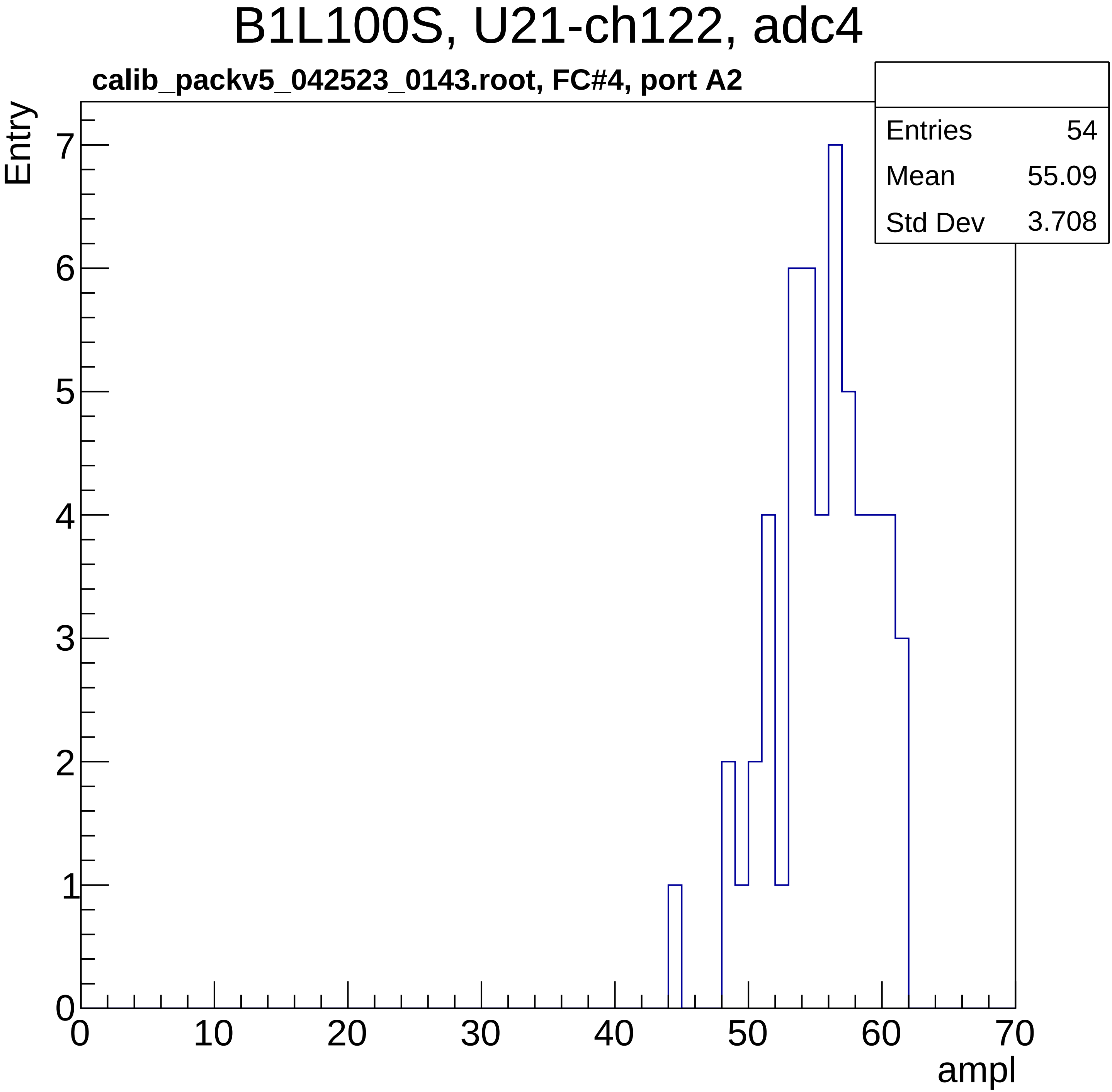

B1L100S, U21-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.21
Std Dev 3.952
Gaus mean : 29.6228
Gaus Width: 4.3651
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

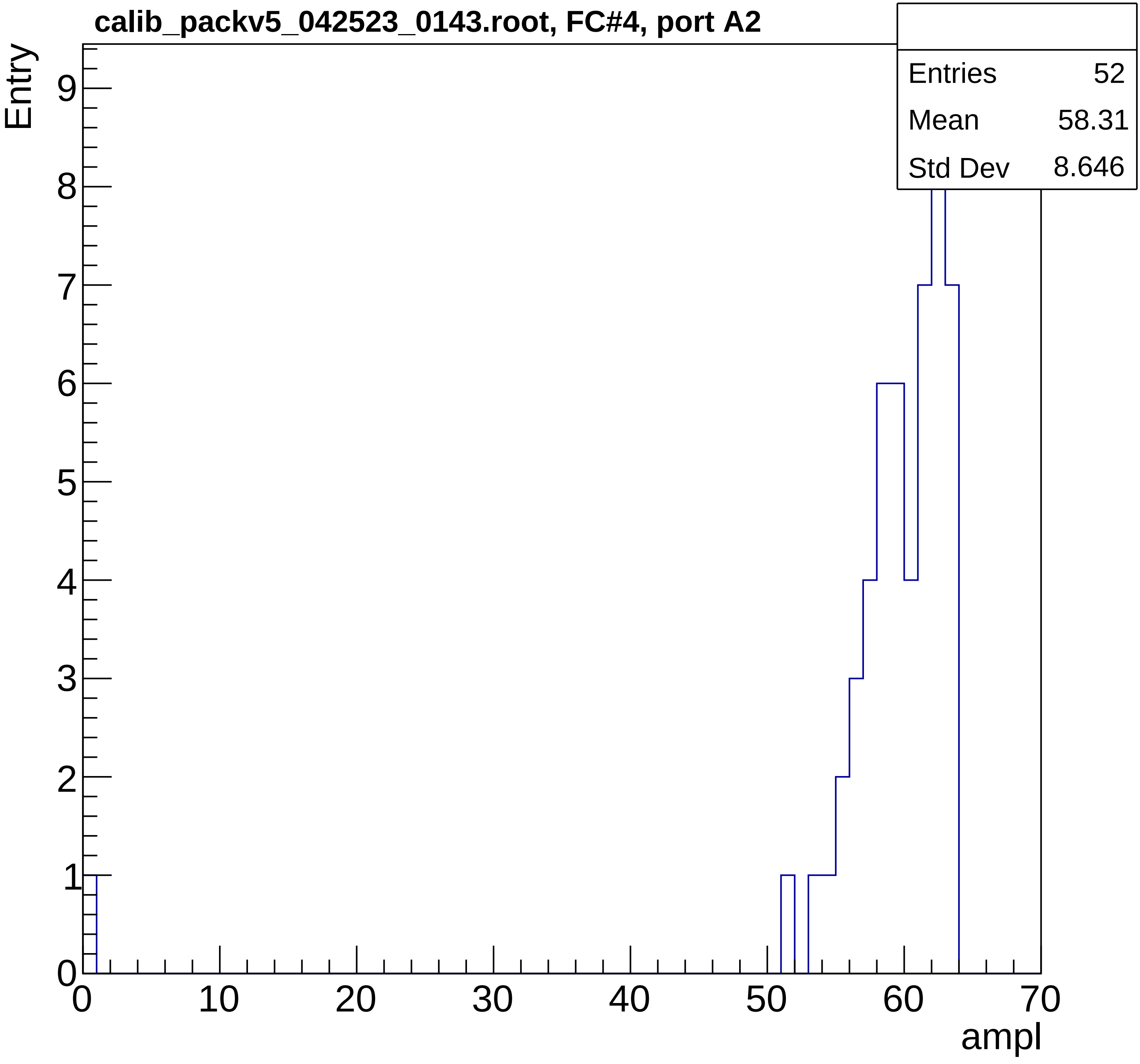

B1L100S, U21-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.8
Std Dev 3.816
Gaus mean : 36.1818
Gaus Width: 4.3433
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 61
Mean 42.82
Std Dev 3.196
Gaus mean : 43.6875
Gaus Width: 3.2825
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U21-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.48
Std Dev 3.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

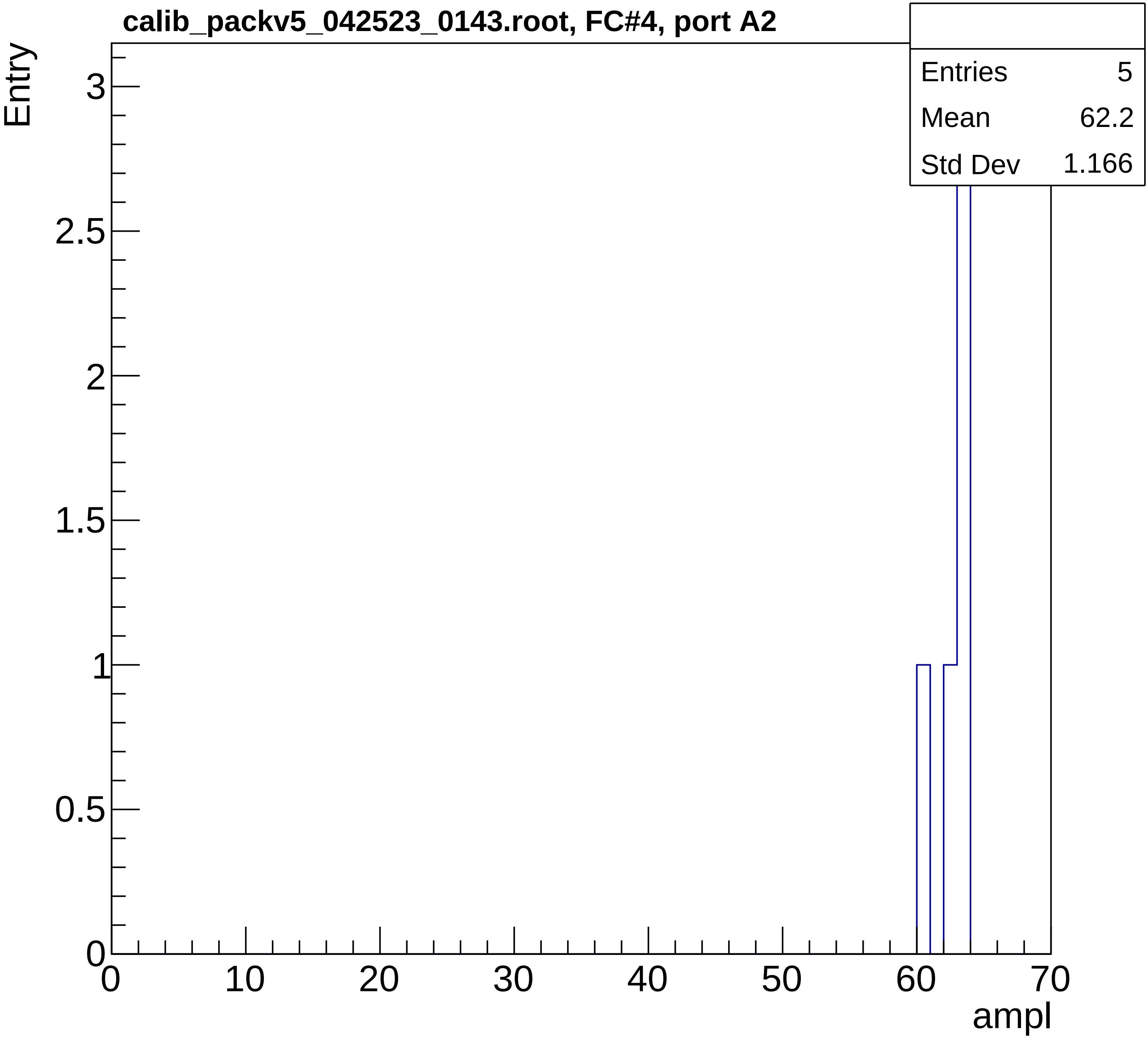

B1L100S, U21-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.09
Std Dev 3.708
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch122, adc5

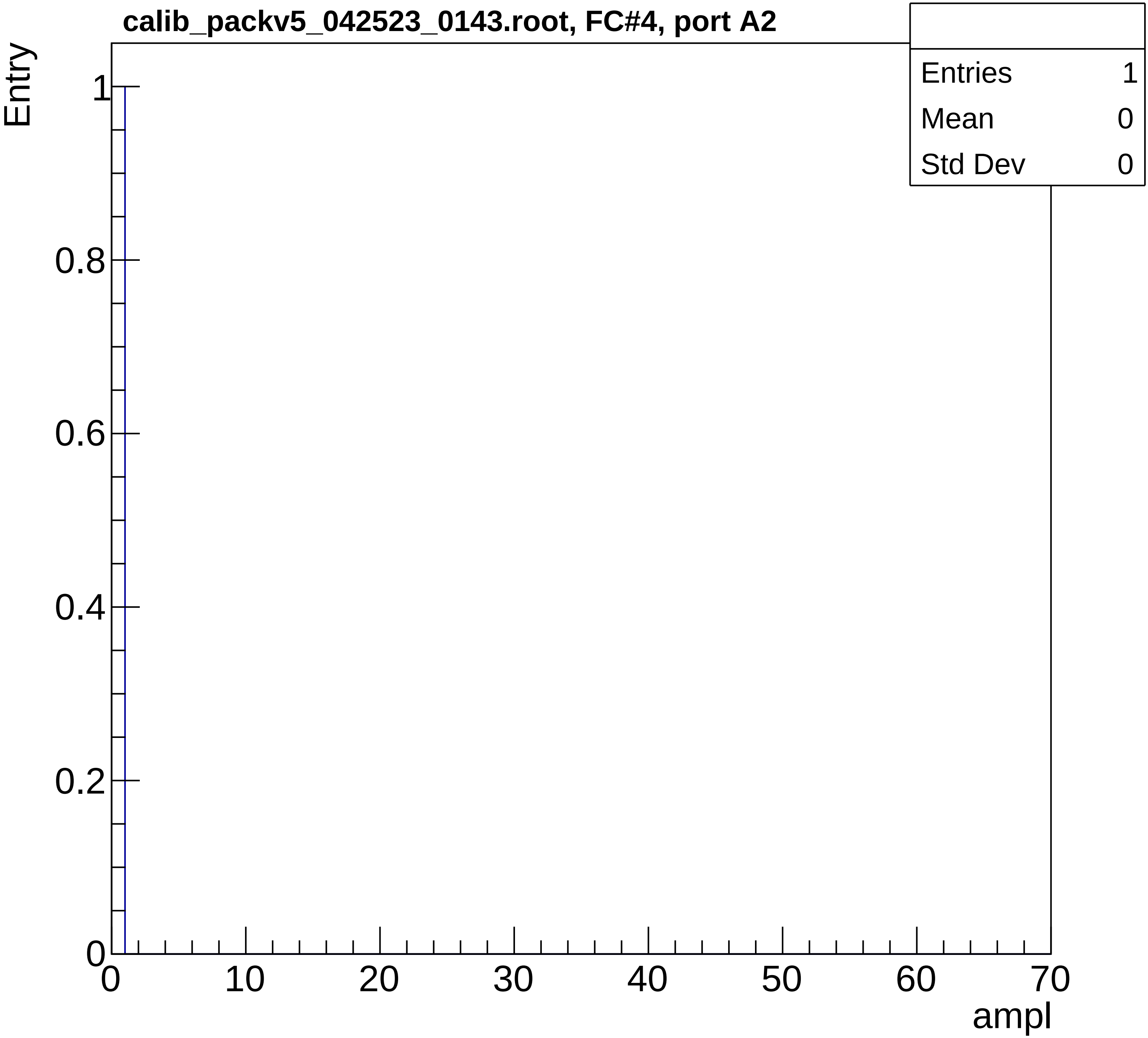


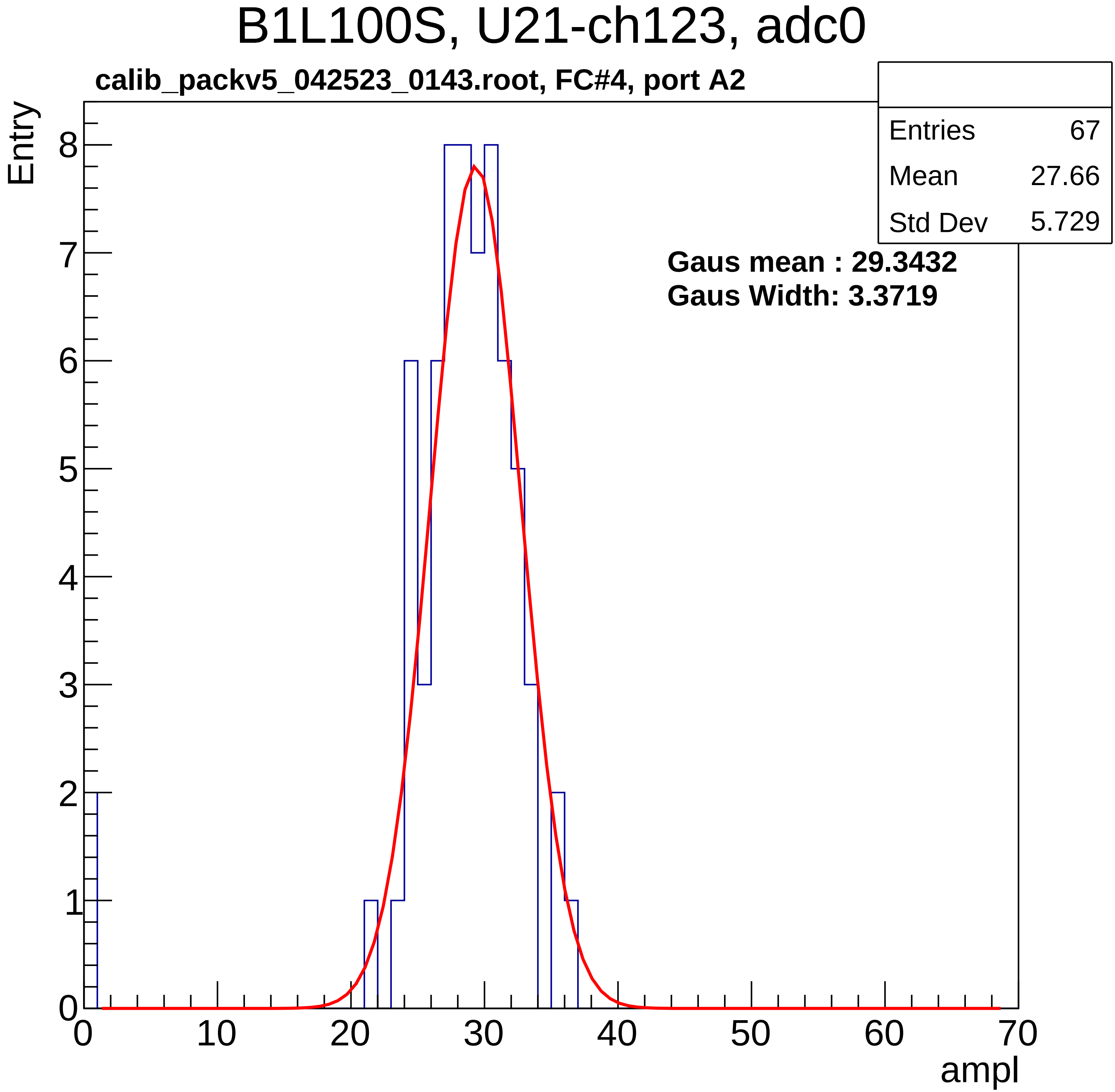
B1L100S, U21-ch122, adc6

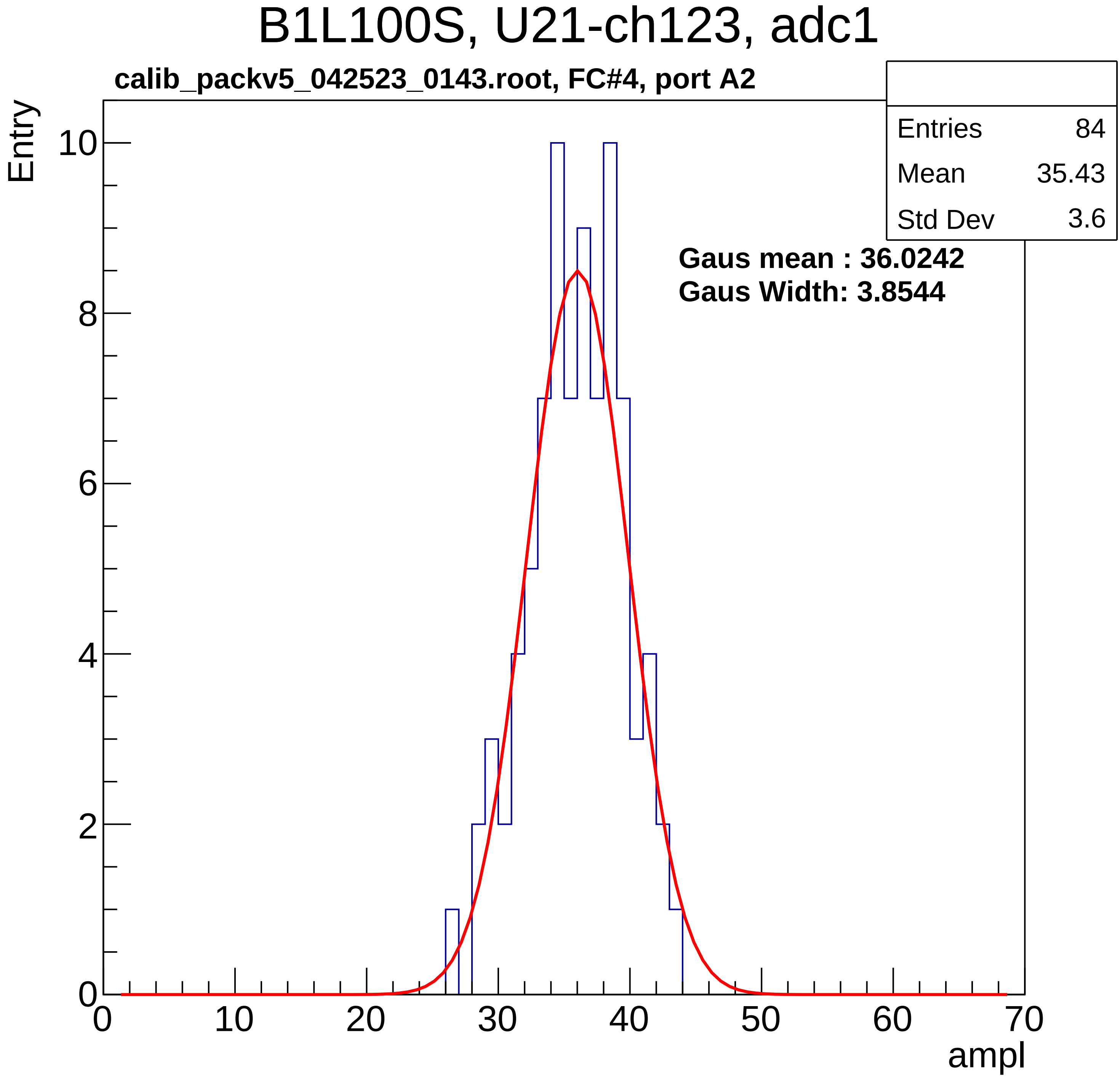
calib_packv5_042523_0143.root, FC#4, port A2

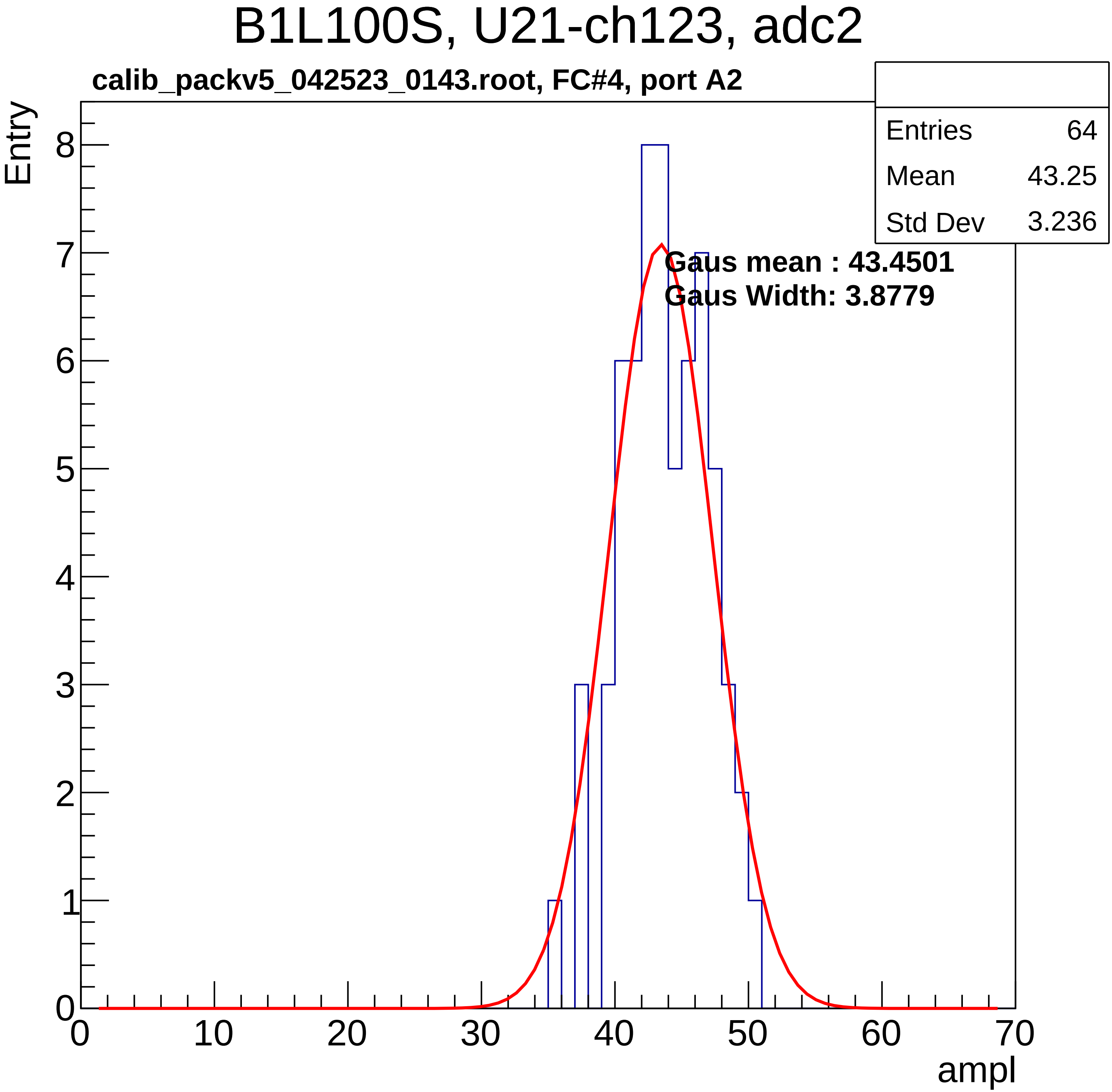


B1L100S, U21-ch122, adc7

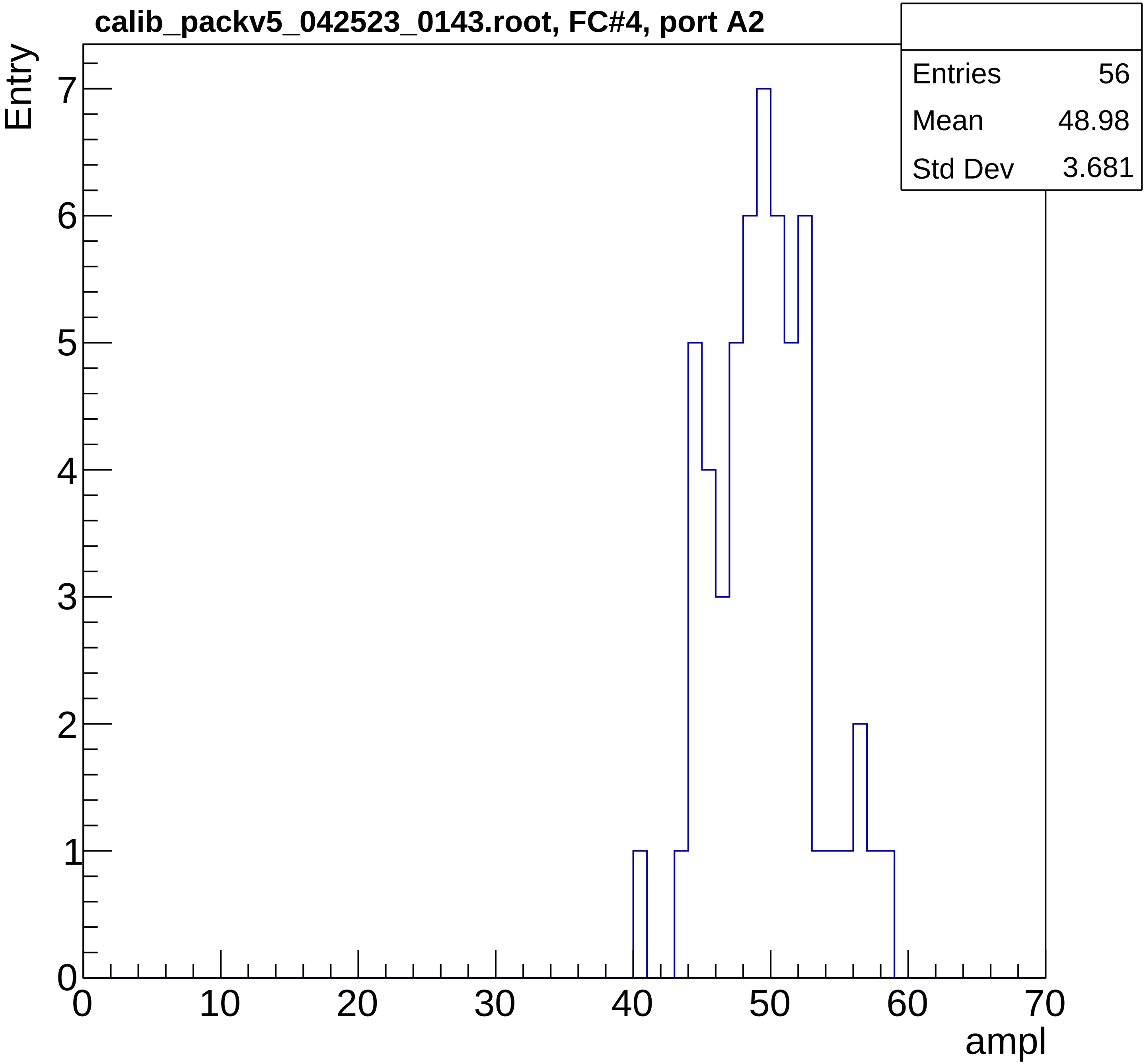


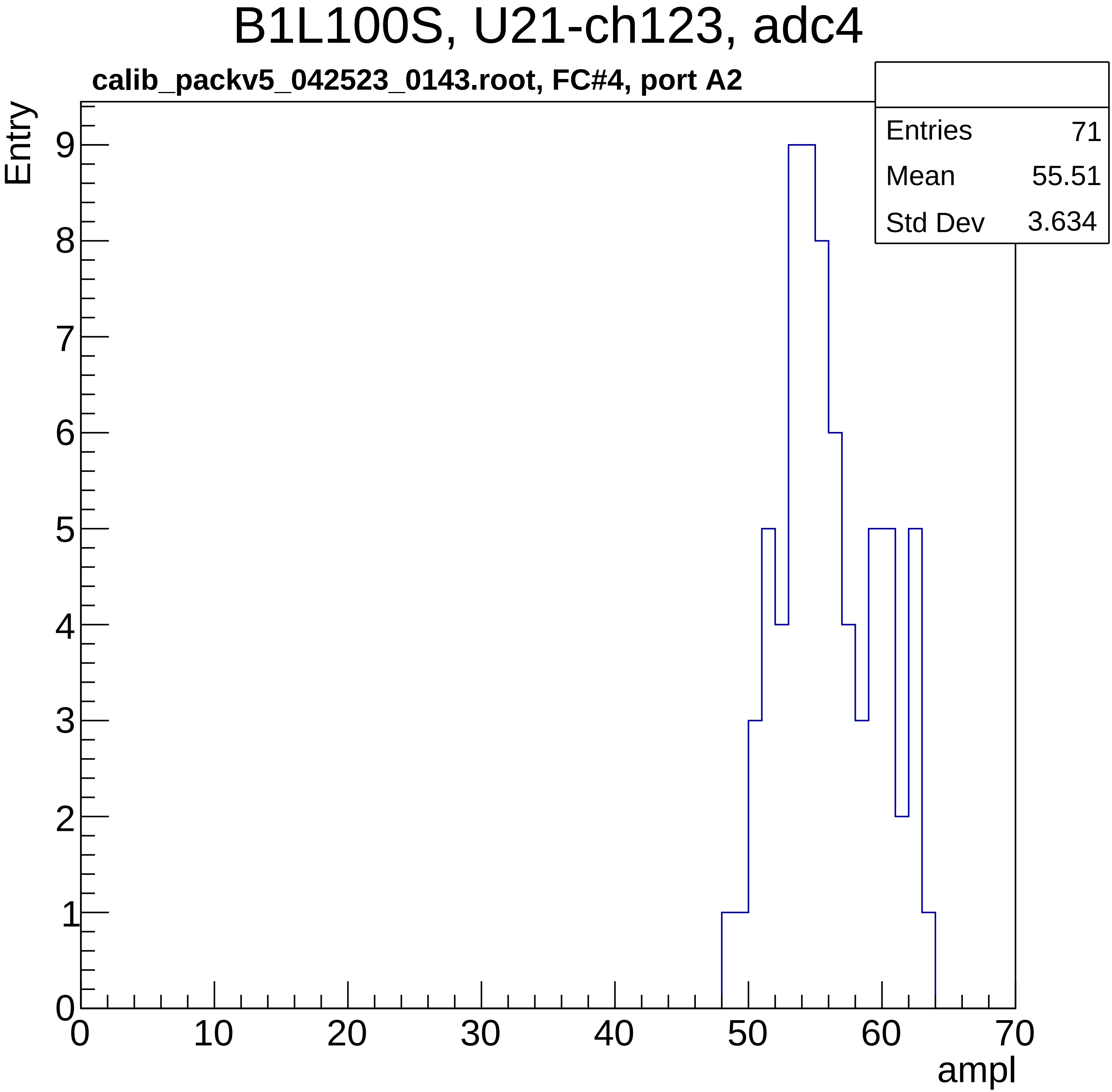

B1L100S, U21-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.66
Std Dev 5.729
Gaus mean : 29.3432
Gaus Width: 3.3719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

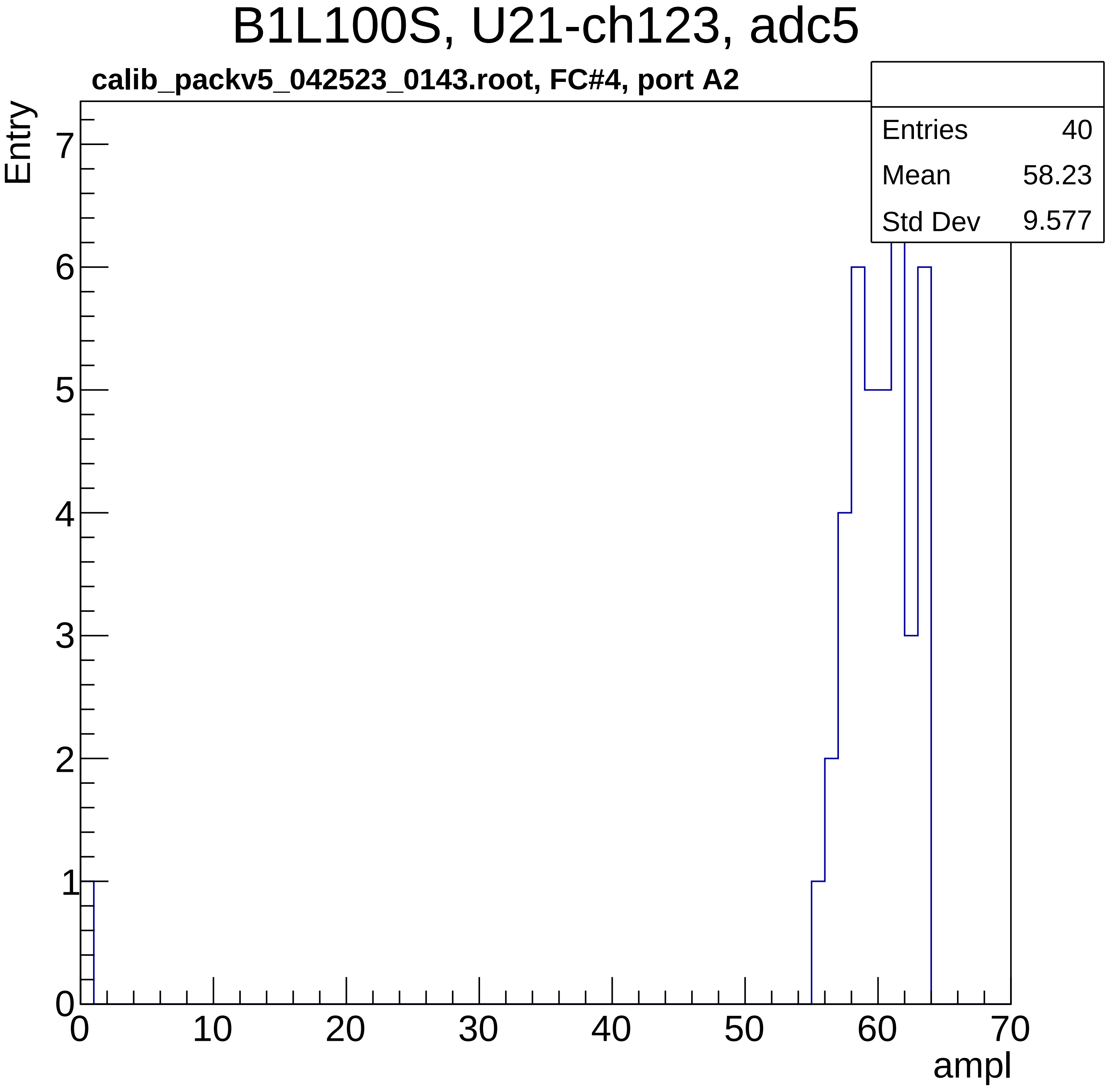

B1L100S, U21-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 35.43
Std Dev 3.6
Gaus mean : 36.0242
Gaus Width: 3.8544
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

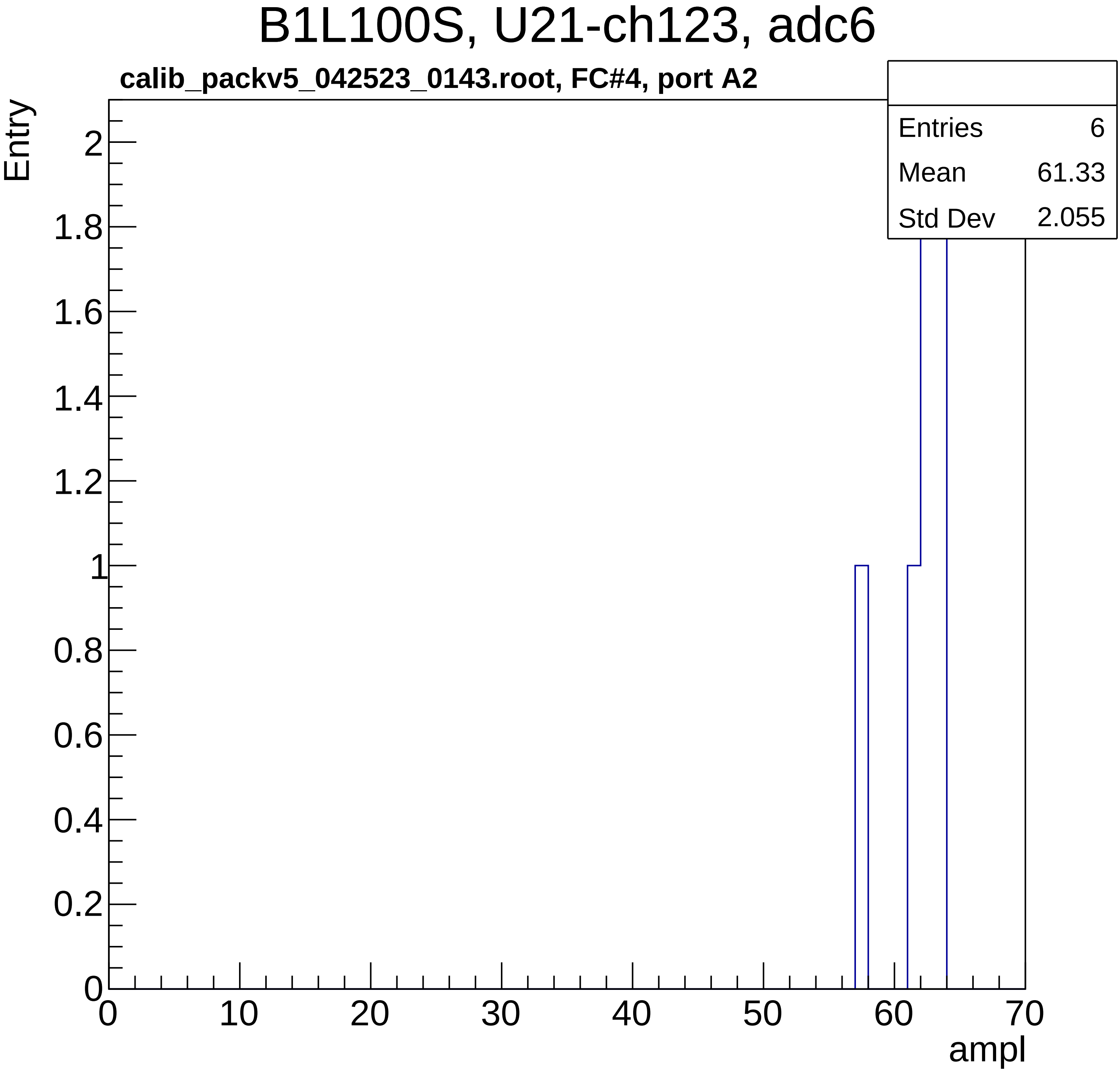

B1L100S, U21-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.25
Std Dev 3.236
Gaus mean : 43.4501
Gaus Width: 3.8779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

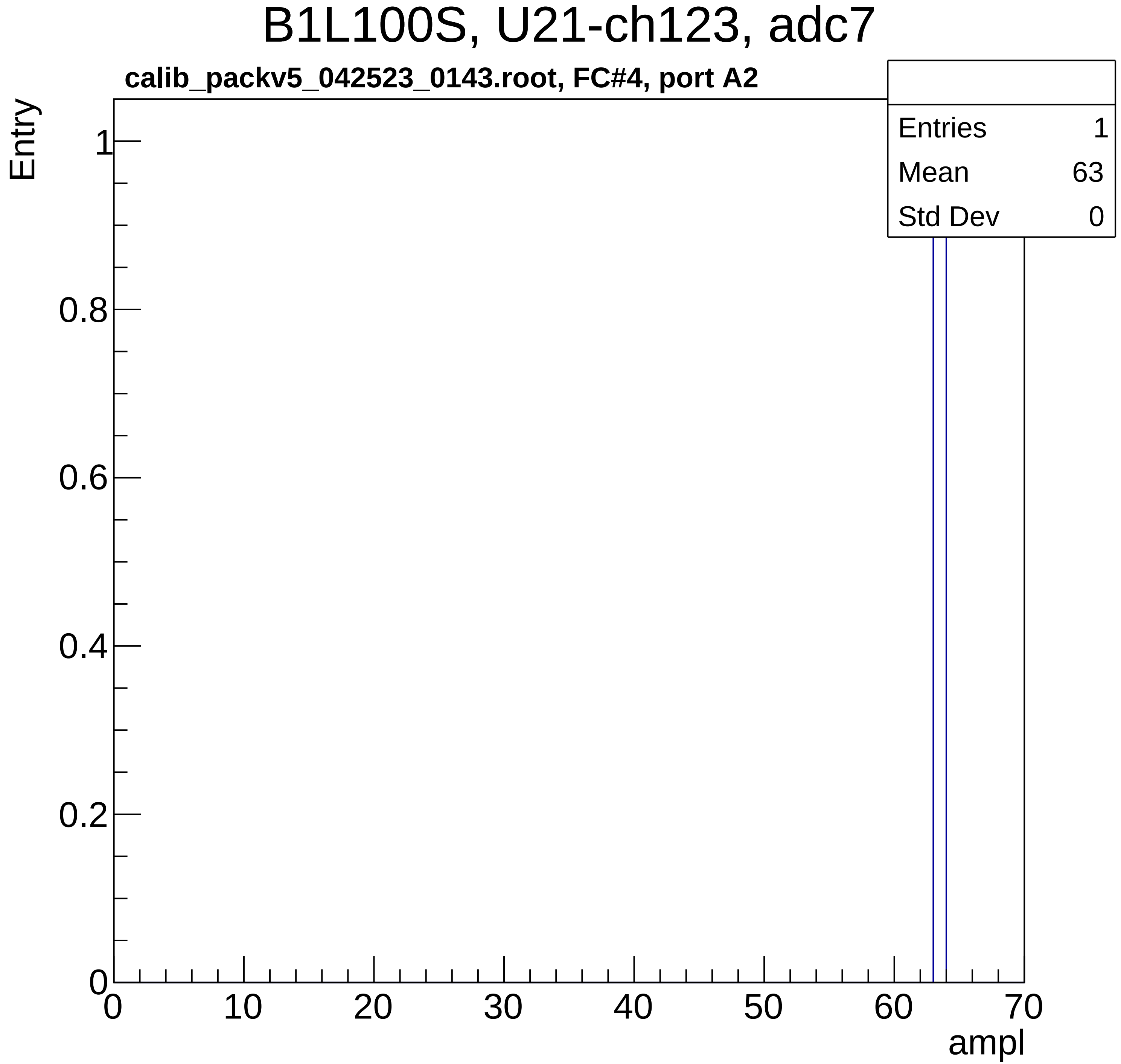
B1L100S, U21-ch123, adc3

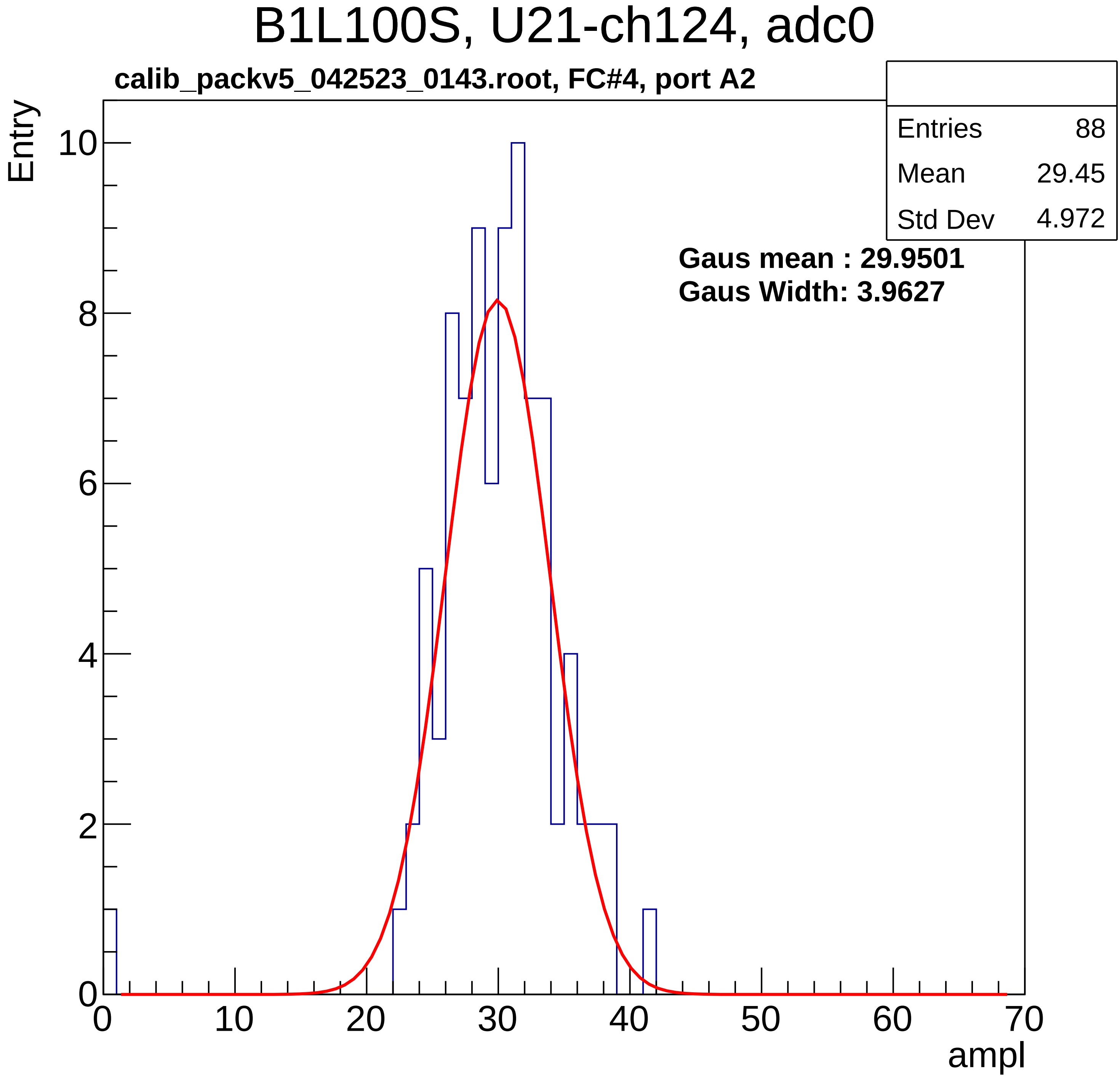


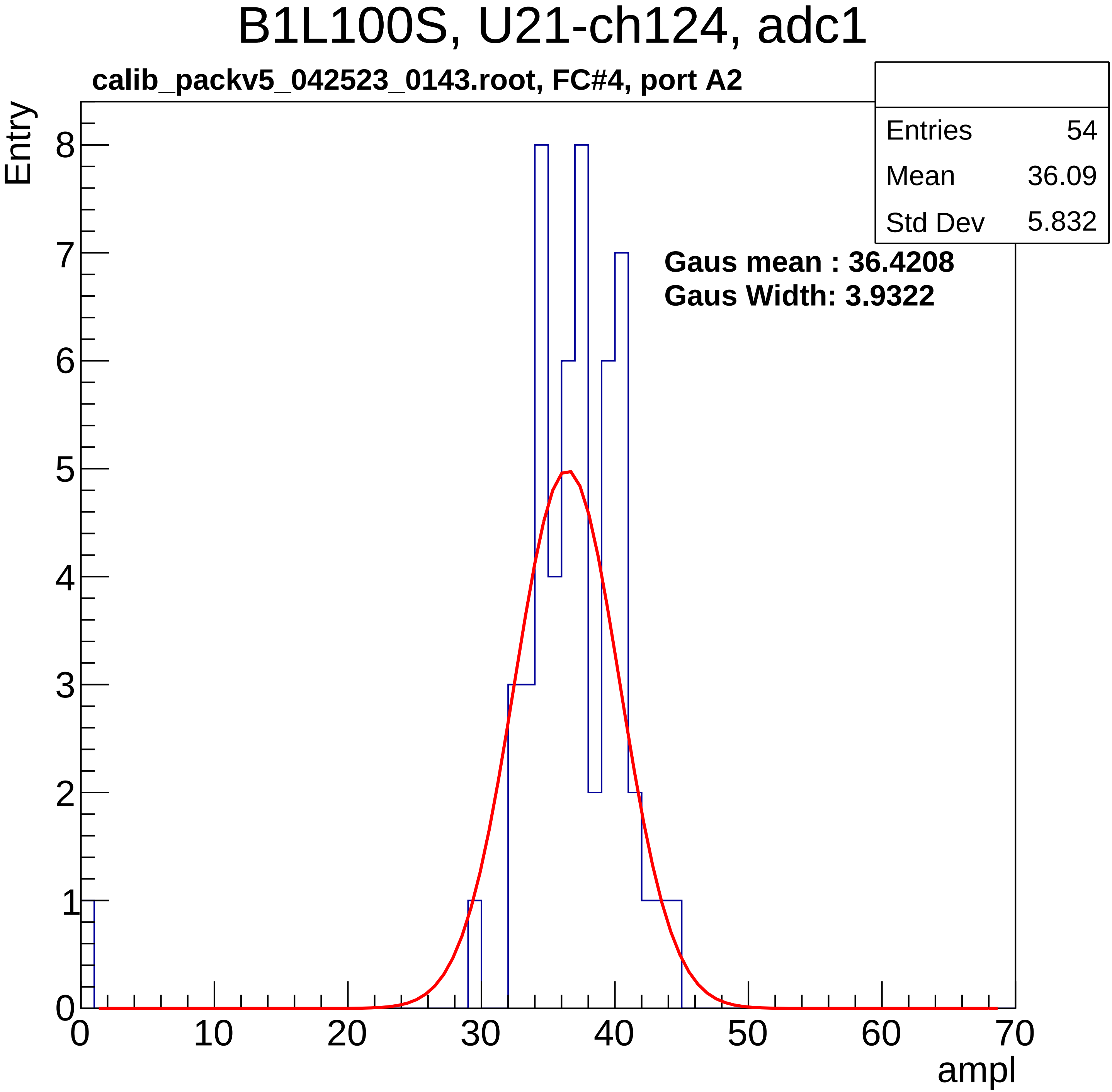

B1L100S, U21-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 55.51
Std Dev 3.634
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

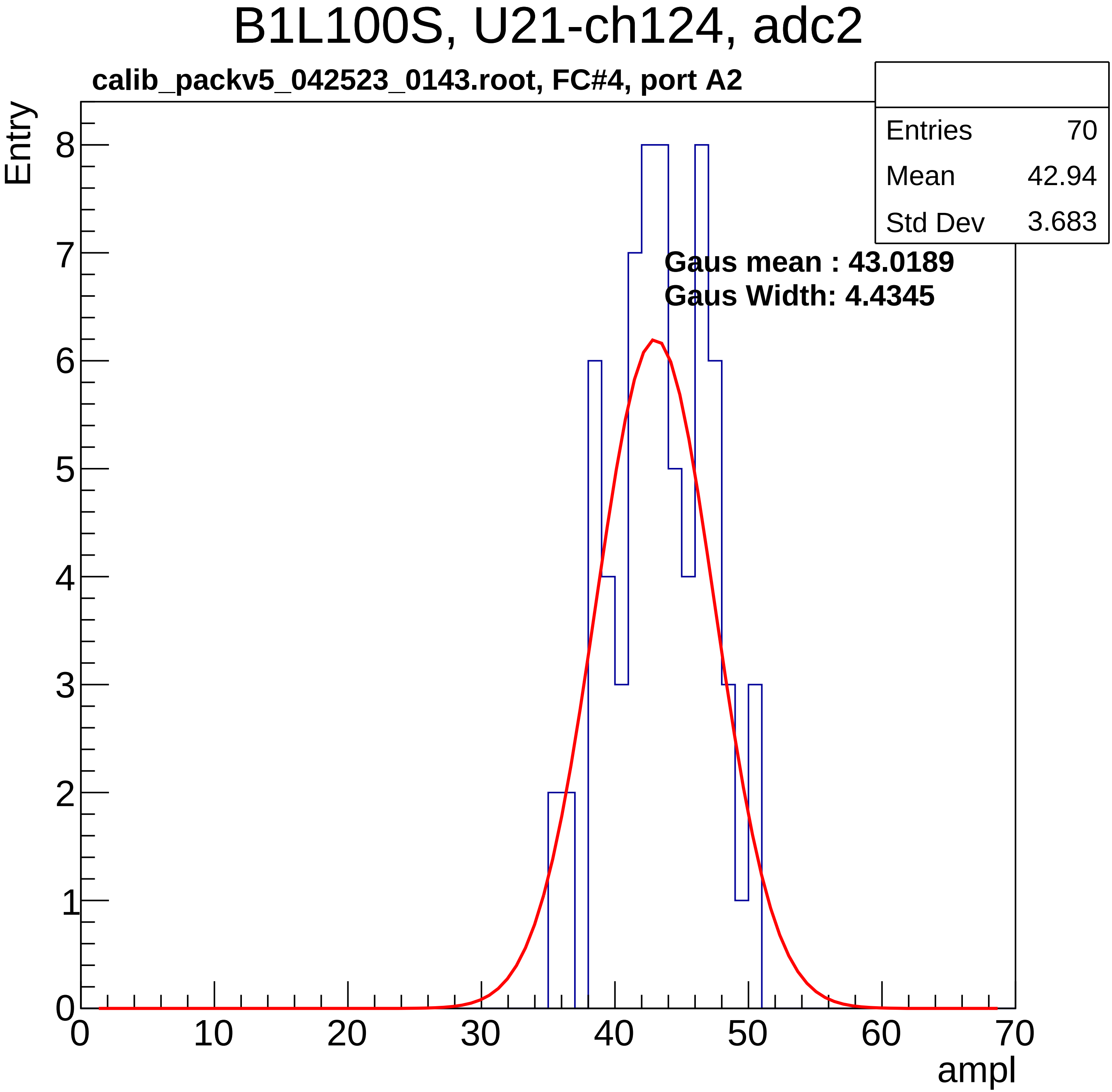

B1L100S, U21-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.23
Std Dev 9.577
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

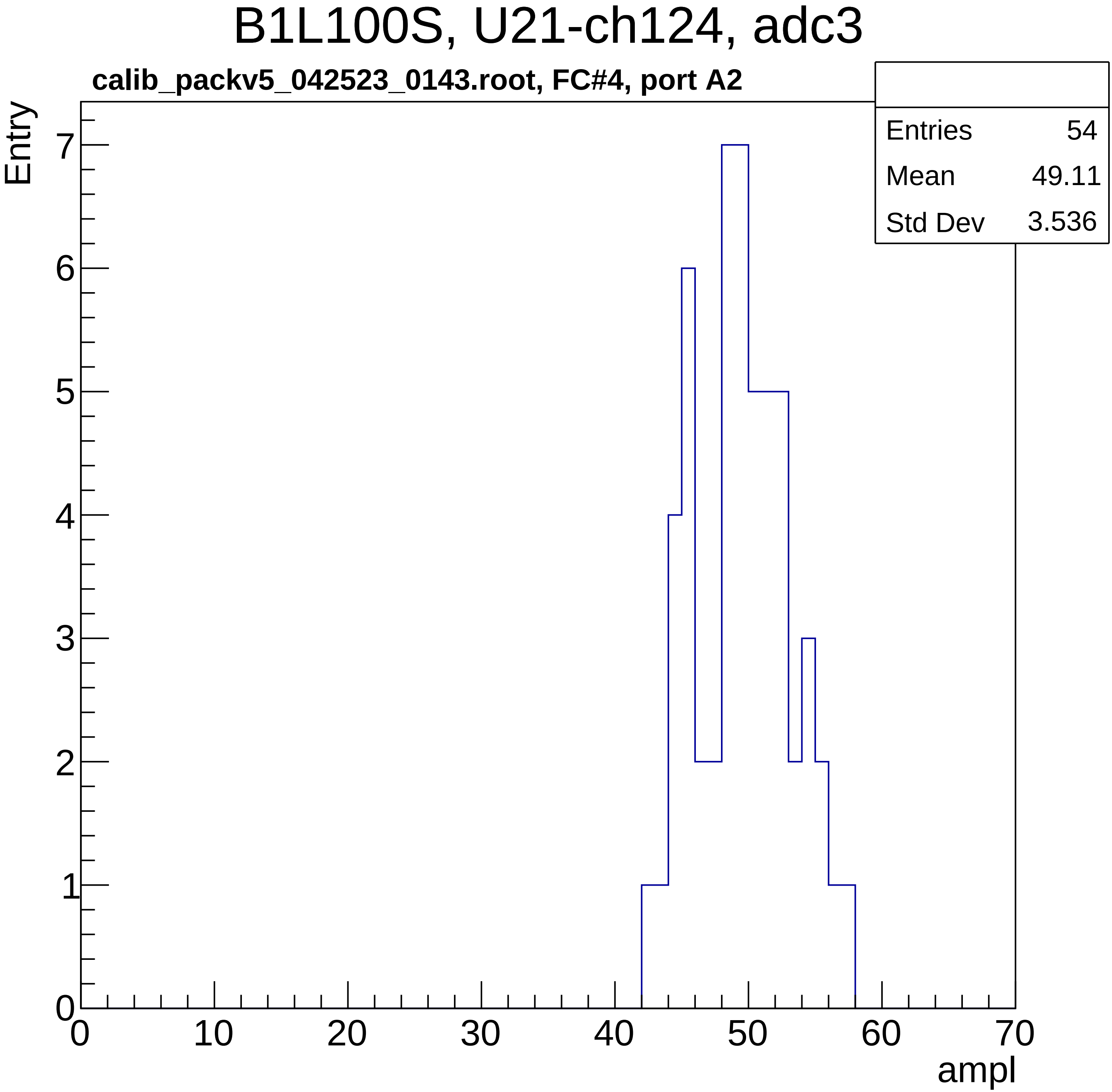

B1L100S, U21-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.33
Std Dev 2.055
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

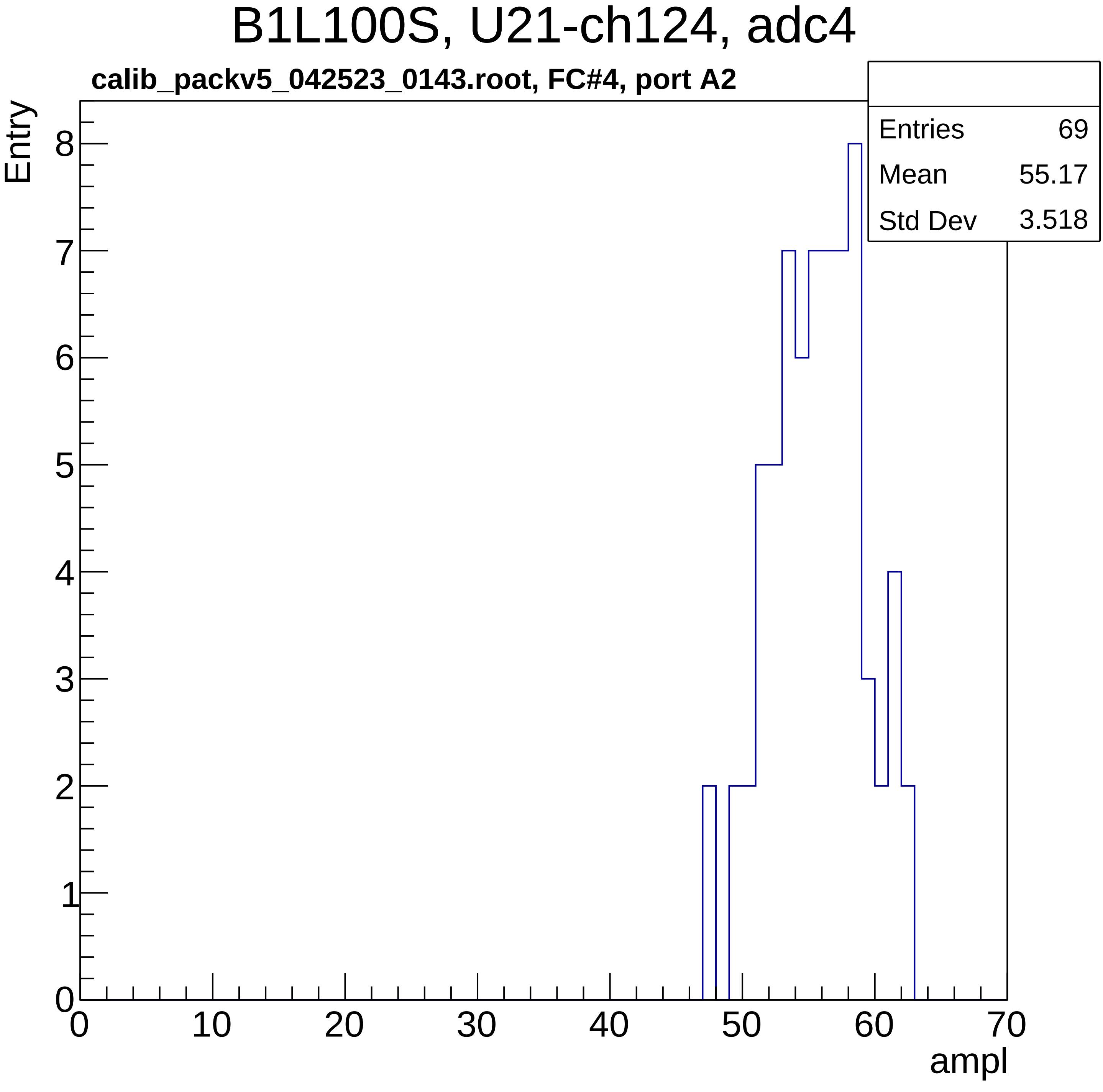

B1L100S, U21-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 88
Mean 29.45
Std Dev 4.972
Gaus mean : 29.9501
Gaus Width: 3.9627
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

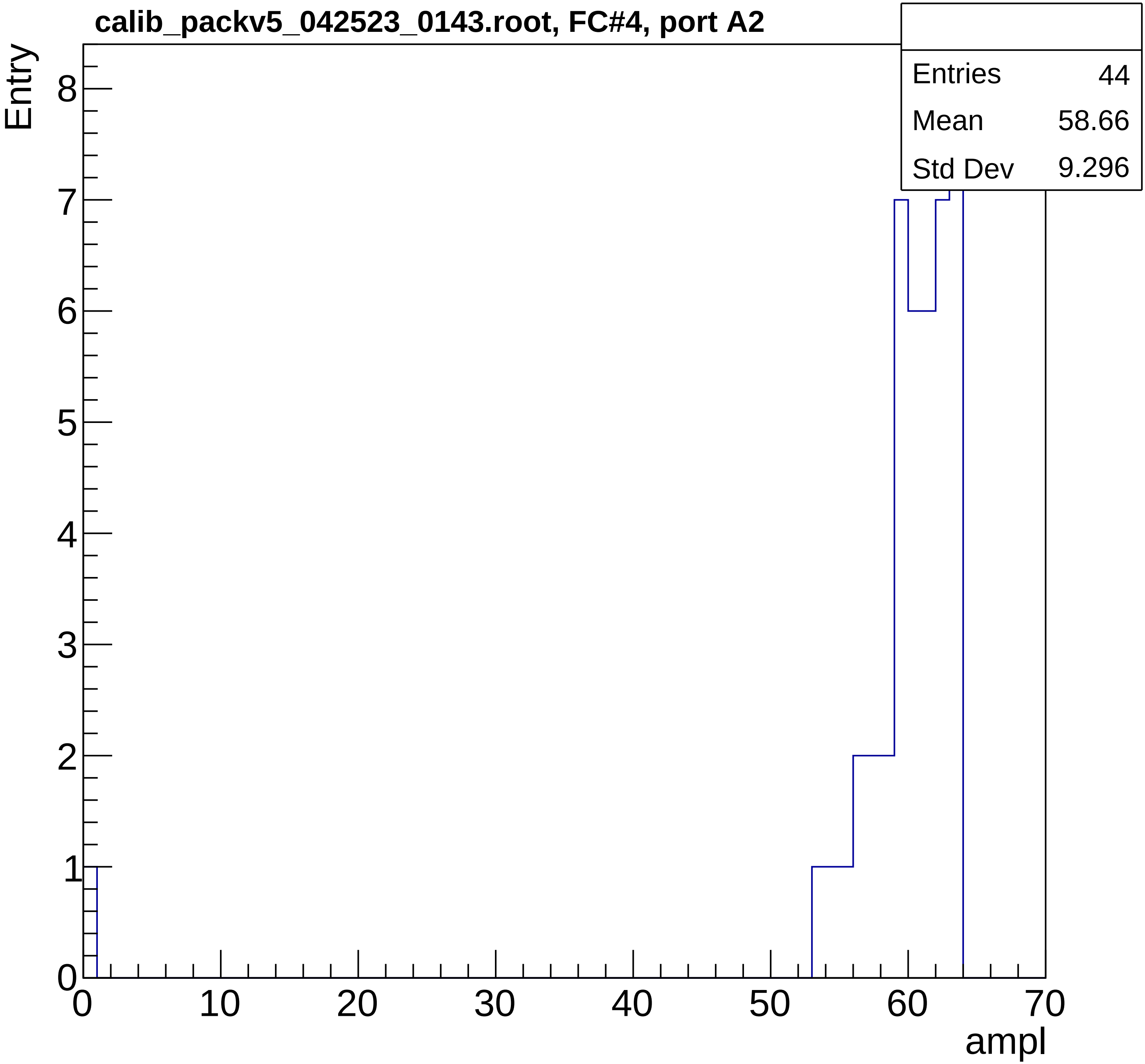

B1L100S, U21-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 36.09
Std Dev 5.832
Gaus mean : 36.4208
Gaus Width: 3.9322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

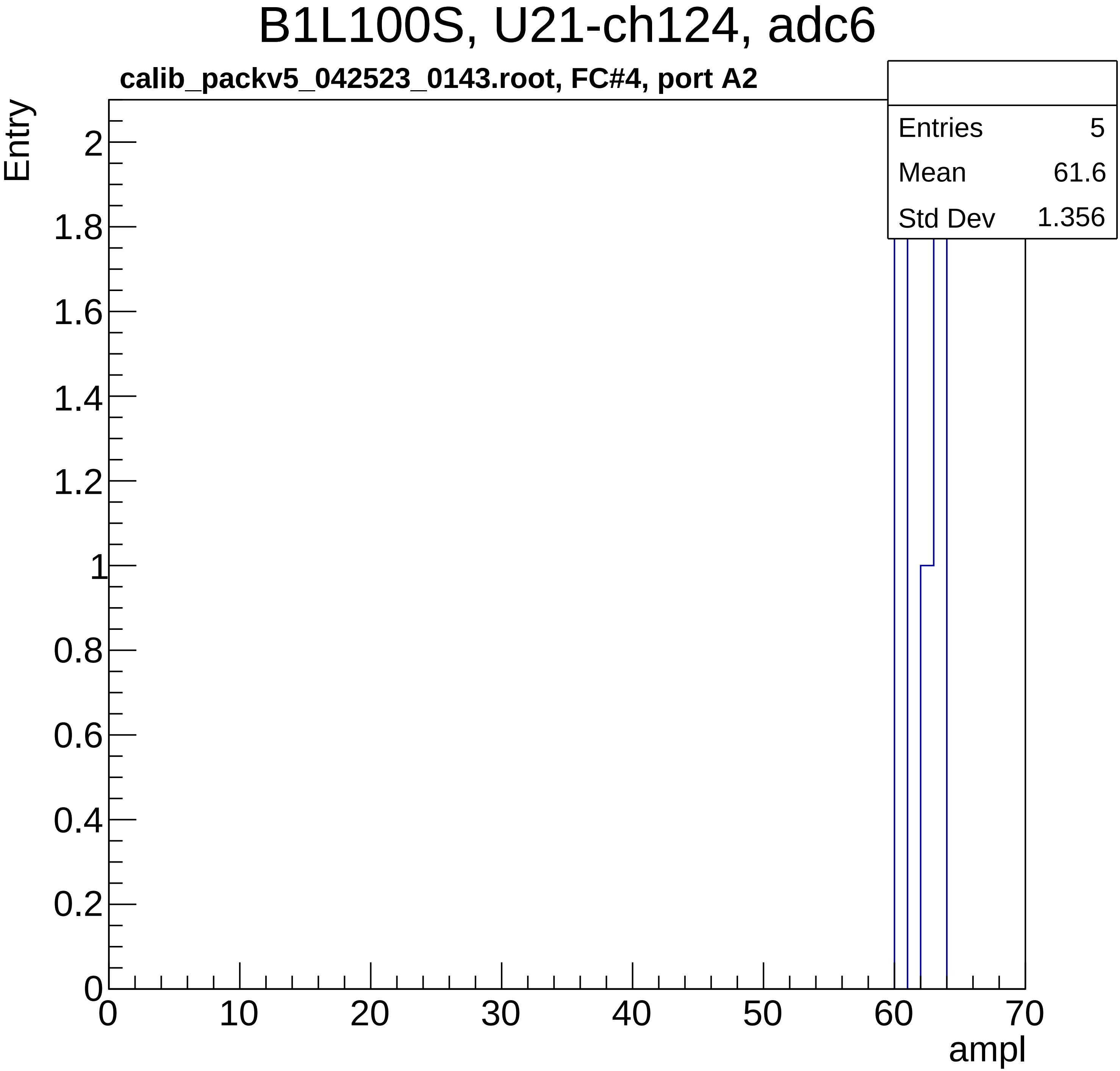

B1L100S, U21-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 42.94
Std Dev 3.683
Gaus mean : 43.0189
Gaus Width: 4.4345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

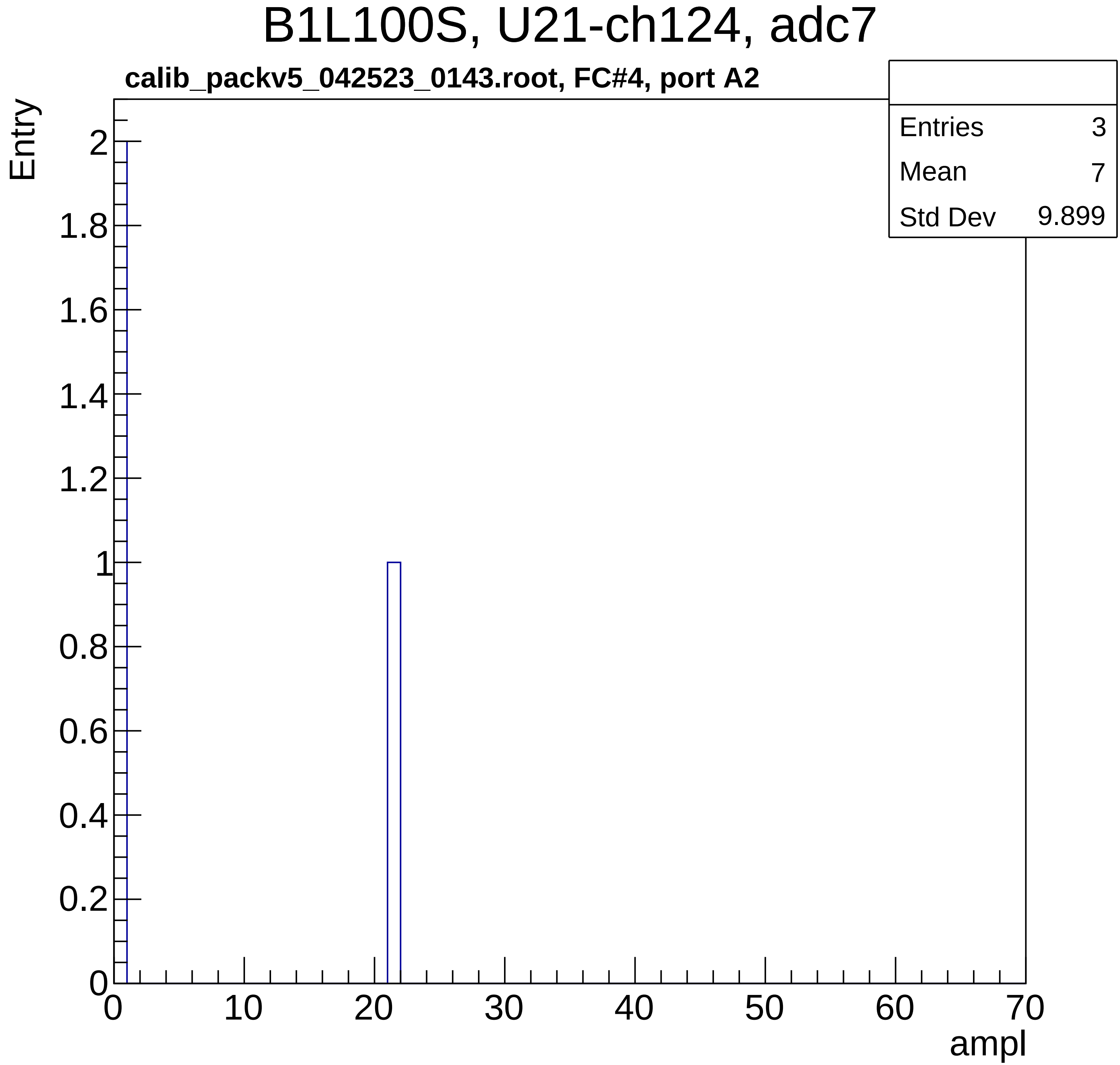

B1L100S, U21-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 49.11
Std Dev 3.536
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

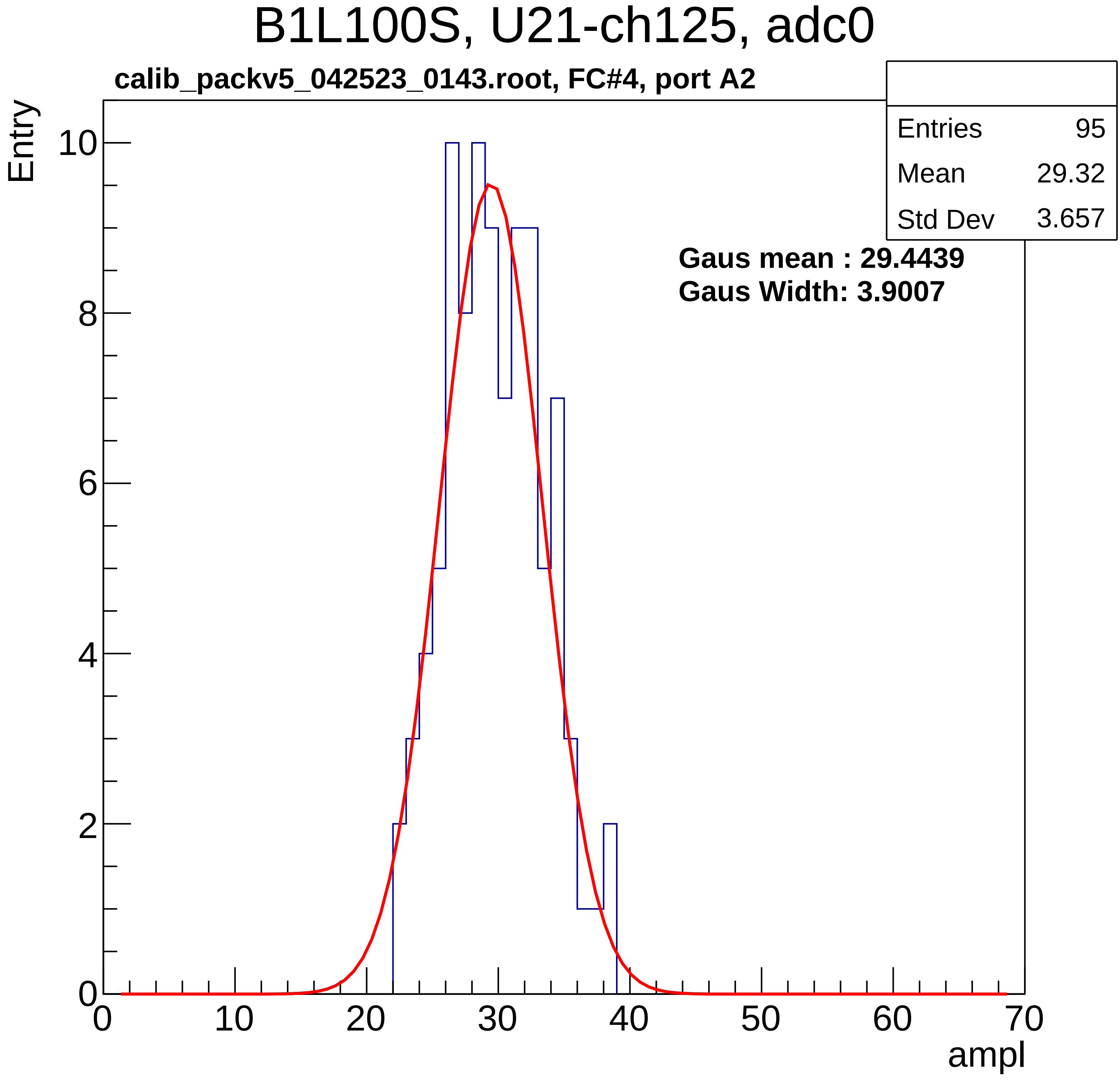

B1L100S, U21-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.17
Std Dev 3.518
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

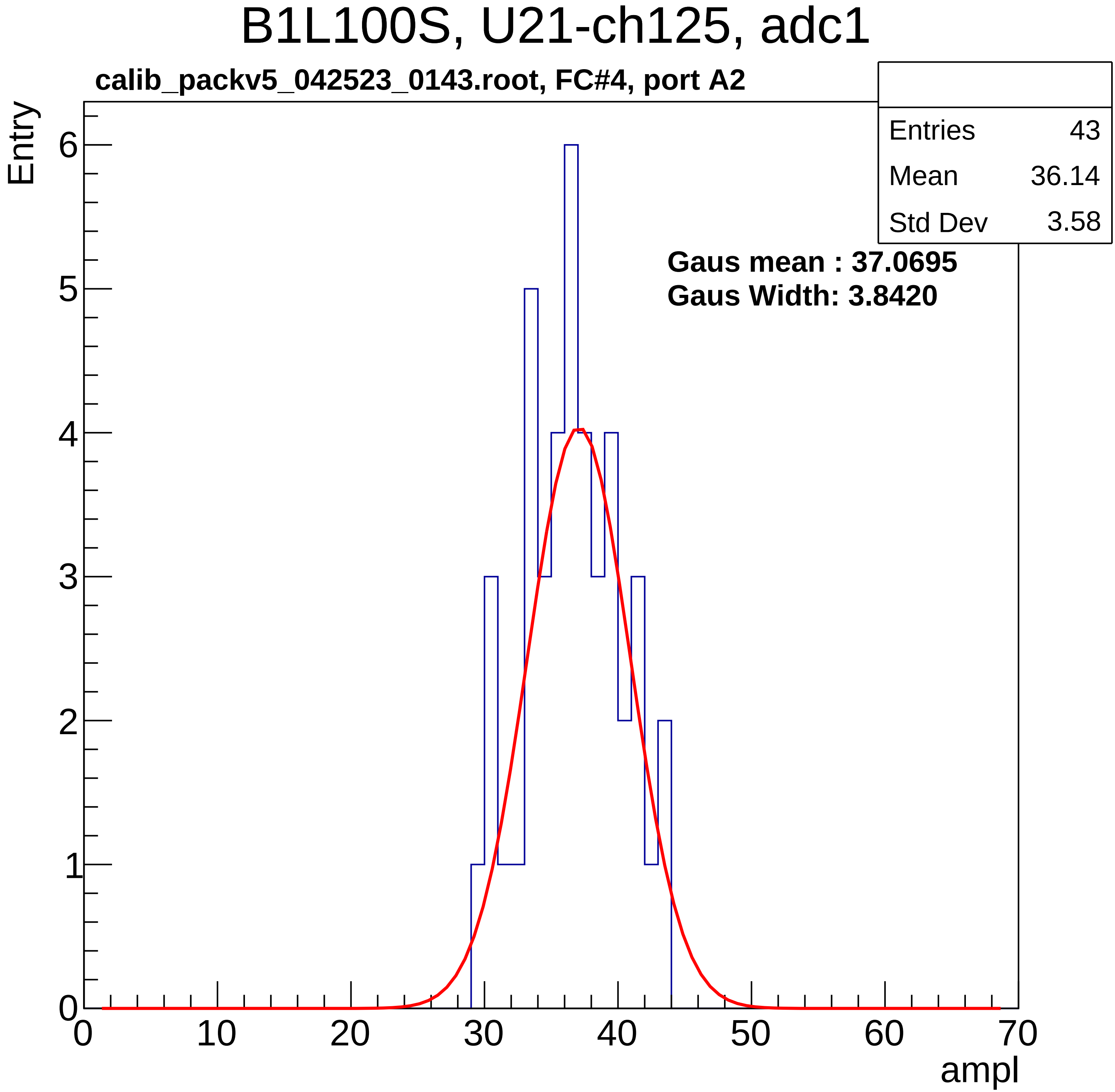
B1L100S, U21-ch124, adc5

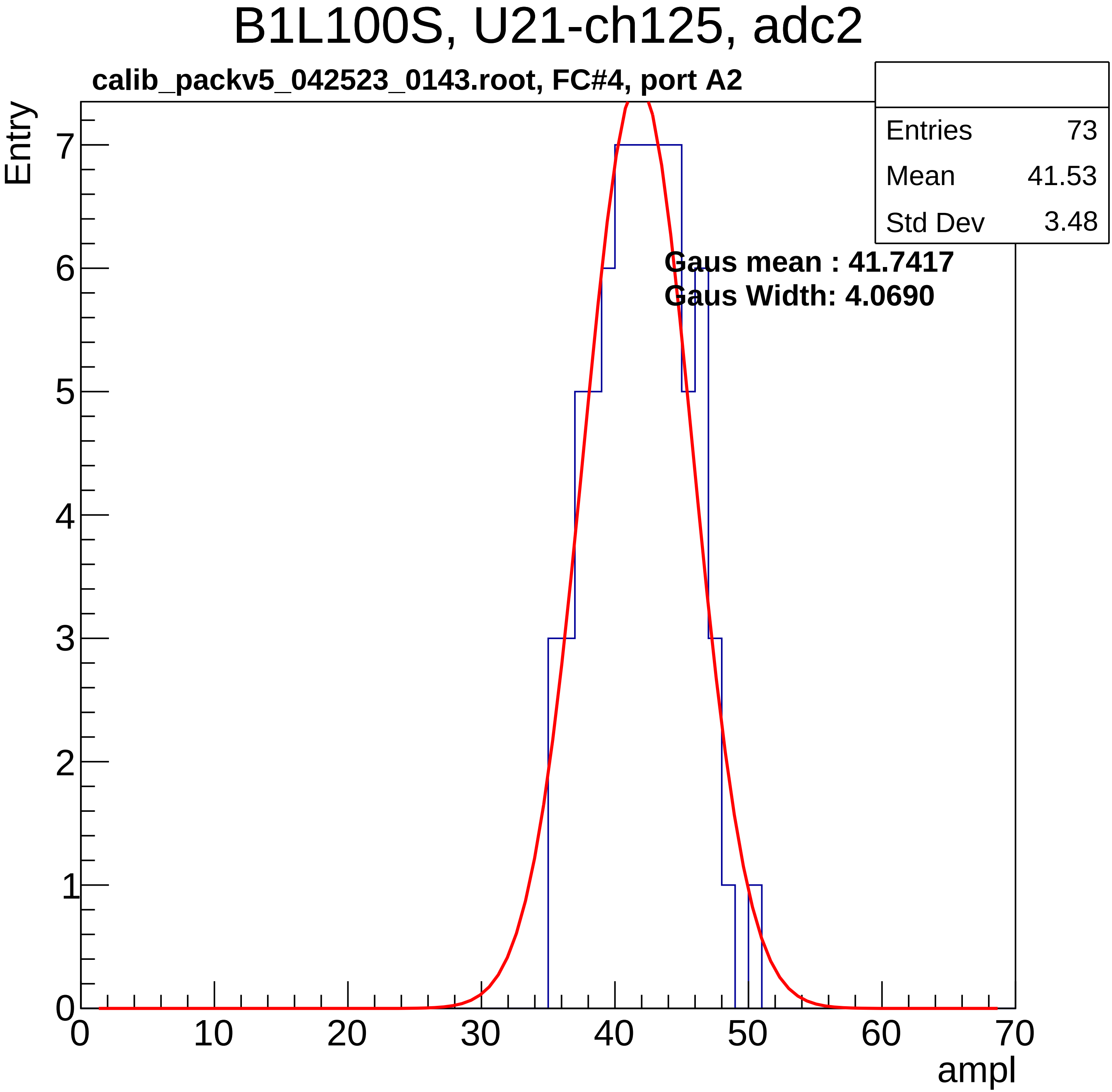


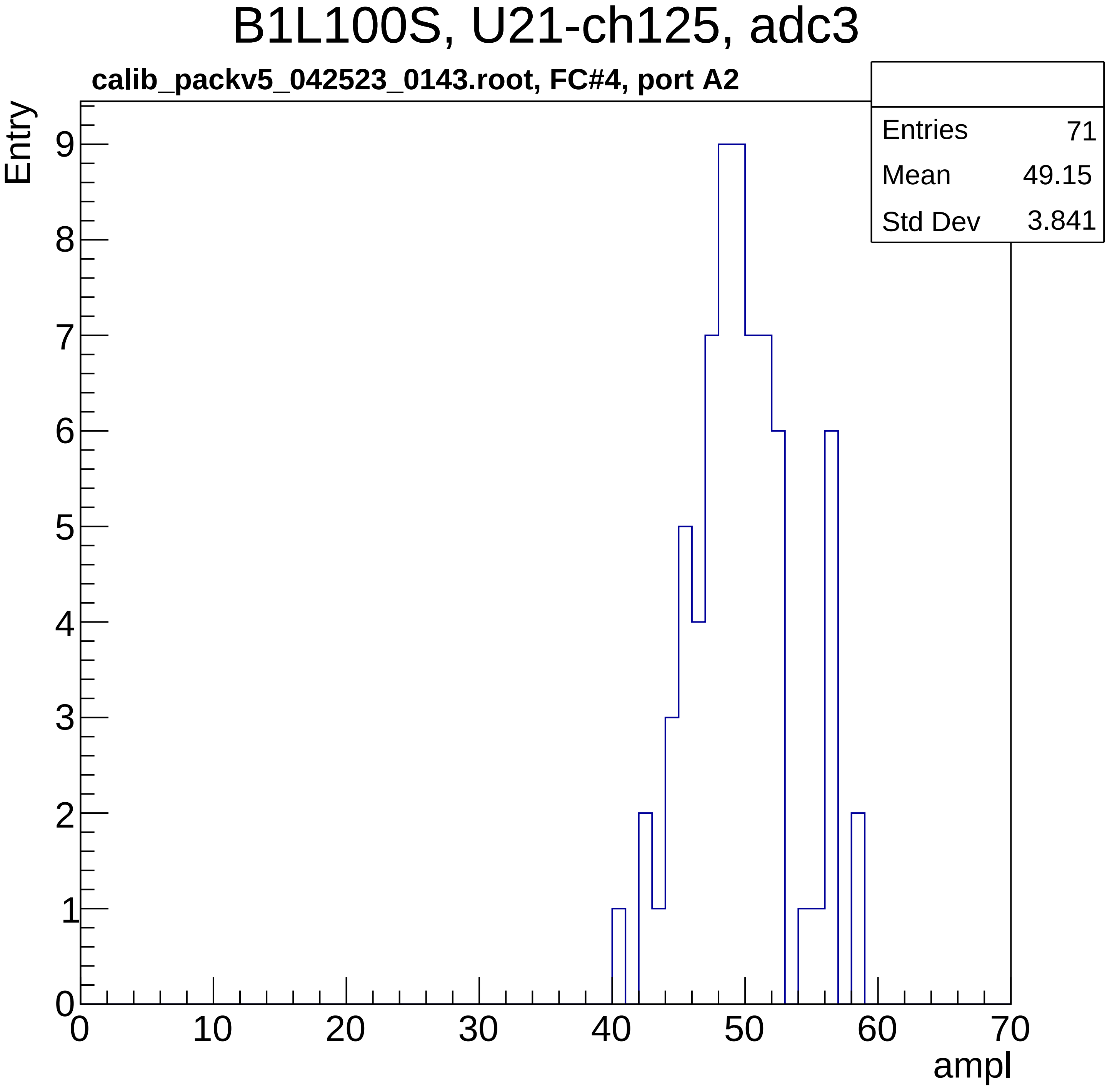

B1L100S, U21-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.6
Std Dev 1.356
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

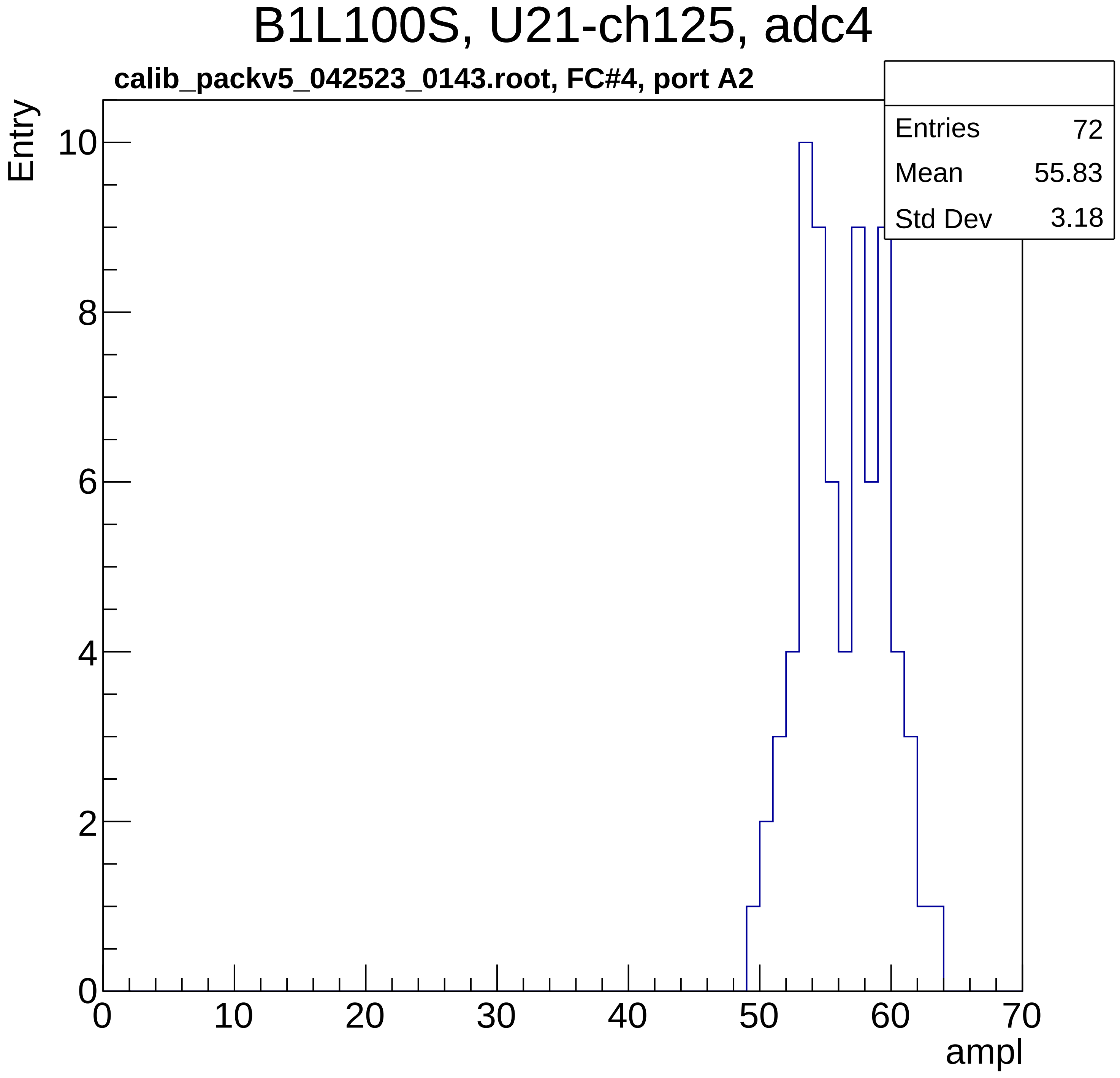

B1L100S, U21-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entry
Entries 3
Mean 7
Std Dev 9.899
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

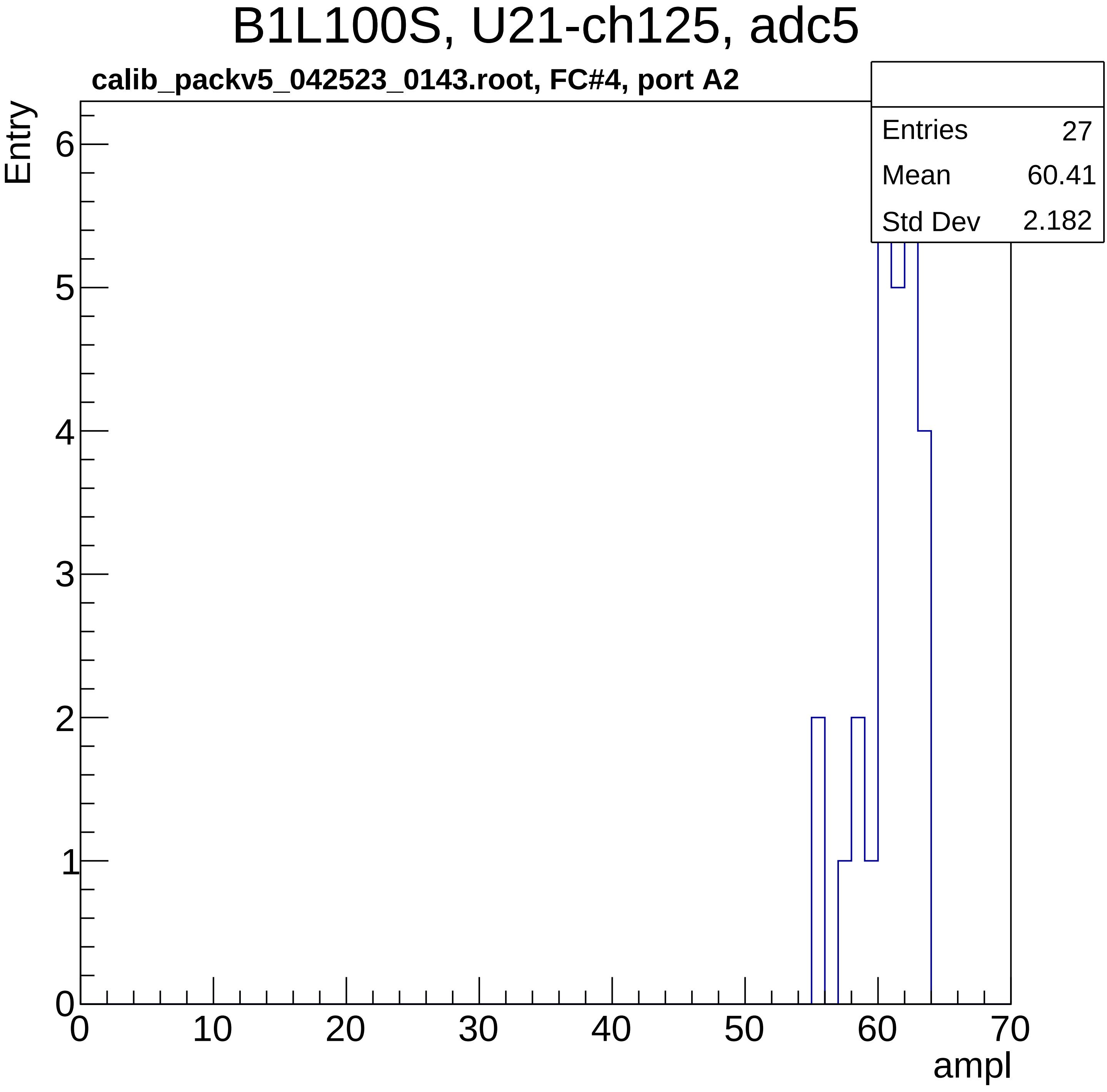

B1L100S, U21-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 29.32
Std Dev 3.657
Gaus mean : 29.4439
Gaus Width: 3.9007
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

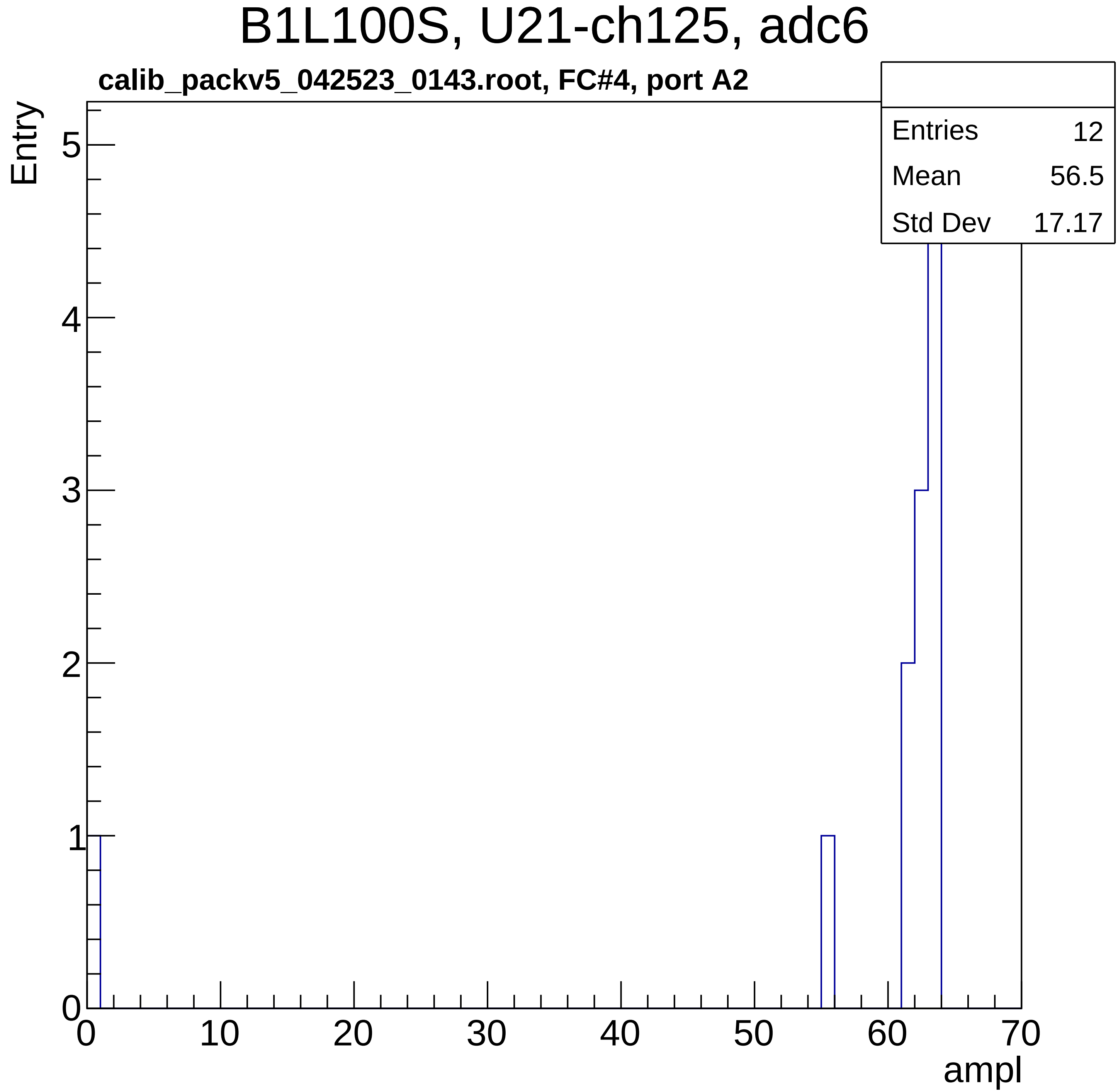

B1L100S, U21-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 36.14
Std Dev 3.58
Gaus mean : 37.0695
Gaus Width: 3.8420
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U21-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 41.53
Std Dev 3.48
Gaus mean : 41.7417
Gaus Width: 4.0690
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U21-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.15
Std Dev 3.841
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

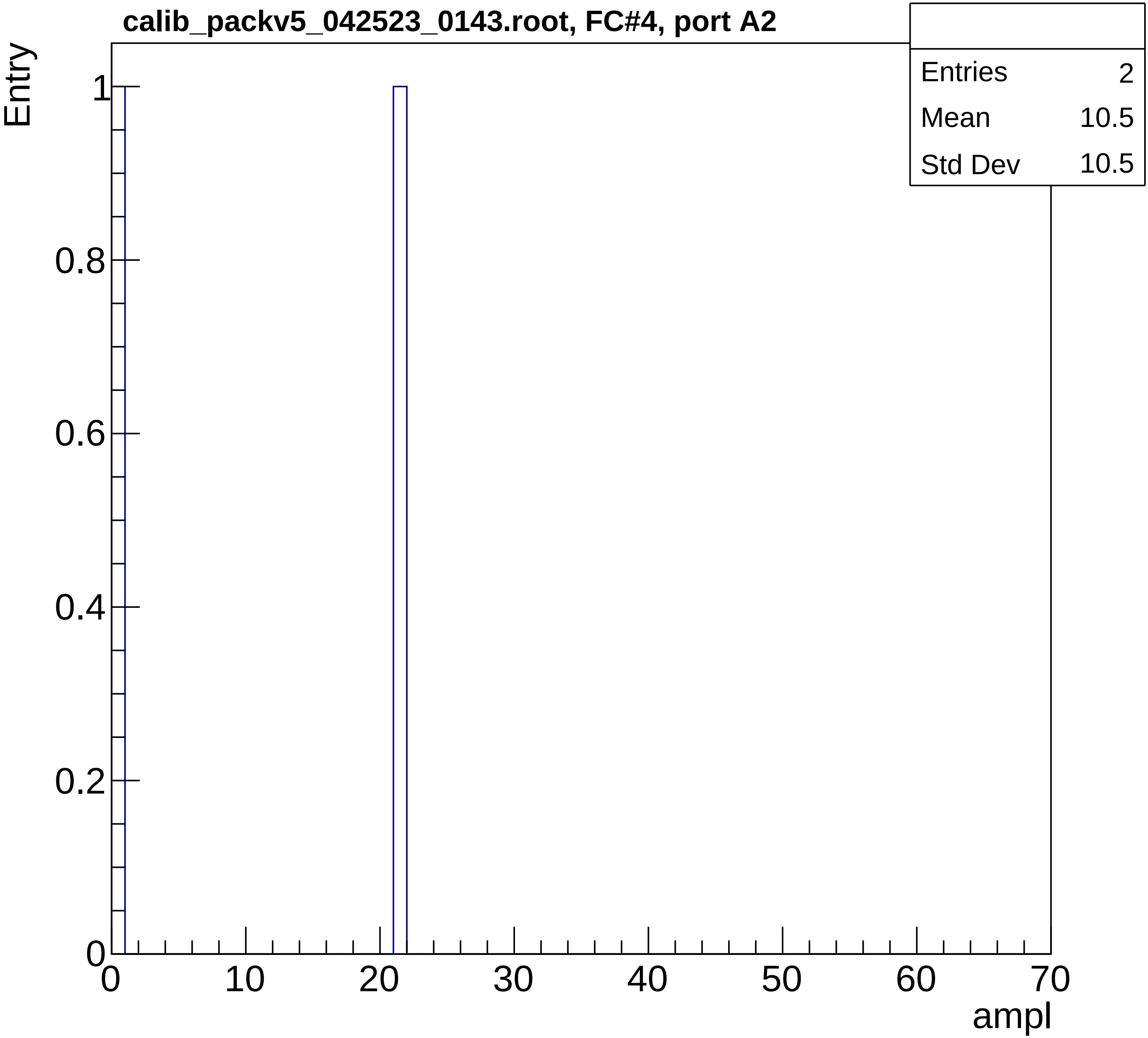

B1L100S, U21-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 72
Mean 55.83
Std Dev 3.18
0
2
4
6
8
10
0
10
20
30
40
50
60
70

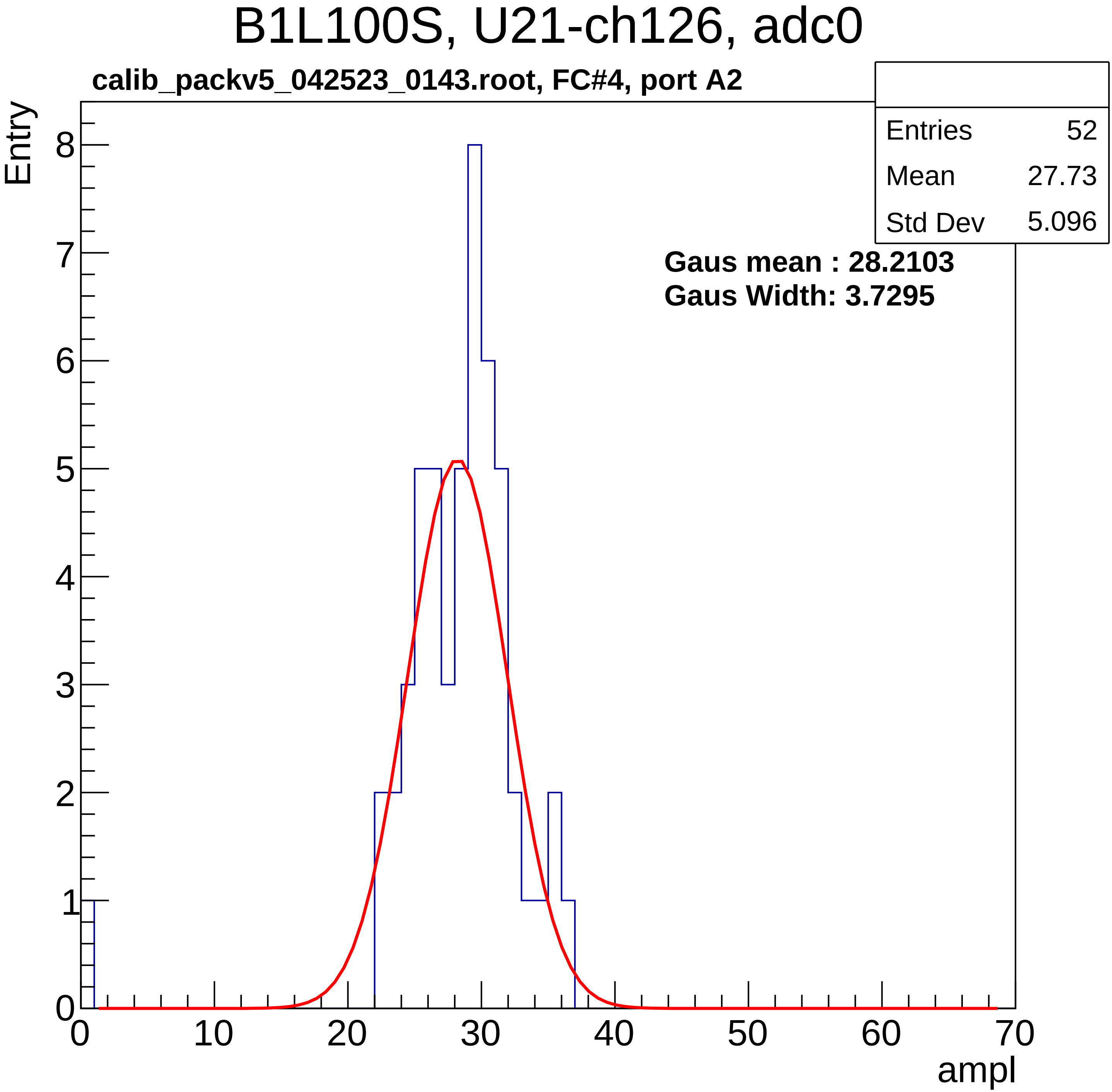

B1L100S, U21-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 60.41
Std Dev 2.182
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.5
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

B1L100S, U21-ch125, adc7

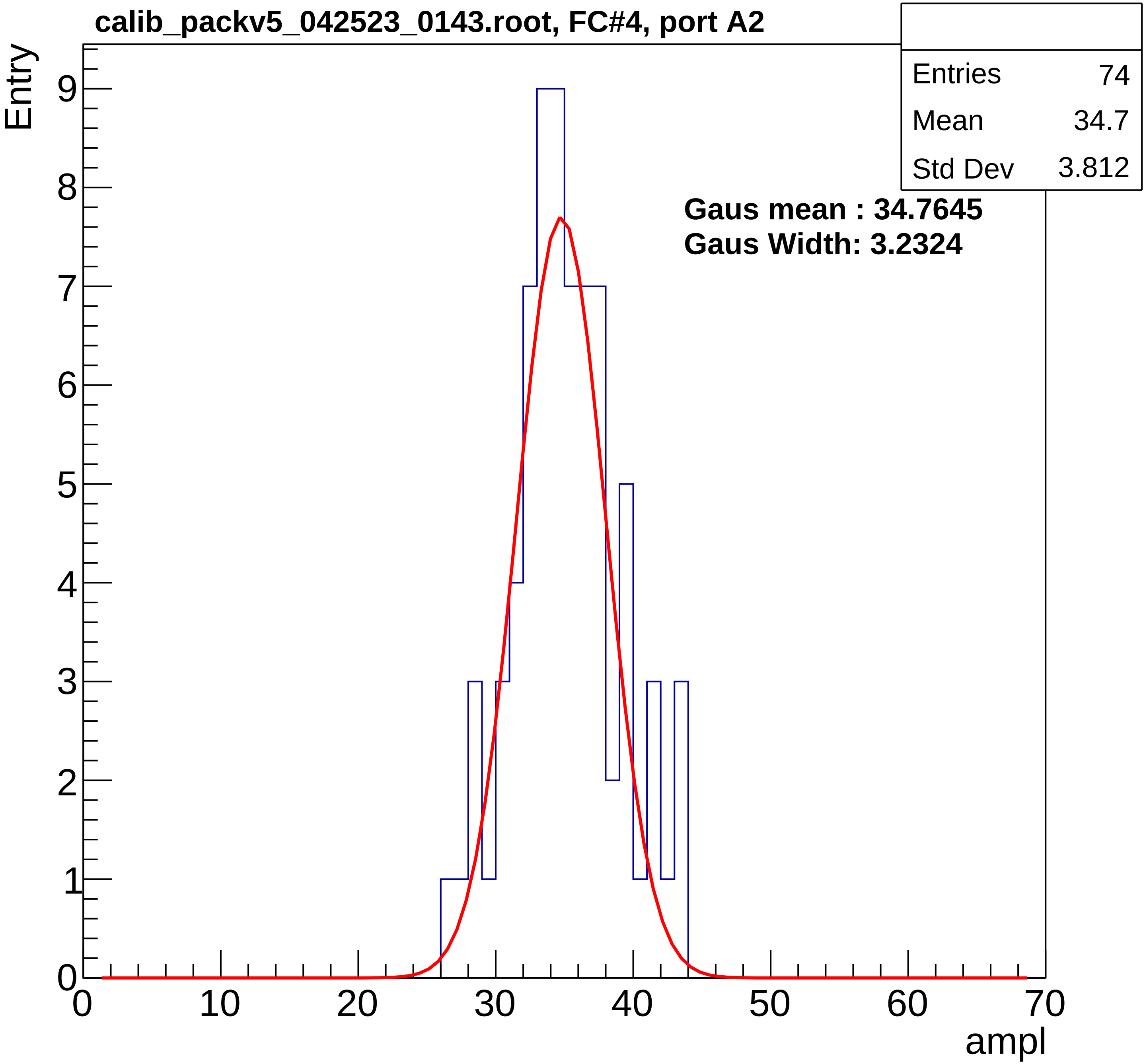
calib_packv5_042523_0143.root, FC#4, port A2

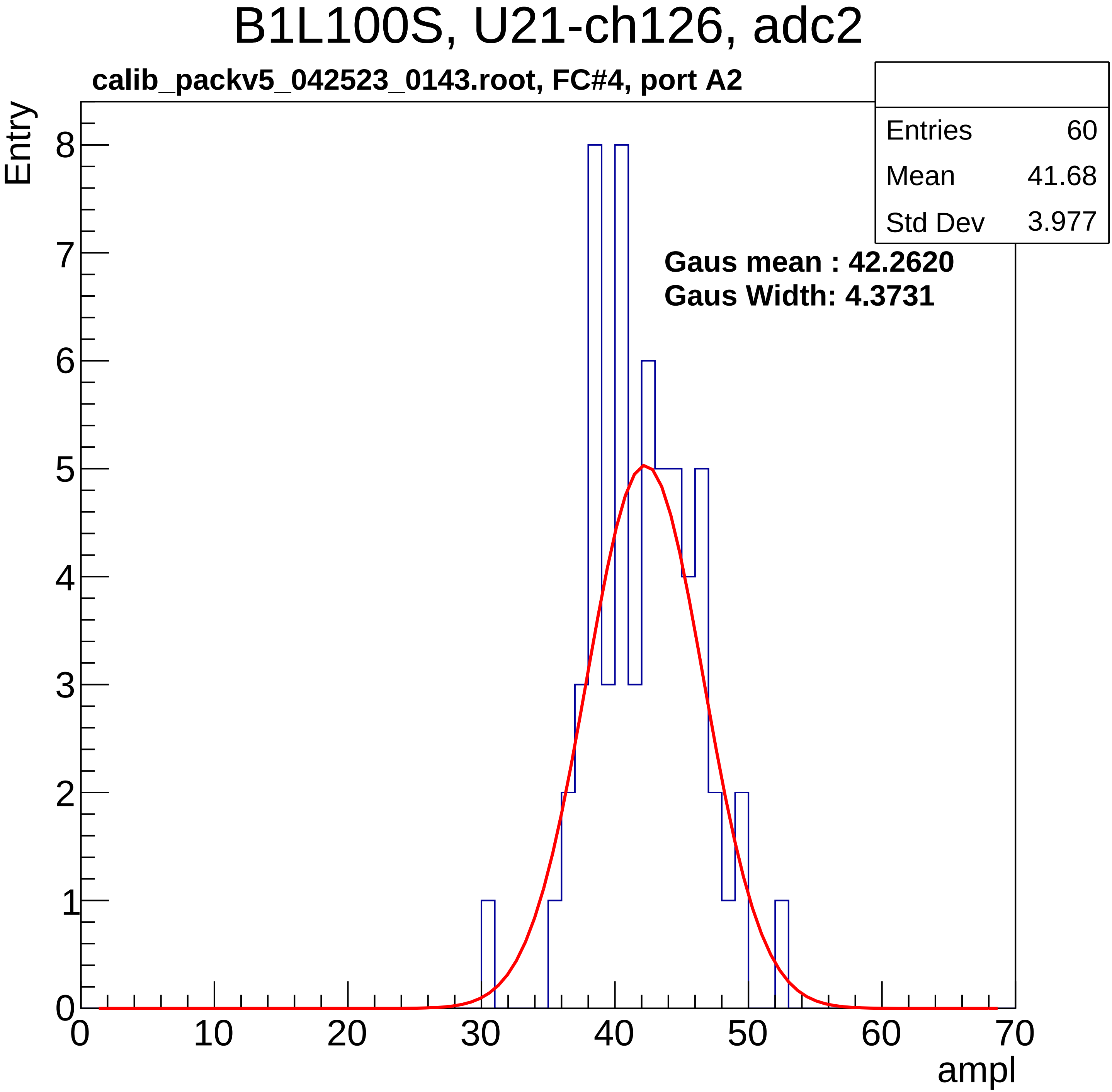



B1L100S, U21-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 27.73
Std Dev 5.096
Gaus mean : 28.2103
Gaus Width: 3.7295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

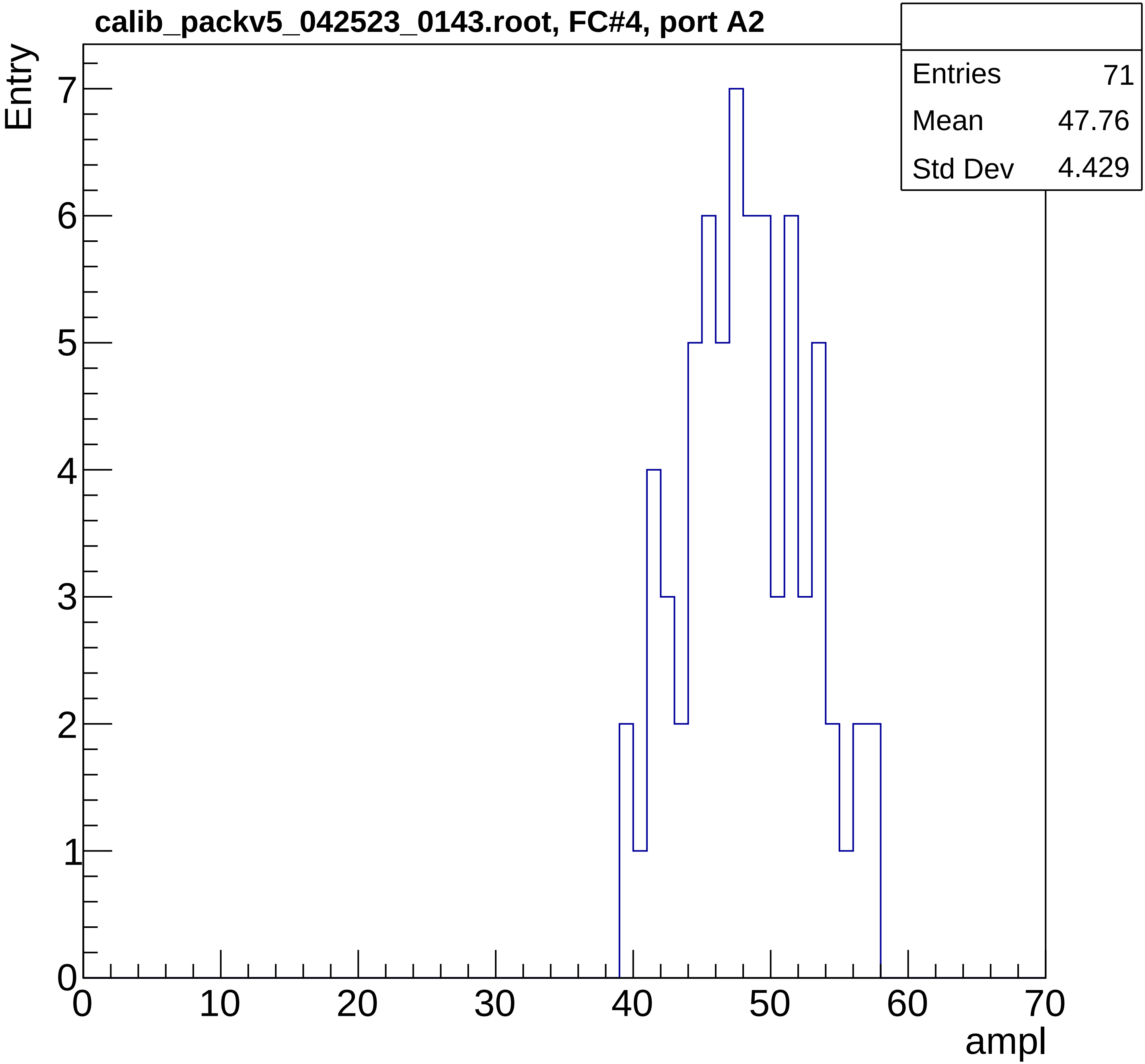
B1L100S, U21-ch126, adc1

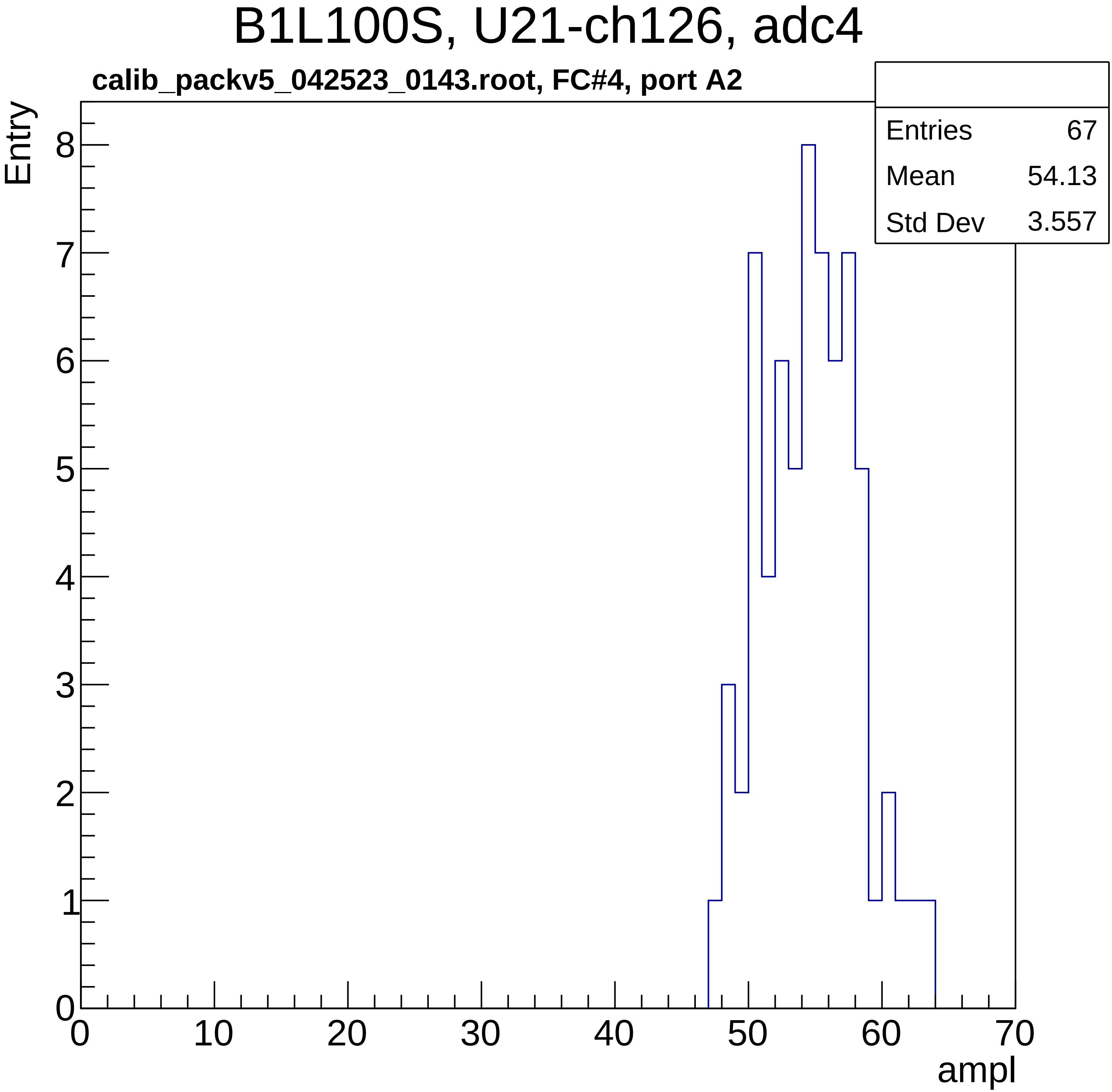
calib_packv5_042523_0143.root, FC#4, port A2

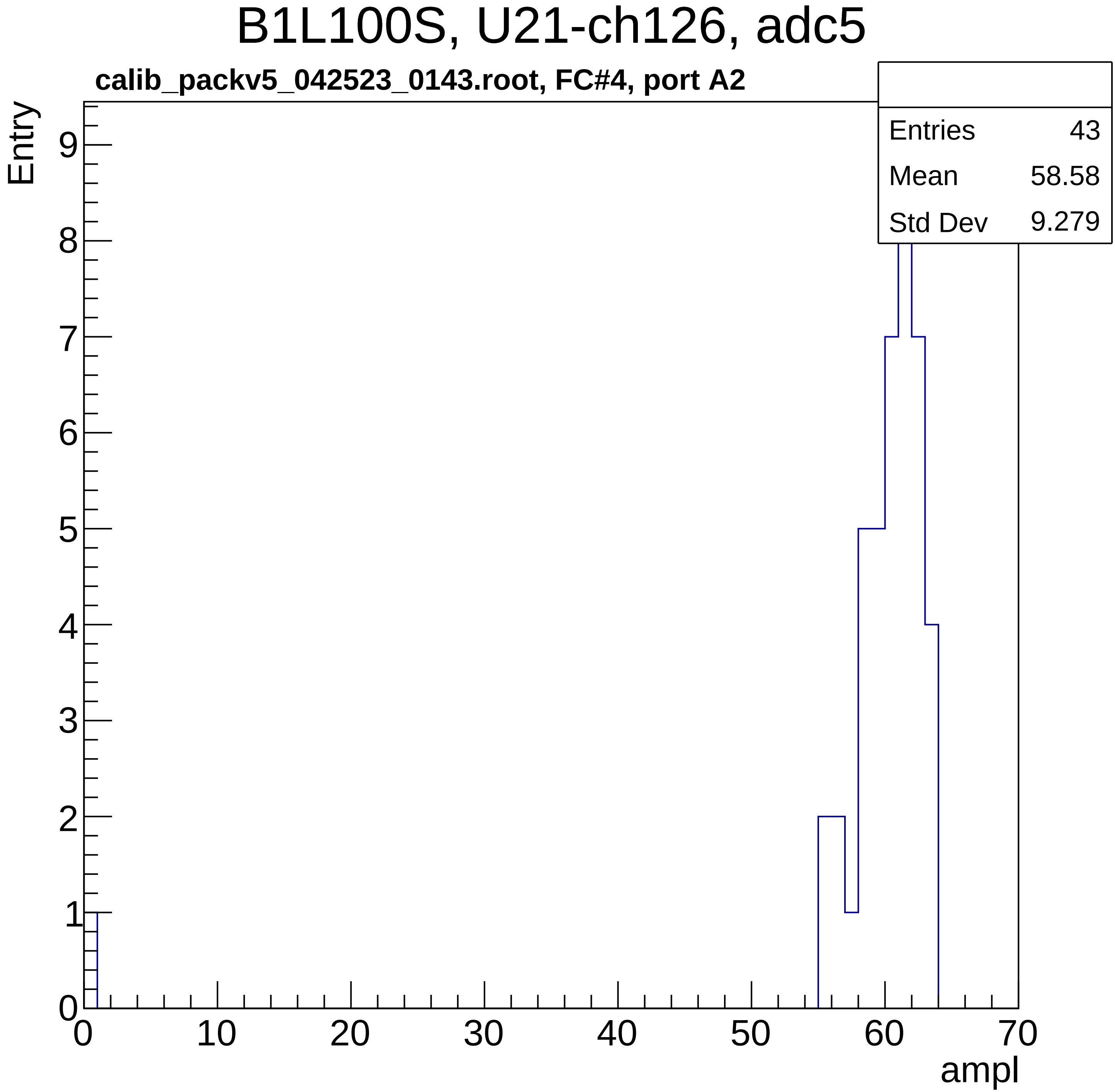



B1L100S, U21-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.68
Std Dev 3.977
Gaus mean : 42.2620
Gaus Width: 4.3731
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

B1L100S, U21-ch126, adc3

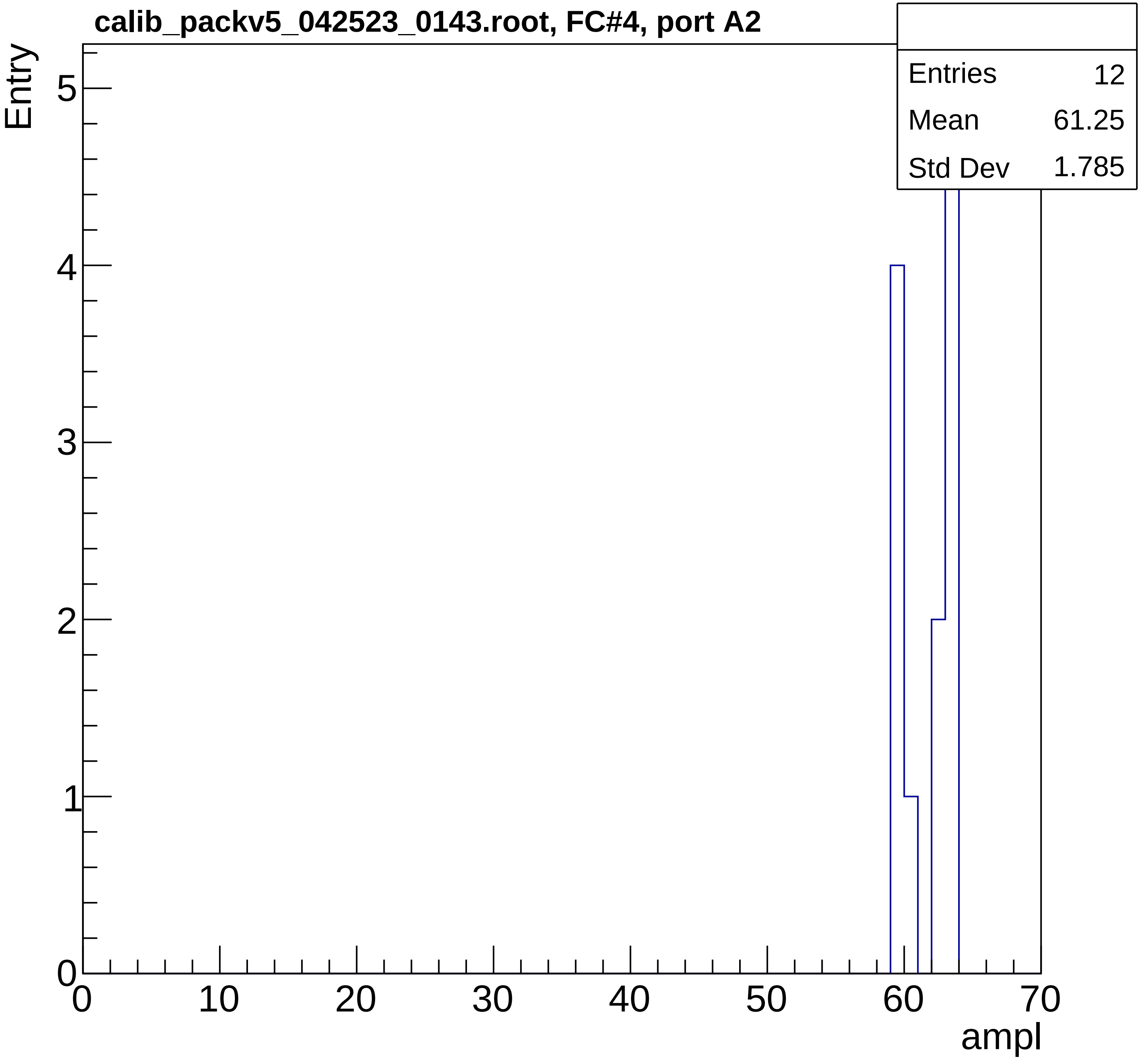



B1L100S, U21-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 54.13
Std Dev 3.557
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.58
Std Dev 9.279
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

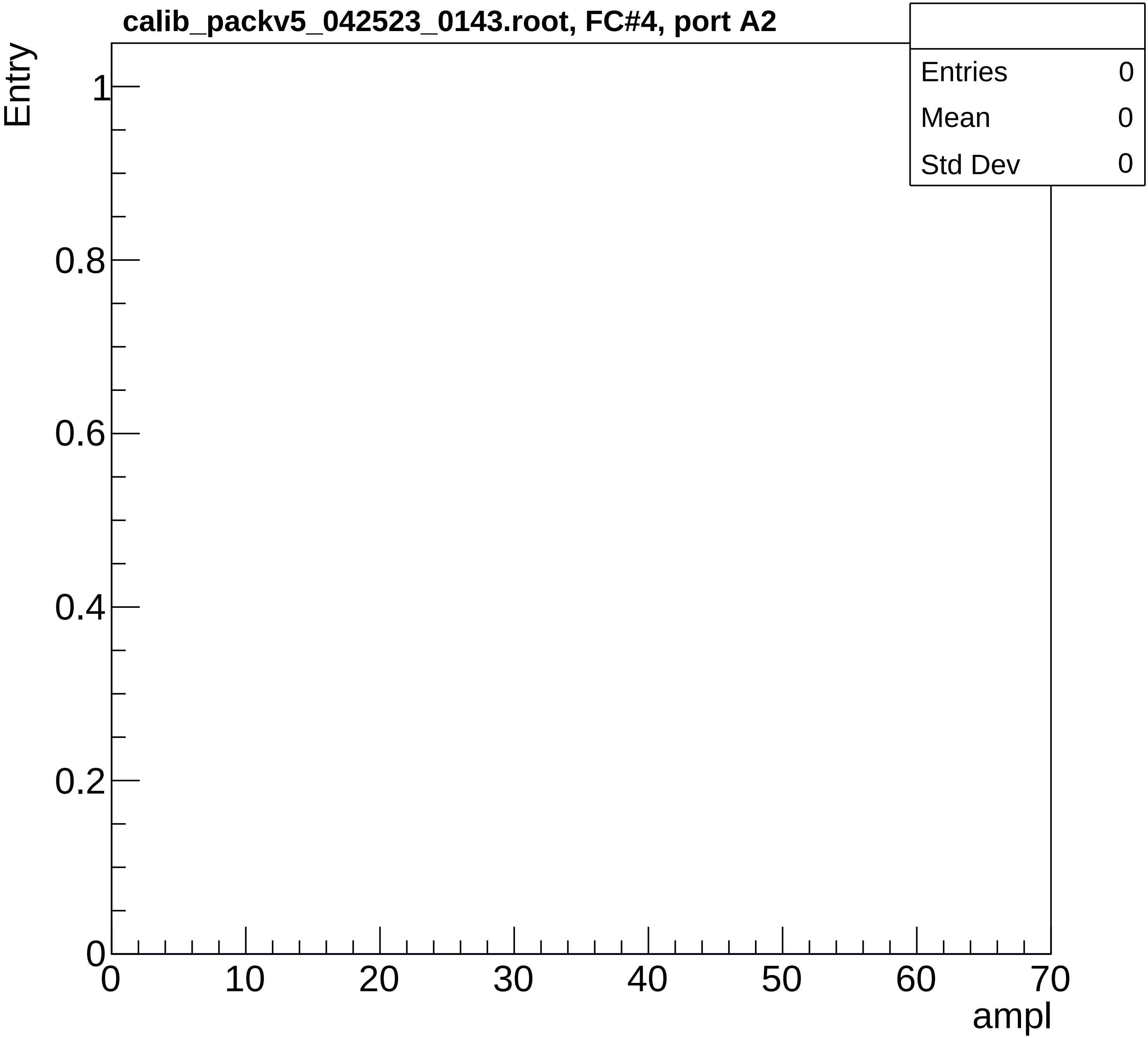
B1L100S, U21-ch126, adc6

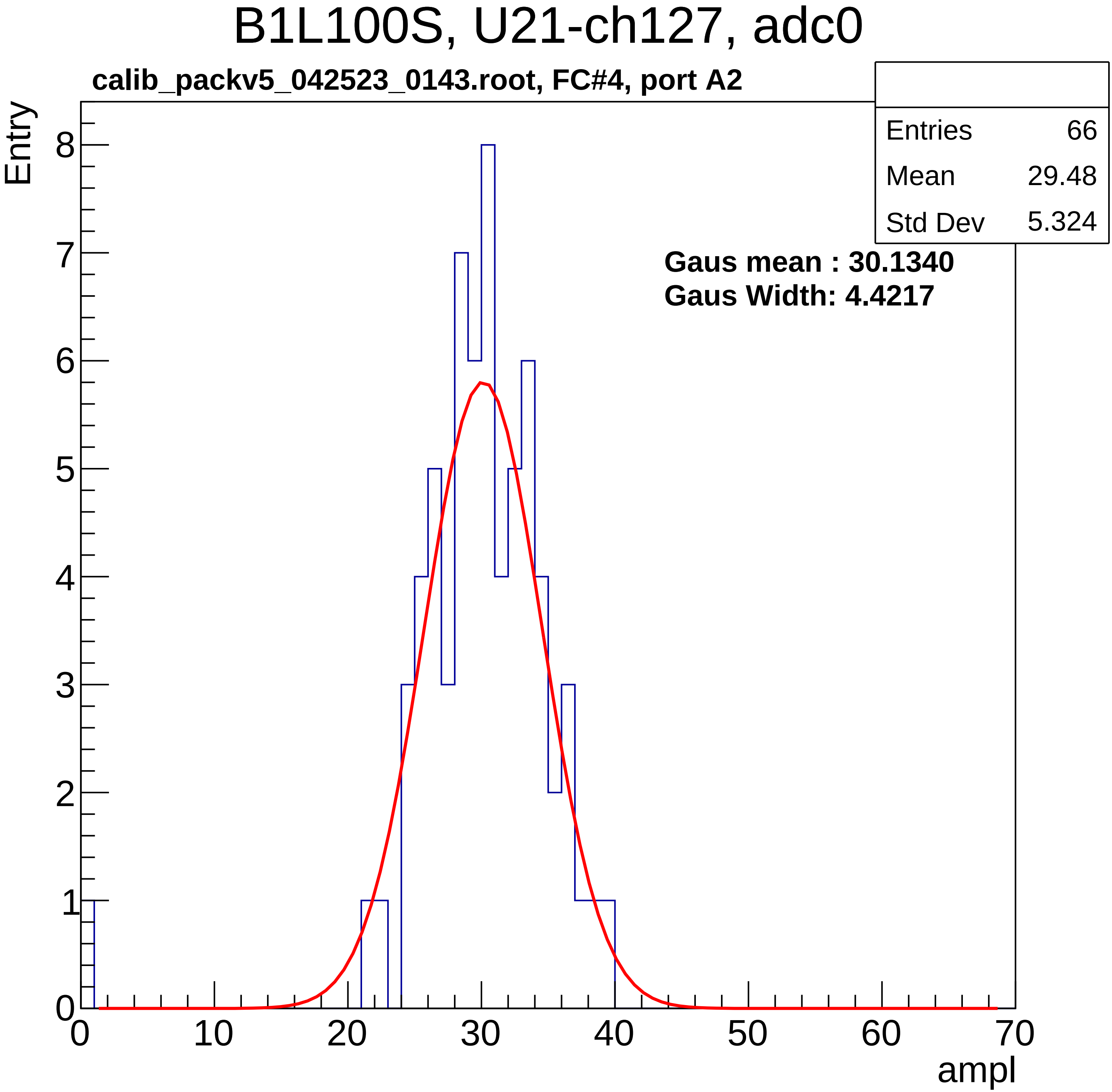
calib_packv5_042523_0143.root, FC#4, port A2

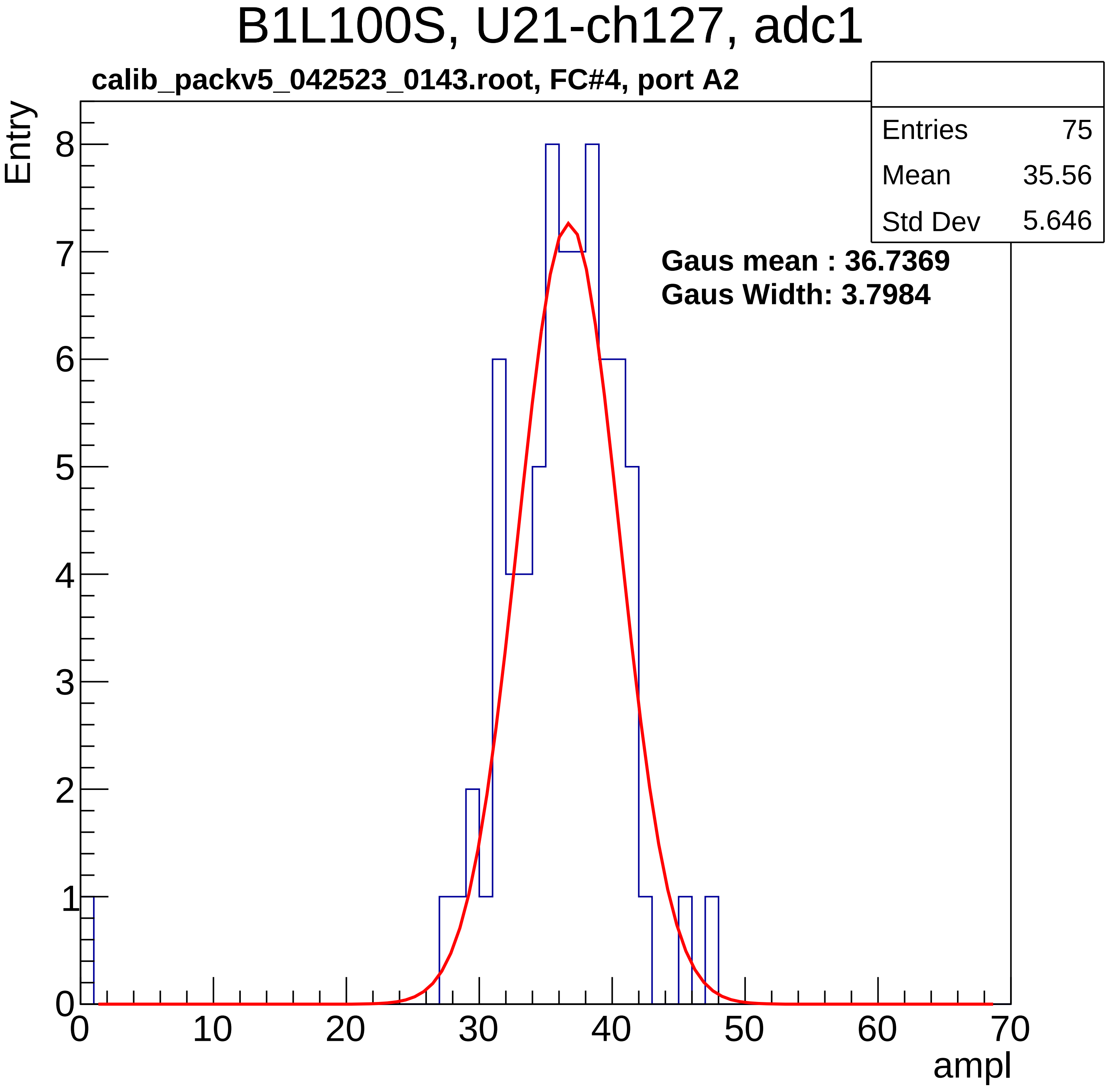


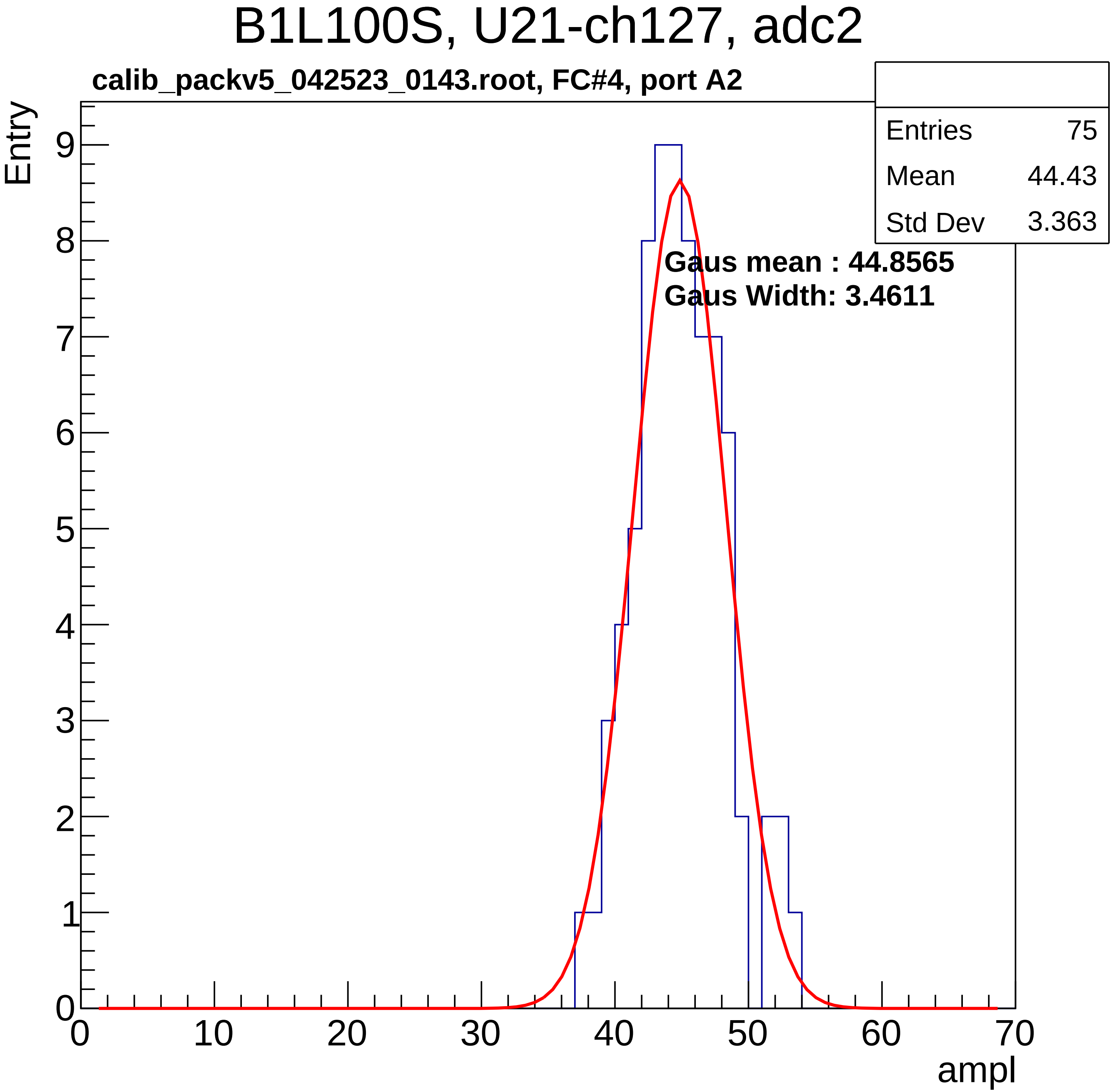
B1L100S, U21-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2



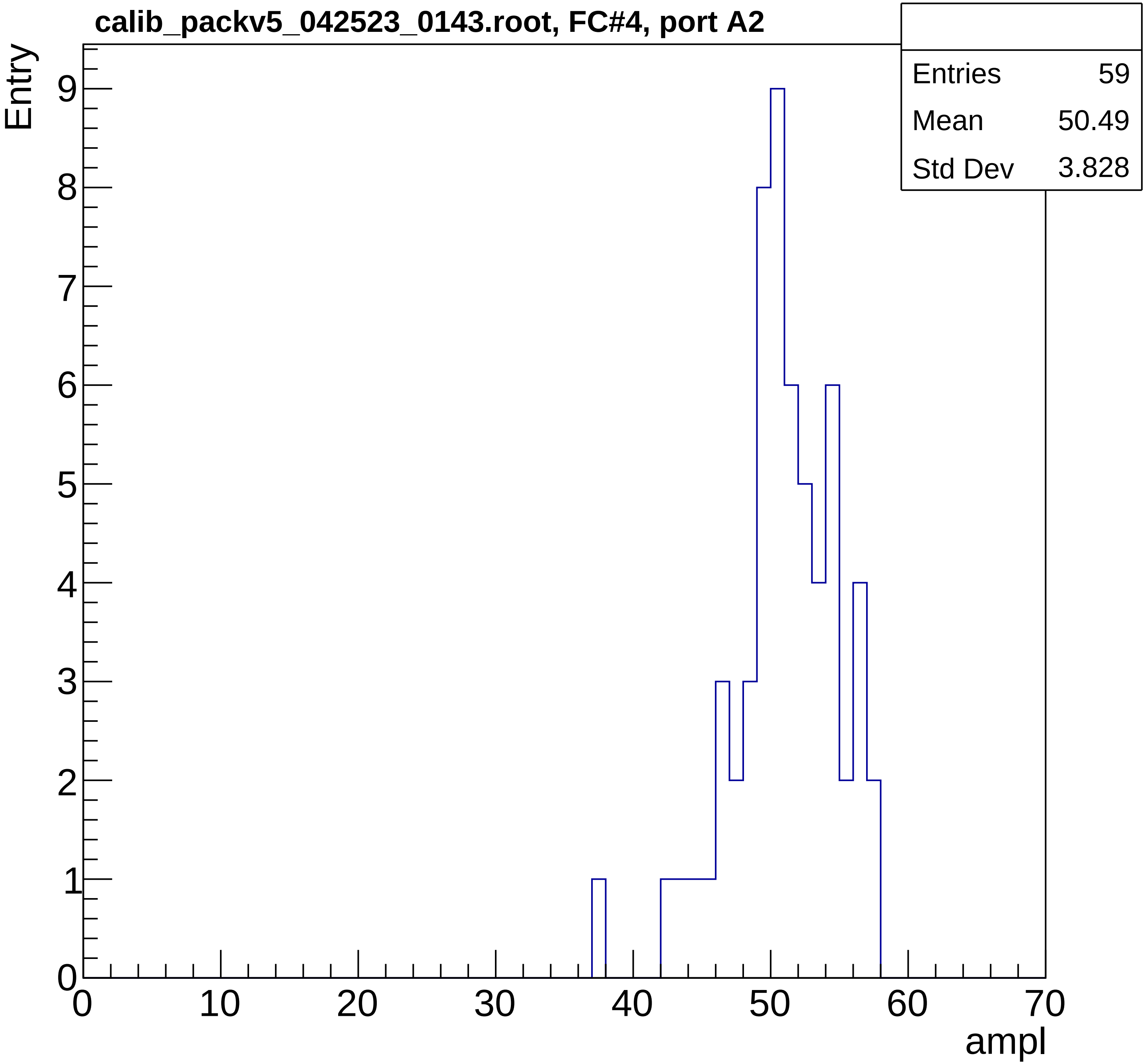

B1L100S, U21-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.48
Std Dev 5.324
Gaus mean : 30.1340
Gaus Width: 4.4217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

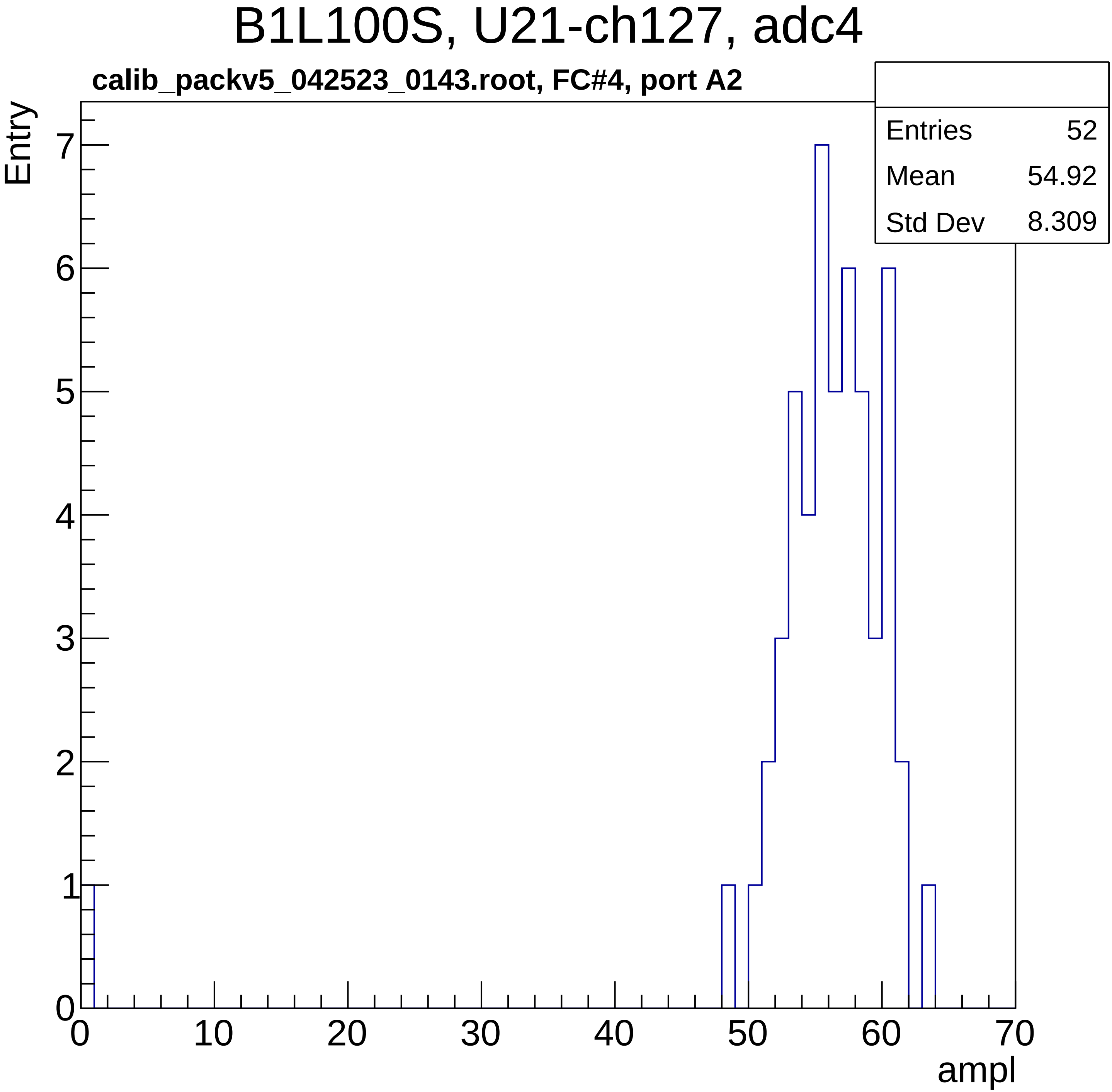

B1L100S, U21-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.56
Std Dev 5.646
Gaus mean : 36.7369
Gaus Width: 3.7984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

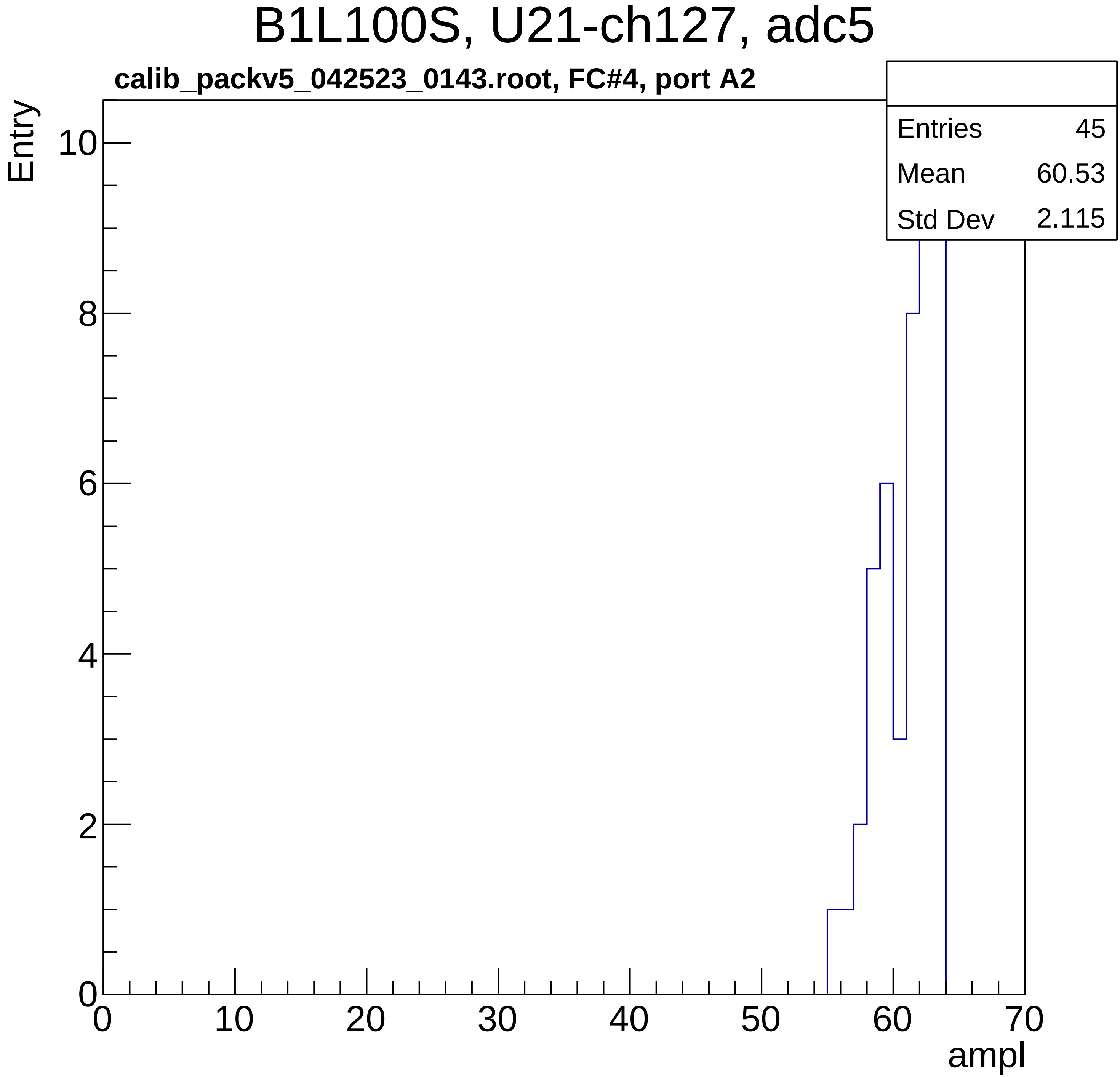

B1L100S, U21-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.43
Std Dev 3.363
Gaus mean : 44.8565
Gaus Width: 3.4611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

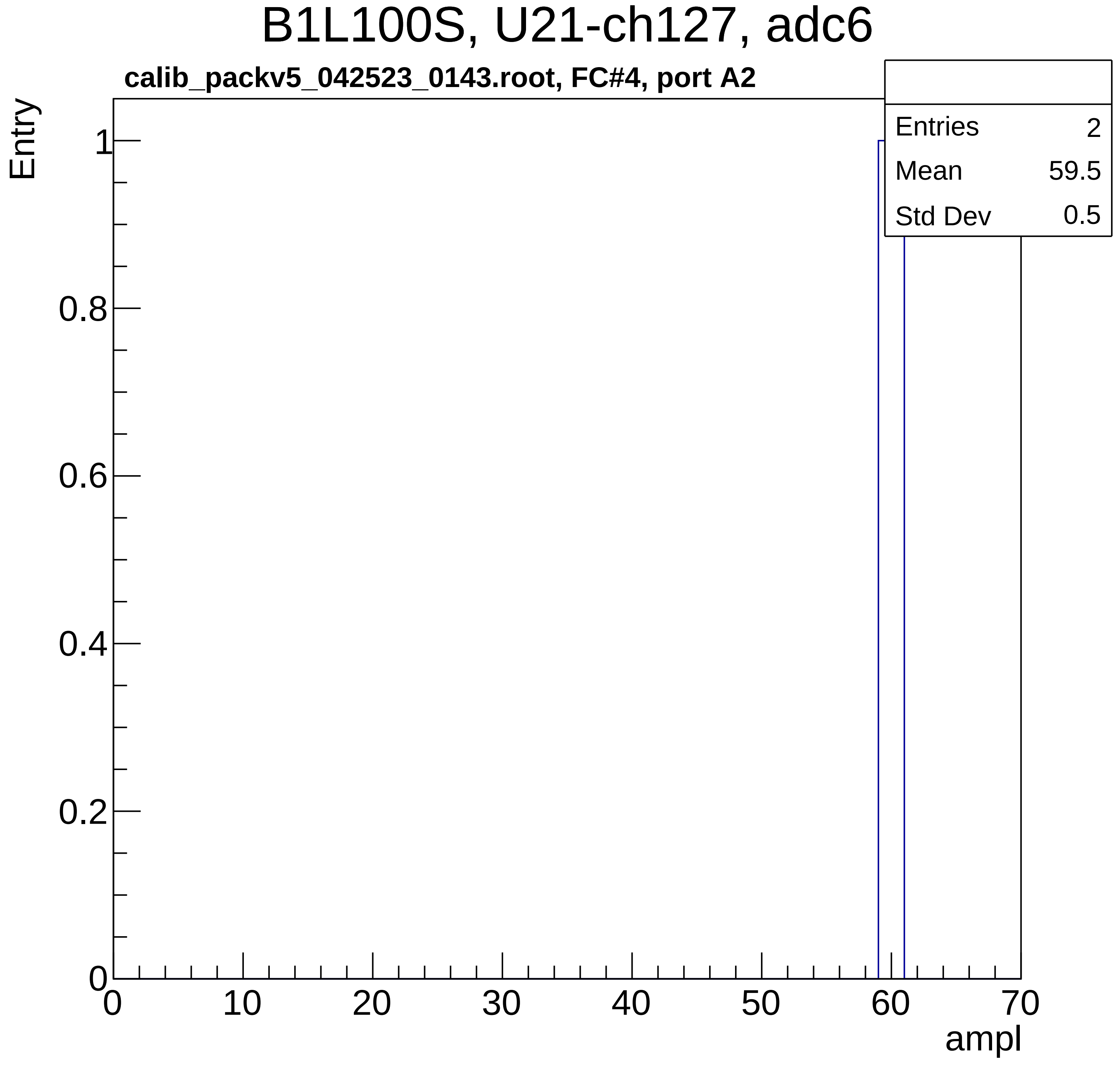
B1L100S, U21-ch127, adc3

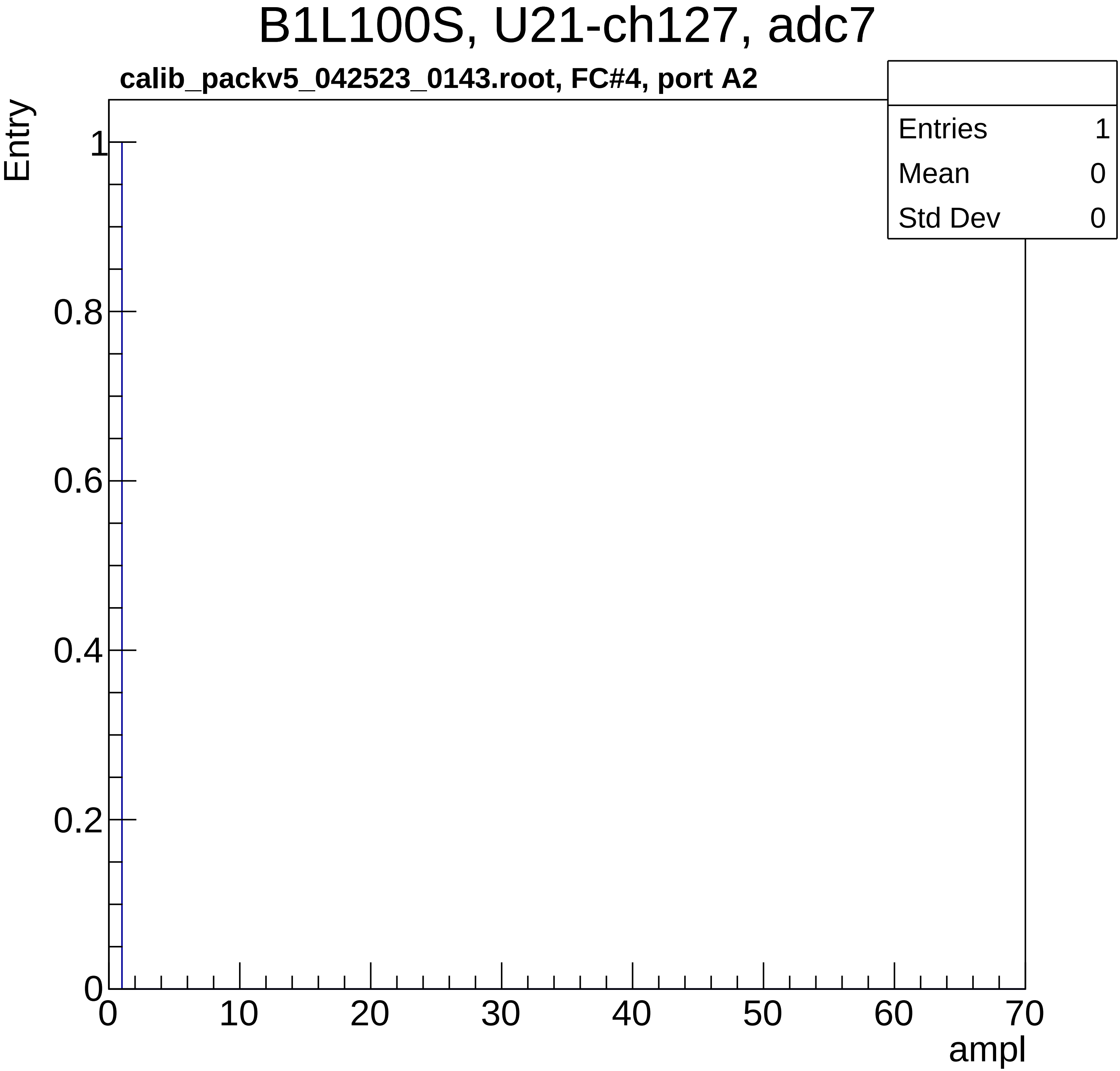
calib_packv5_042523_0143.root, FC#4, port A2

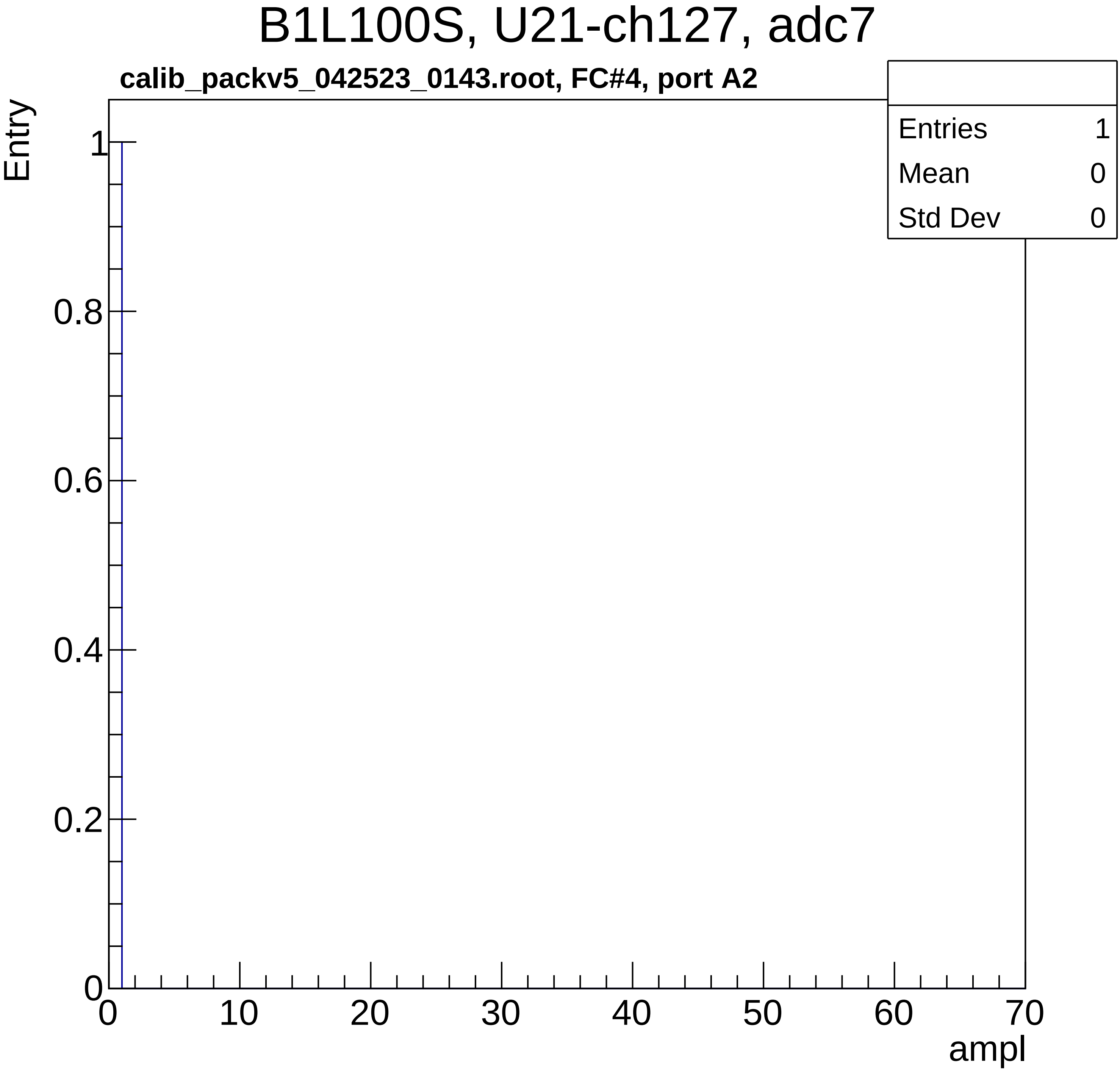



B1L100S, U21-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.92
Std Dev 8.309
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 60.53
Std Dev 2.115
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entry
ampl
Entries 2
Mean 59.5
Std Dev 0.5
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U21-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L100S, U21-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70