

B1L101S, U22-ch0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.9924

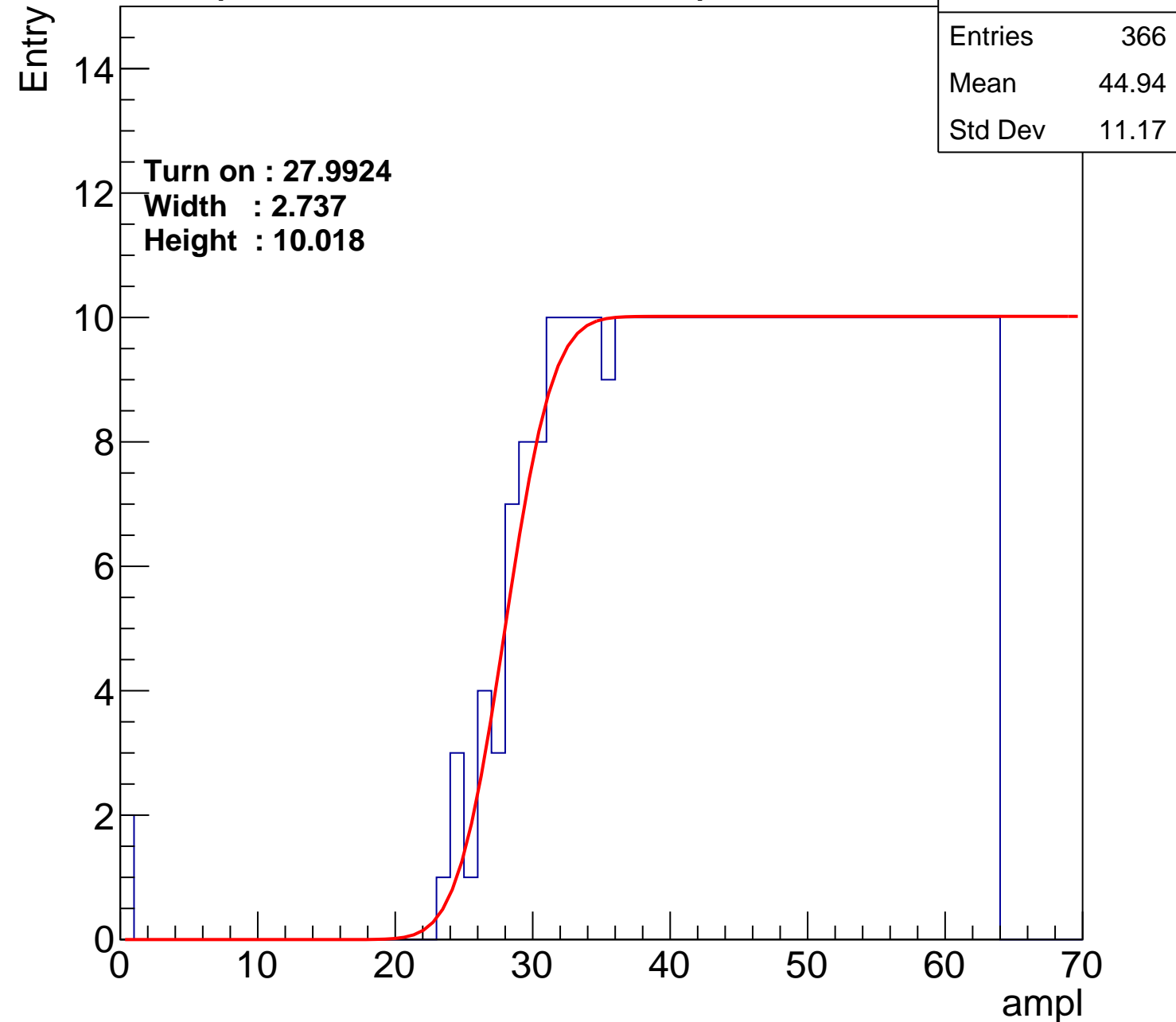
Width : 2.737

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch1

calib_packv5_042523_0143.root, FC#0, port D2

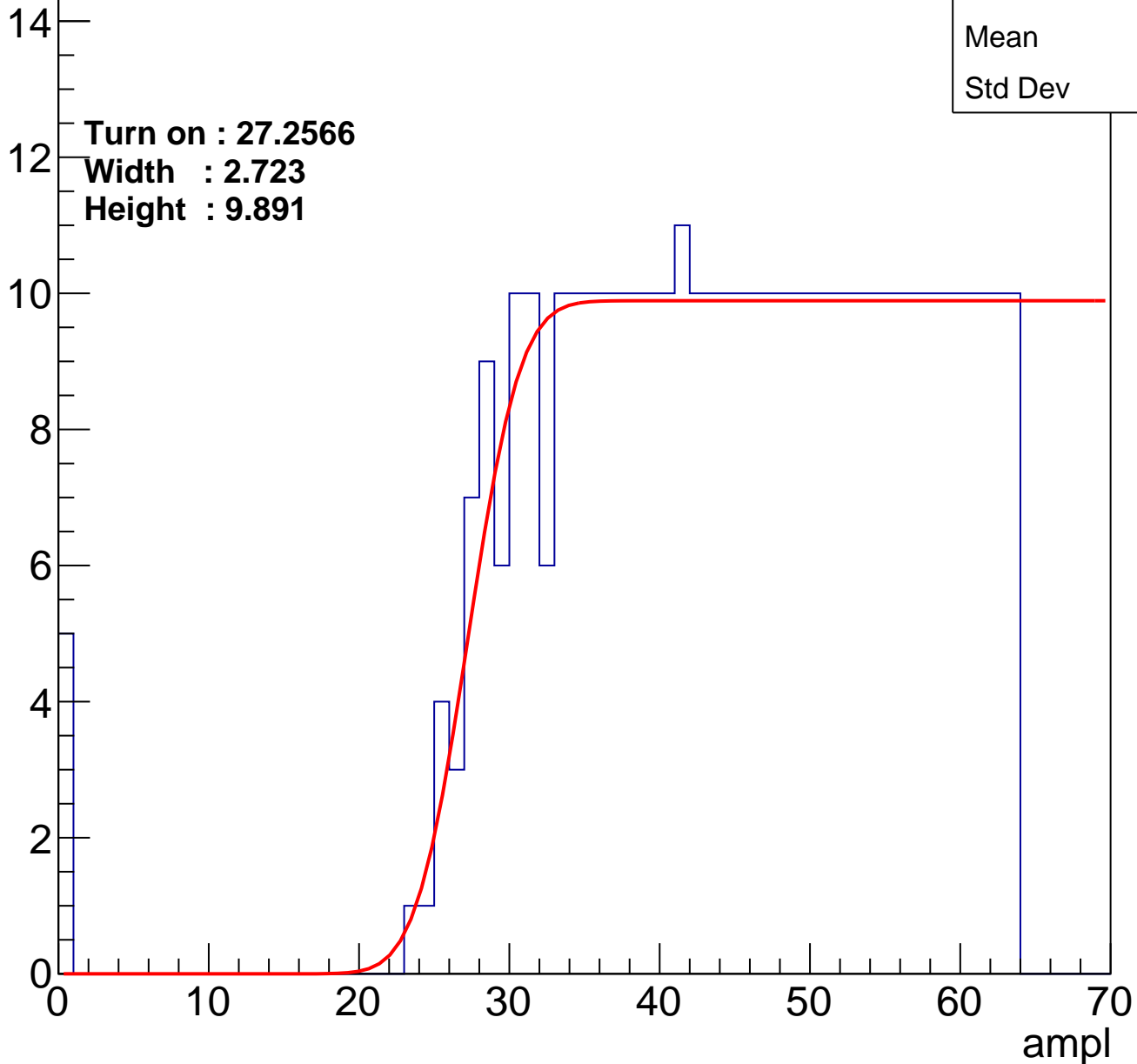
Entries	373
Mean	44.4
Std Dev	11.9

Turn on : 27.2566

Width : 2.723

Height : 9.891

Entry



B1L101S, U22-ch2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.48
Std Dev	11.28

Turn on : 26.4293

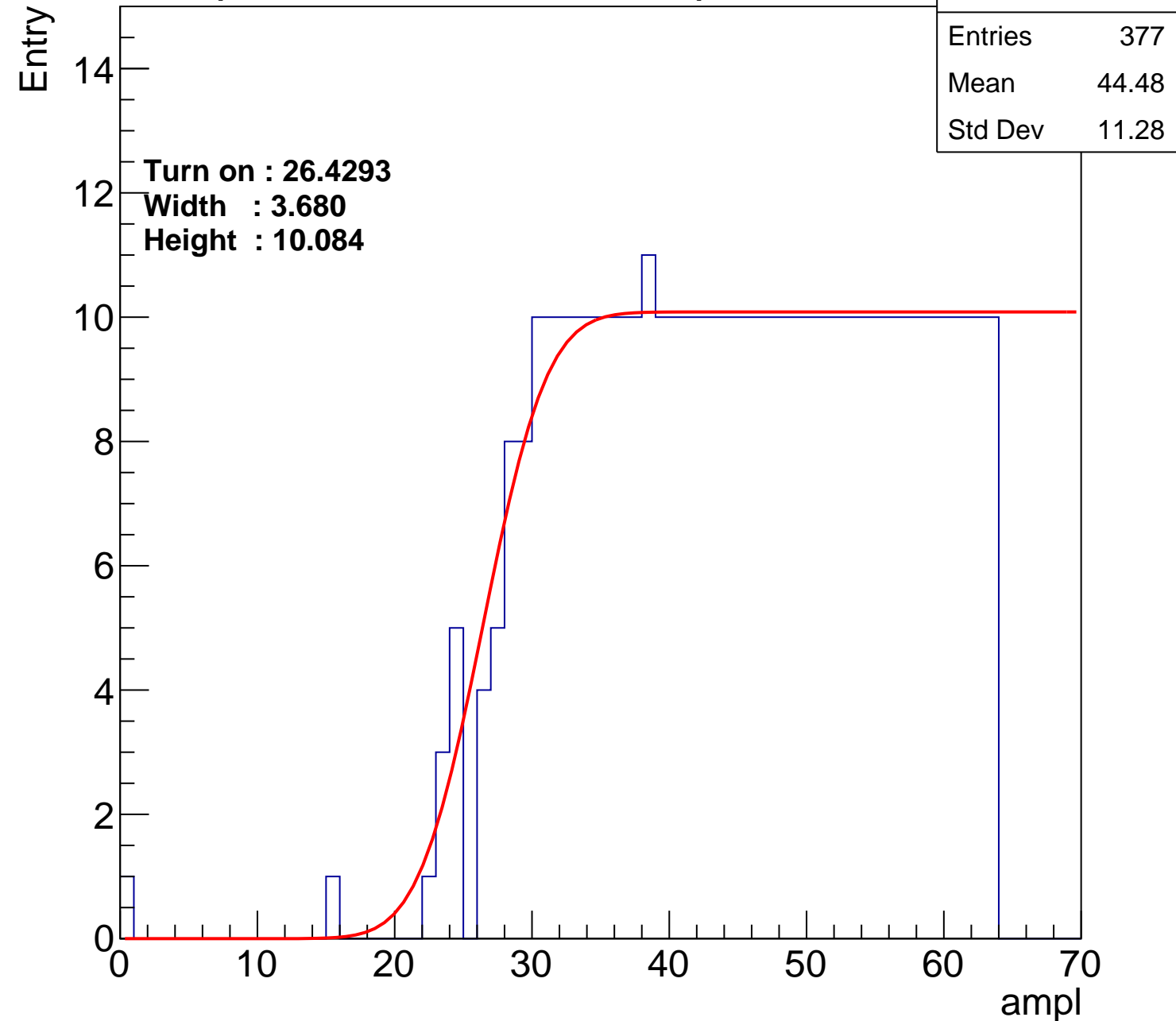
Width : 3.680

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	345
Mean	45.72
Std Dev	11.26

Turn on : 30.1645

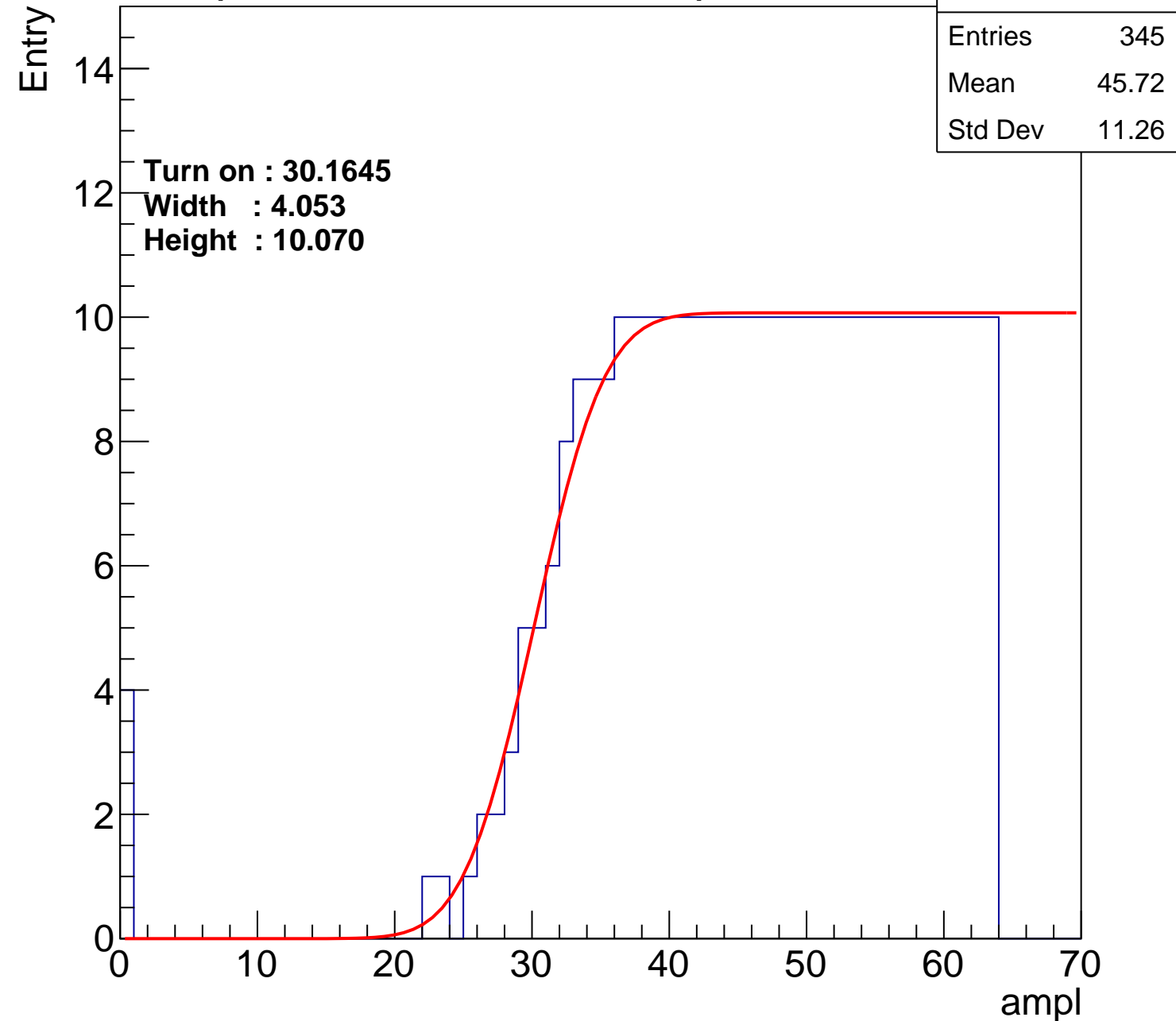
Width : 4.053

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.8
Std Dev	11.46

Turn on : 27.7642

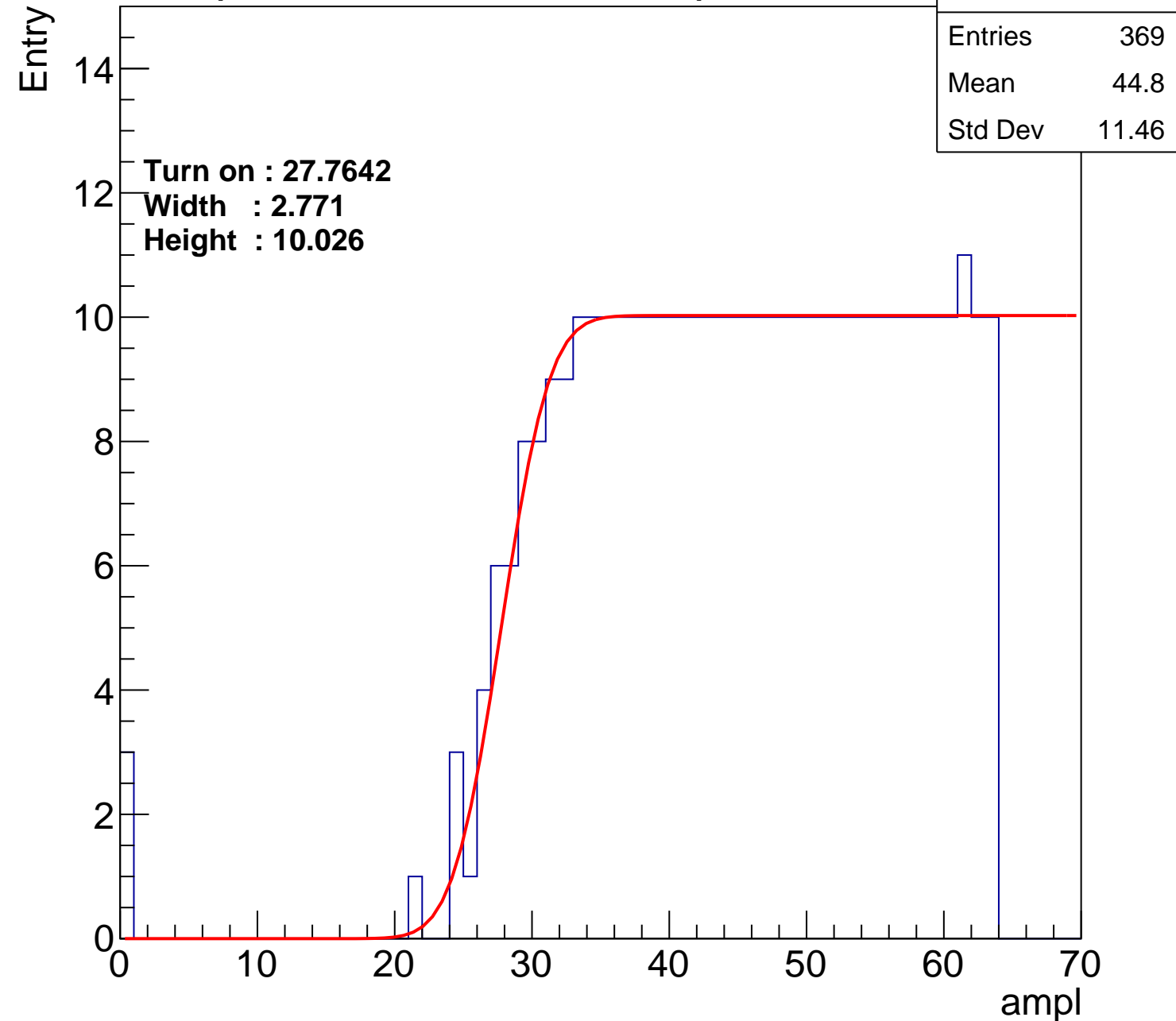
Width : 2.771

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.17
Std Dev	11.08

Turn on : 28.9297

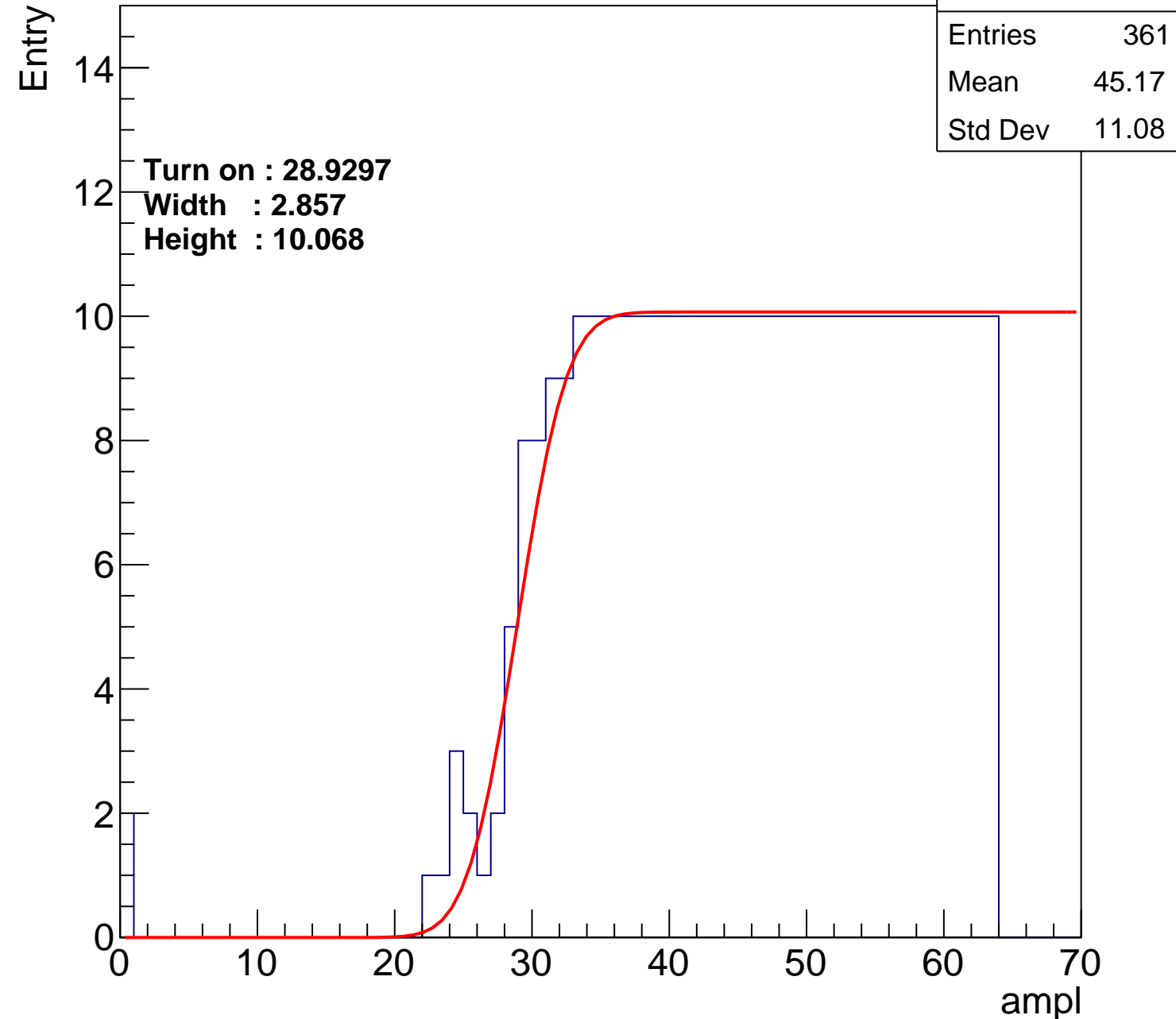
Width : 2.857

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch6

calib_packv5_042523_0143.root, FC#0, port D2

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.1385

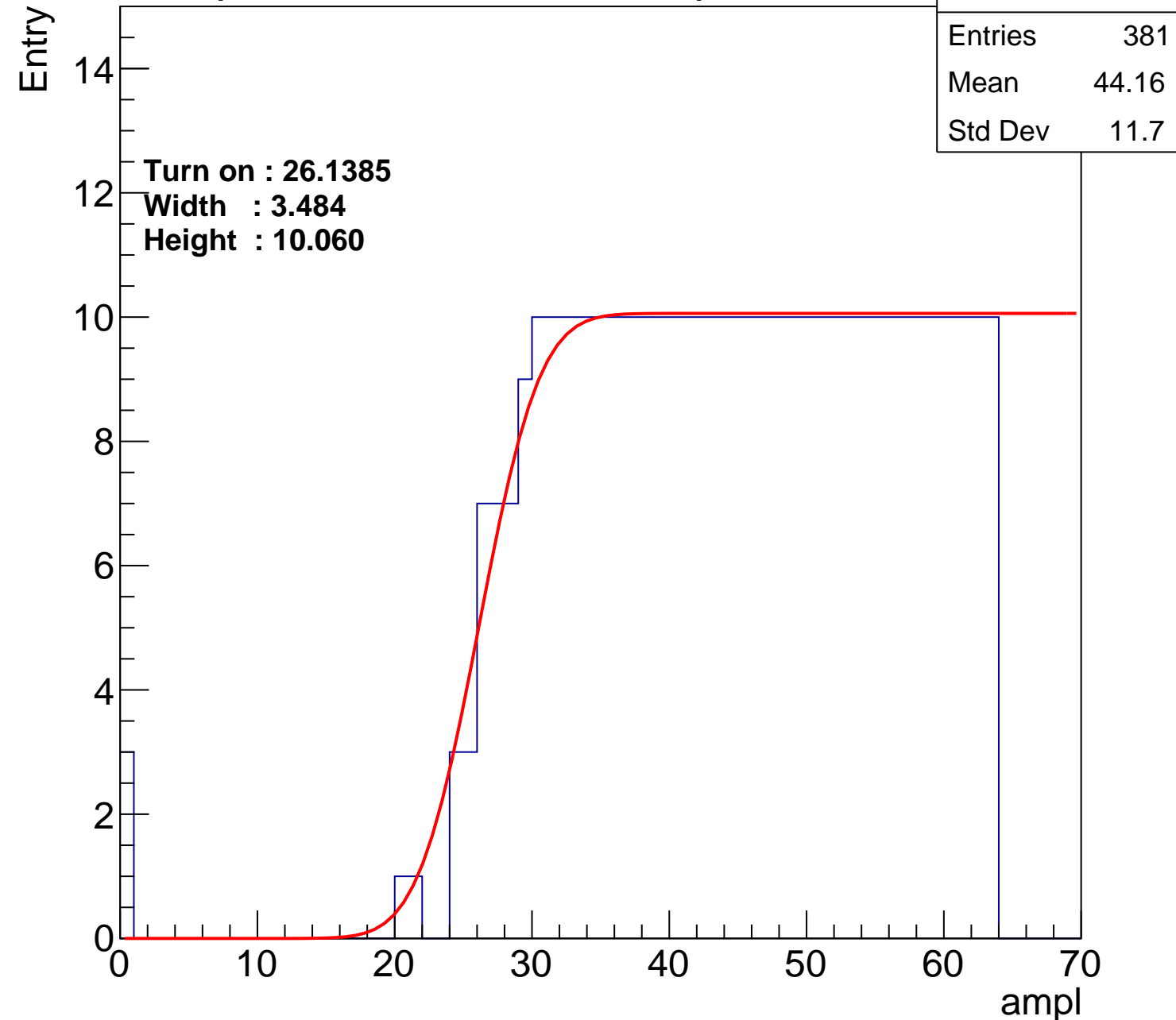
Width : 3.484

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch7

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 27.4886

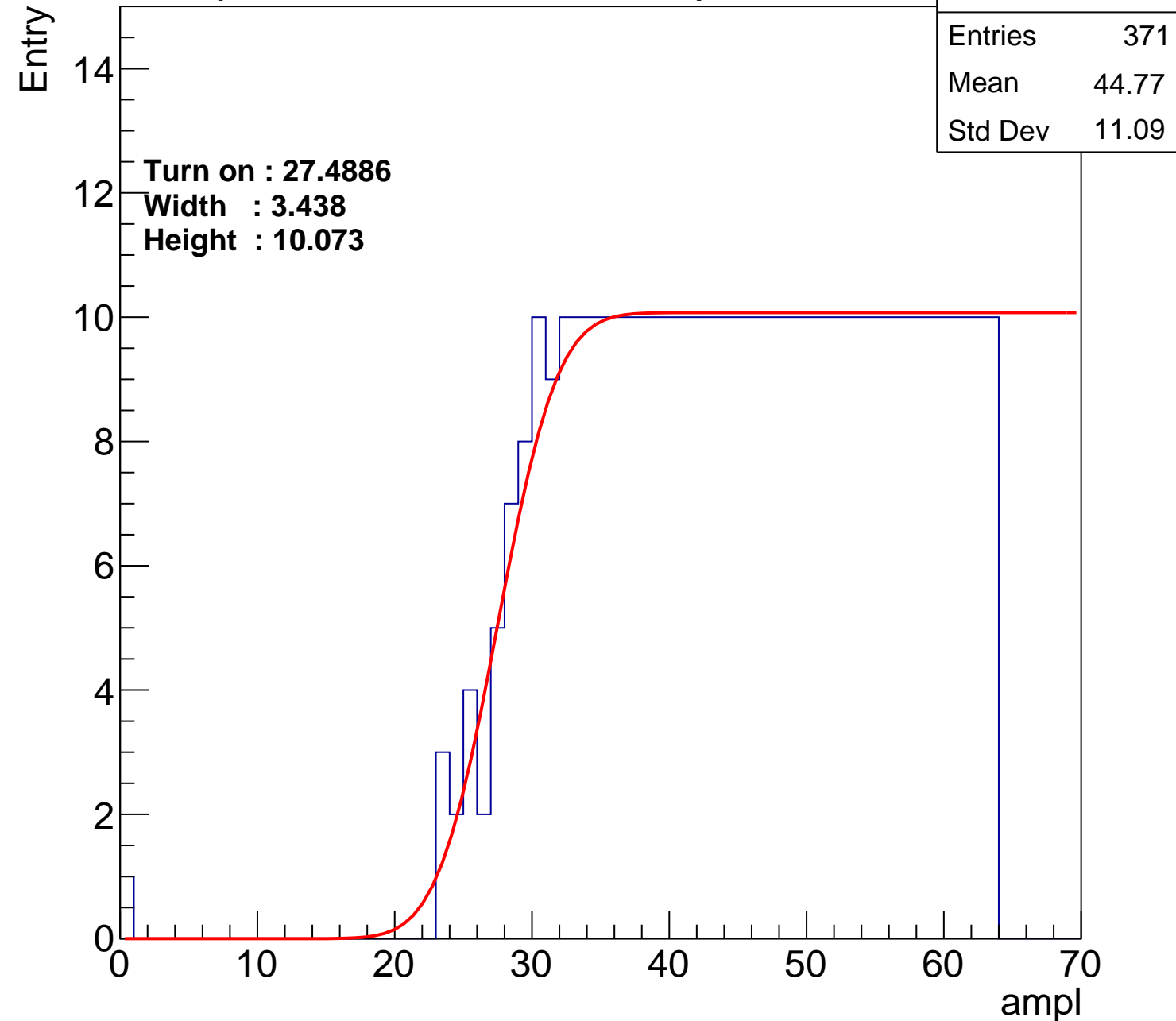
Width : 3.438

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch8

calib_packv5_042523_0143.root, FC#0, port D2

Entries	378
Mean	44.31
Std Dev	11.63

Turn on : 27.1041

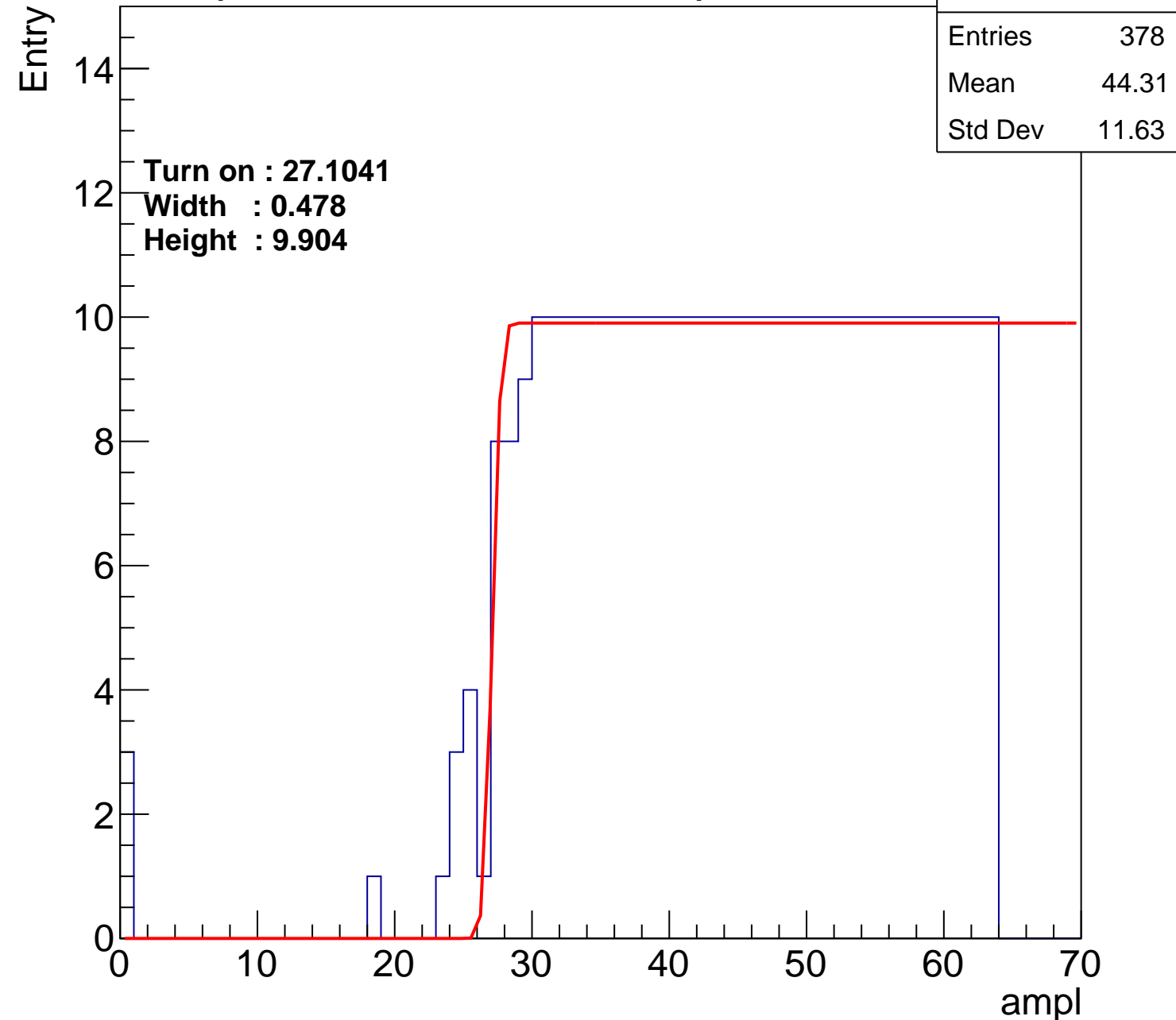
Width : 0.478

Height : 9.904

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch9

calib_packv5_042523_0143.root, FC#0, port D2

Entries	352
Mean	45.65
Std Dev	10.8

Turn on : 29.3505

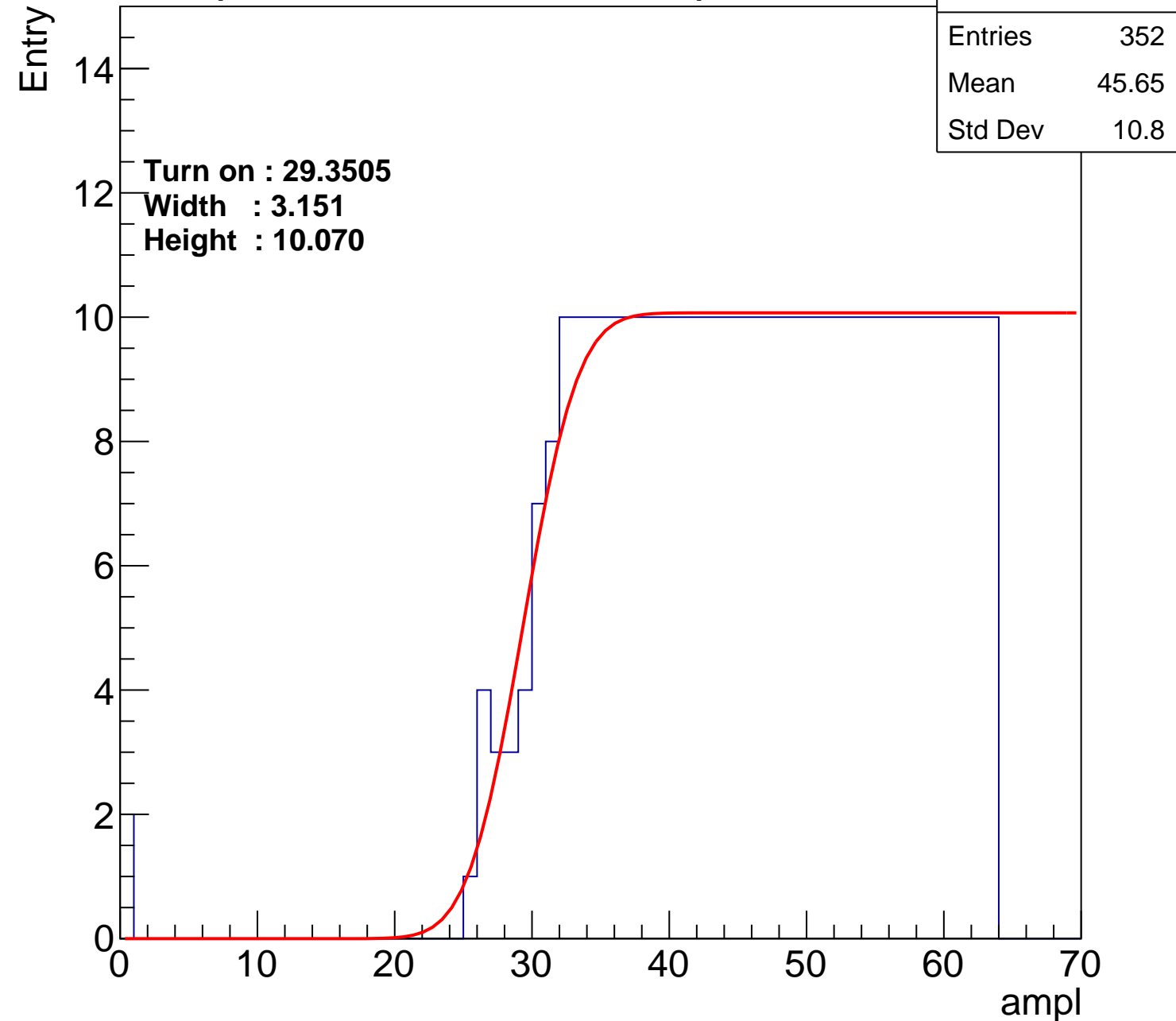
Width : 3.151

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch10

calib_packv5_042523_0143.root, FC#0, port D2

Entries	336
Mean	46.39
Std Dev	10.63

Turn on : 31.1534

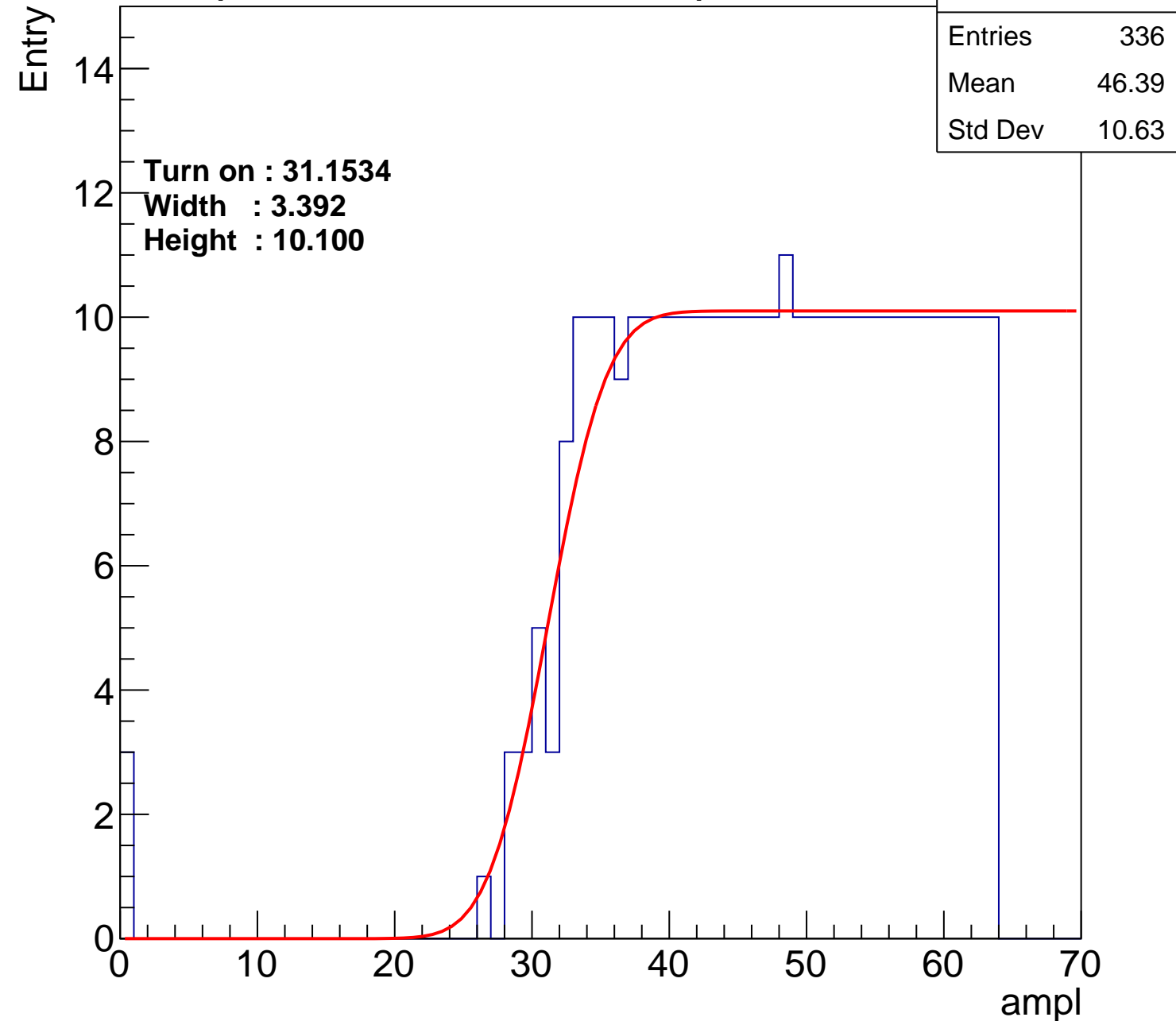
Width : 3.392

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch11

calib_packv5_042523_0143.root, FC#0, port D2

Entries	360
Mean	45.33
Std Dev	10.79

Turn on : 28.7588

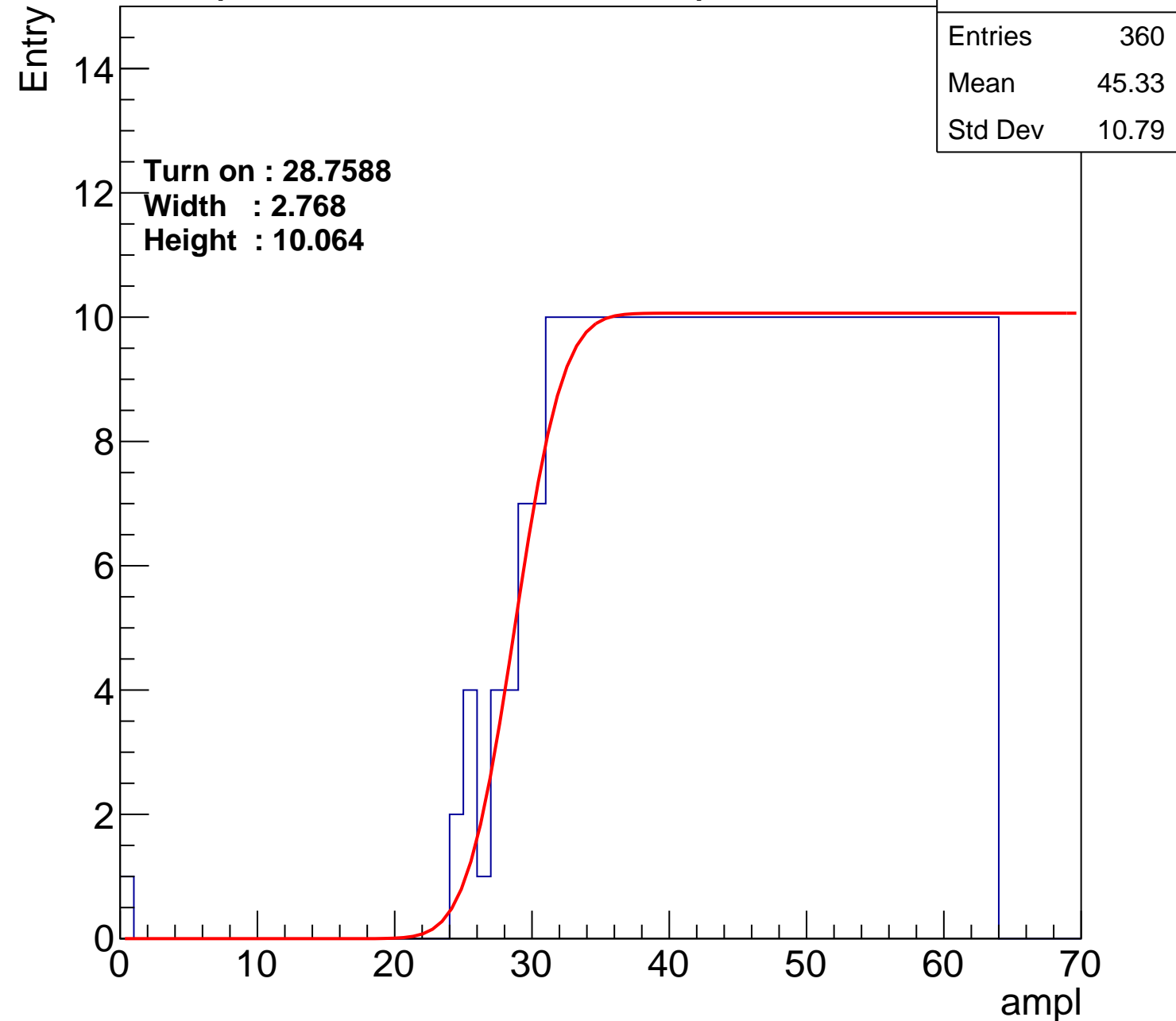
Width : 2.768

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch12

calib_packv5_042523_0143.root, FC#0, port D2

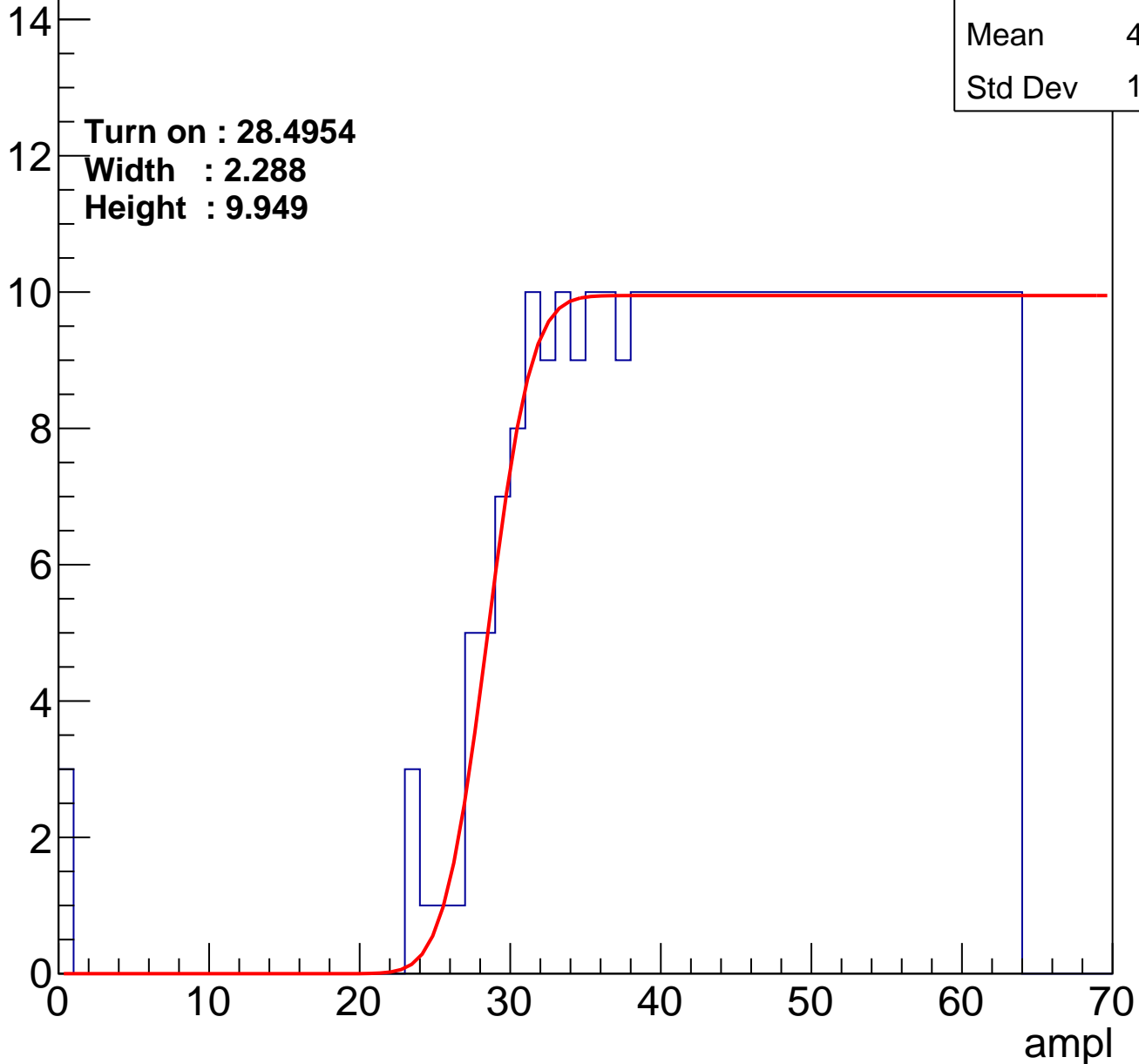
Entries	361
Mean	45.07
Std Dev	11.32

Turn on : 28.4954

Width : 2.288

Height : 9.949

Entry



B1L101S, U22-ch13

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.97
Std Dev	11.12

Turn on : 27.9236

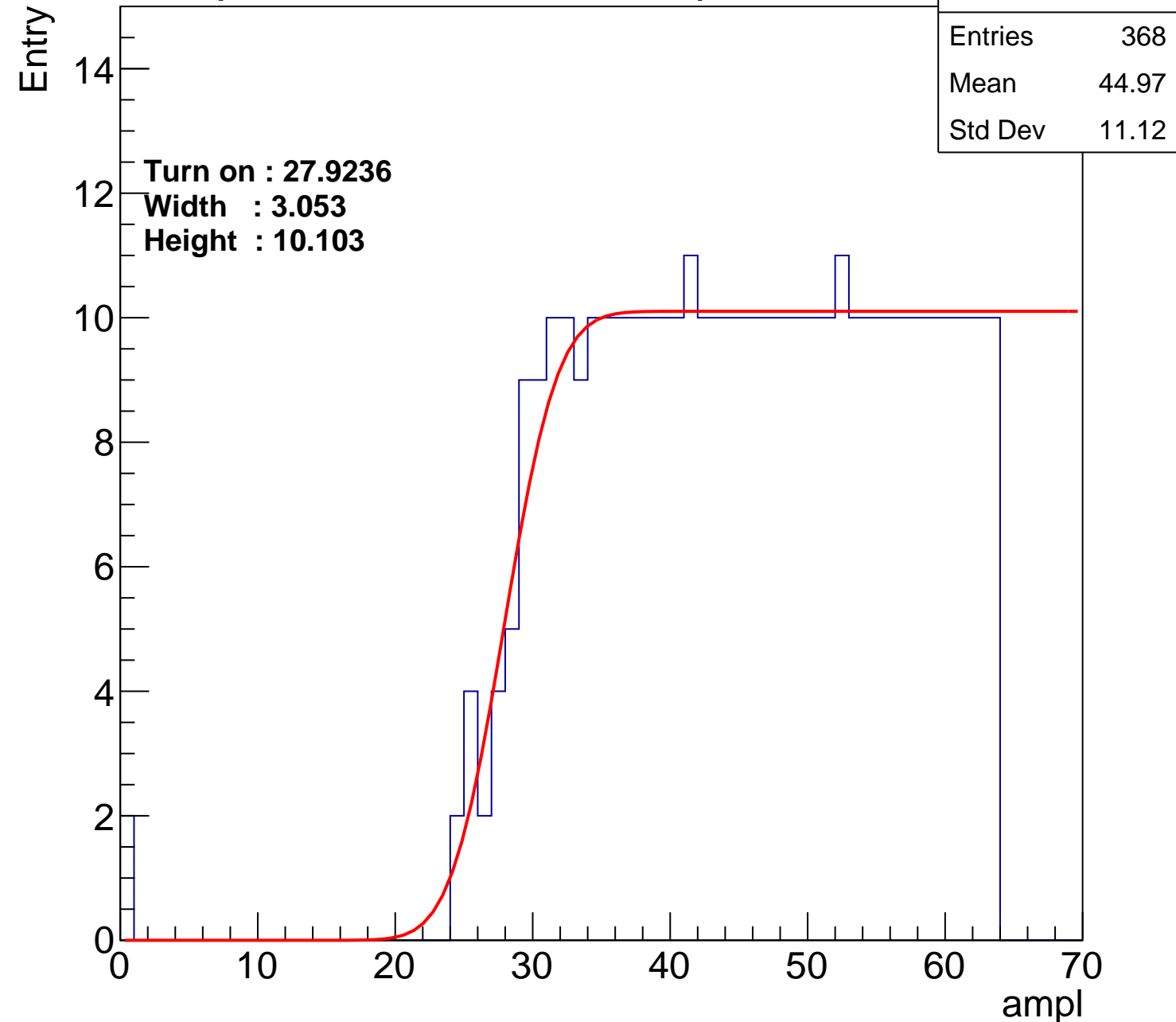
Width : 3.053

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch14

calib_packv5_042523_0143.root, FC#0, port D2

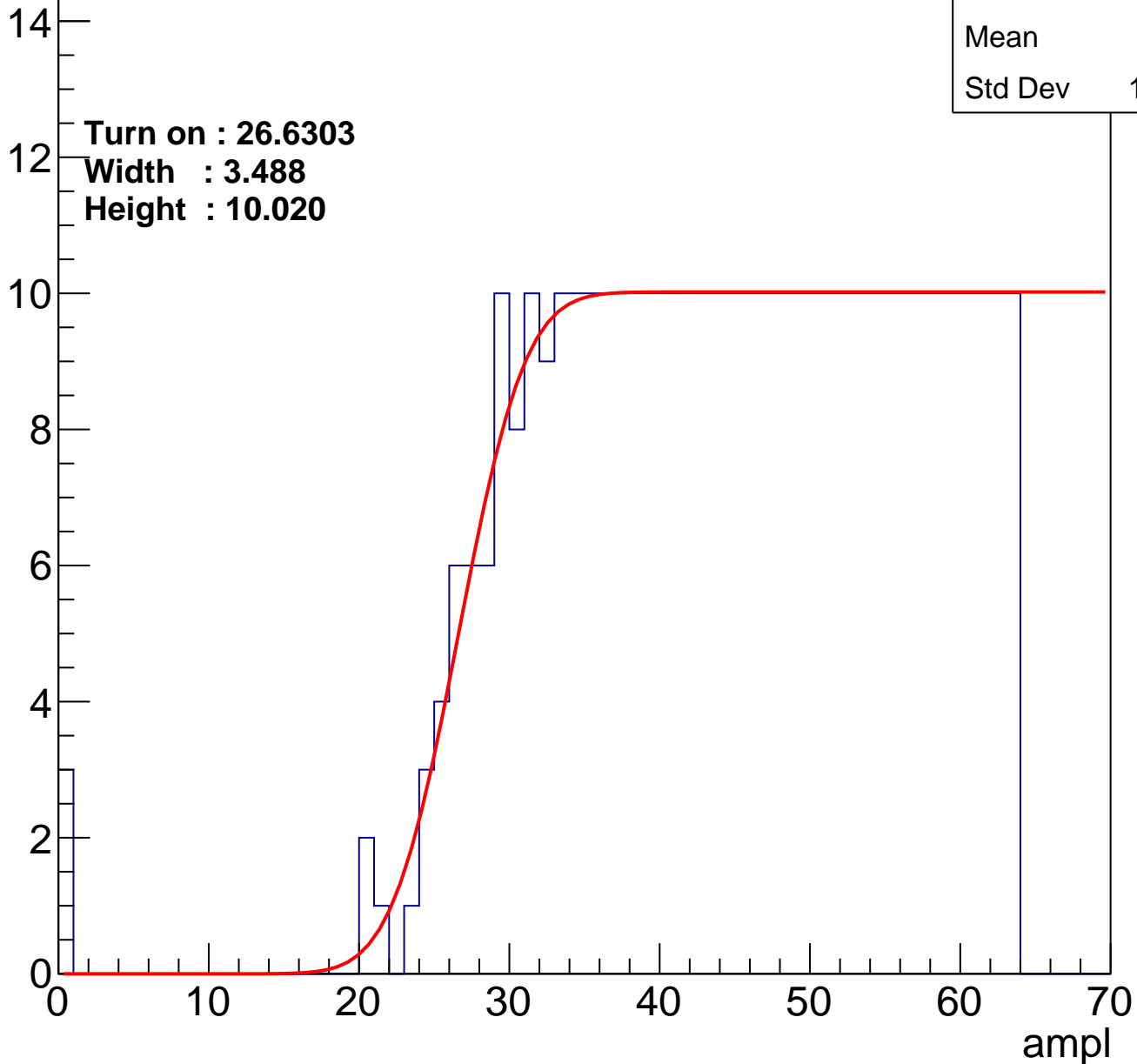
Entries	379
Mean	44.2
Std Dev	11.75

Turn on : 26.6303

Width : 3.488

Height : 10.020

Entry



B1L101S, U22-ch15

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.6418

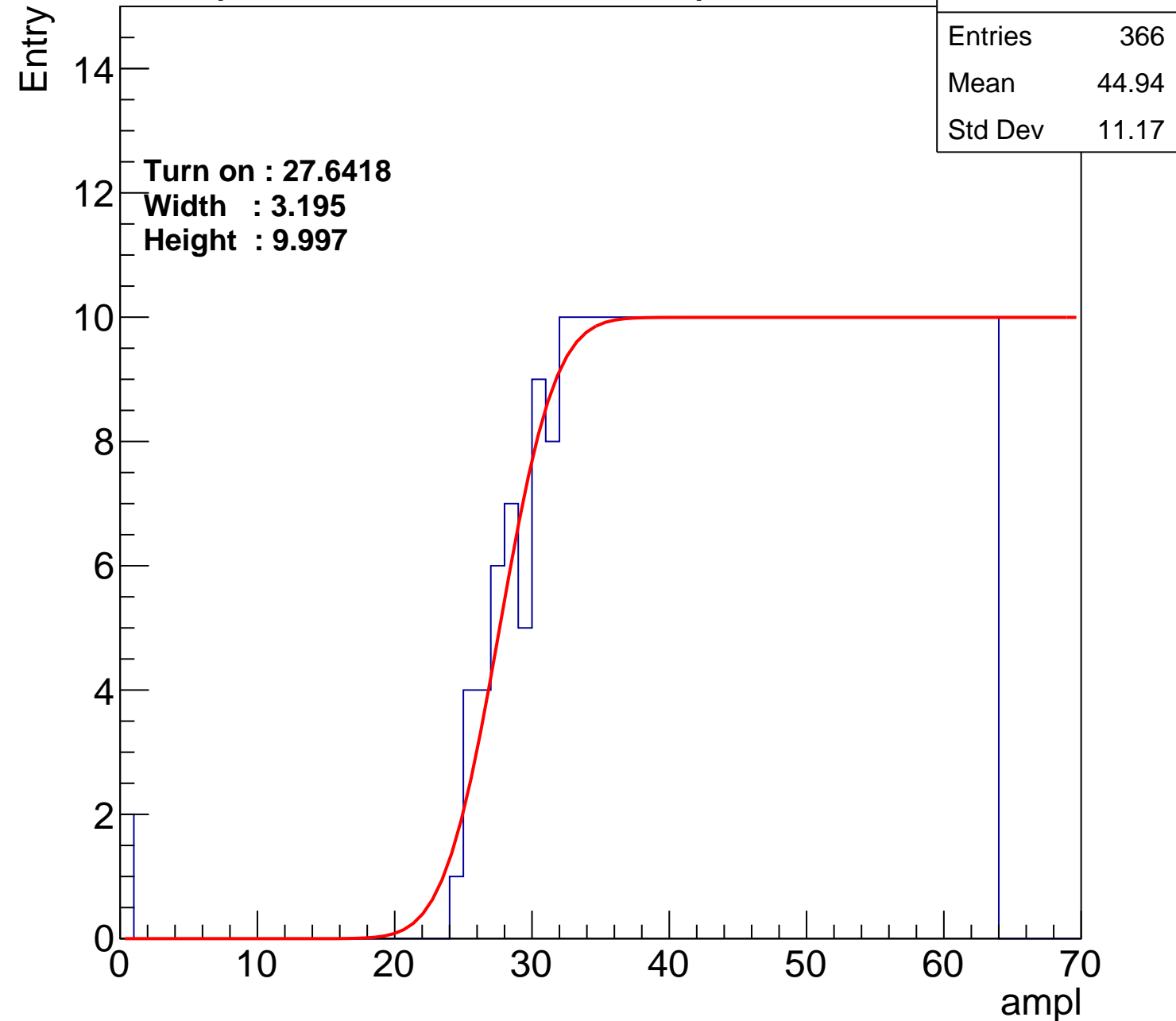
Width : 3.195

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch16

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.6397

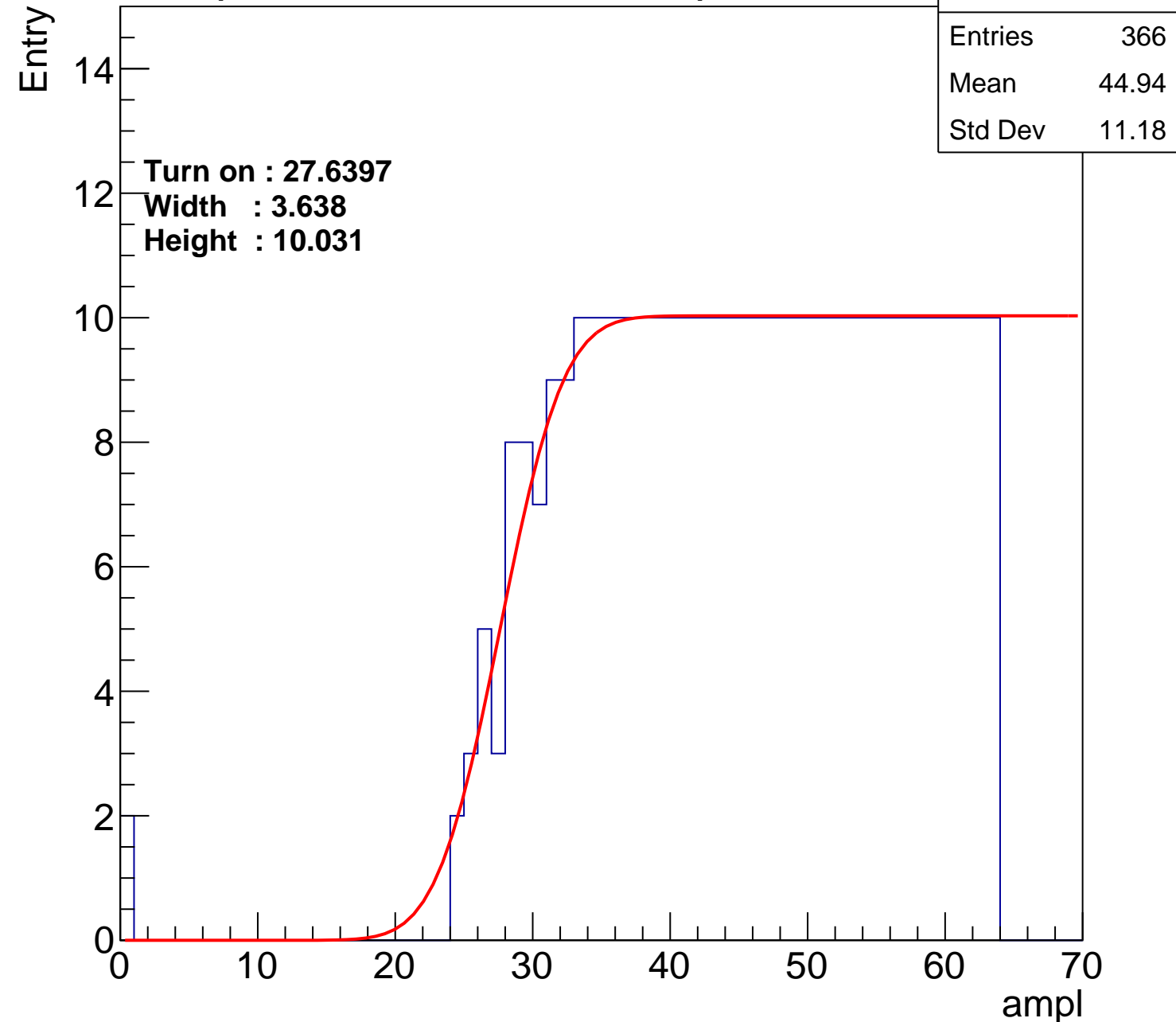
Width : 3.638

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch17

calib_packv5_042523_0143.root, FC#0, port D2

Entries	362
Mean	45.11
Std Dev	11.11

Turn on : 27.8547

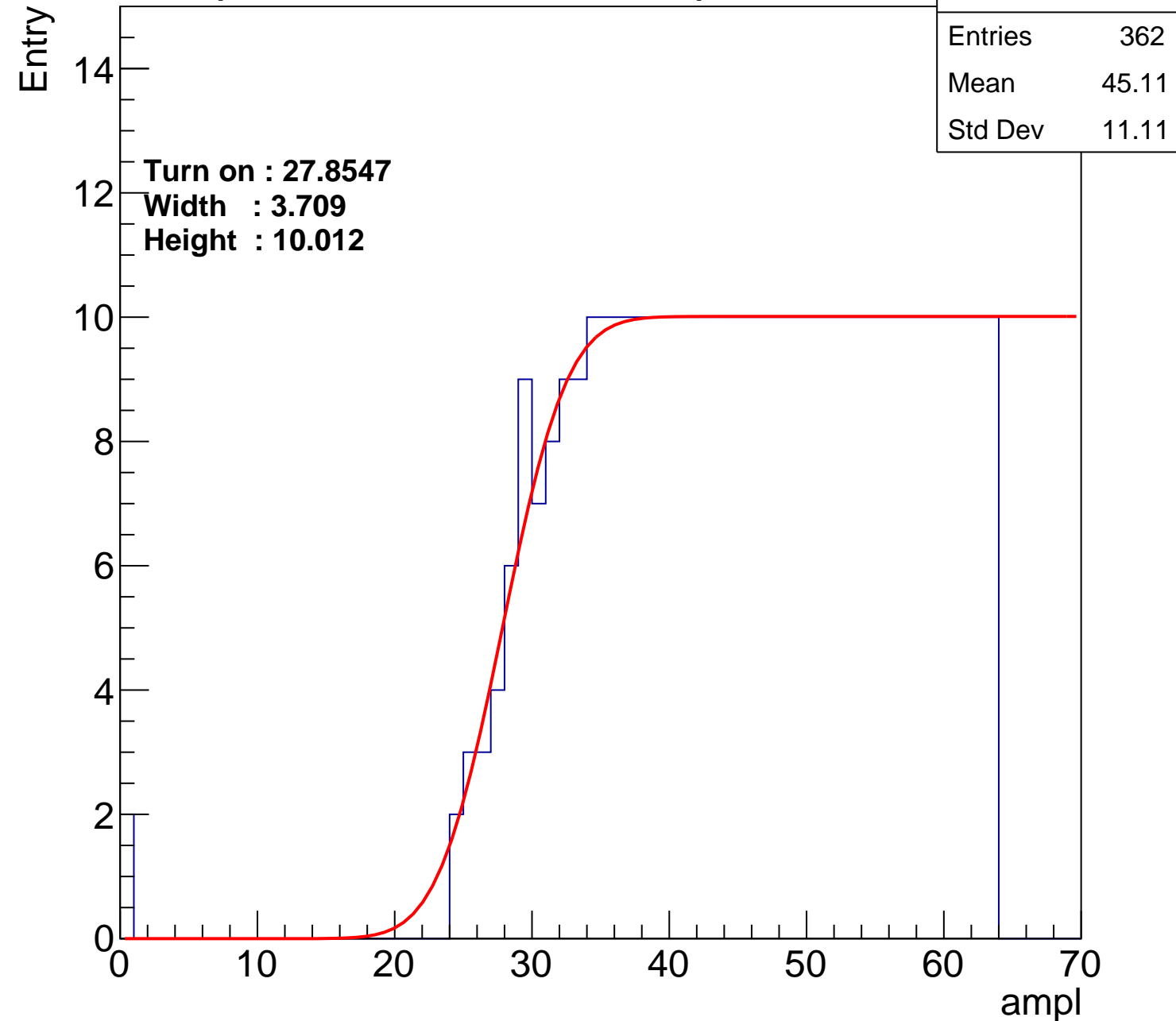
Width : 3.709

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch18

calib_packv5_042523_0143.root, FC#0, port D2

Entries	382
Mean	44.33
Std Dev	11.59

Turn on : 26.5731

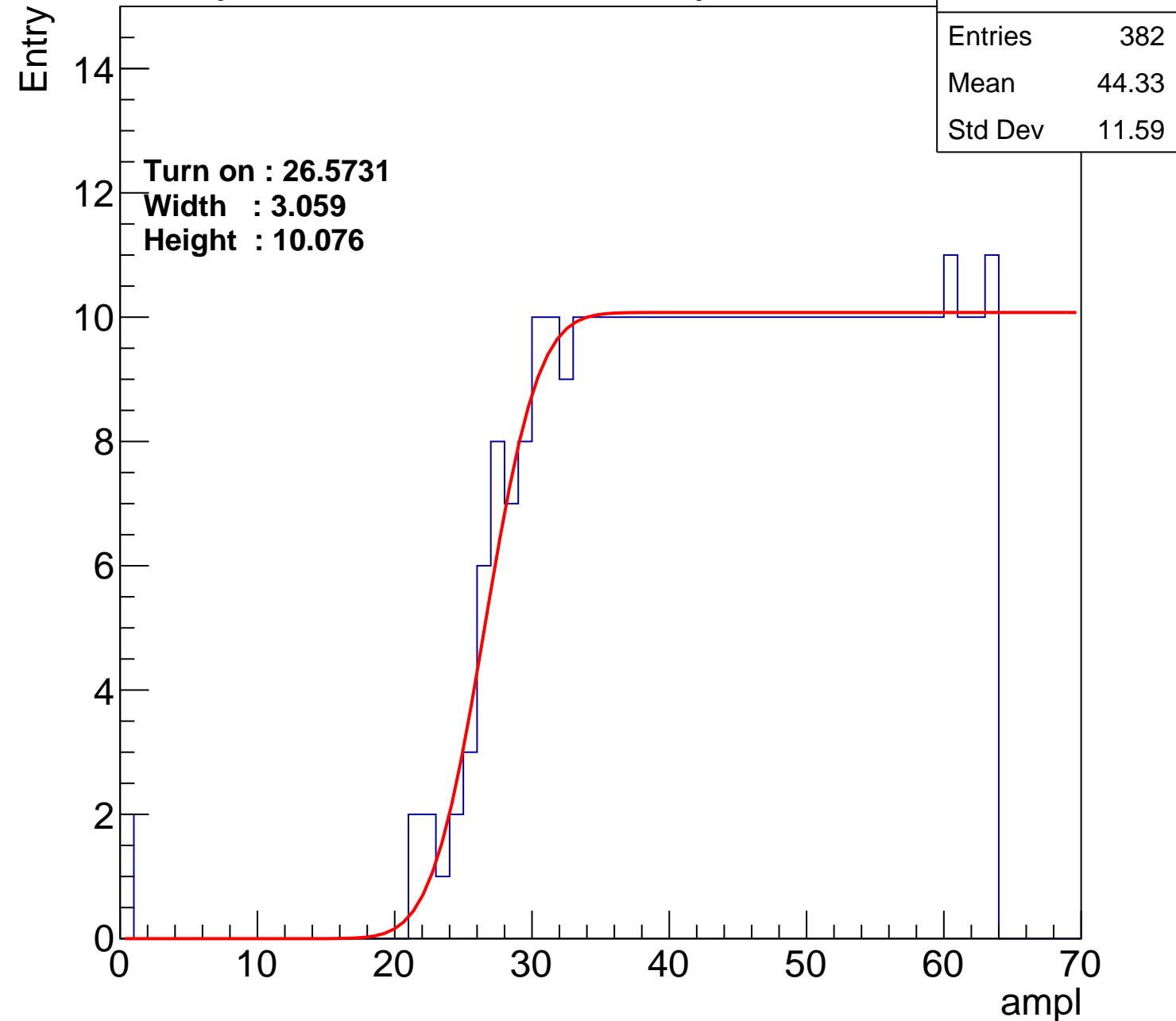
Width : 3.059

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch19

calib_packv5_042523_0143.root, FC#0, port D2

Entries	355
Mean	45.37
Std Dev	11.27

Turn on : 29.0834

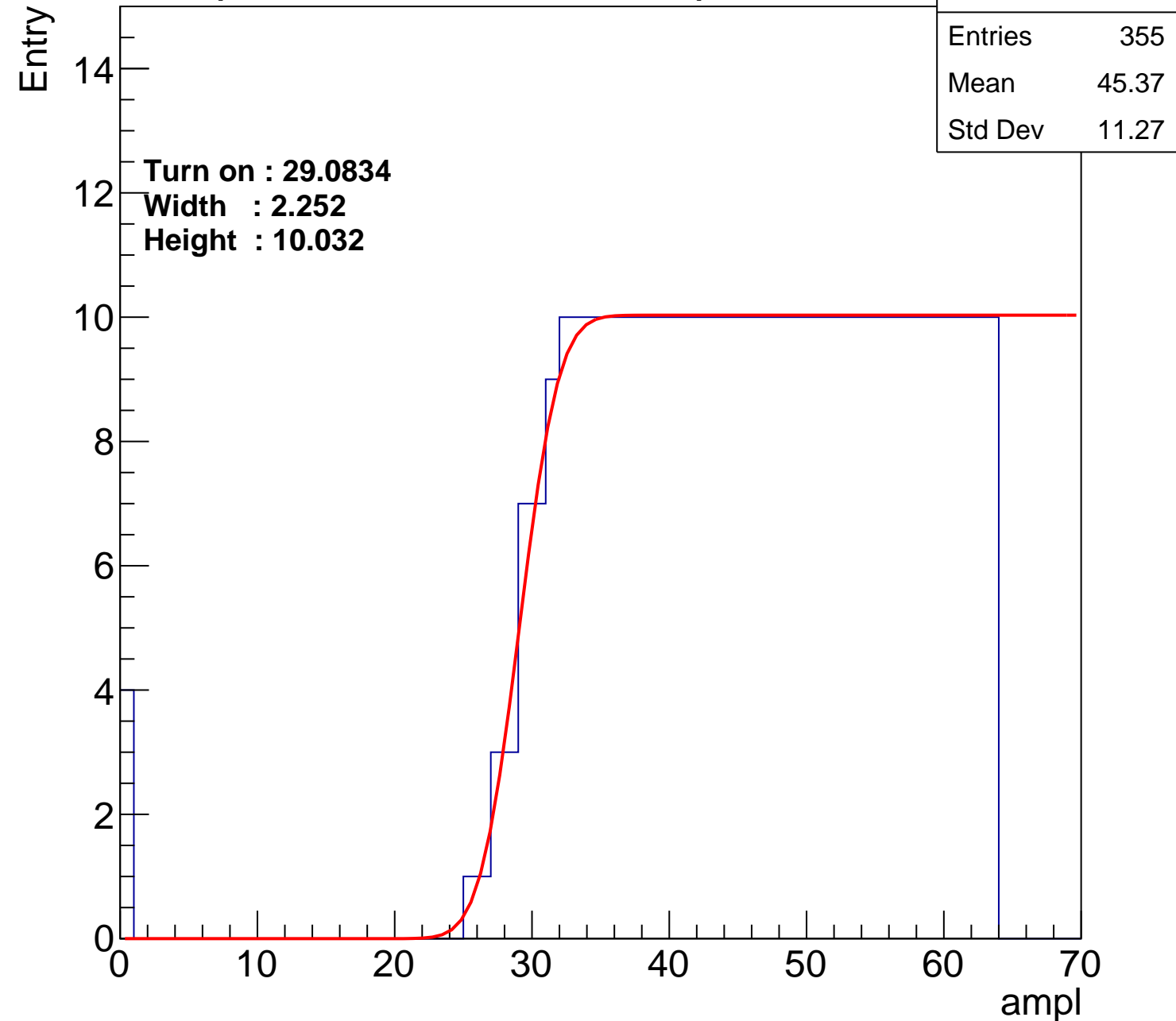
Width : 2.252

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch20

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.4287

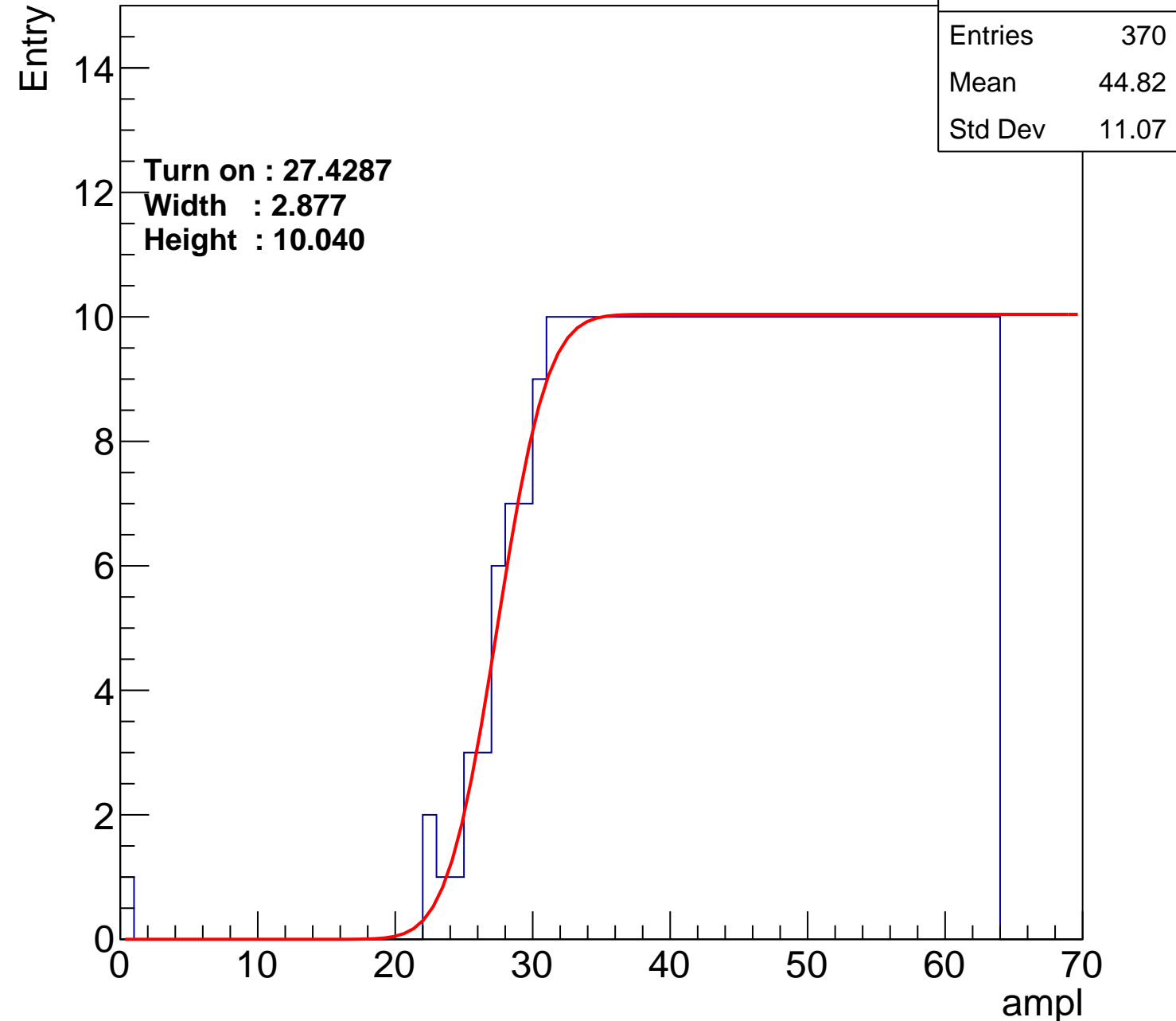
Width : 2.877

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch21

calib_packv5_042523_0143.root, FC#0, port D2

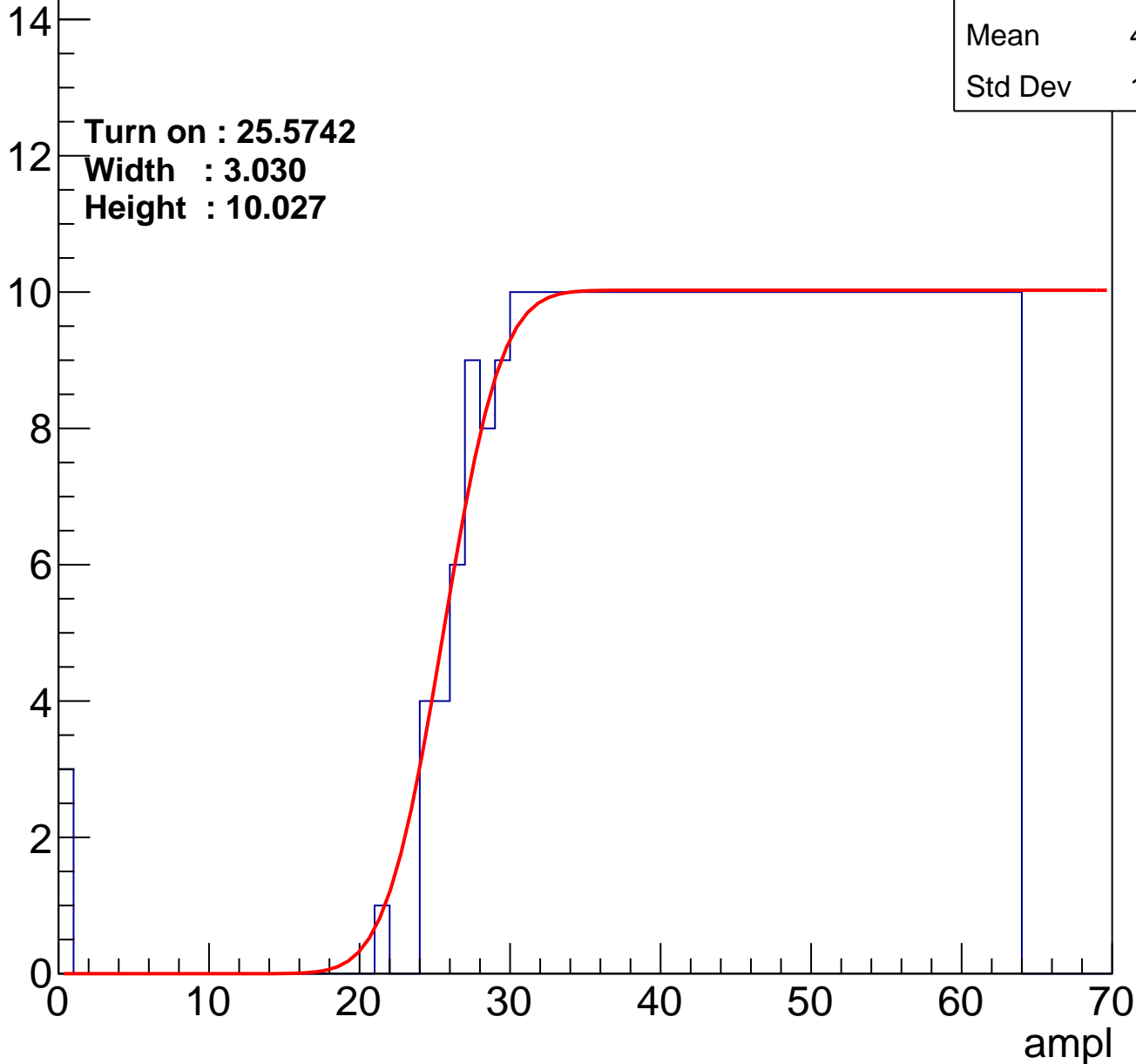
Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 25.5742

Width : 3.030

Height : 10.027

Entry



B1L101S, U22-ch22

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.36
Std Dev	11.64

Turn on : 26.9060

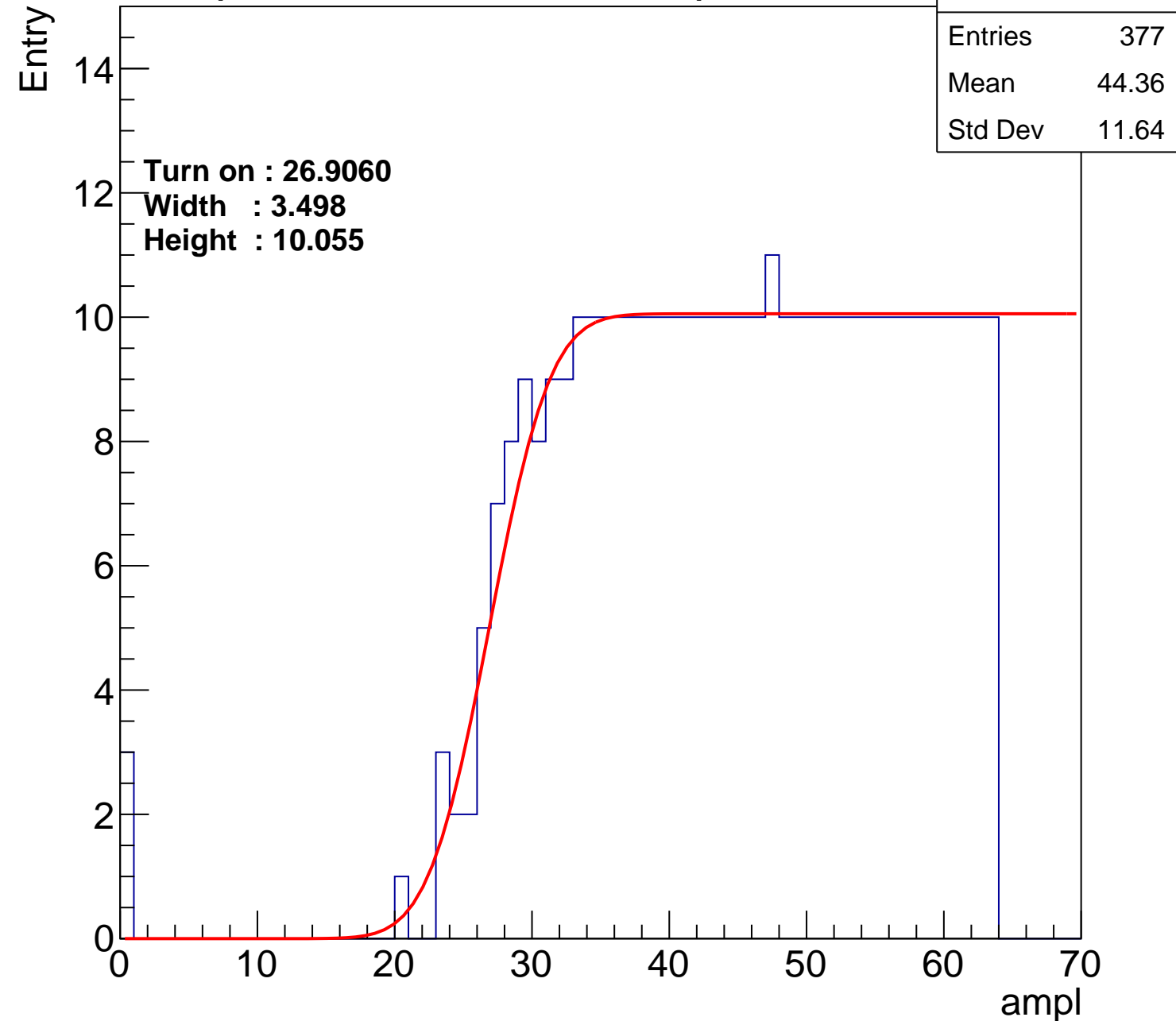
Width : 3.498

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch23

calib_packv5_042523_0143.root, FC#0, port D2

Entries	348
Mean	45.94
Std Dev	10.43

Turn on : 29.3480

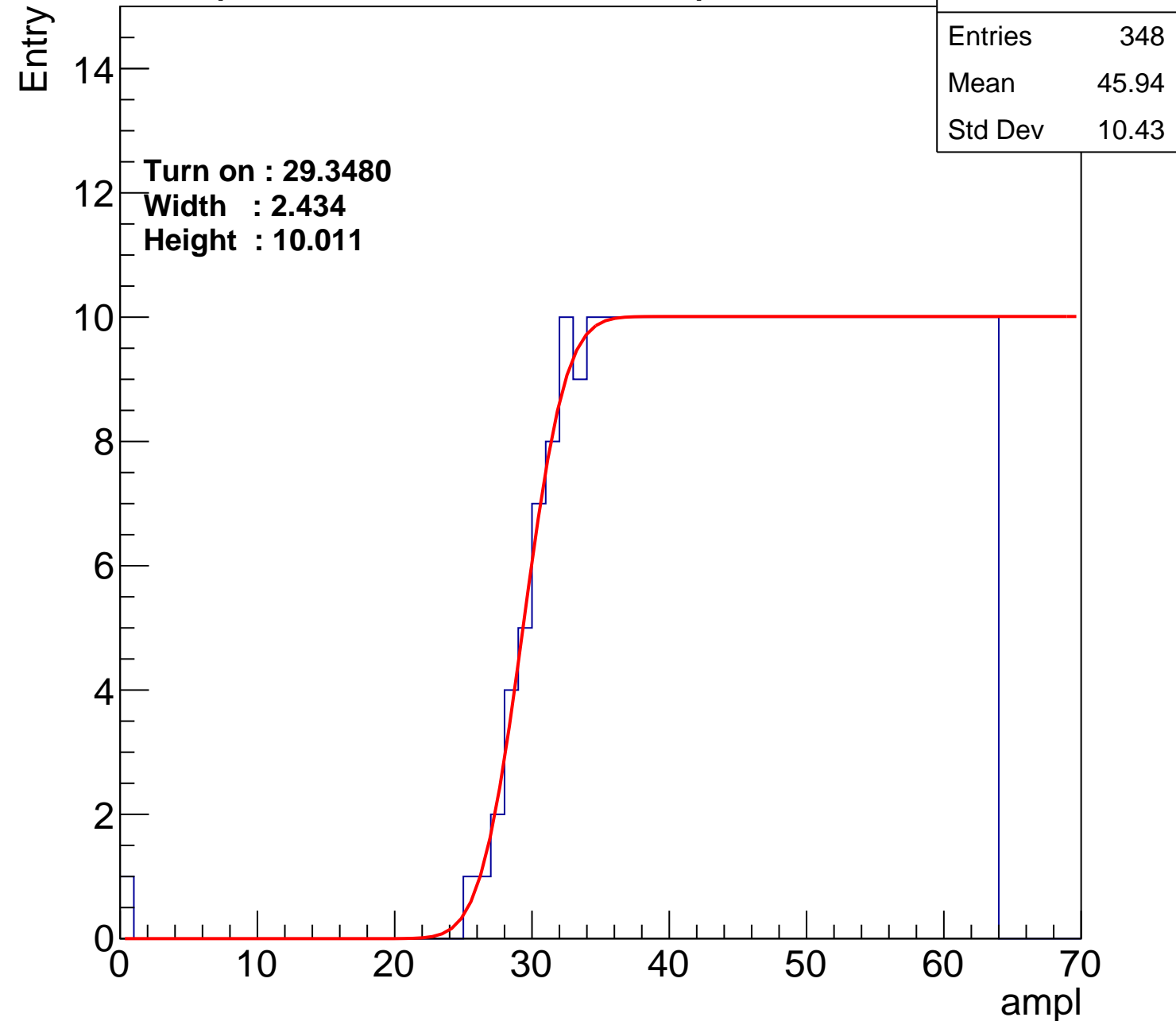
Width : 2.434

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch24

calib_packv5_042523_0143.root, FC#0, port D2

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.5119

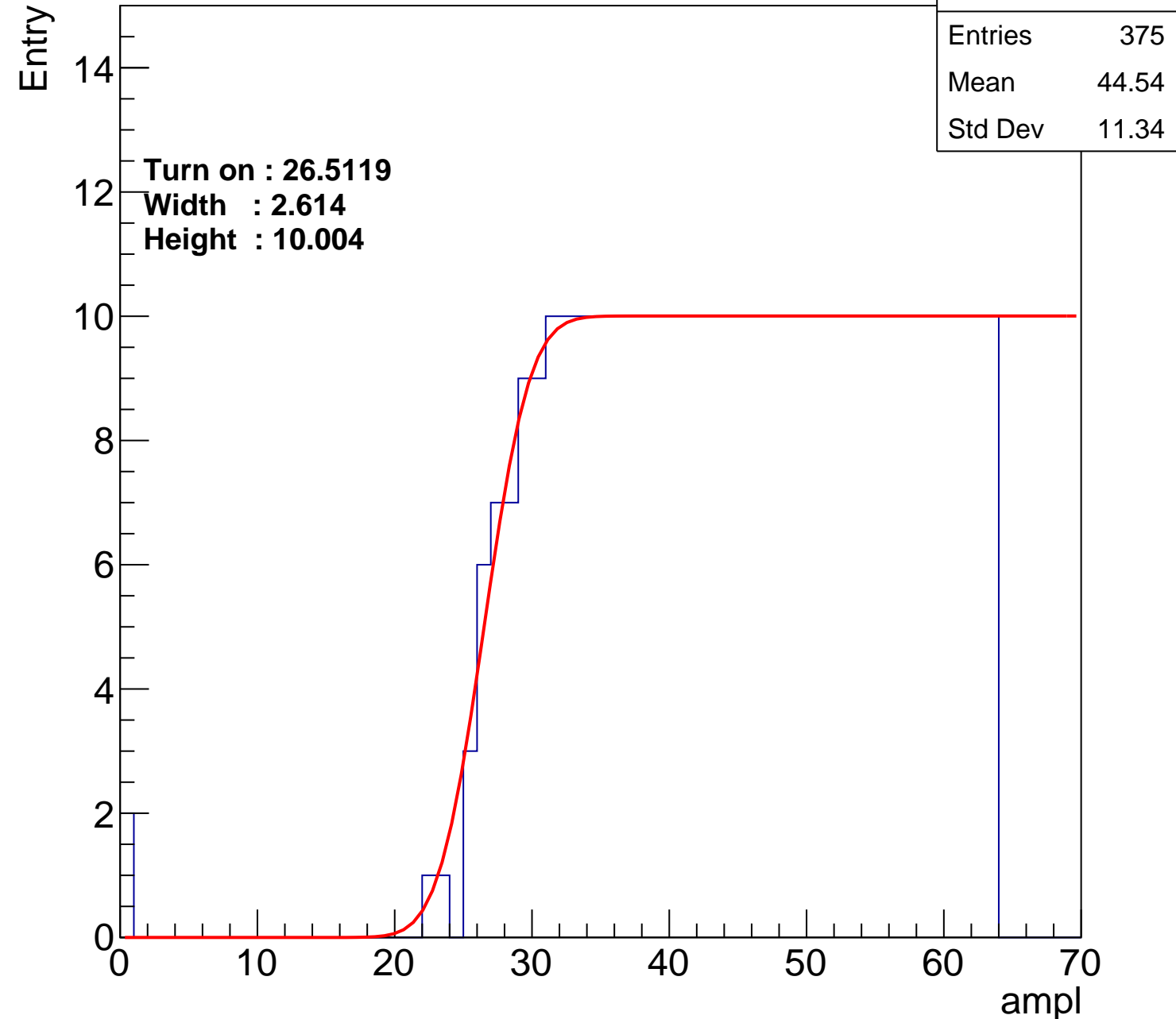
Width : 2.614

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch25

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.3971

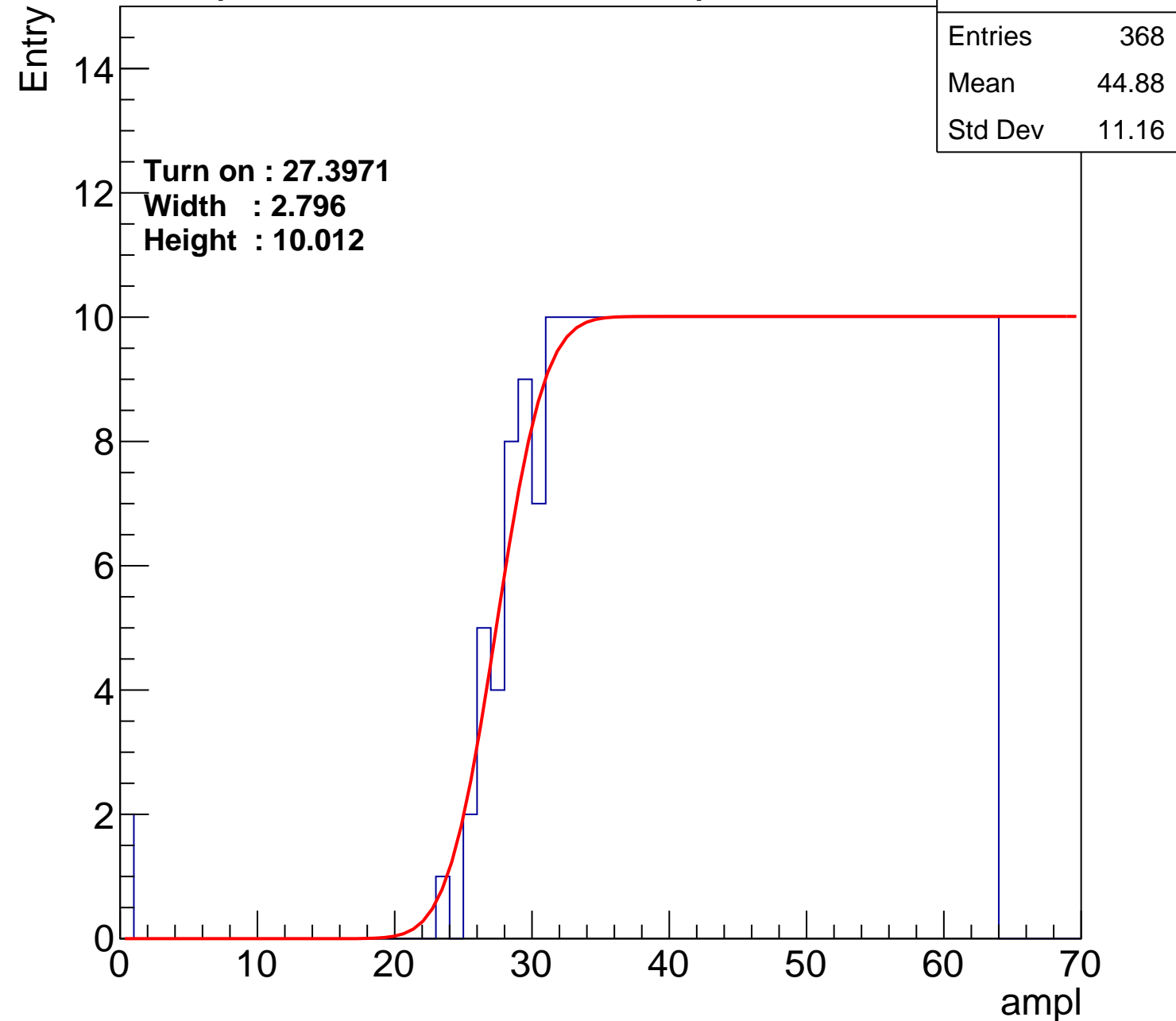
Width : 2.796

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch26

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.29
Std Dev	11.6

Turn on : 26.7469

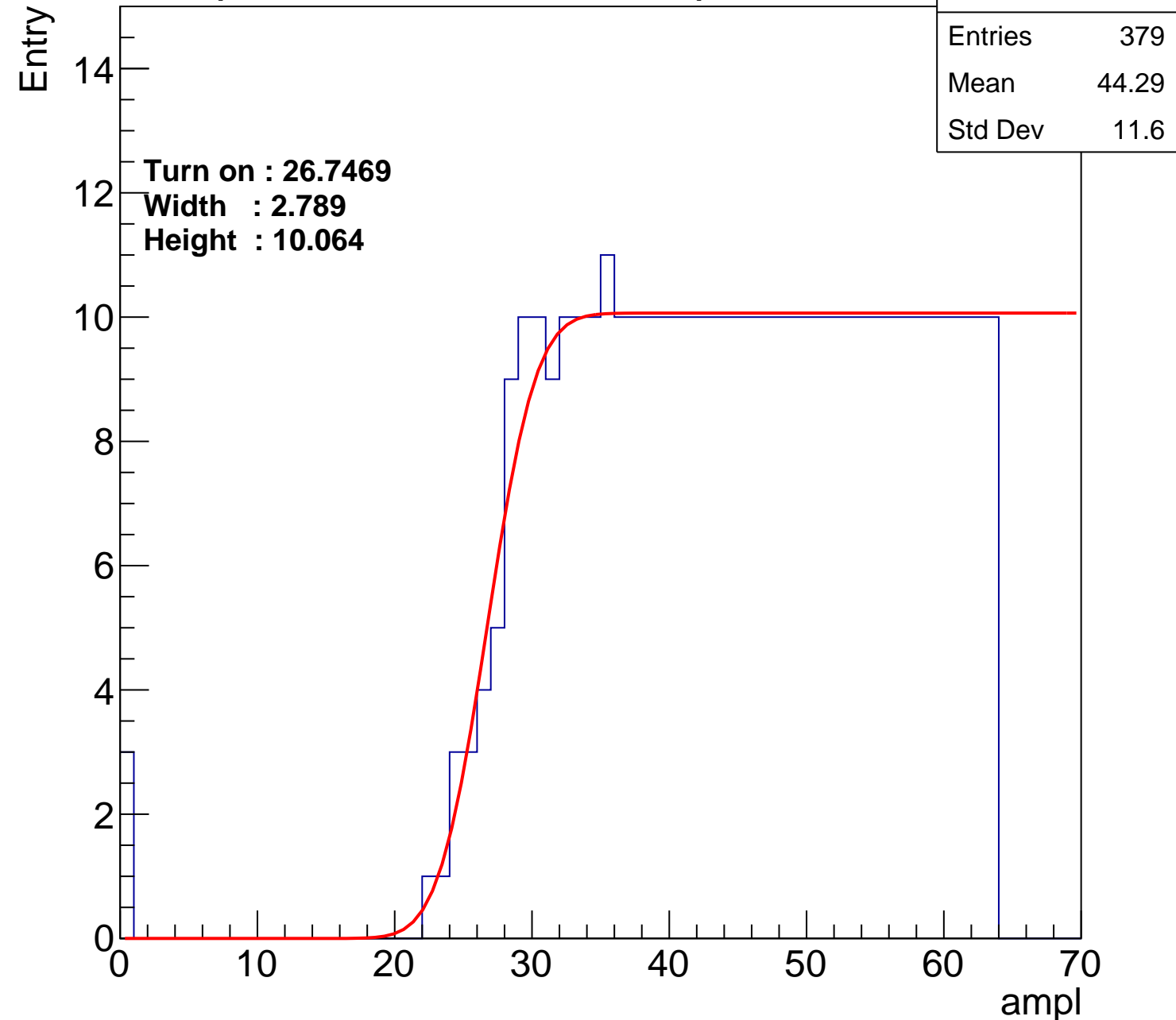
Width : 2.789

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch27

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.69
Std Dev	11.64

Turn on : 28.0349

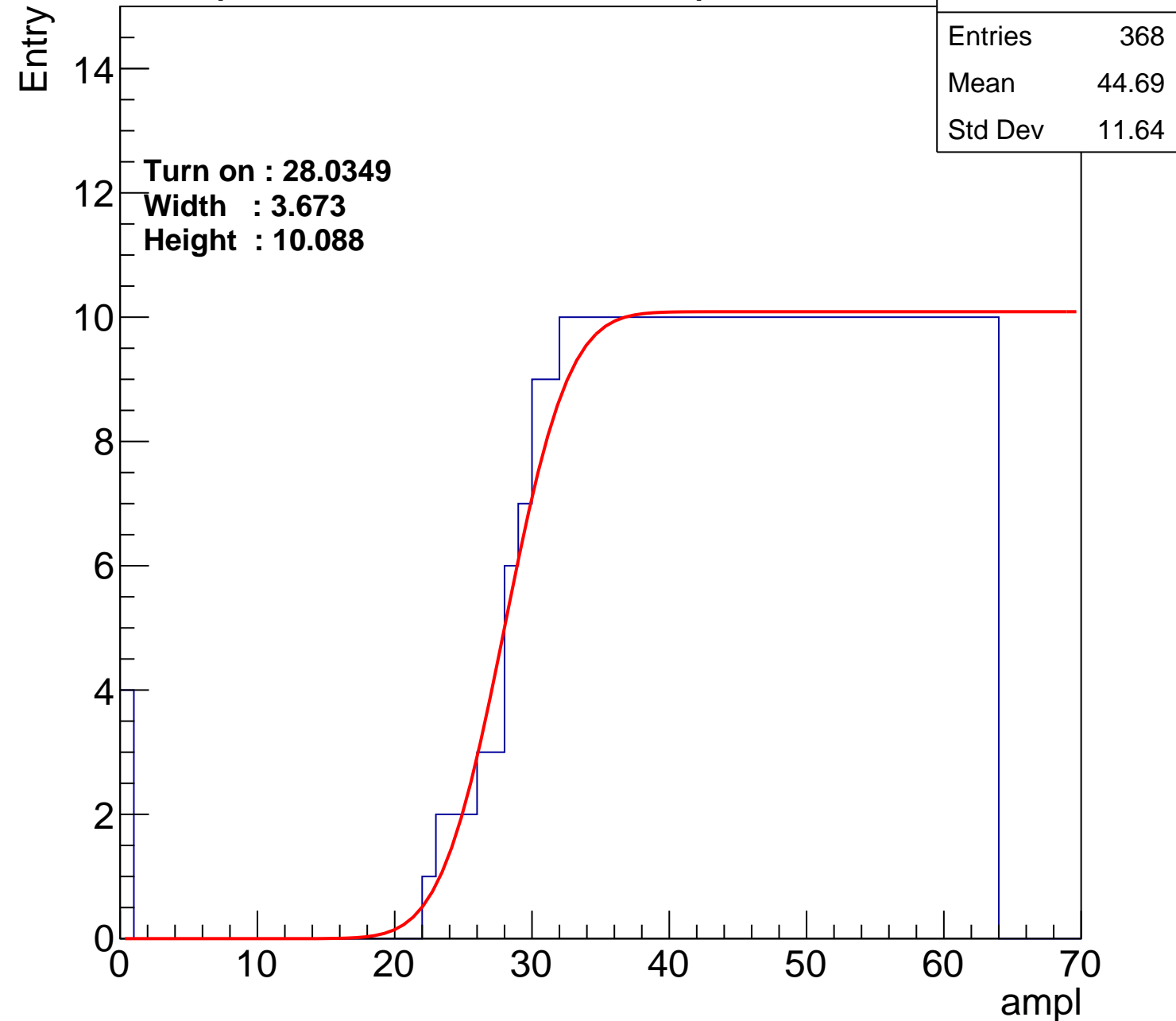
Width : 3.673

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch28

calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 27.3273

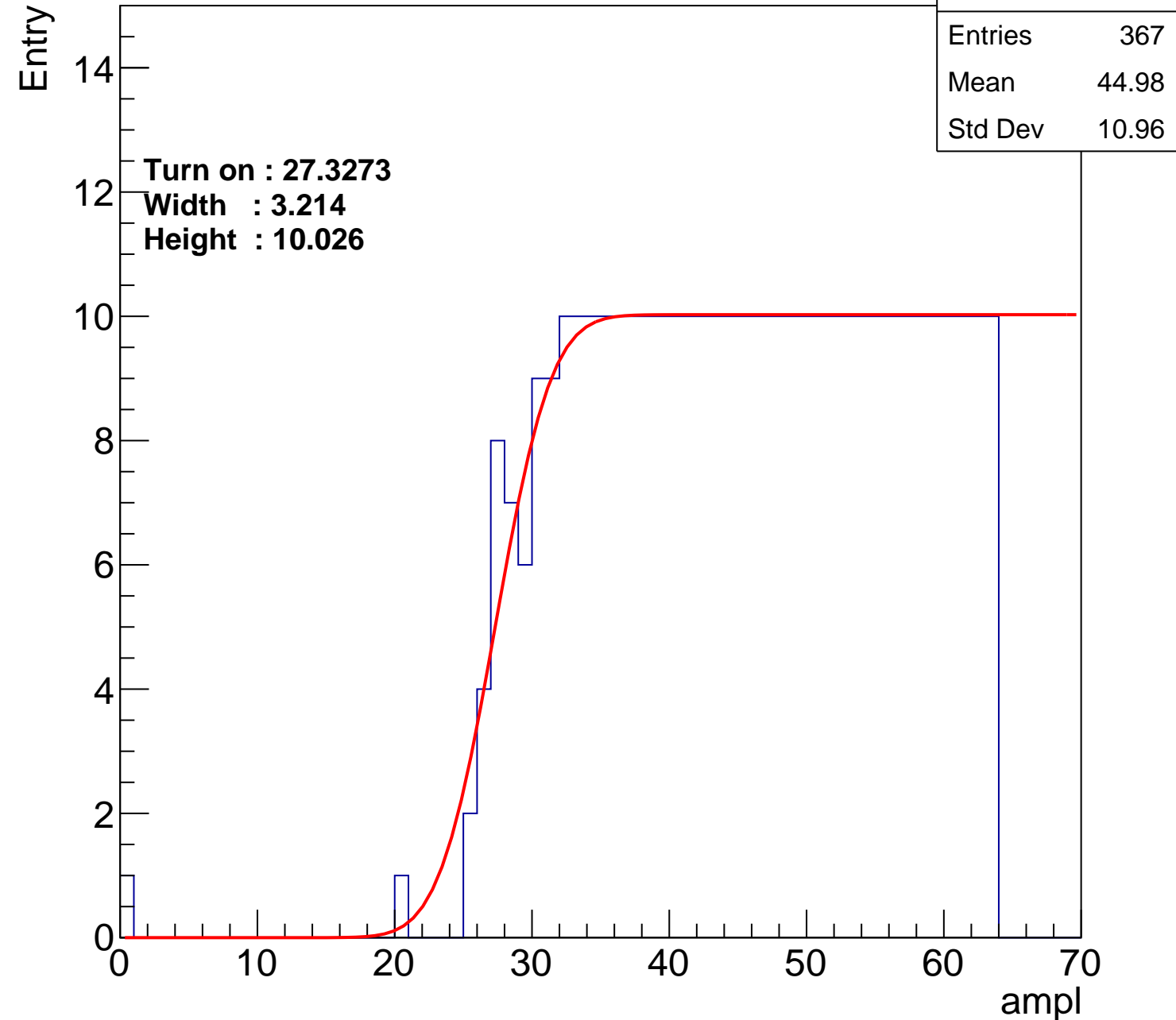
Width : 3.214

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch29

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.93
Std Dev	11.28

Turn on : 27.8946

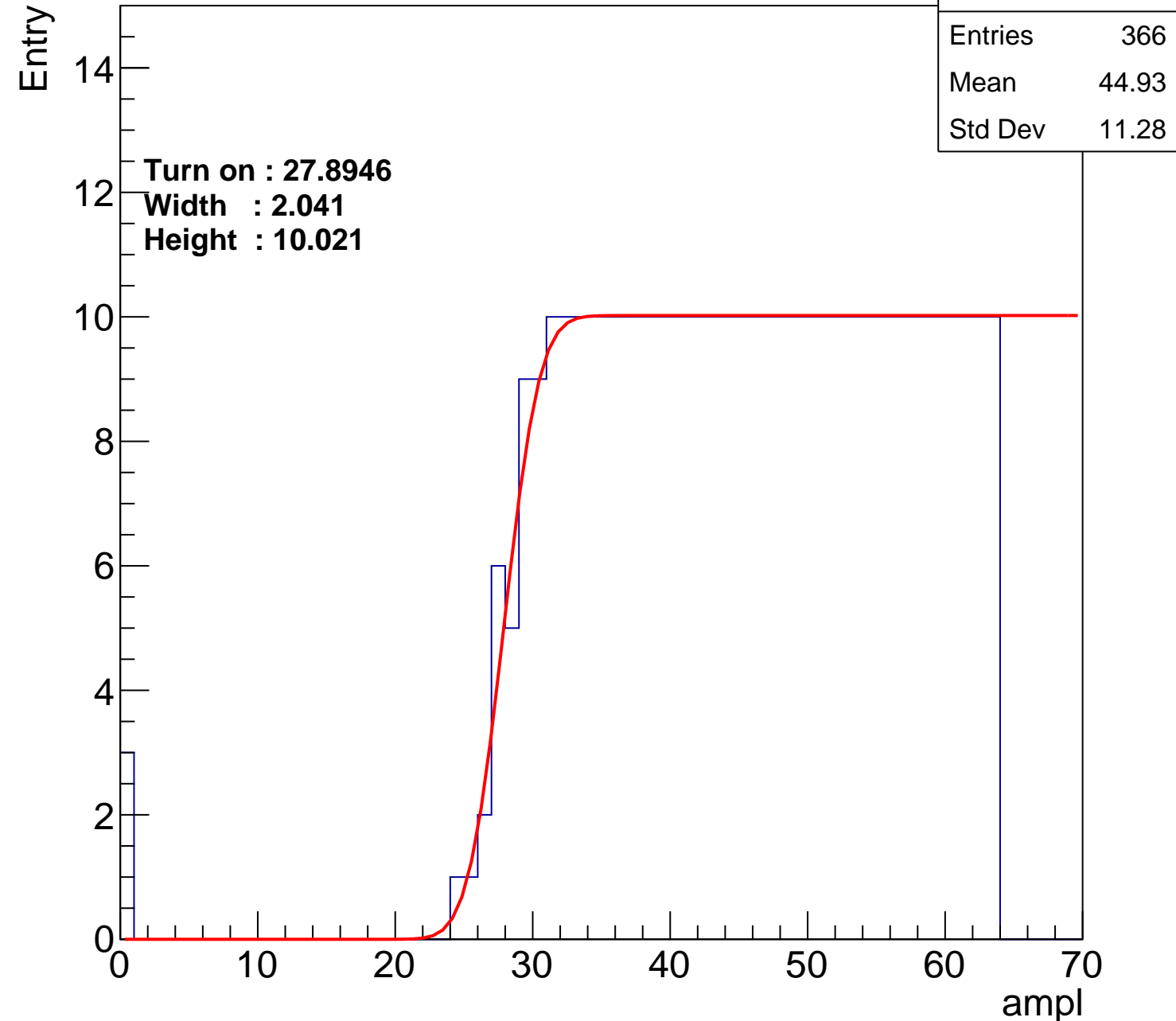
Width : 2.041

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch30

calib_packv5_042523_0143.root, FC#0, port D2

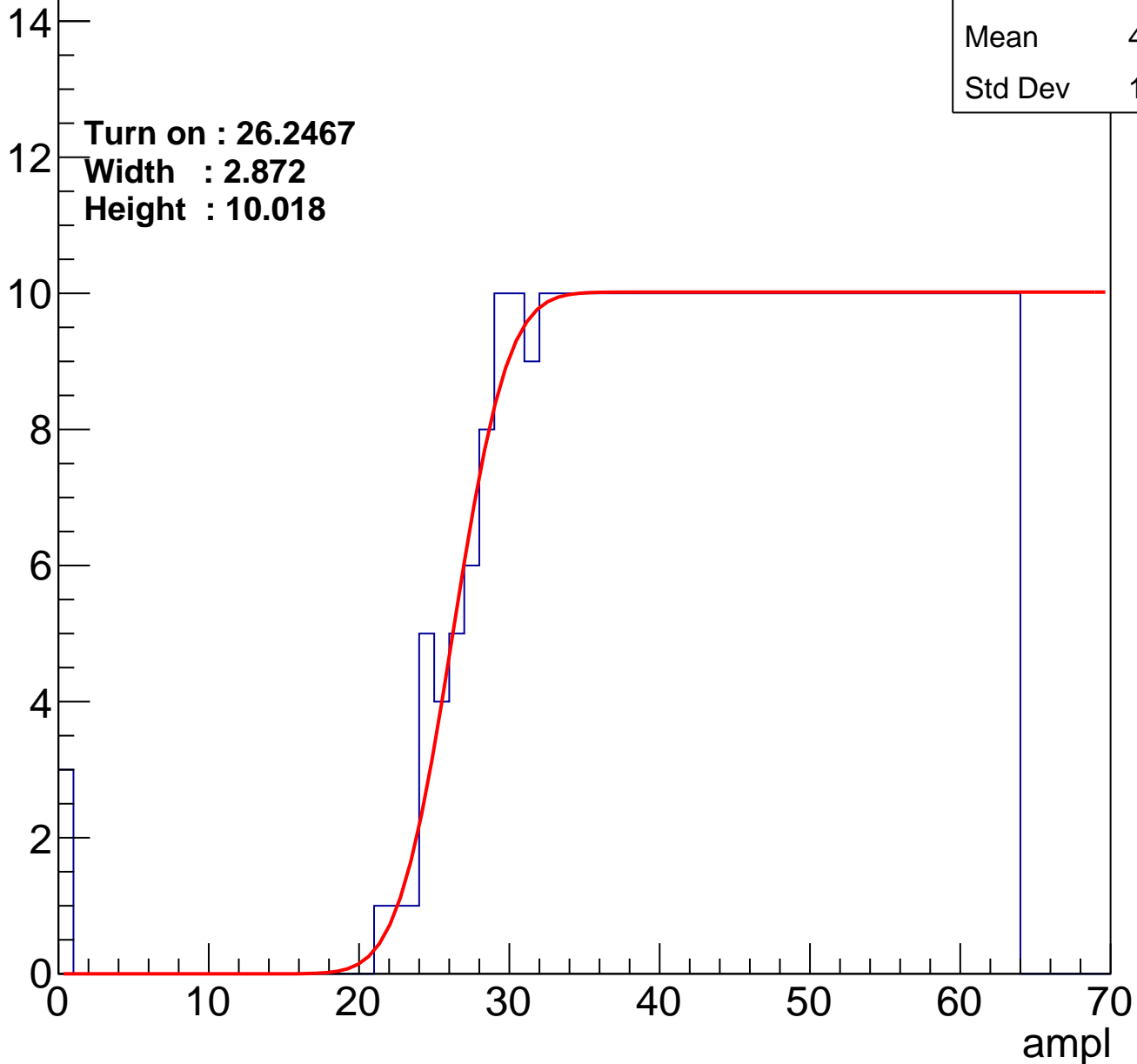
Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.2467

Width : 2.872

Height : 10.018

Entry



B1L101S, U22-ch31

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.77
Std Dev	11.42

Turn on : 28.2193

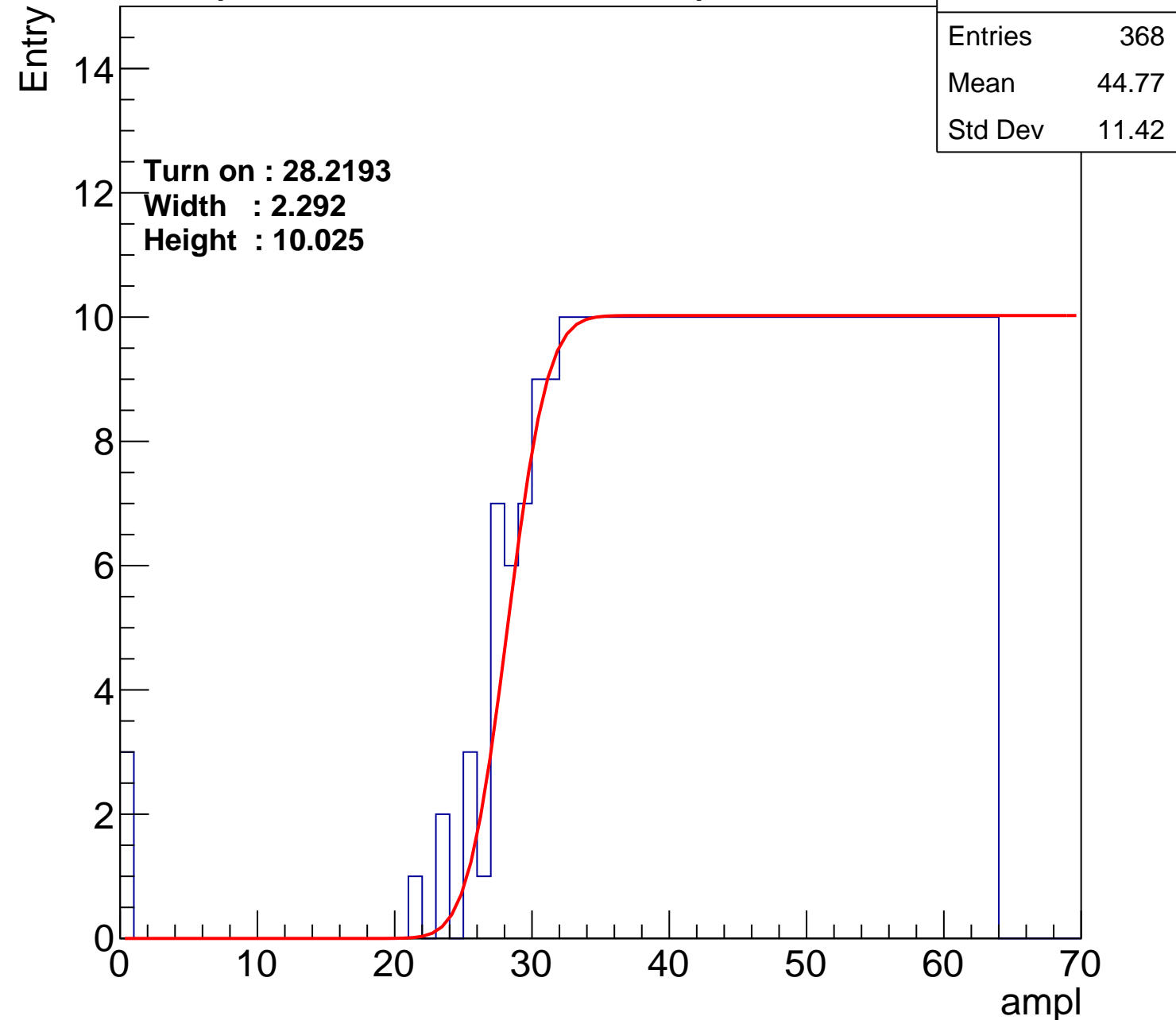
Width : 2.292

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch32

calib_packv5_042523_0143.root, FC#0, port D2

Entries	383
Mean	44.03
Std Dev	11.81

Turn on : 26.4007

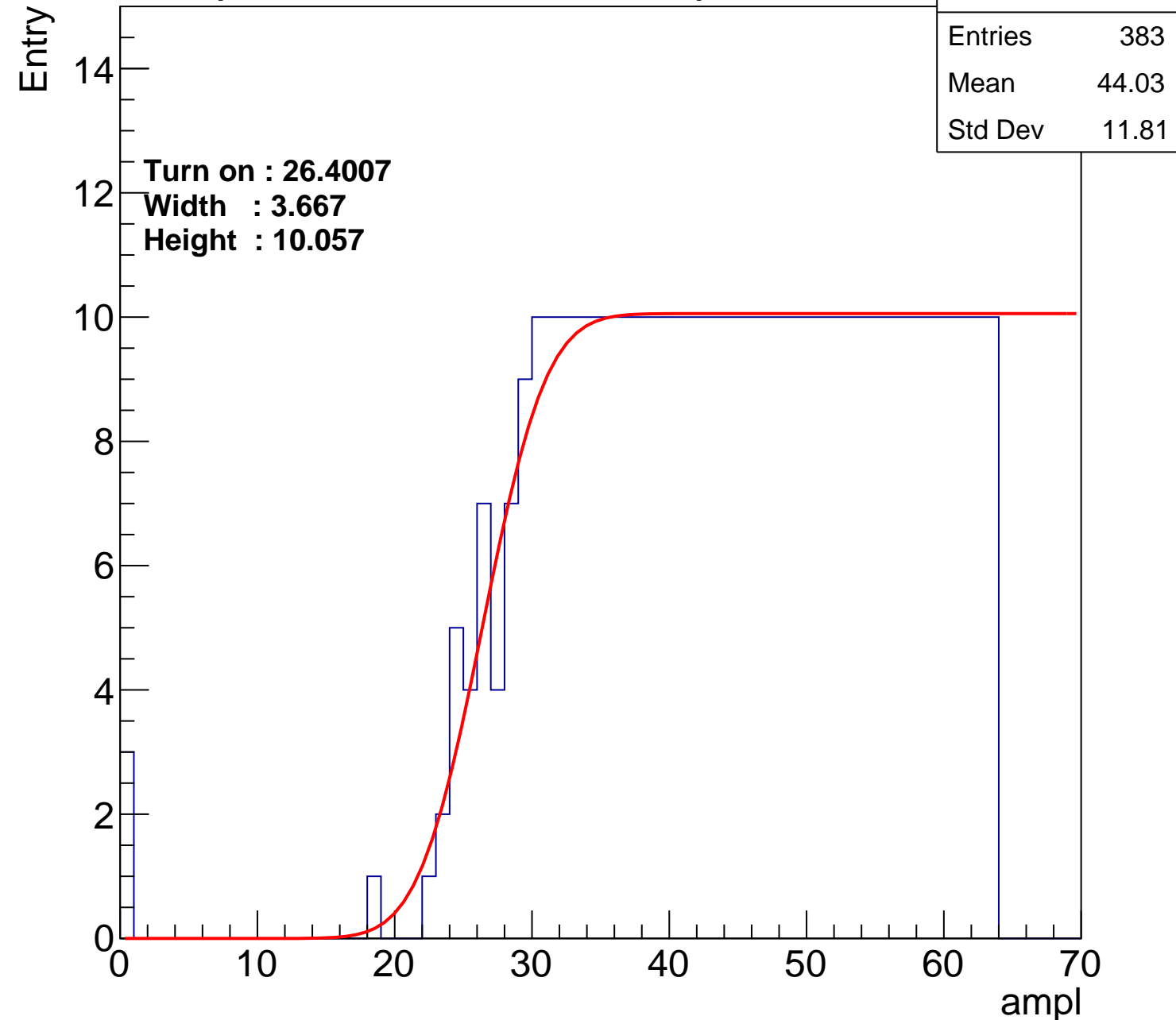
Width : 3.667

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch33

calib_packv5_042523_0143.root, FC#0, port D2

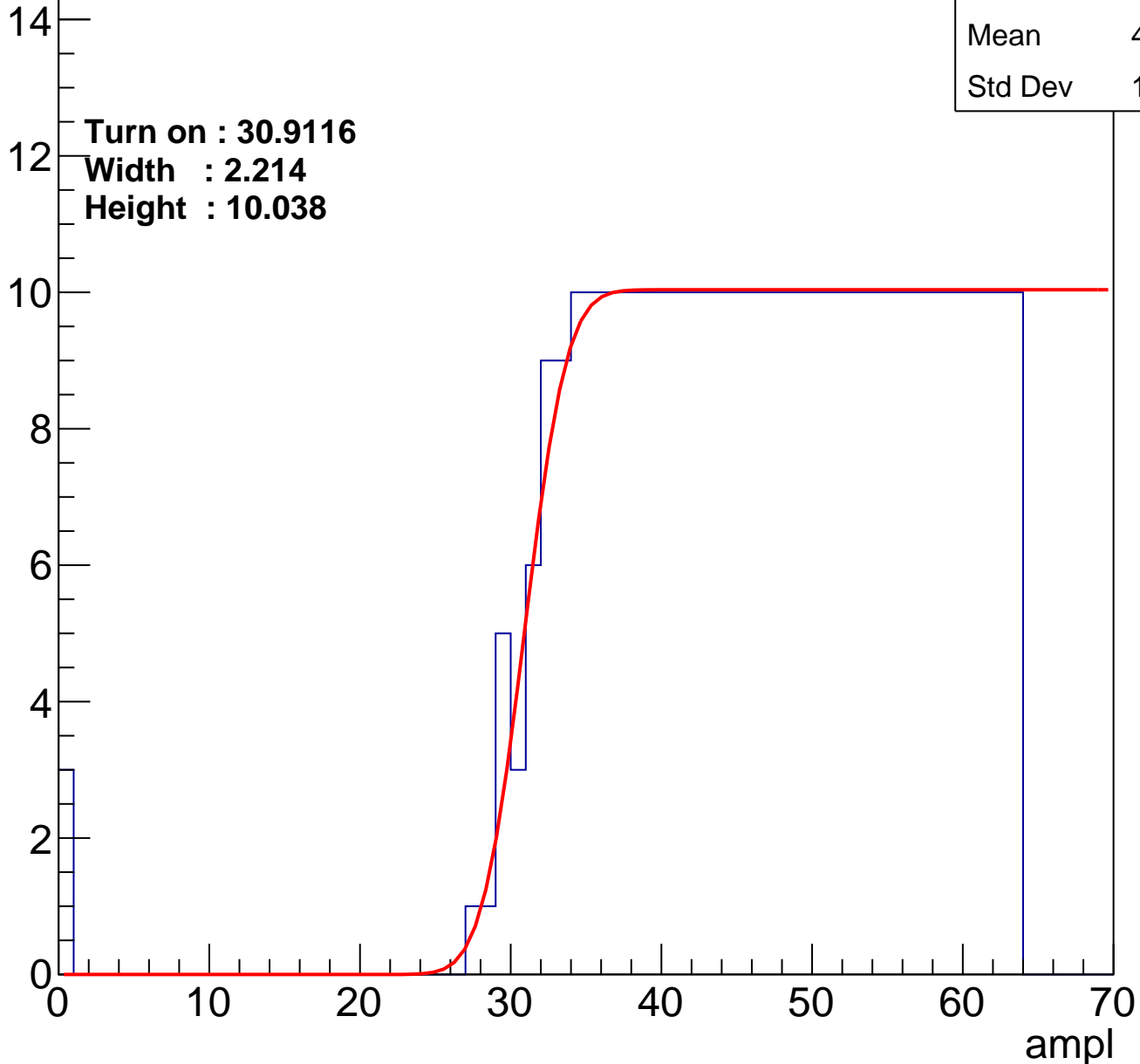
Entries	337
Mean	46.32
Std Dev	10.65

Turn on : 30.9116

Width : 2.214

Height : 10.038

Entry



B1L101S, U22-ch34

calib_packv5_042523_0143.root, FC#0, port D2

Entries	386
Mean	44.01
Std Dev	11.53

Turn on : 25.4798

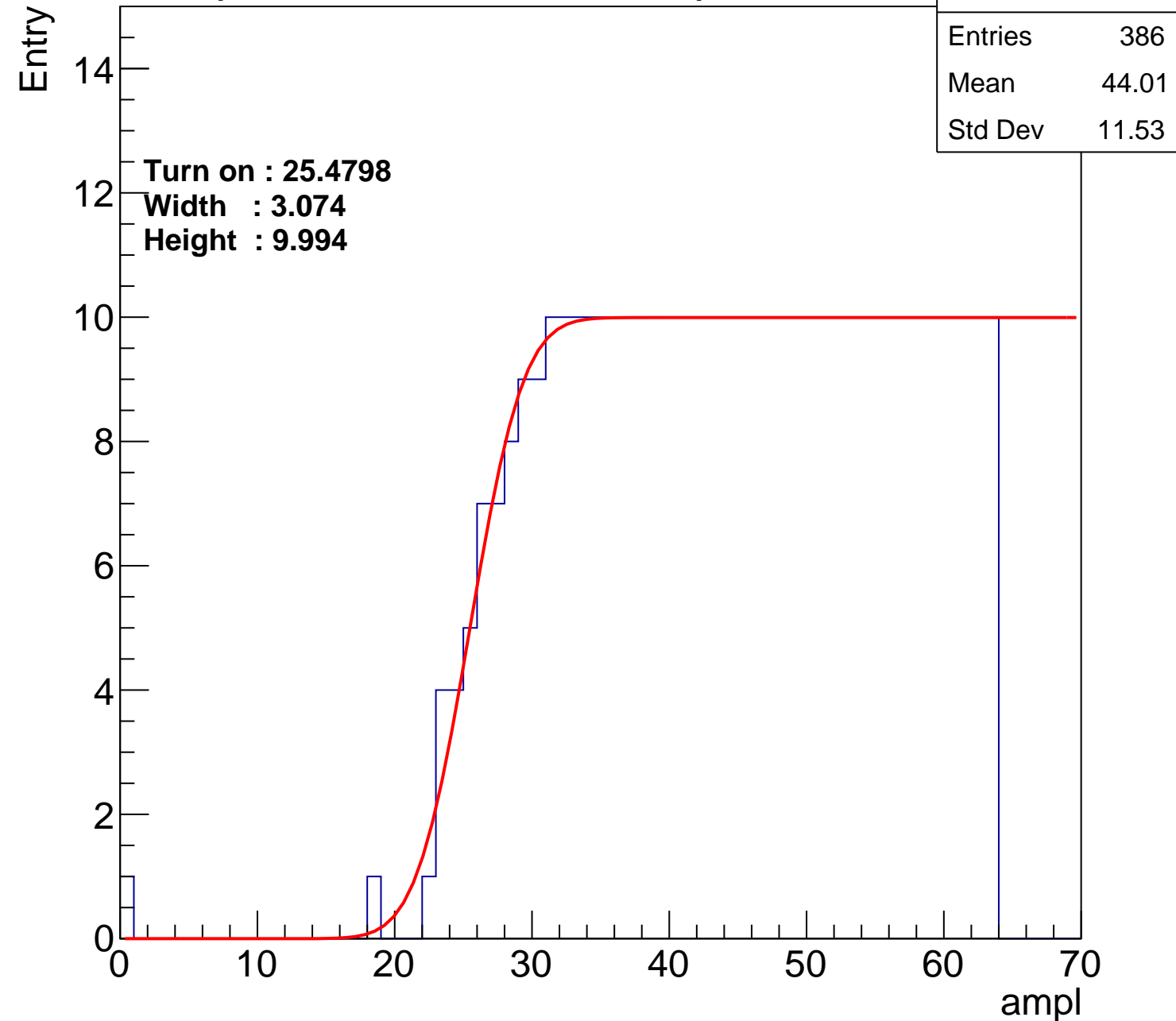
Width : 3.074

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch35

calib_packv5_042523_0143.root, FC#0, port D2

Entries	353
Mean	45.59
Std Dev	11.02

Turn on : 29.0871

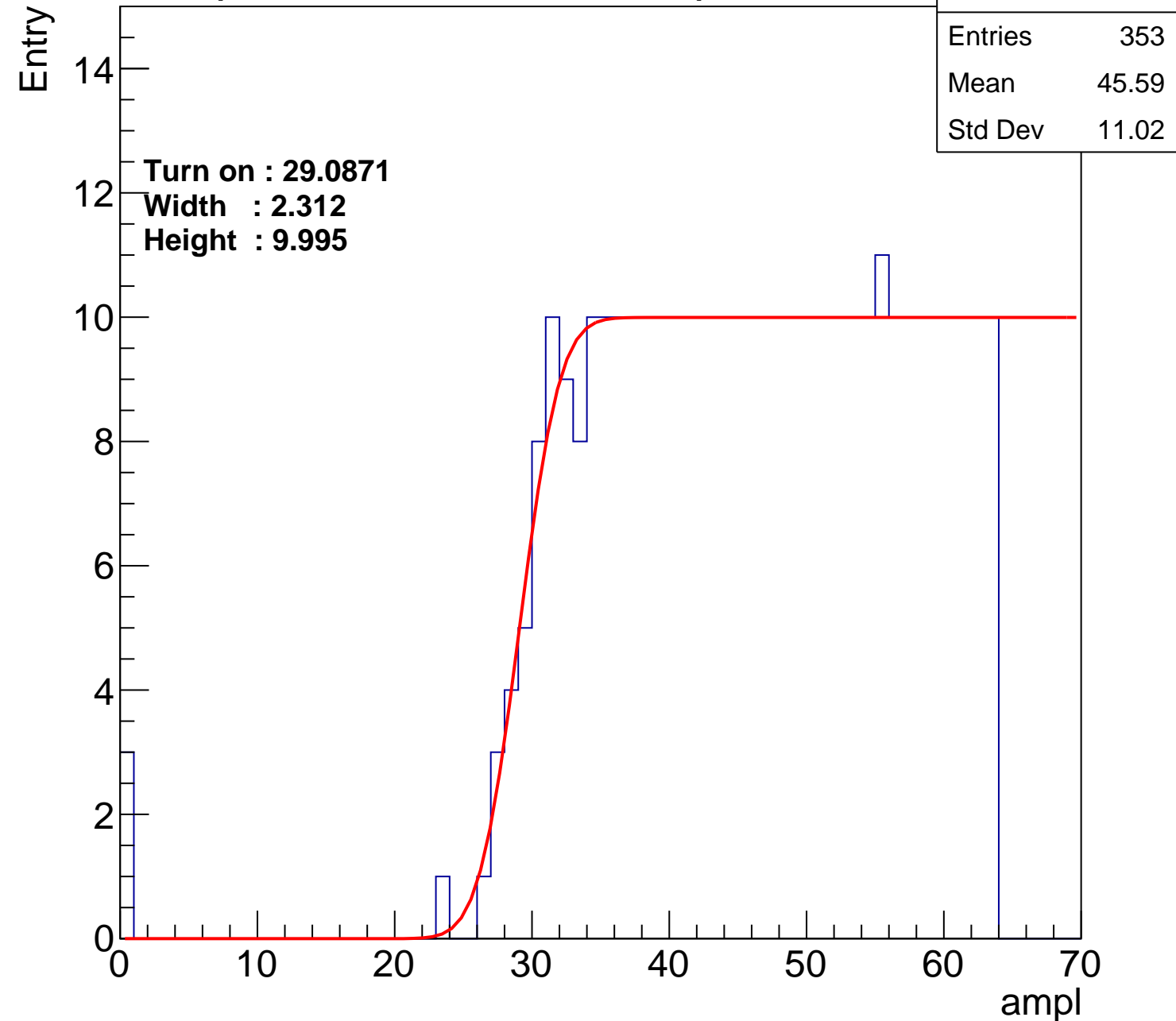
Width : 2.312

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch36

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.93
Std Dev	11

Turn on : 27.9660

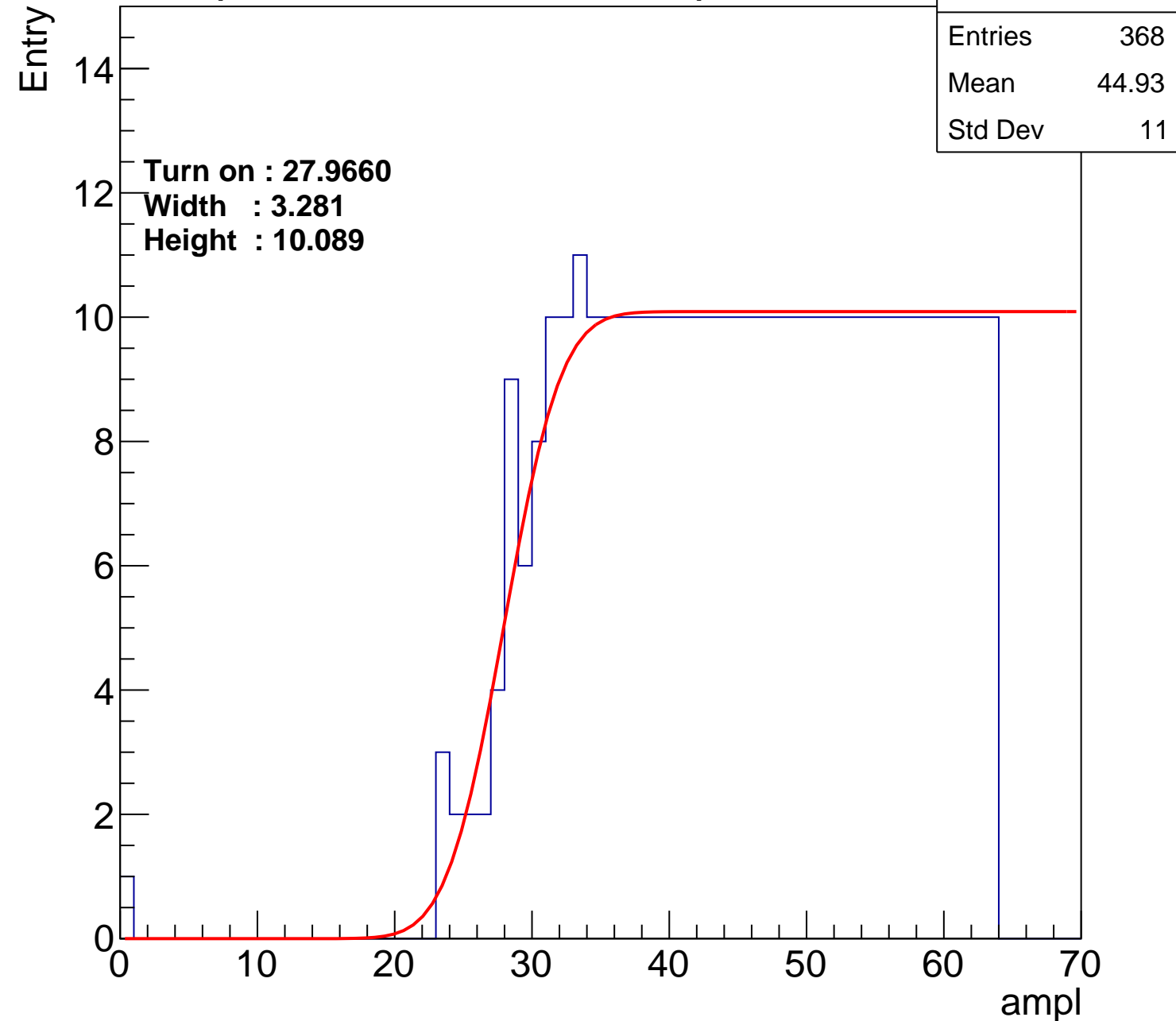
Width : 3.281

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch37

calib_packv5_042523_0143.root, FC#0, port D2

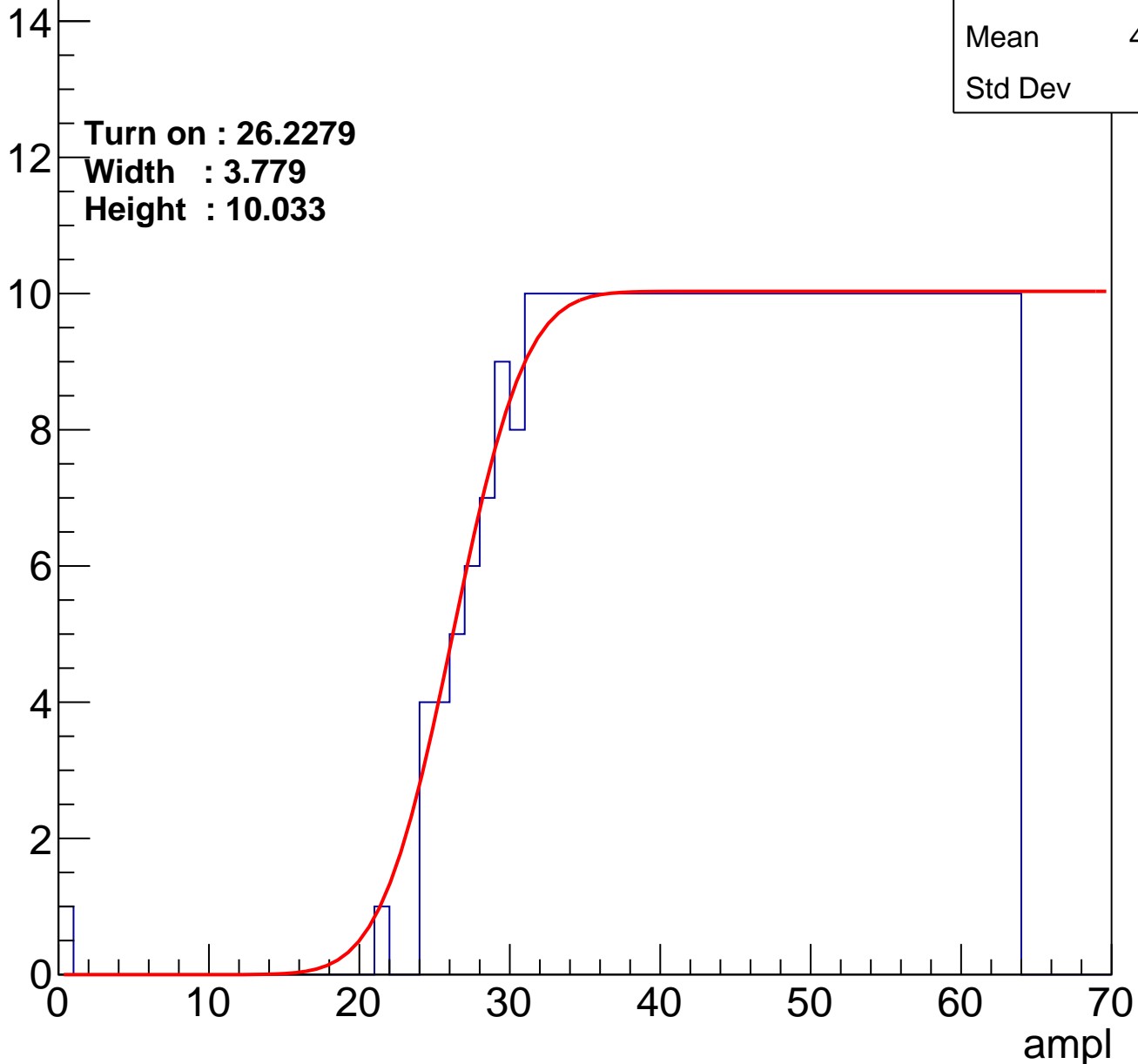
Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 26.2279

Width : 3.779

Height : 10.033

Entry



B1L101S, U22-ch38

calib_packv5_042523_0143.root, FC#0, port D2

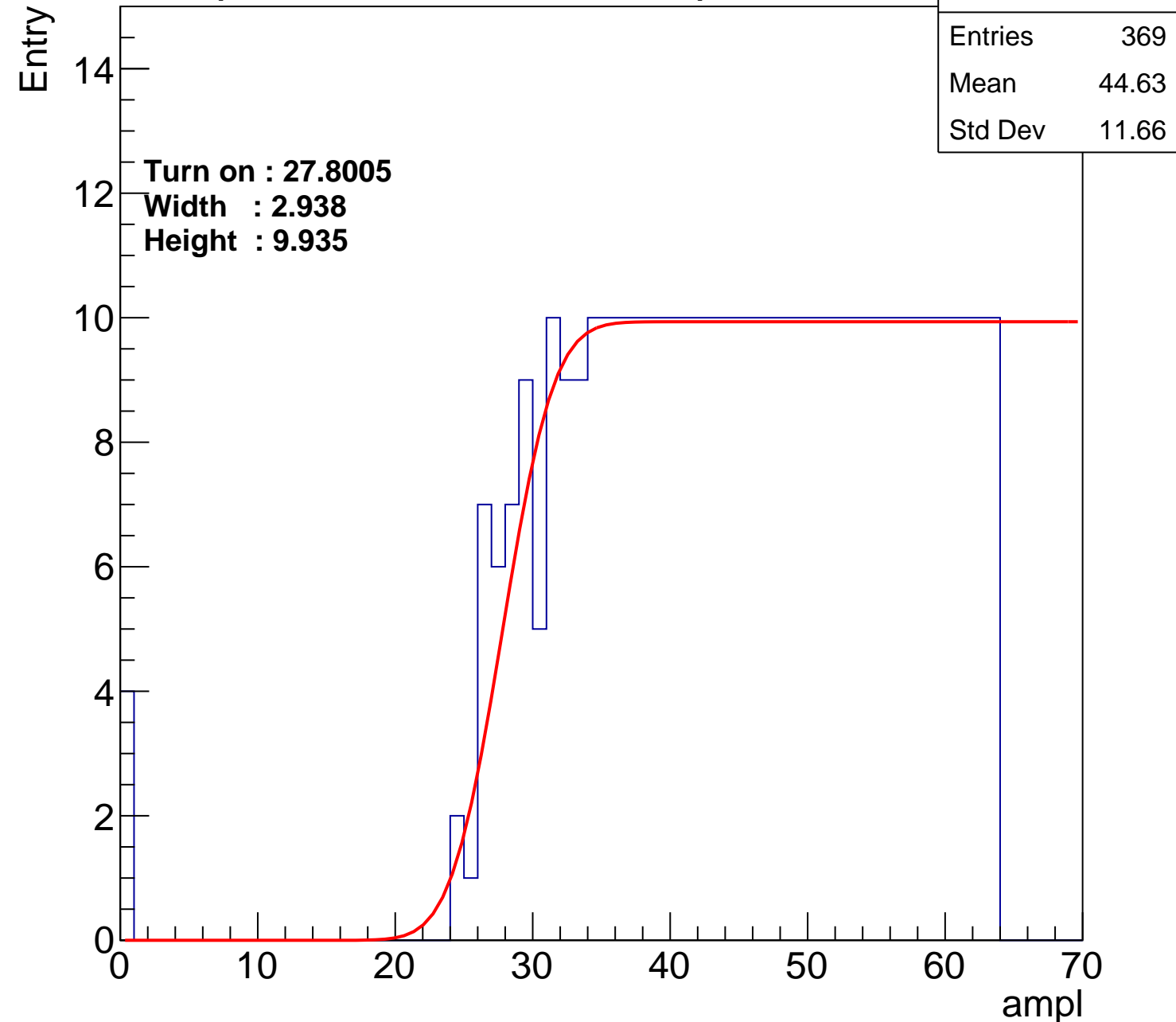
Entry

14
12
10
8
6
4
2
0

Turn on : 27.8005
Width : 2.938
Height : 9.935

Entries	369
Mean	44.63
Std Dev	11.66

ampl



B1L101S, U22-ch39

calib_packv5_042523_0143.root, FC#0, port D2

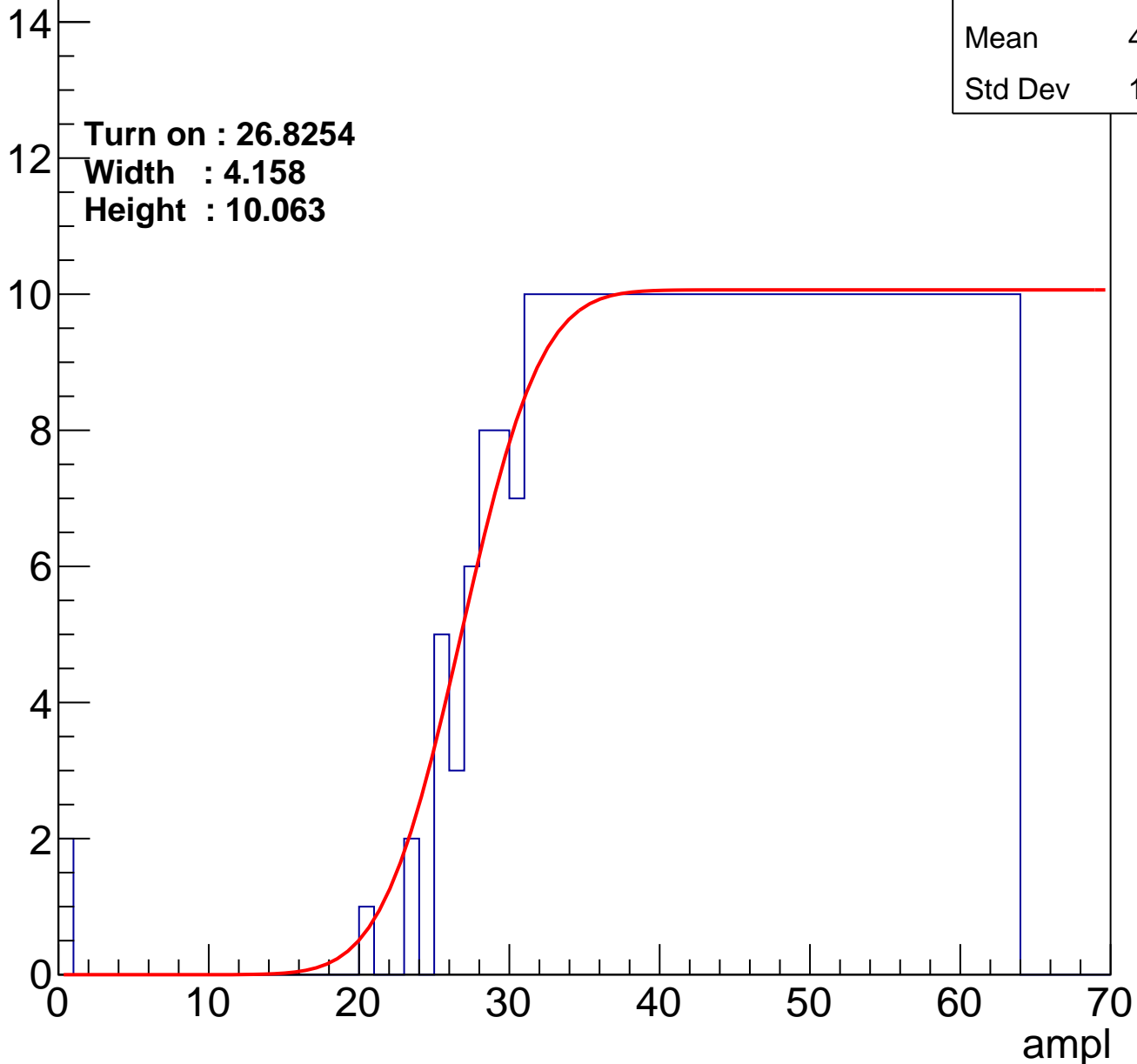
Entries	372
Mean	44.64
Std Dev	11.34

Turn on : 26.8254

Width : 4.158

Height : 10.063

Entry



B1L101S, U22-ch40

calib_packv5_042523_0143.root, FC#0, port D2

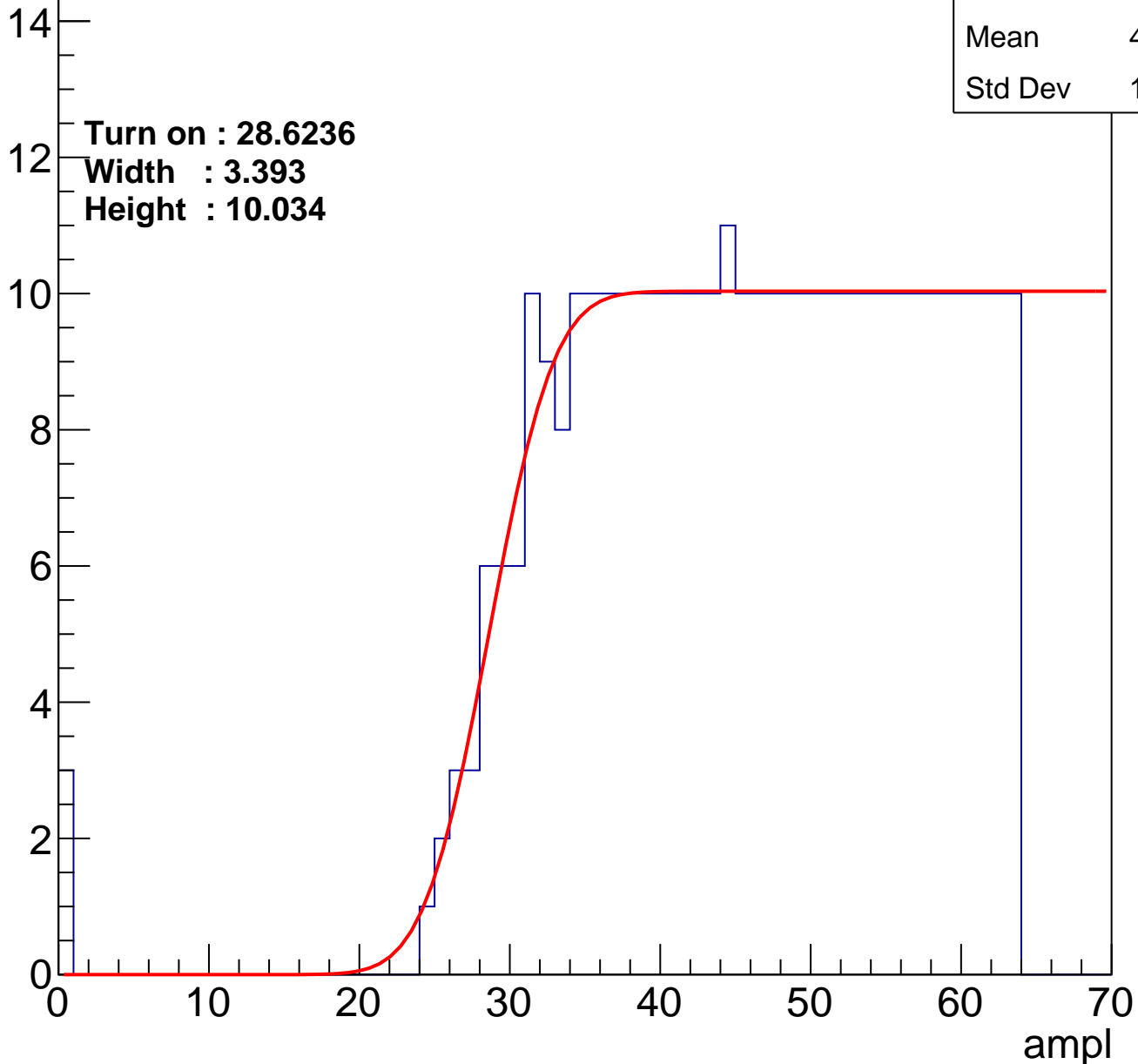
Entries	358
Mean	45.28
Std Dev	11.18

Turn on : 28.6236

Width : 3.393

Height : 10.034

Entry



B1L101S, U22-ch41

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.8
Std Dev	11.05

Turn on : 27.0350

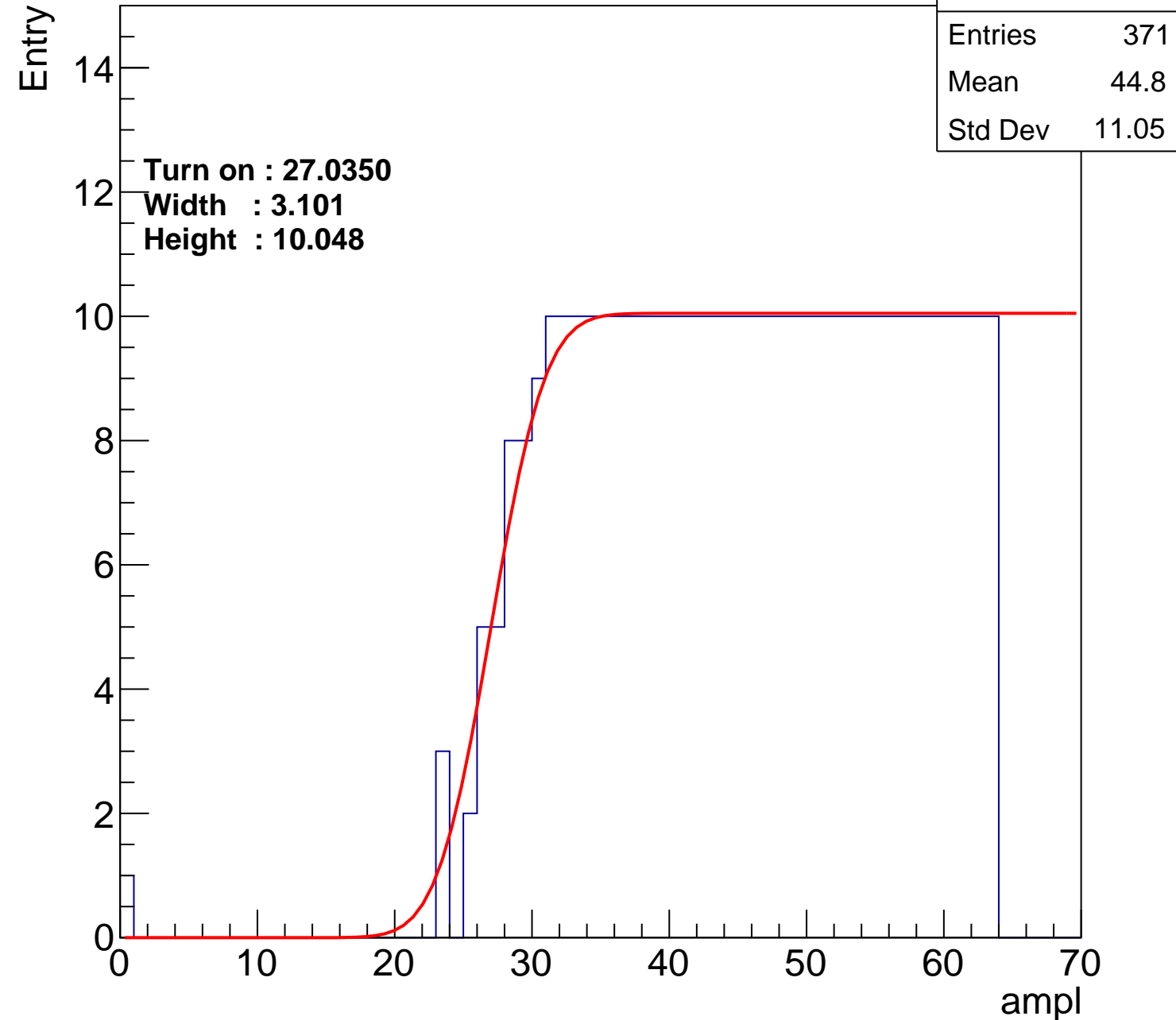
Width : 3.101

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch42

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.17
Std Dev	10.91

Turn on : 28.2064

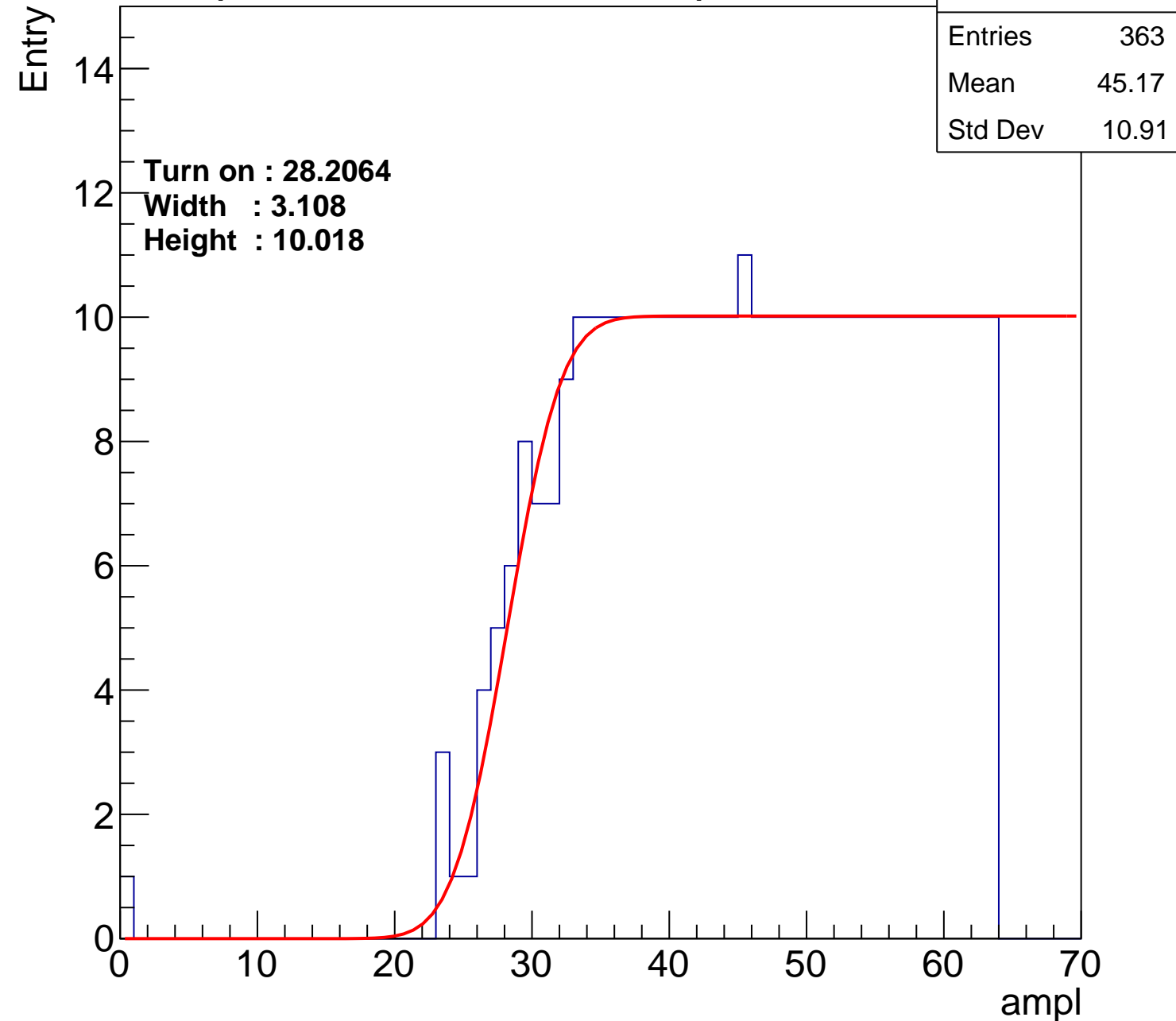
Width : 3.108

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch43

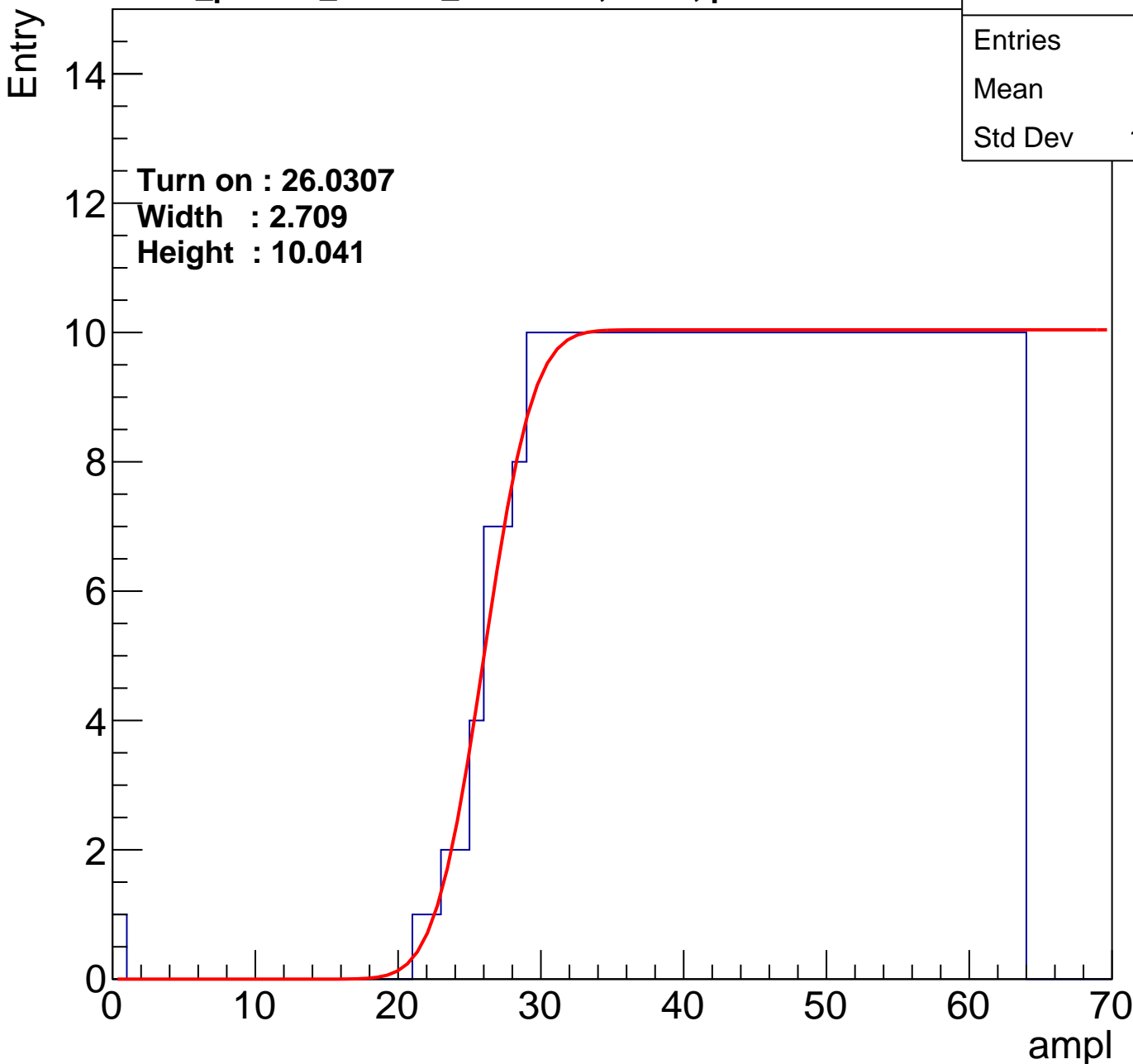
calib_packv5_042523_0143.root, FC#0, port D2

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.0307

Width : 2.709

Height : 10.041



B1L101S, U22-ch44

calib_packv5_042523_0143.root, FC#0, port D2

Entries	394
Mean	43.48
Std Dev	12.08

Turn on : 24.8663

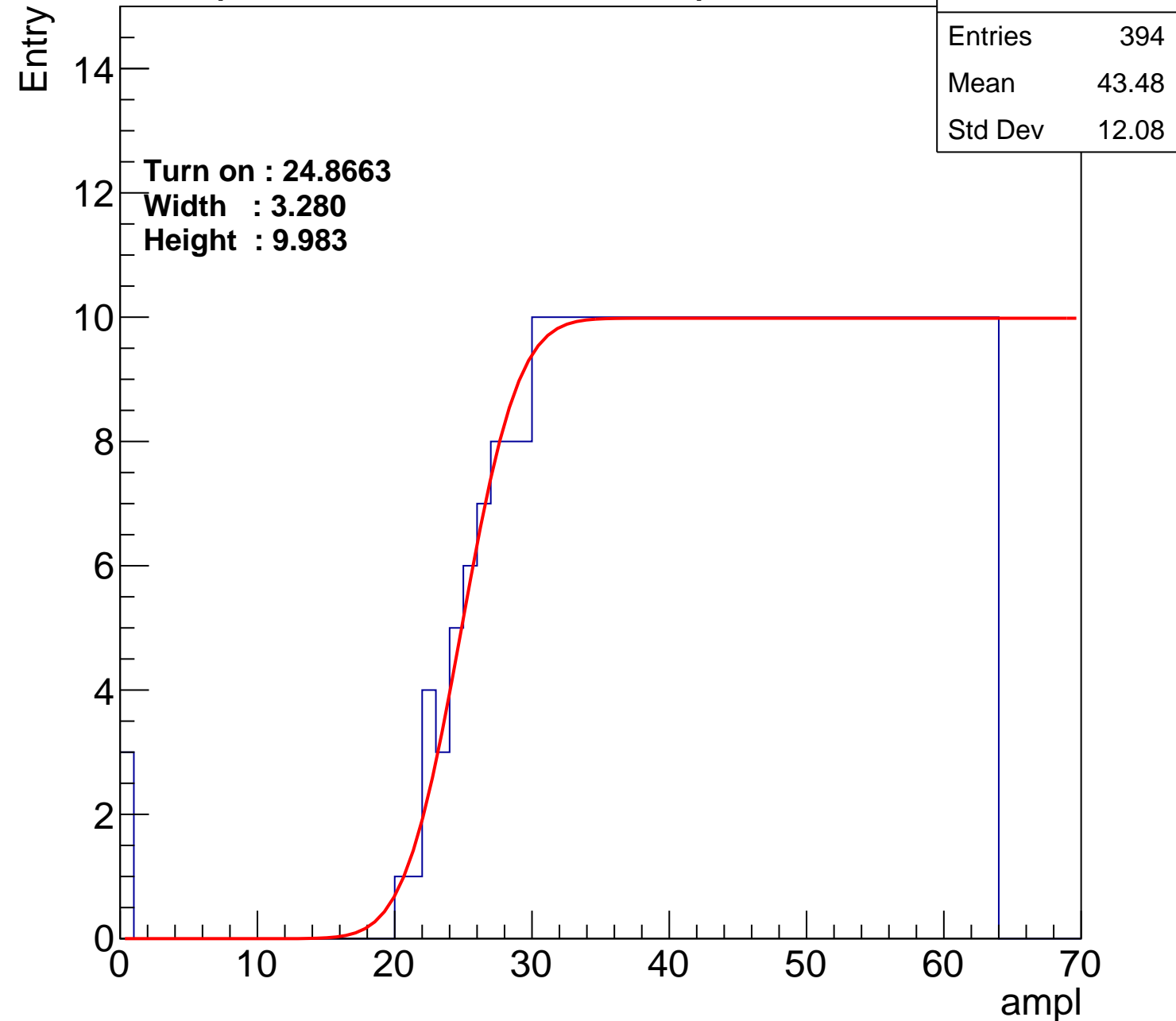
Width : 3.280

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch45

calib_packv5_042523_0143.root, FC#0, port D2

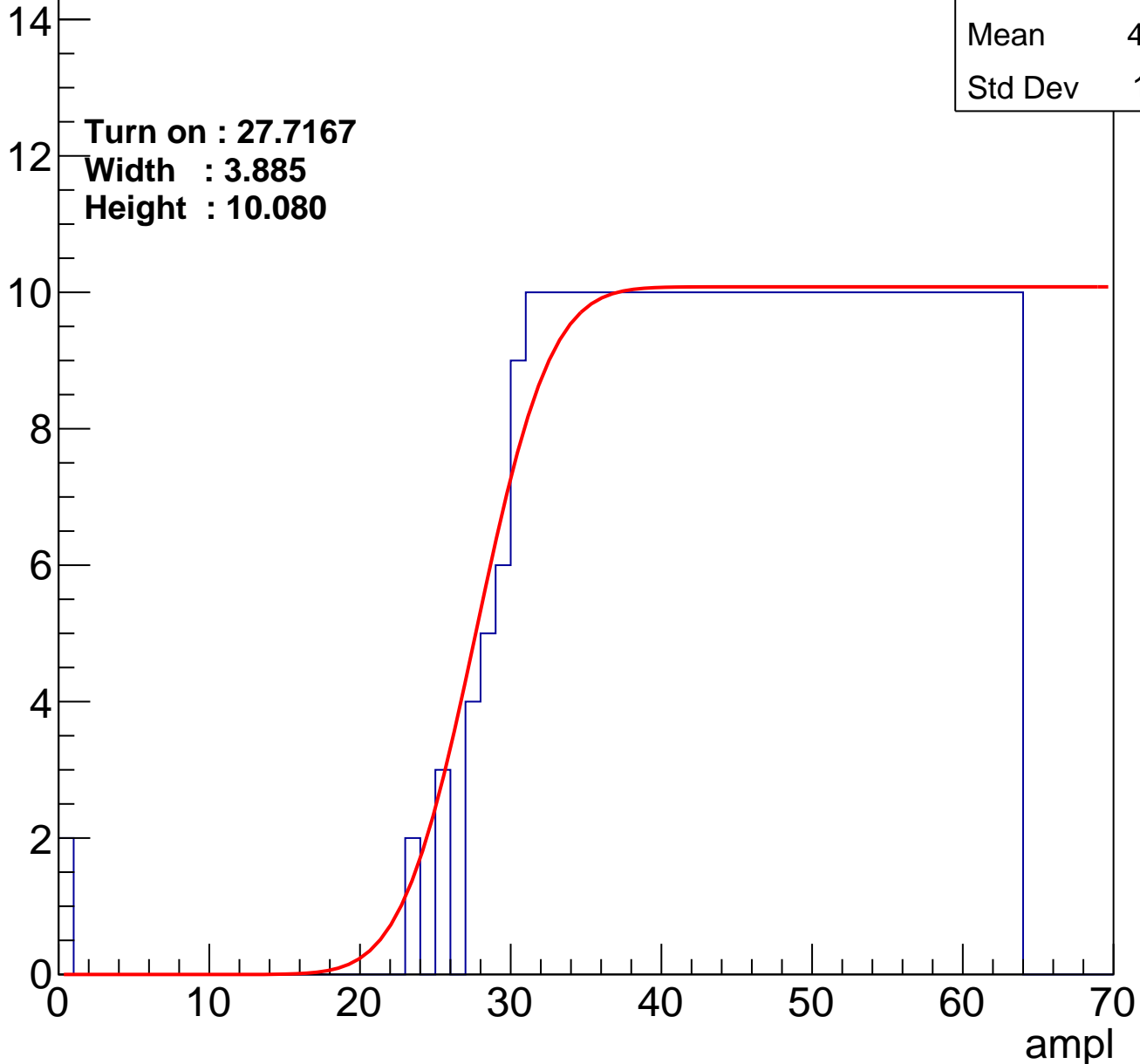
Entries	361
Mean	45.22
Std Dev	11.01

Turn on : 27.7167

Width : 3.885

Height : 10.080

Entry



B1L101S, U22-ch46

calib_packv5_042523_0143.root, FC#0, port D2

Entries	382
Mean	44.09
Std Dev	11.92

Turn on : 26.6534

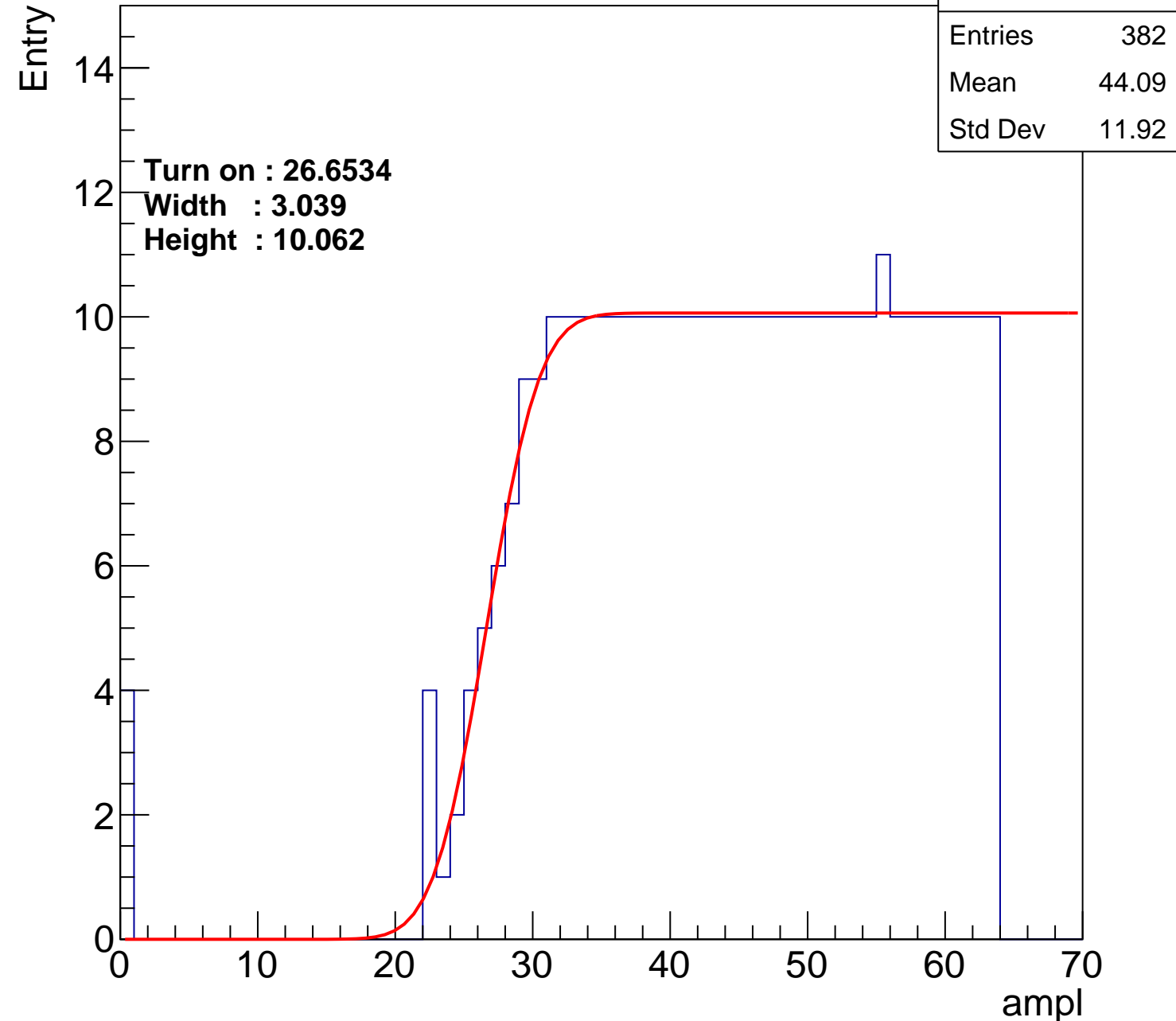
Width : 3.039

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch47

calib_packv5_042523_0143.root, FC#0, port D2

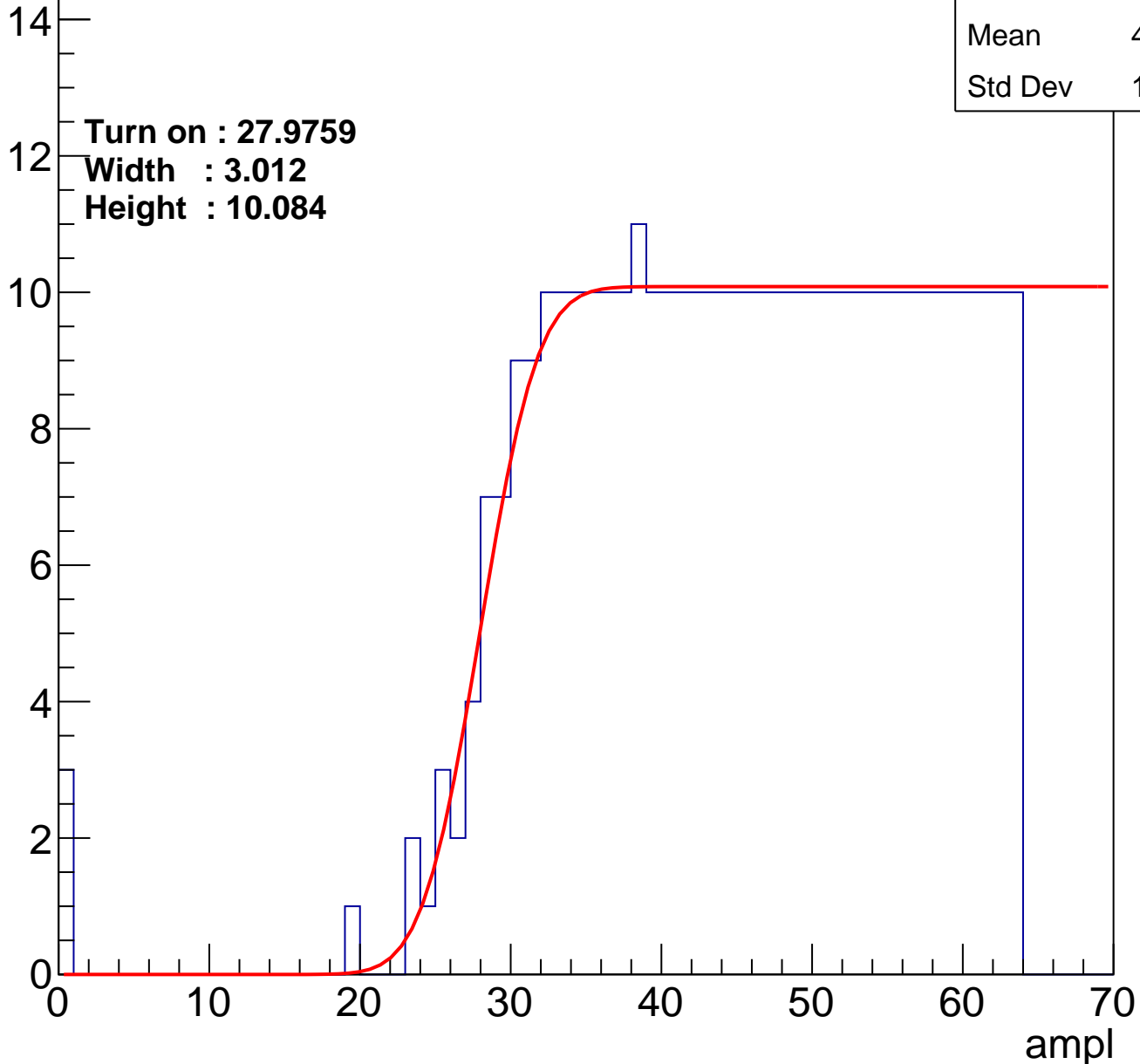
Entries	369
Mean	44.74
Std Dev	11.44

Turn on : 27.9759

Width : 3.012

Height : 10.084

Entry



calib_packv5_042523_0143.root, FC#0, port D2

Entries	367
Mean	44.89
Std Dev	11.2

Std Dev	11.2
---------	------

Height : 9.975



B1L101S, U22-ch49

calib_packv5_042523_0143.root, FC#0, port D2

Entries	400
Mean	43.29
Std Dev	12.07

Turn on : 24.2971

Width : 1.123

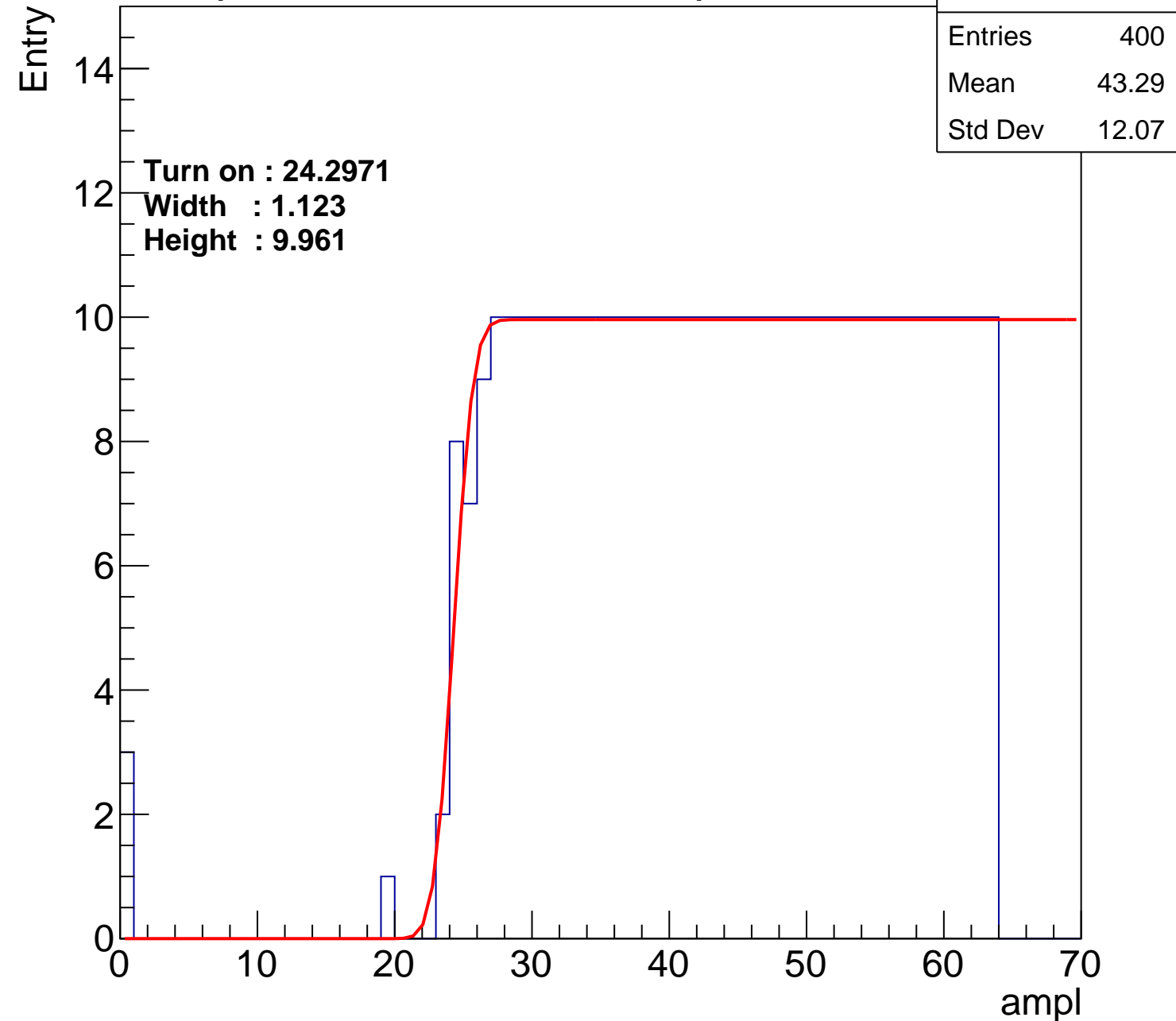
Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U22-ch50

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.07
Std Dev	11.22

Turn on : 28.2601

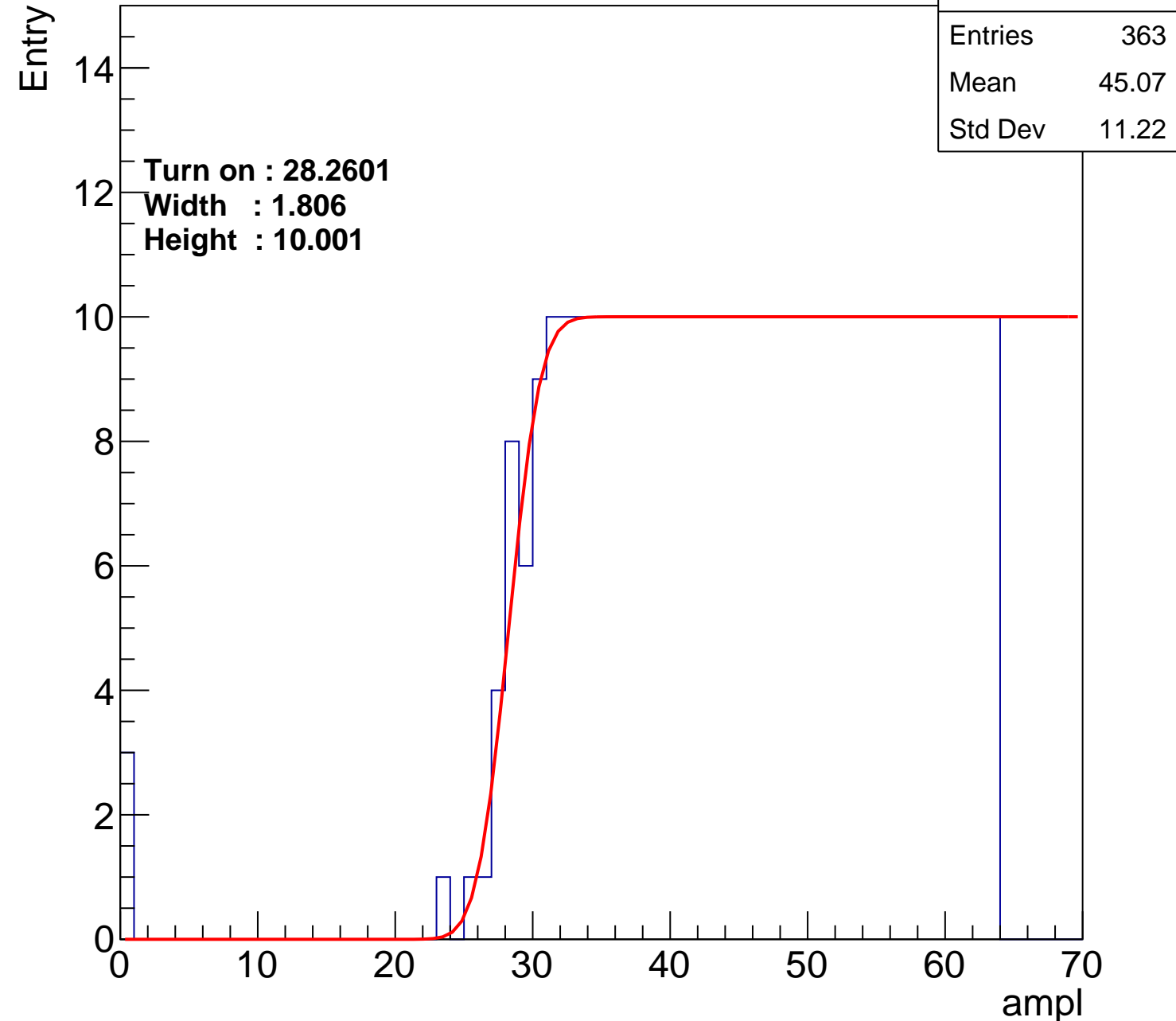
Width : 1.806

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch51

calib_packv5_042523_0143.root, FC#0, port D2

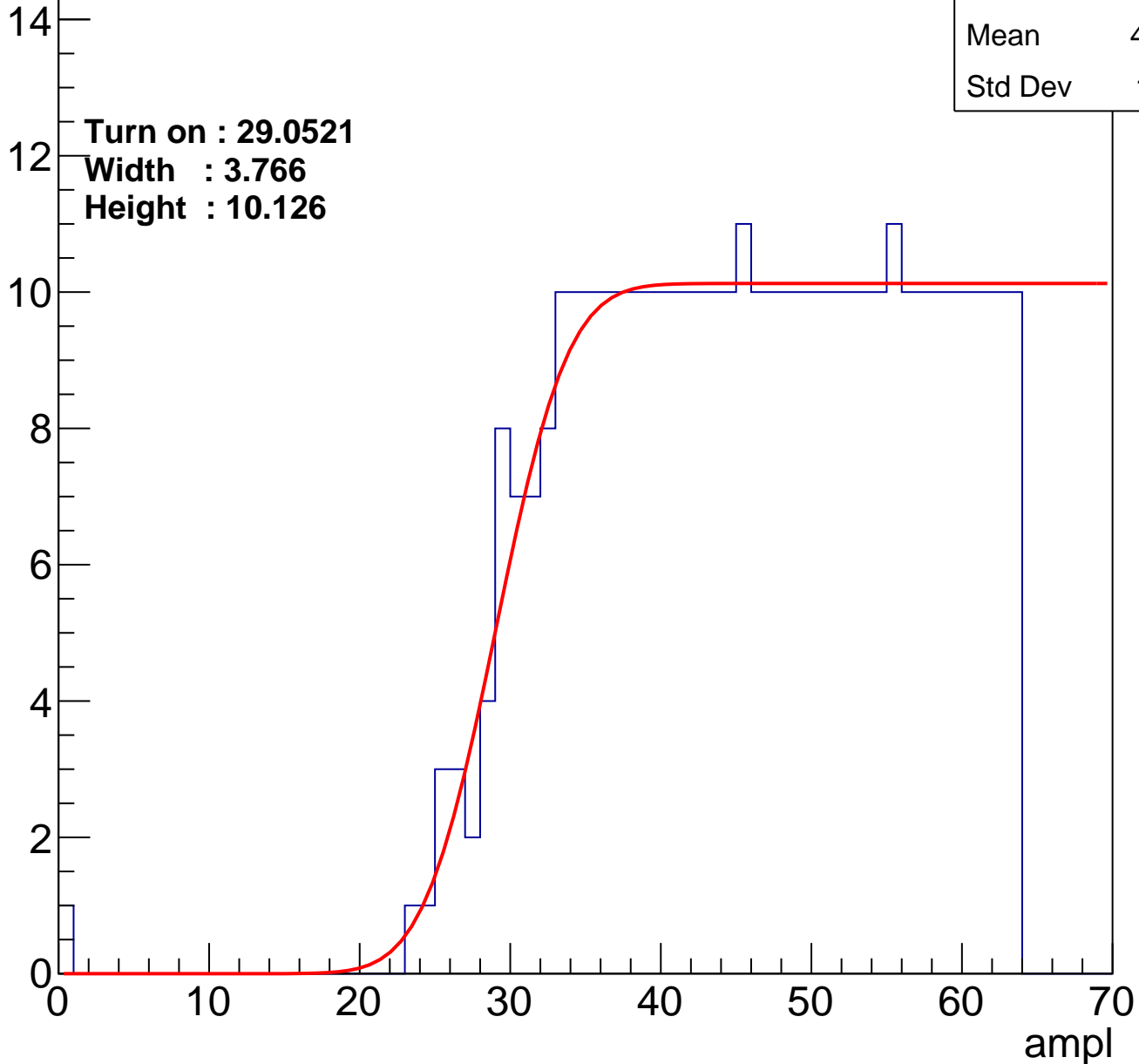
Entries	357
Mean	45.55
Std Dev	10.71

Turn on : 29.0521

Width : 3.766

Height : 10.126

Entry



B1L101S, U22-ch52

calib_packv5_042523_0143.root, FC#0, port D2

Entries	378
Mean	44.4
Std Dev	11.44

Turn on : 27.1504

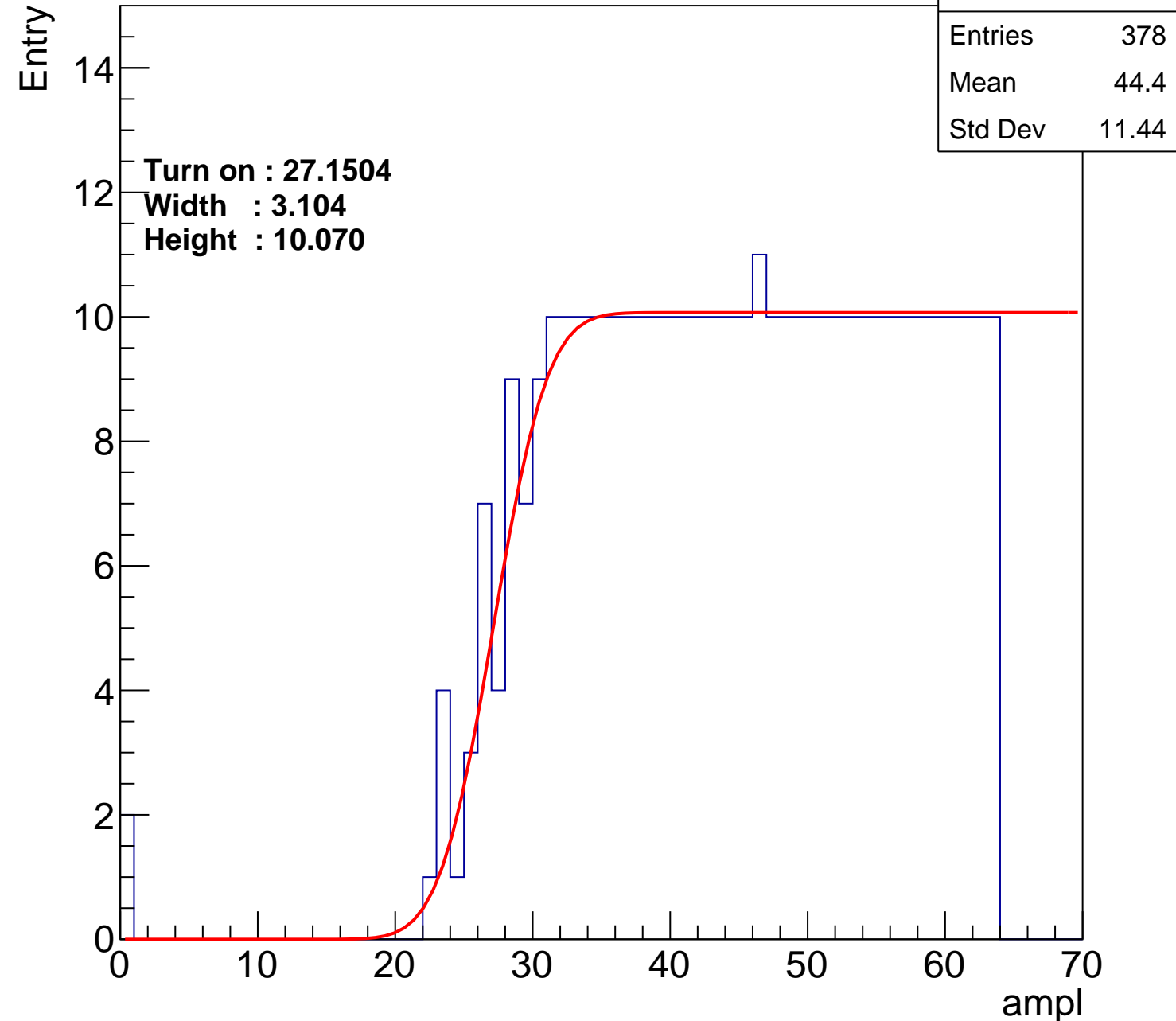
Width : 3.104

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch53

calib_packv5_042523_0143.root, FC#0, port D2

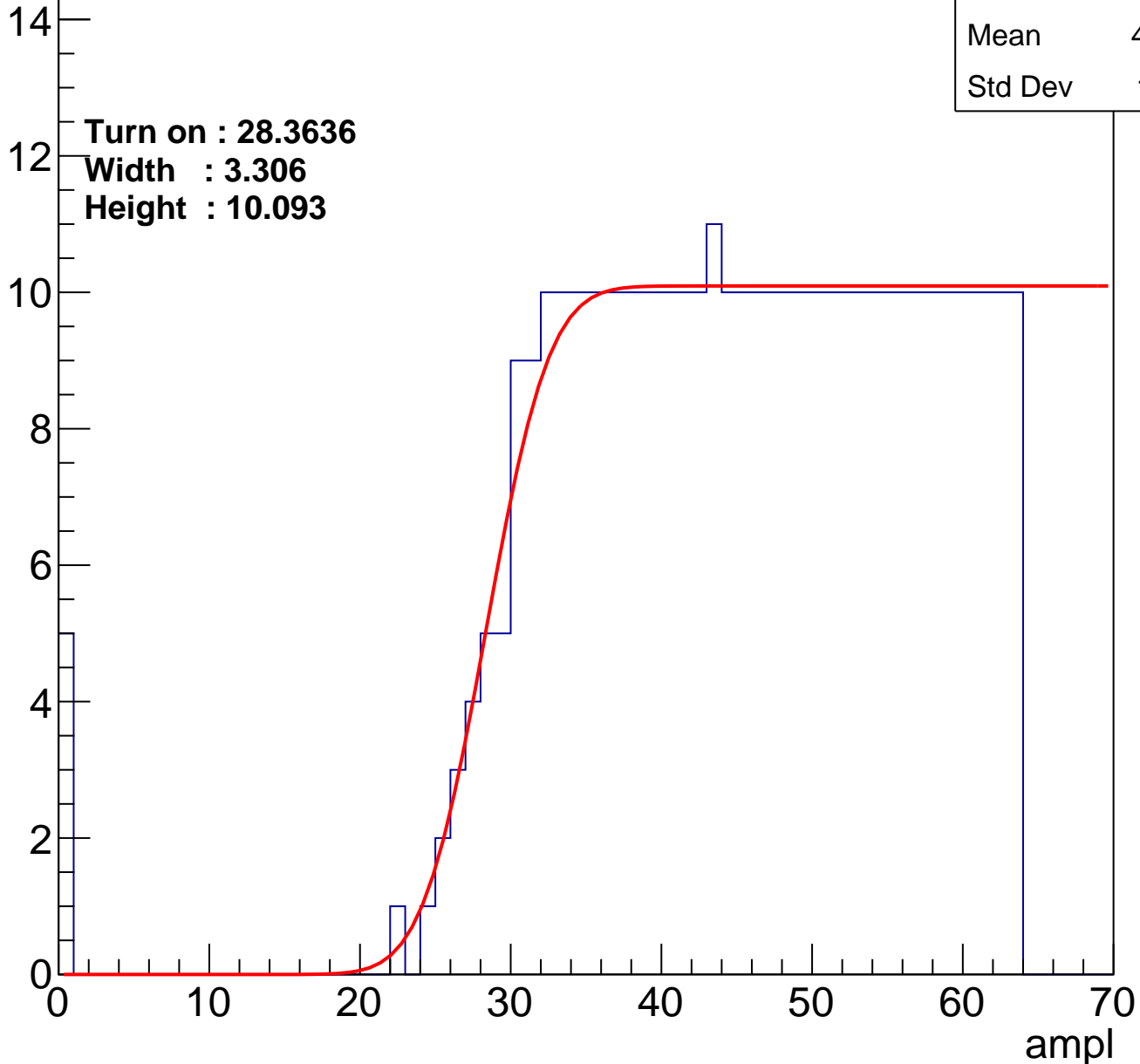
Entries	365
Mean	44.82
Std Dev	11.71

Turn on : 28.3636

Width : 3.306

Height : 10.093

Entry



B1L101S, U22-ch54

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.58
Std Dev	11.46

Turn on : 27.4735

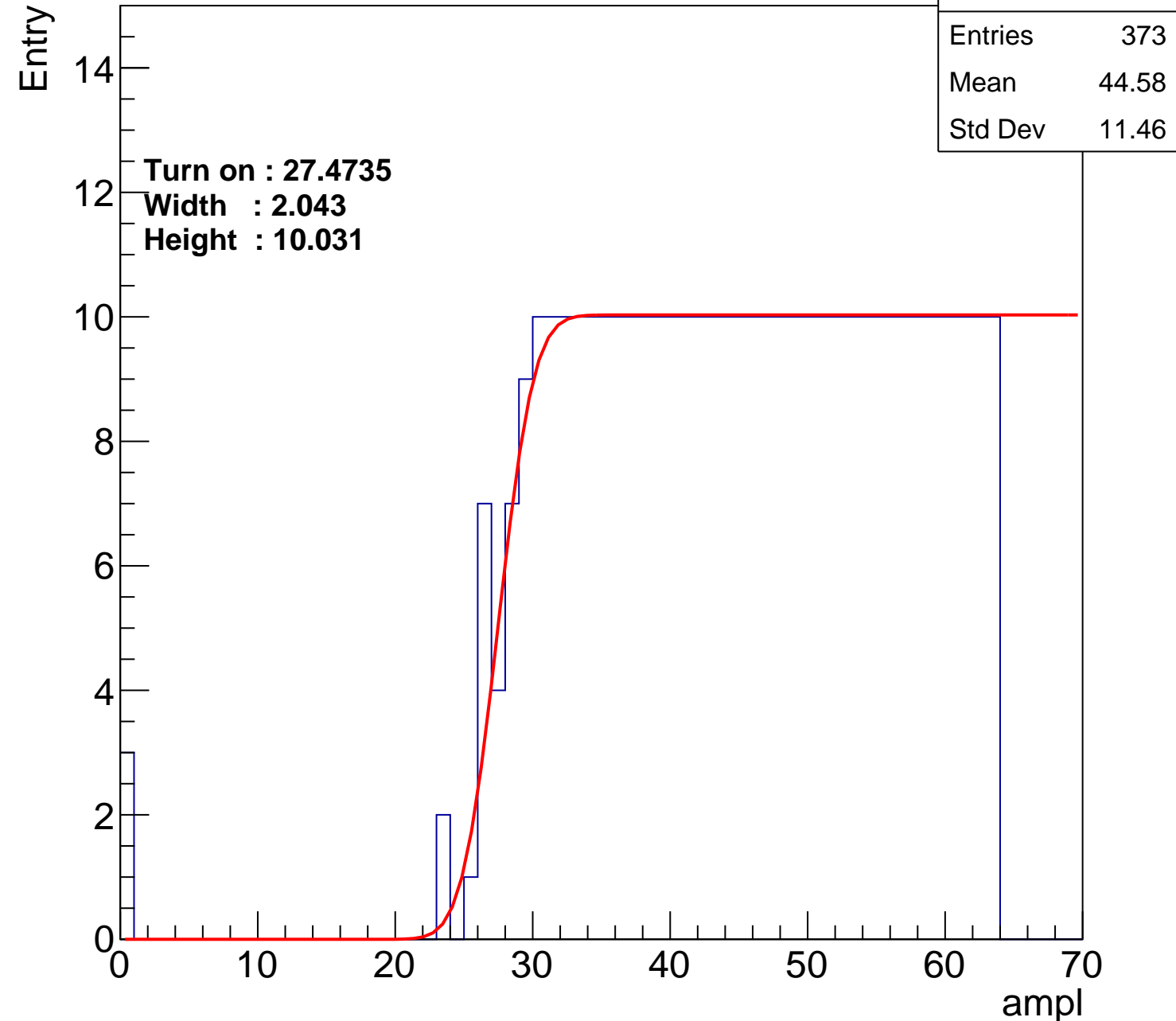
Width : 2.043

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch55

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.76
Std Dev	11.47

Turn on : 27.7107

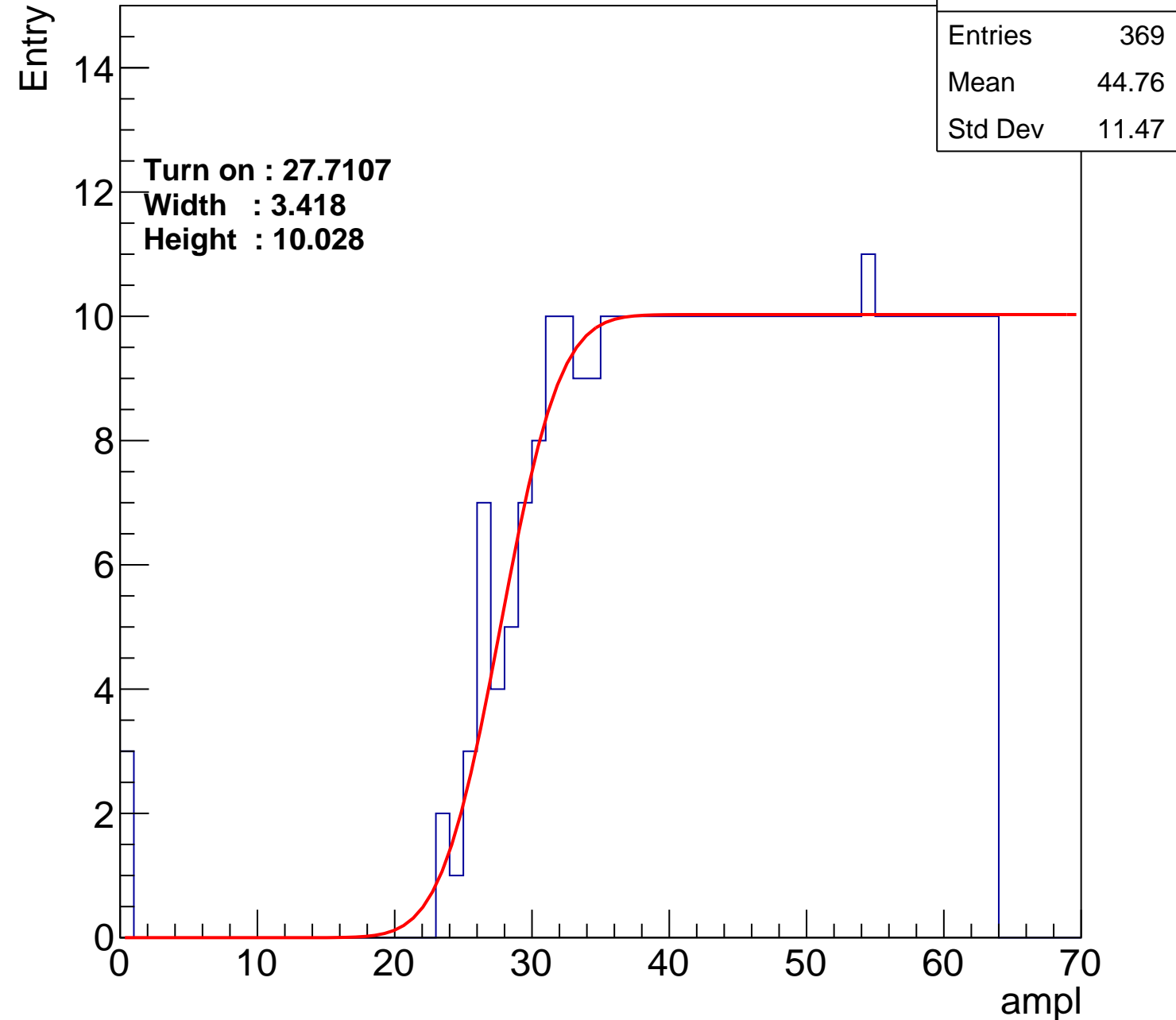
Width : 3.418

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch56

calib_packv5_042523_0143.root, FC#0, port D2

Entries	355
Mean	45.22
Std Dev	11.61

Turn on : 29.0375

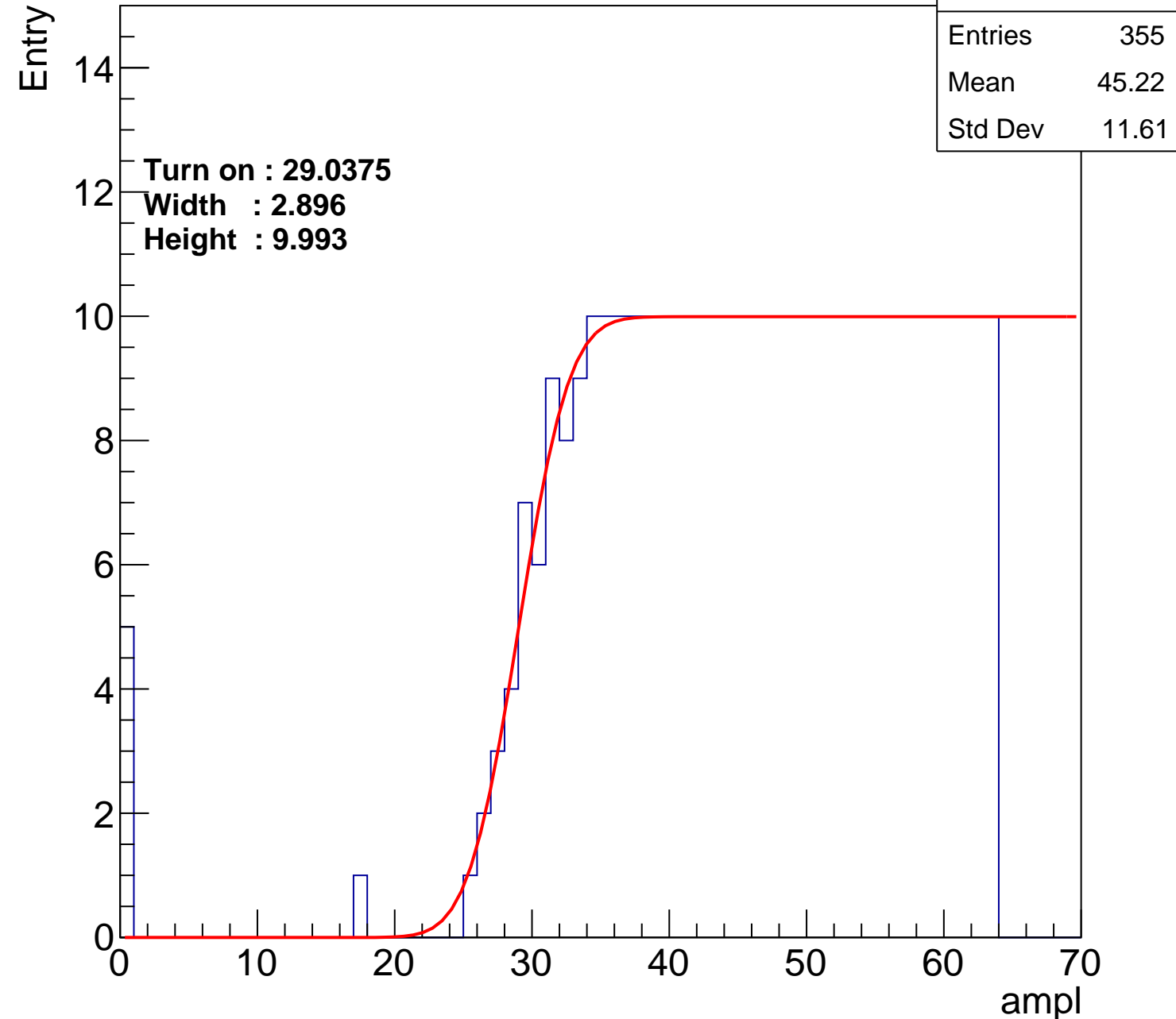
Width : 2.896

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch57

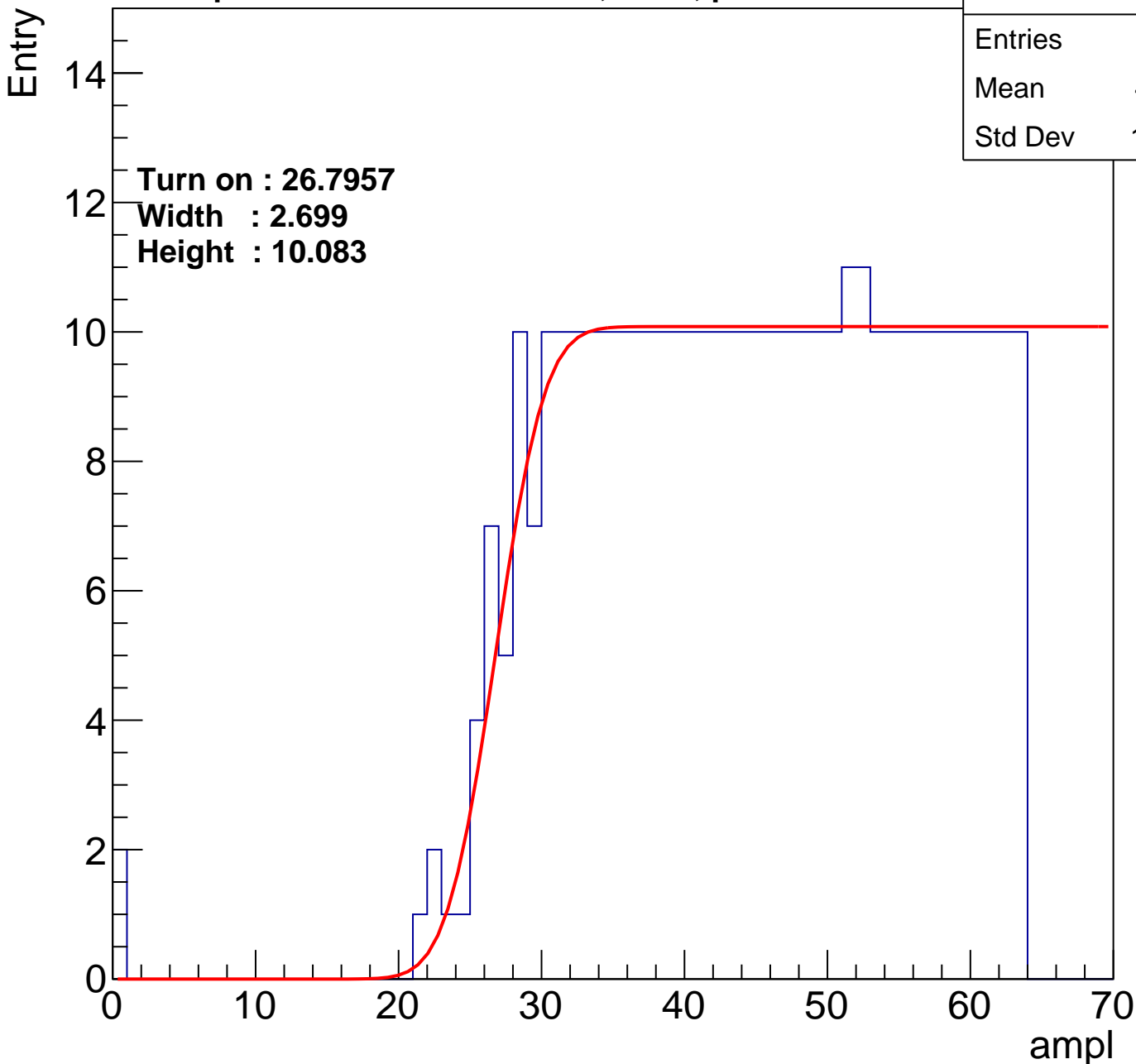
calib_packv5_042523_0143.root, FC#0, port D2

Entries	382
Mean	44.31
Std Dev	11.49

Turn on : 26.7957

Width : 2.699

Height : 10.083



B1L101S, U22-ch58

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.43
Std Dev	11.77

Turn on : 27.1688

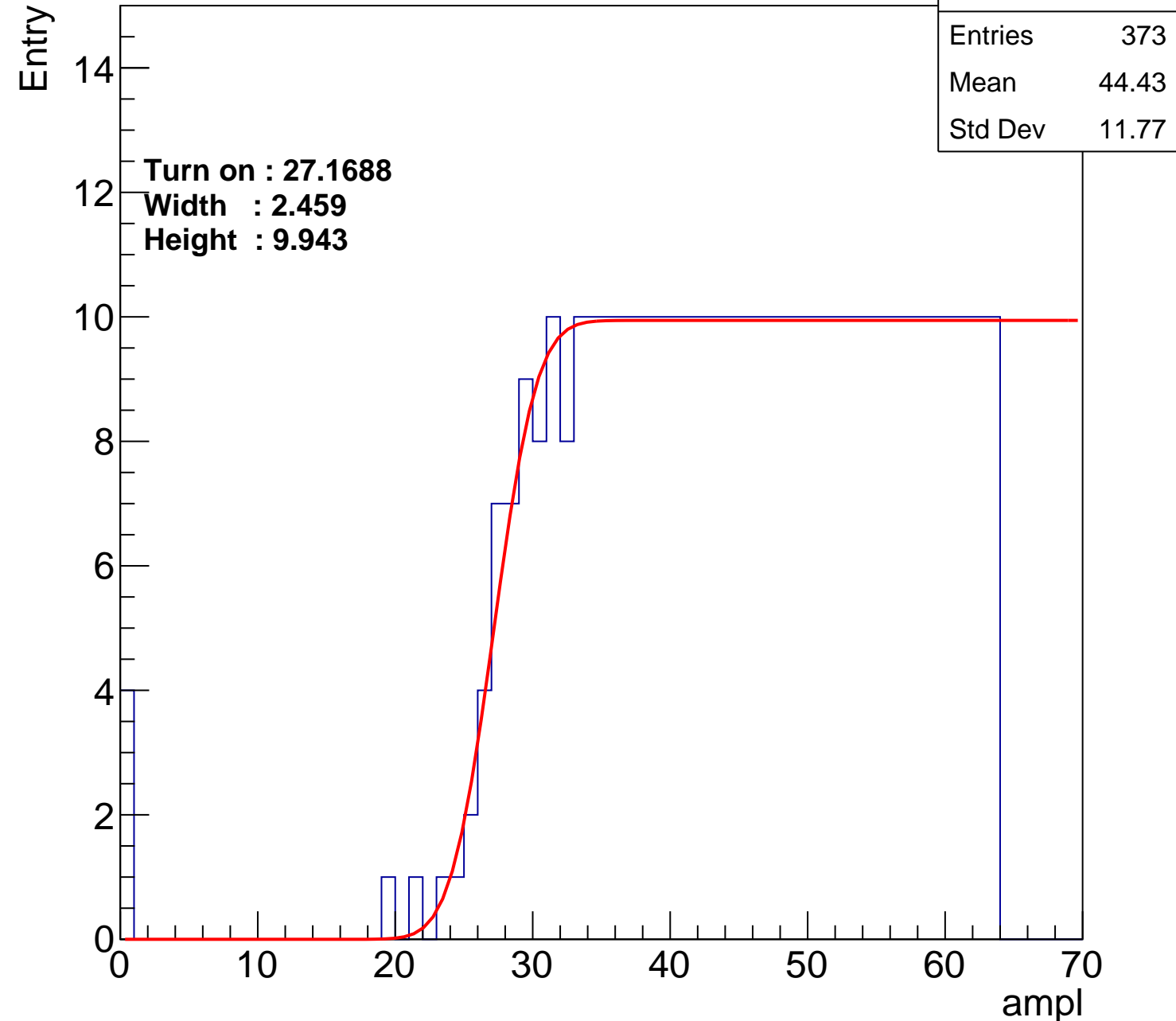
Width : 2.459

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch59

calib_packv5_042523_0143.root, FC#0, port D2

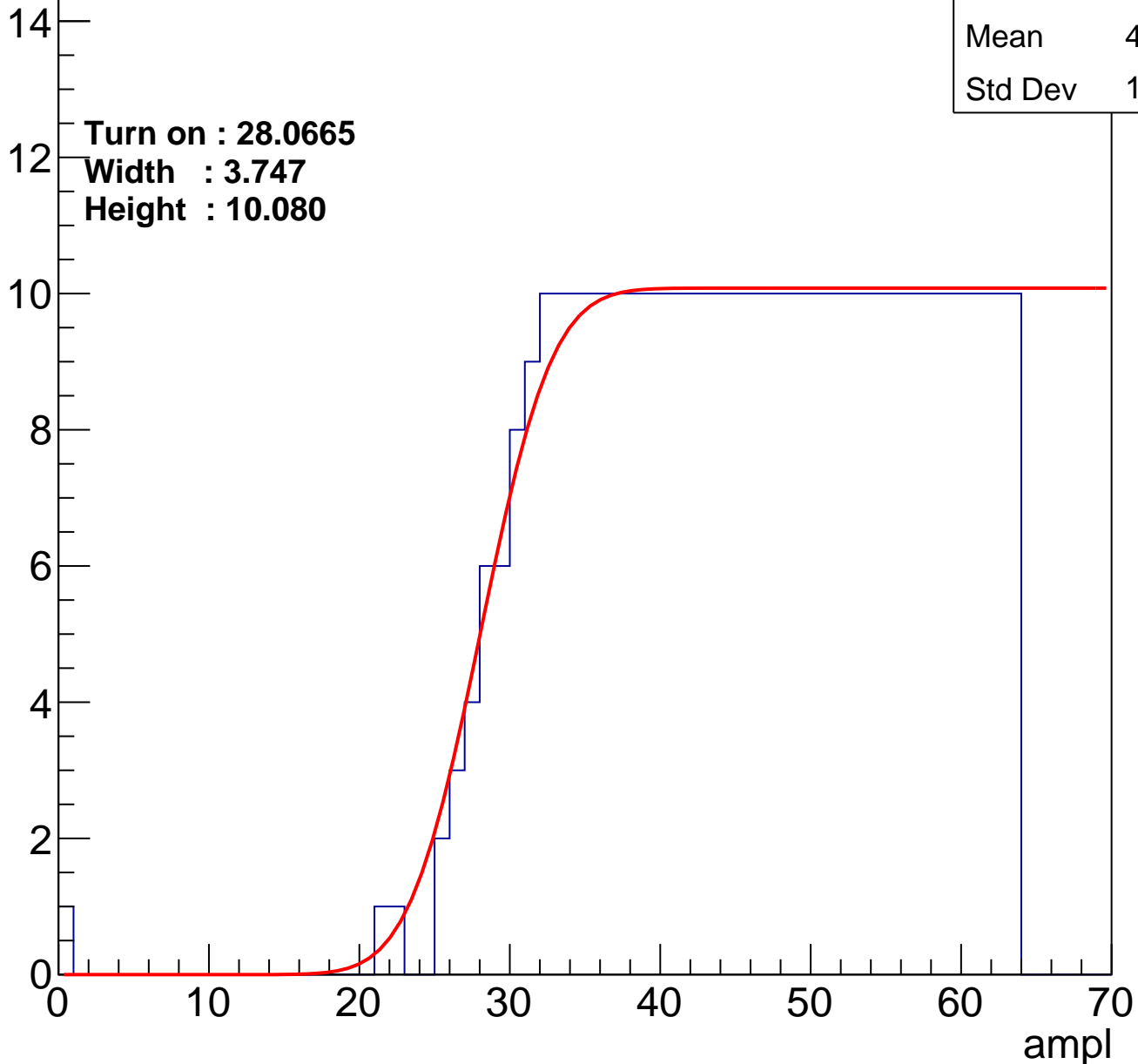
Entries	361
Mean	45.26
Std Dev	10.84

Turn on : 28.0665

Width : 3.747

Height : 10.080

Entry



B1L101S, U22-ch60

calib_packv5_042523_0143.root, FC#0, port D2

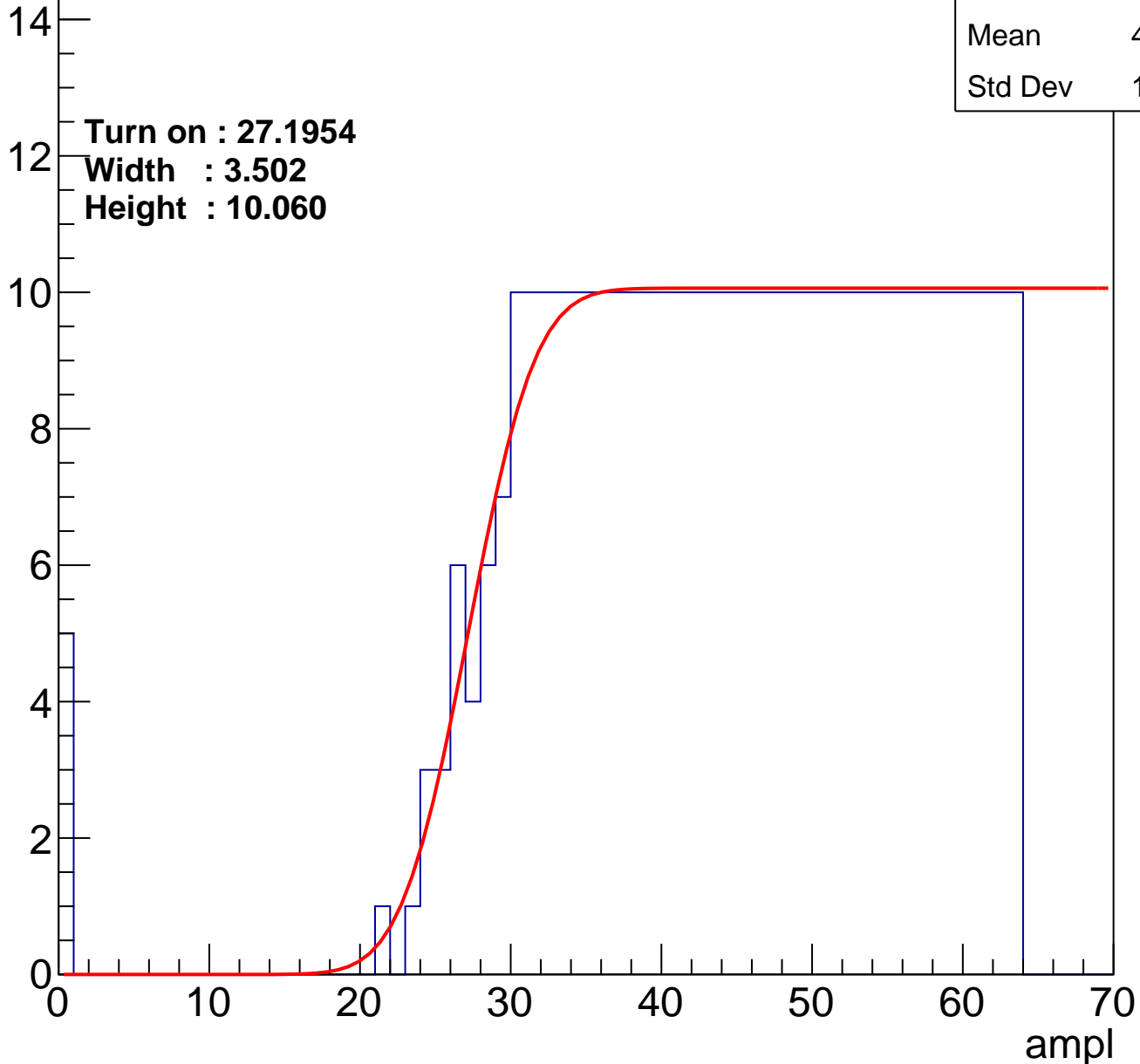
Entries	376
Mean	44.24
Std Dev	11.98

Turn on : 27.1954

Width : 3.502

Height : 10.060

Entry



B1L101S, U22-ch61

calib_packv5_042523_0143.root, FC#0, port D2

Entries	363
Mean	45.18
Std Dev	10.87

Turn on : 27.6067

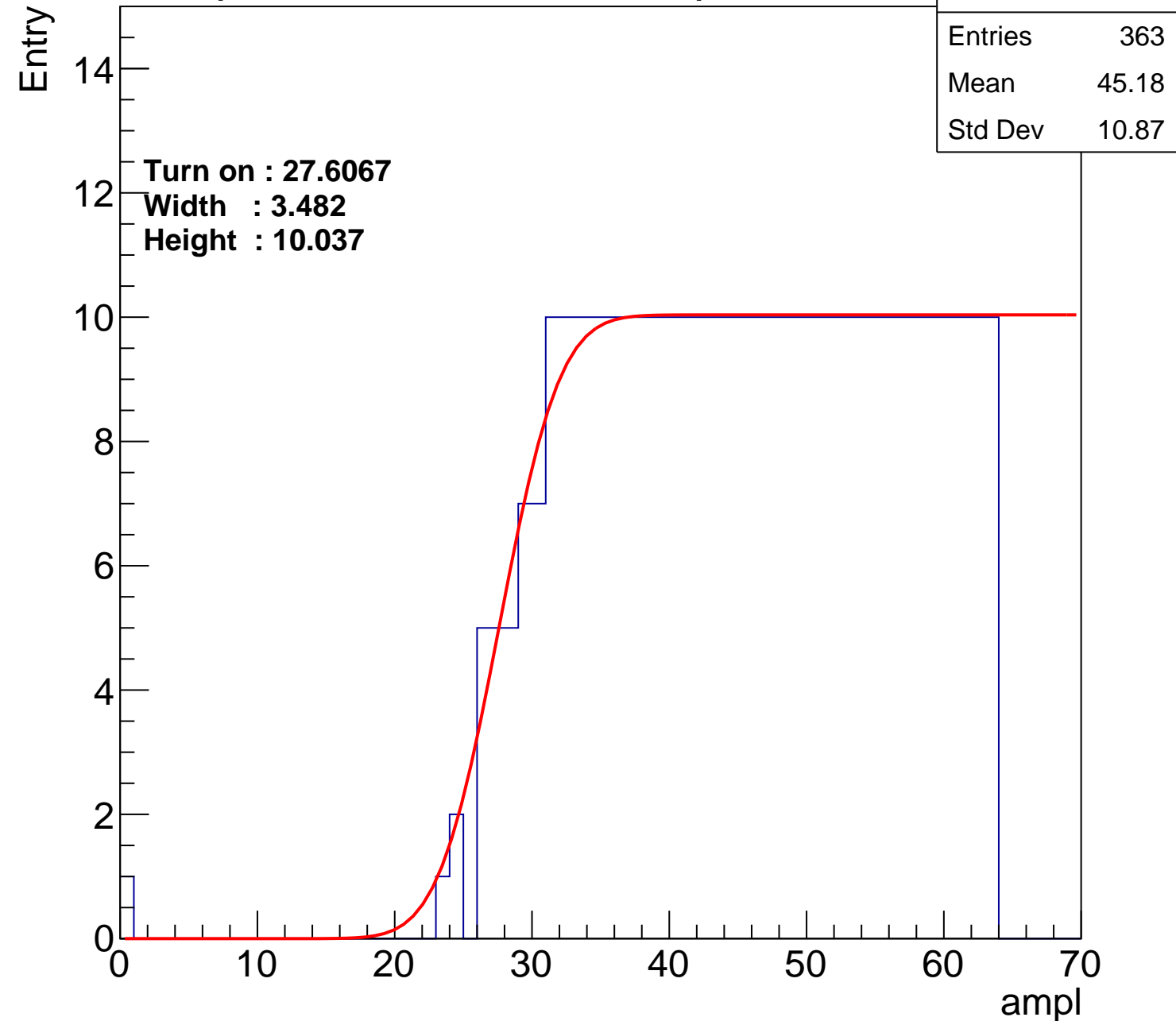
Width : 3.482

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch62

calib_packv5_042523_0143.root, FC#0, port D2

Entries	389
Mean	43.57
Std Dev	12.33

Turn on : 26.5320

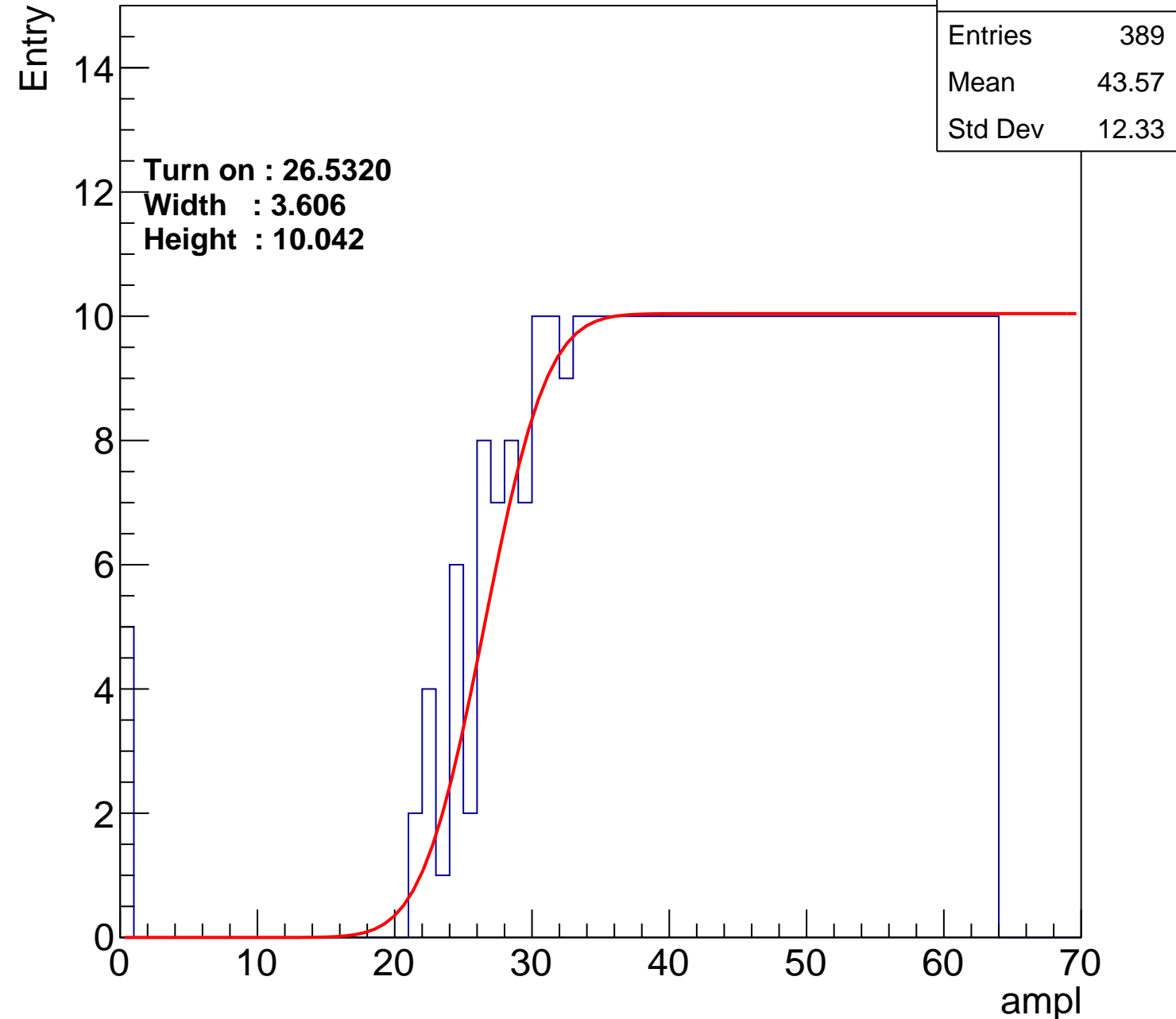
Width : 3.606

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch63

calib_packv5_042523_0143.root, FC#0, port D2

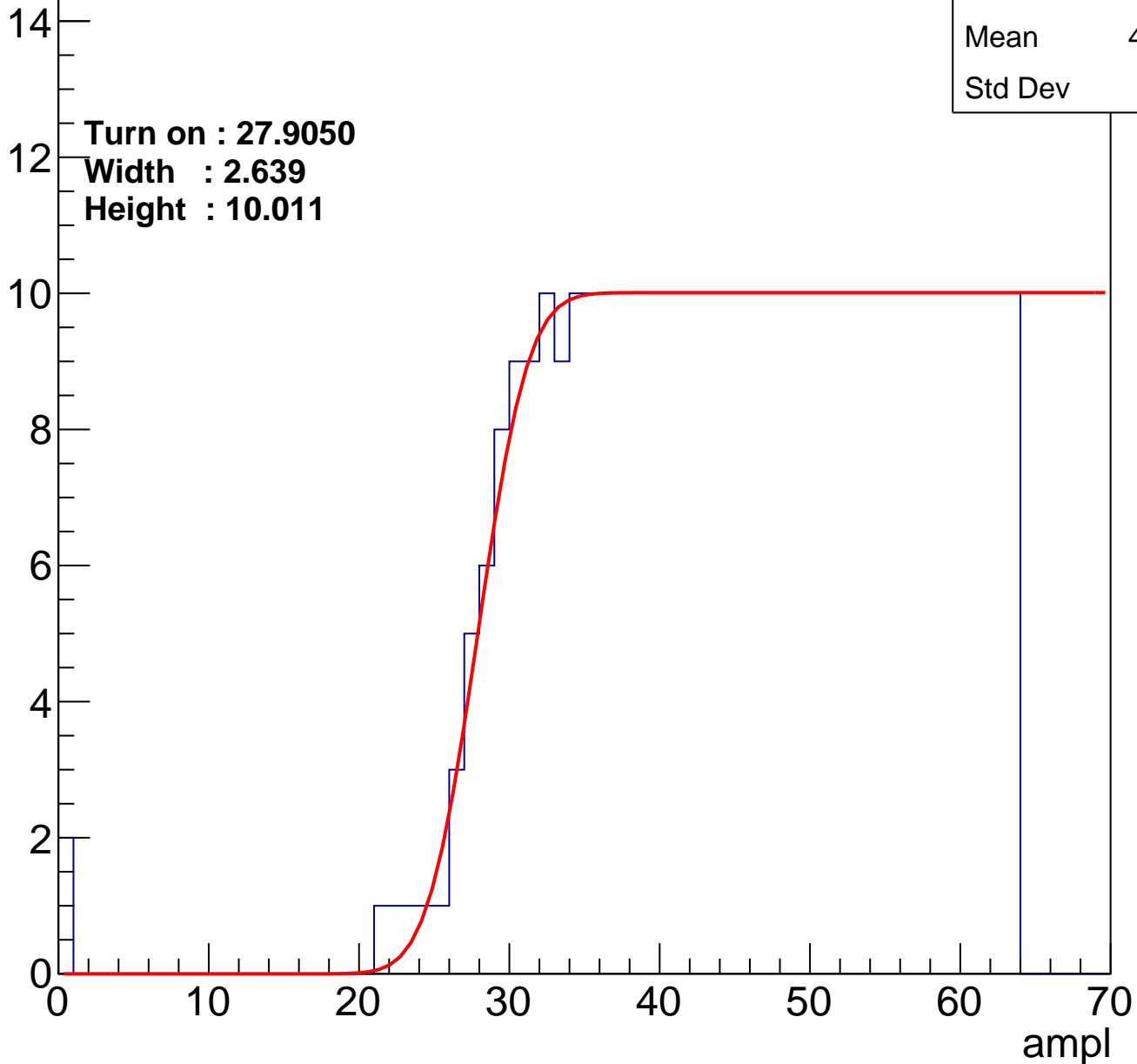
Entries	366
Mean	44.93
Std Dev	11.2

Turn on : 27.9050

Width : 2.639

Height : 10.011

Entry



B1L101S, U22-ch64

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.61
Std Dev	11.66

Turn on : 27.6174

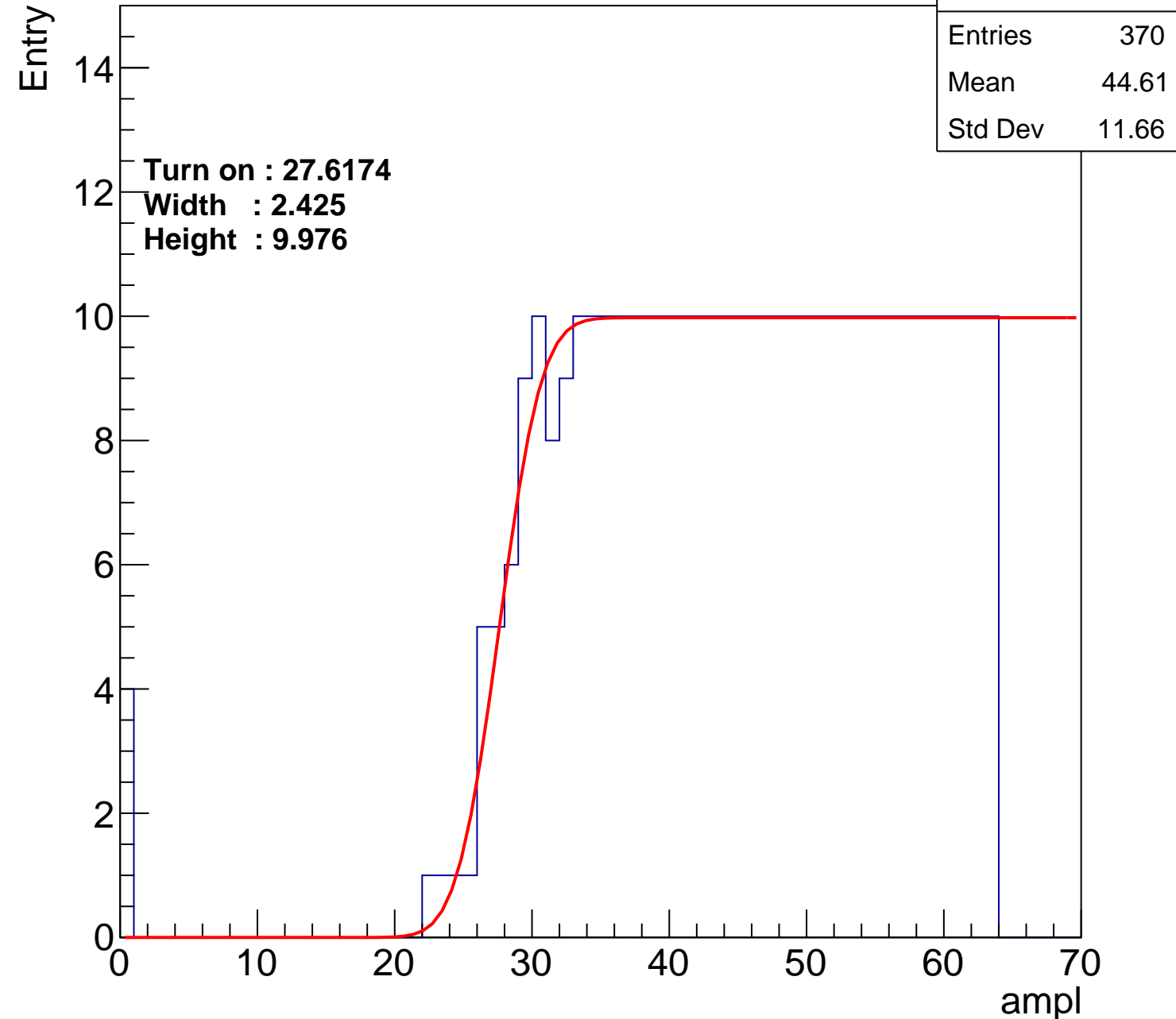
Width : 2.425

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#0, port D2

calib_packv5_042523_0143.root, FC#0, port D2

Turn on : 29.0391
Width : 2.970
Height : 10.022



B1L101S, U22-ch66

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.55
Std Dev	11.65

Turn on : 27.5895

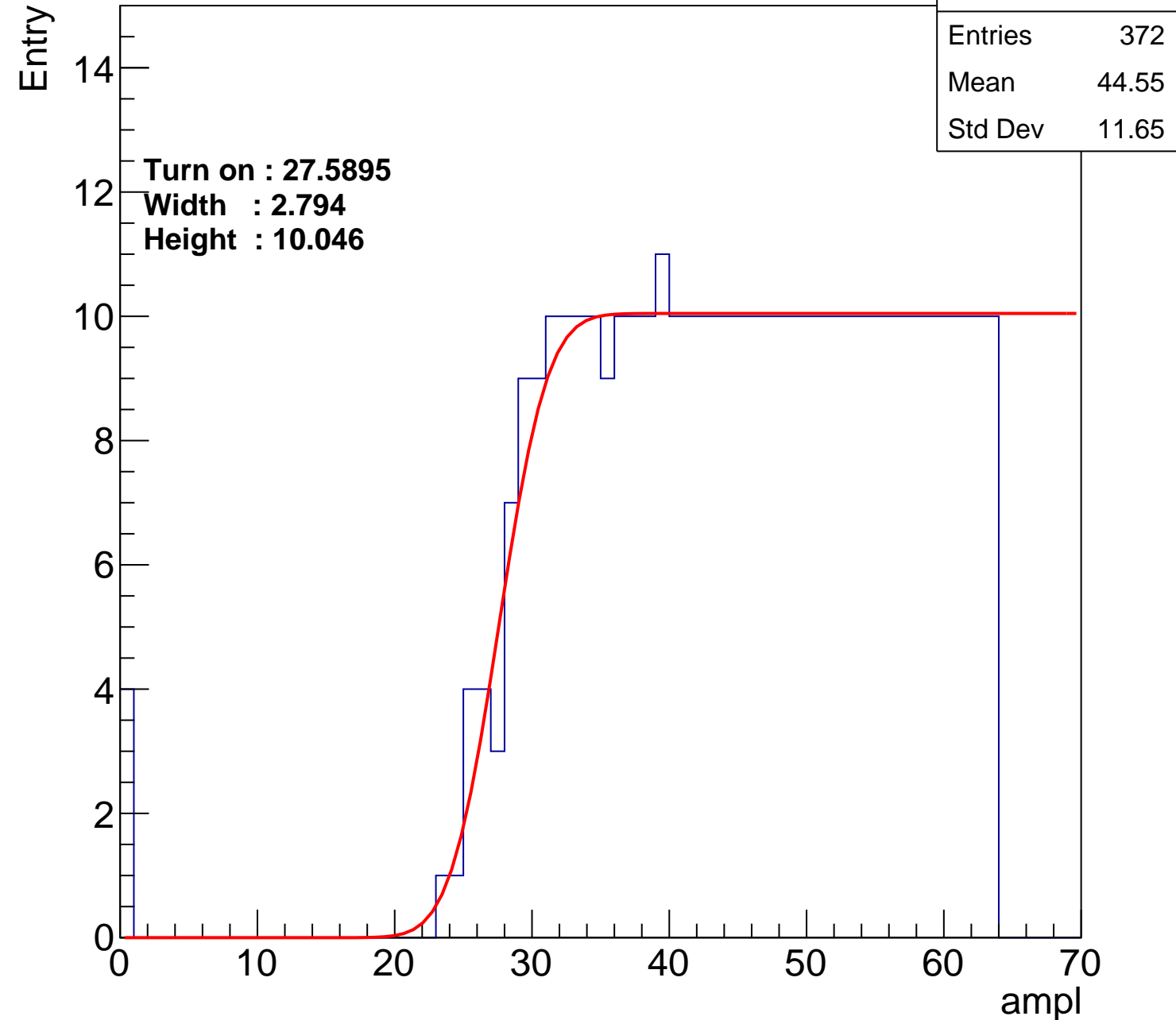
Width : 2.794

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch67

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.45
Std Dev	11.61

Turn on : 26.6649

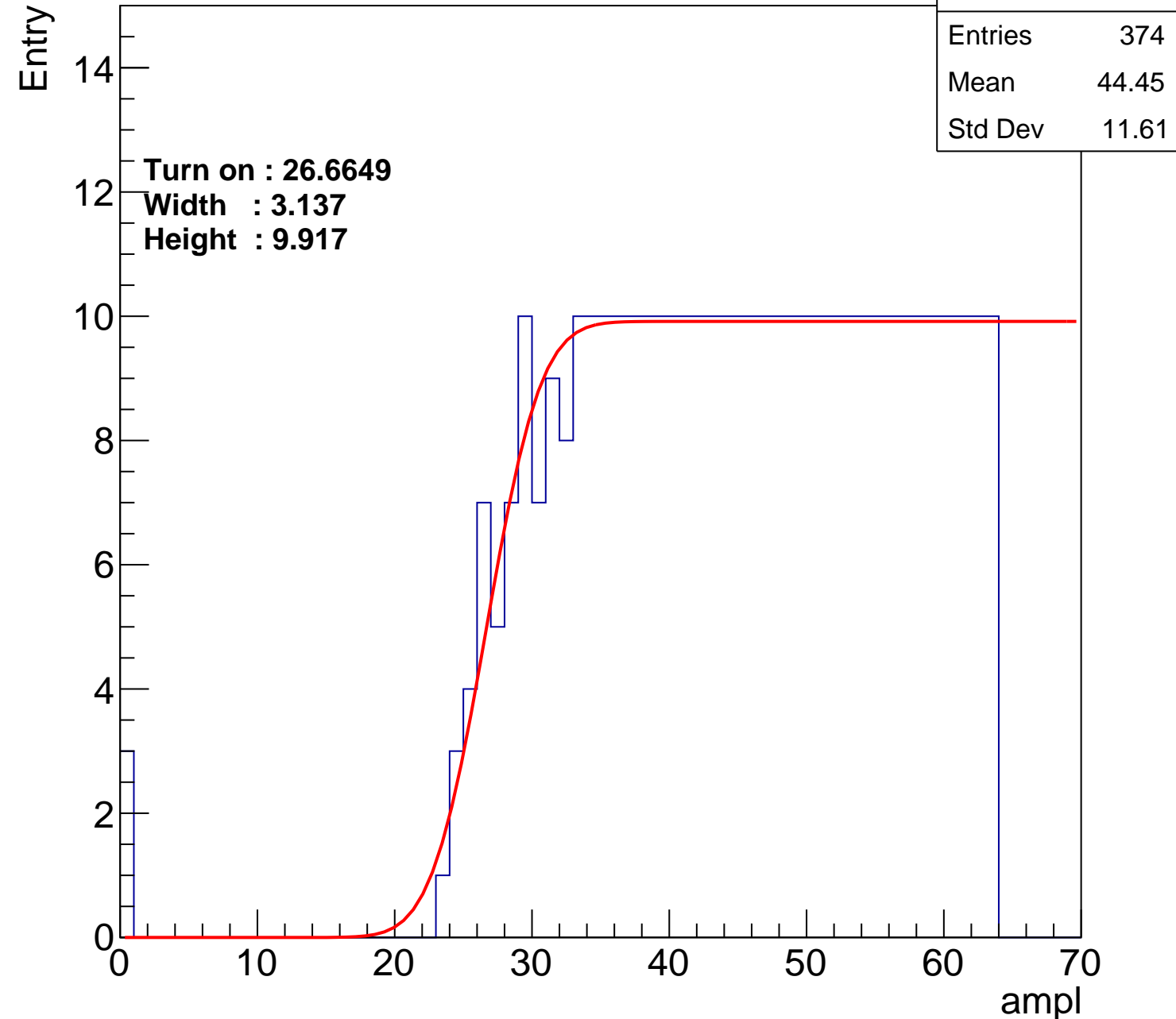
Width : 3.137

Height : 9.917

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch68

calib_packv5_042523_0143.root, FC#0, port D2

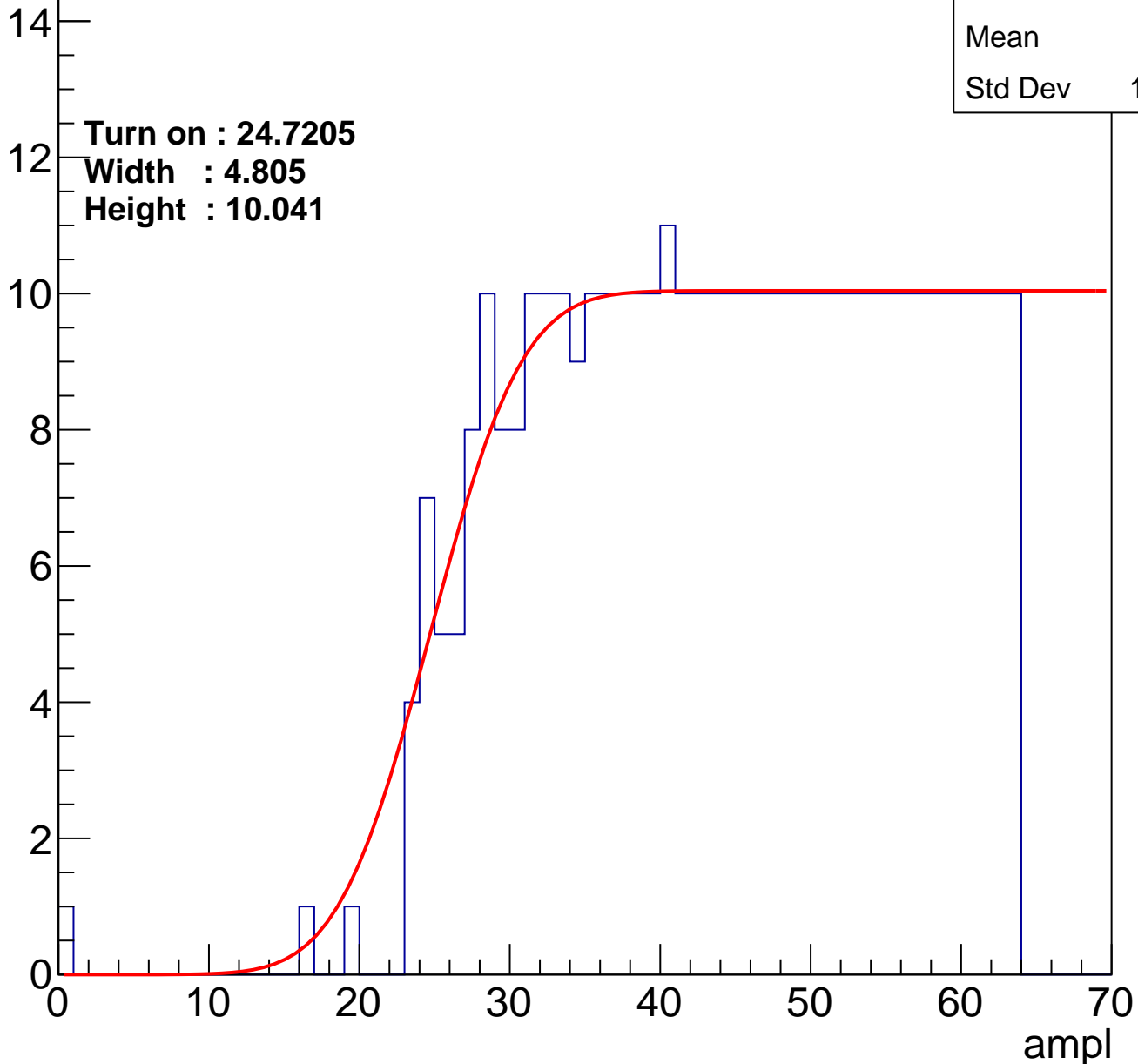
Entries	388
Mean	43.9
Std Dev	11.62

Turn on : 24.7205

Width : 4.805

Height : 10.041

Entry



B1L101S, U22-ch69

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.28
Std Dev	11.8

Turn on : 26.8661

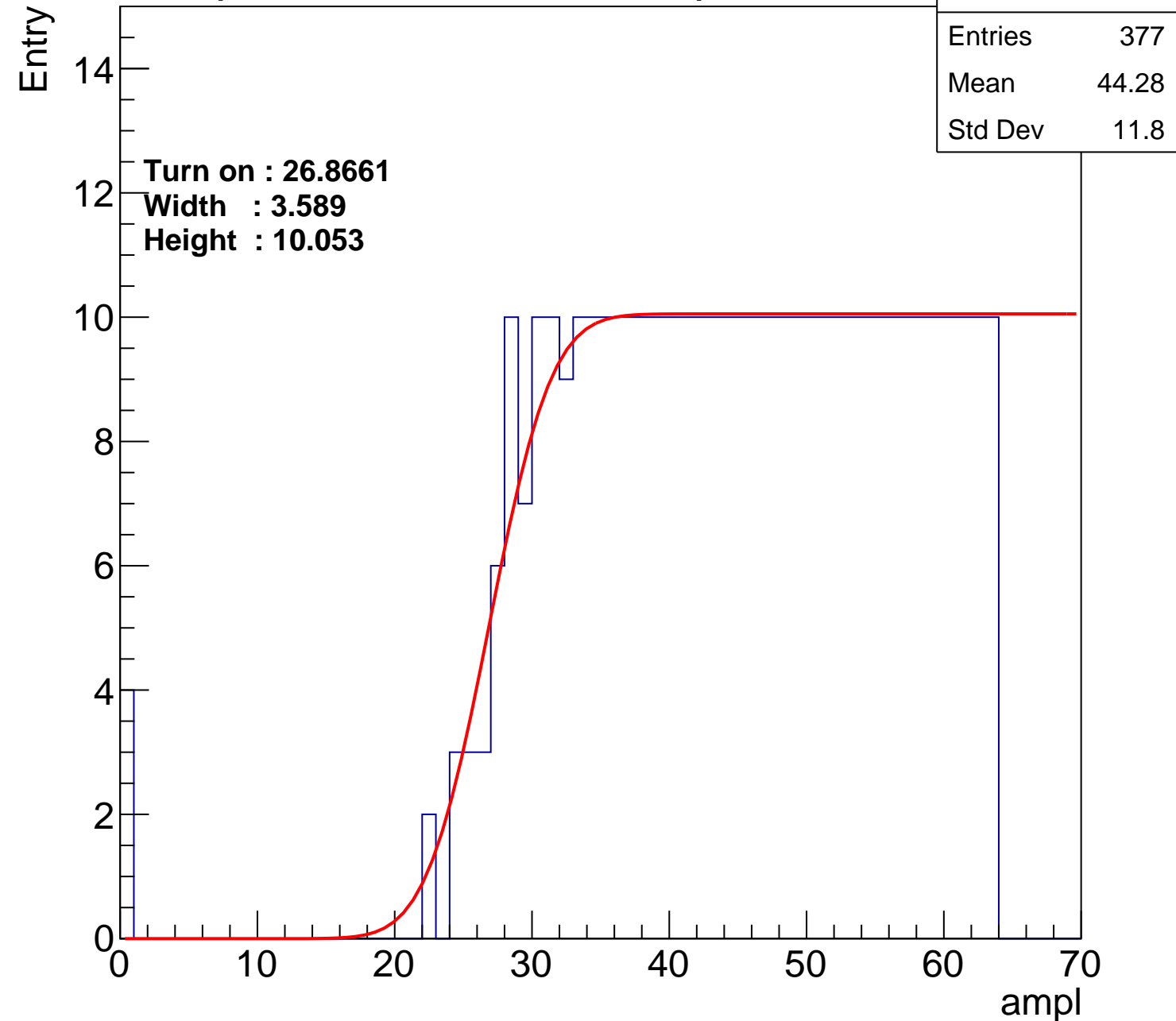
Width : 3.589

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch70

calib_packv5_042523_0143.root, FC#0, port D2

Entries	389
Mean	43.89
Std Dev	11.69

Turn on : 26.2060

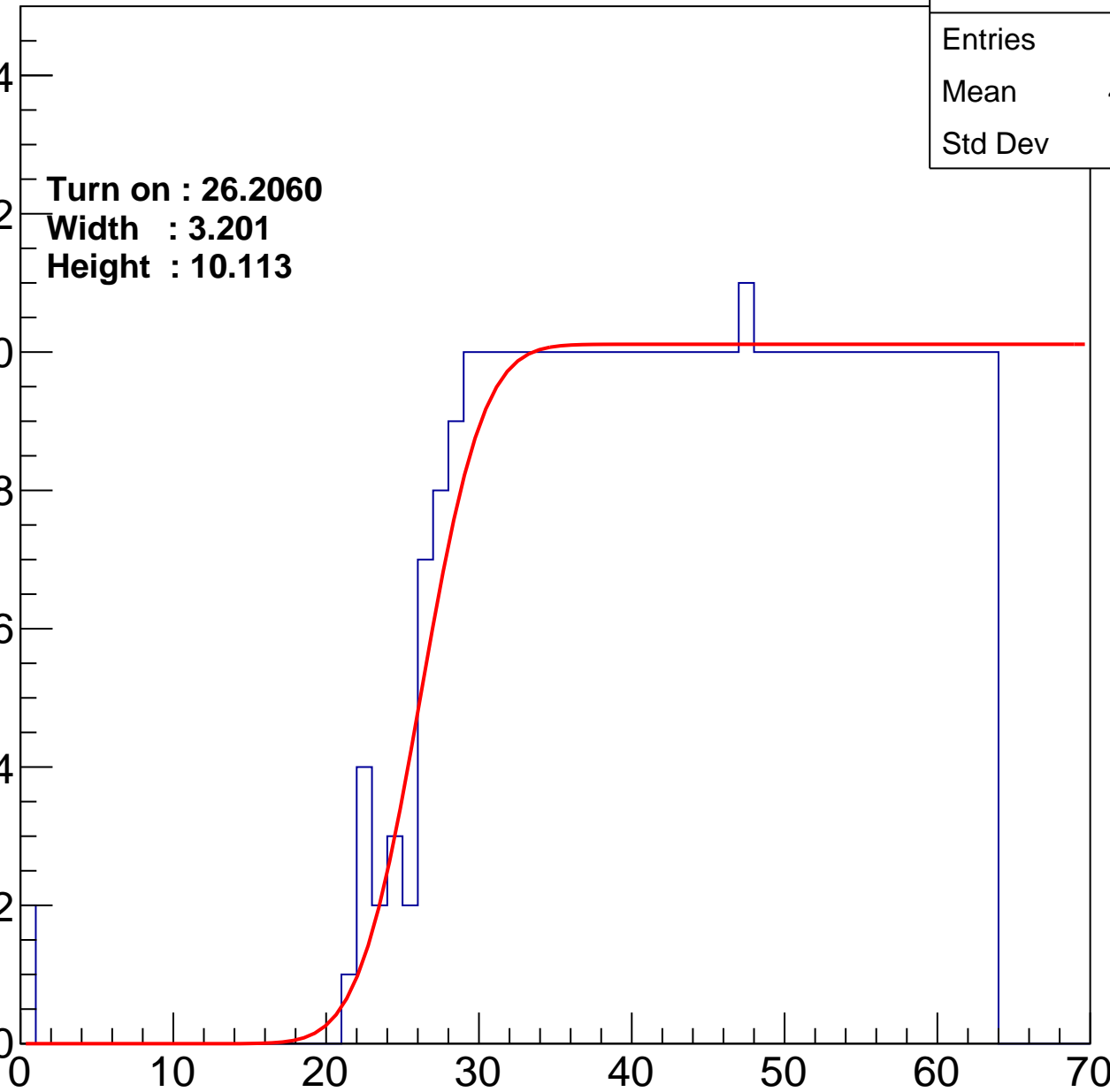
Width : 3.201

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch71

calib_packv5_042523_0143.root, FC#0, port D2

Entries	349
Mean	45.79
Std Dev	10.74

Turn on : 29.6419

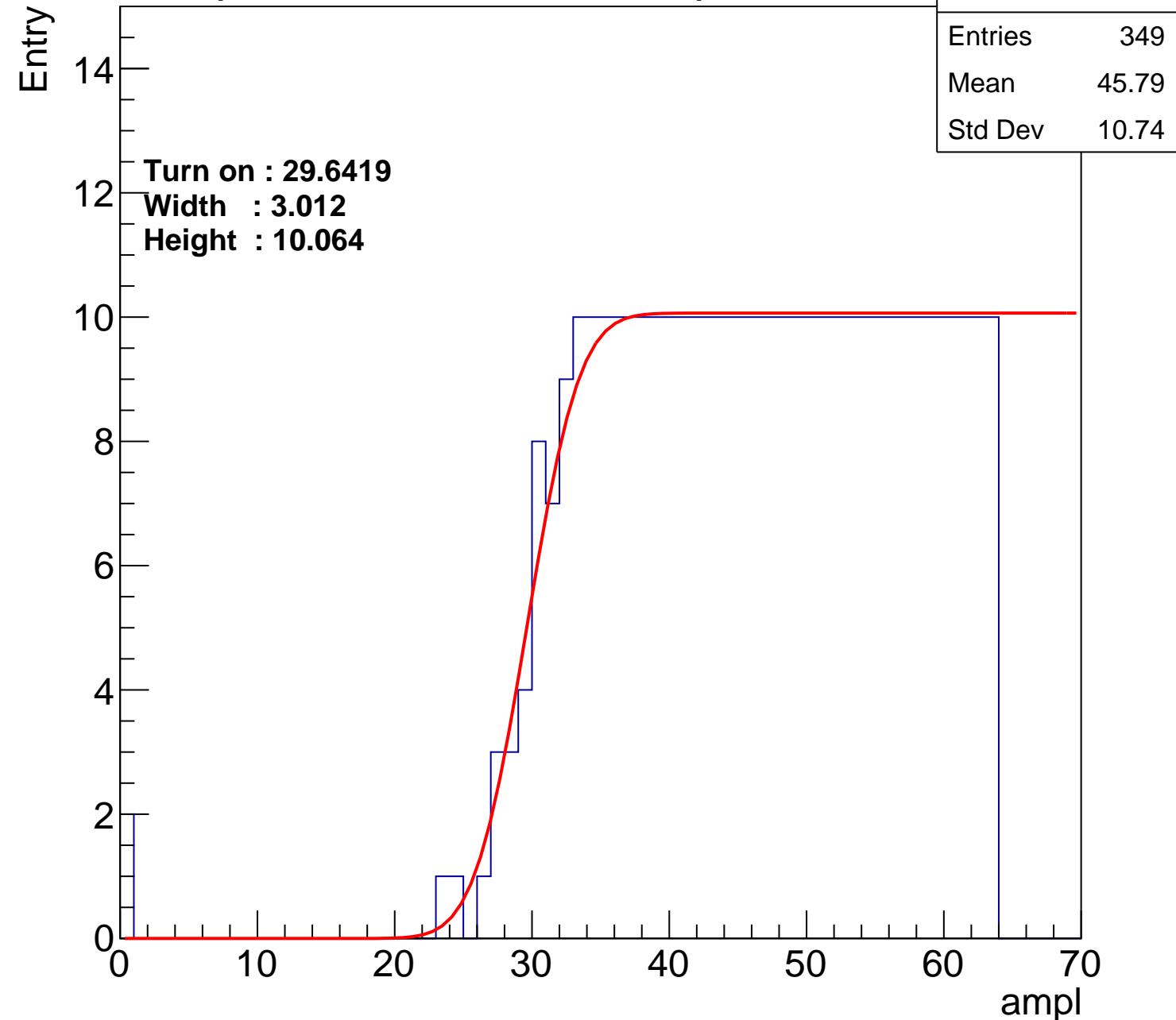
Width : 3.012

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch72

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.36
Std Dev	11.65

Turn on : 27.0747

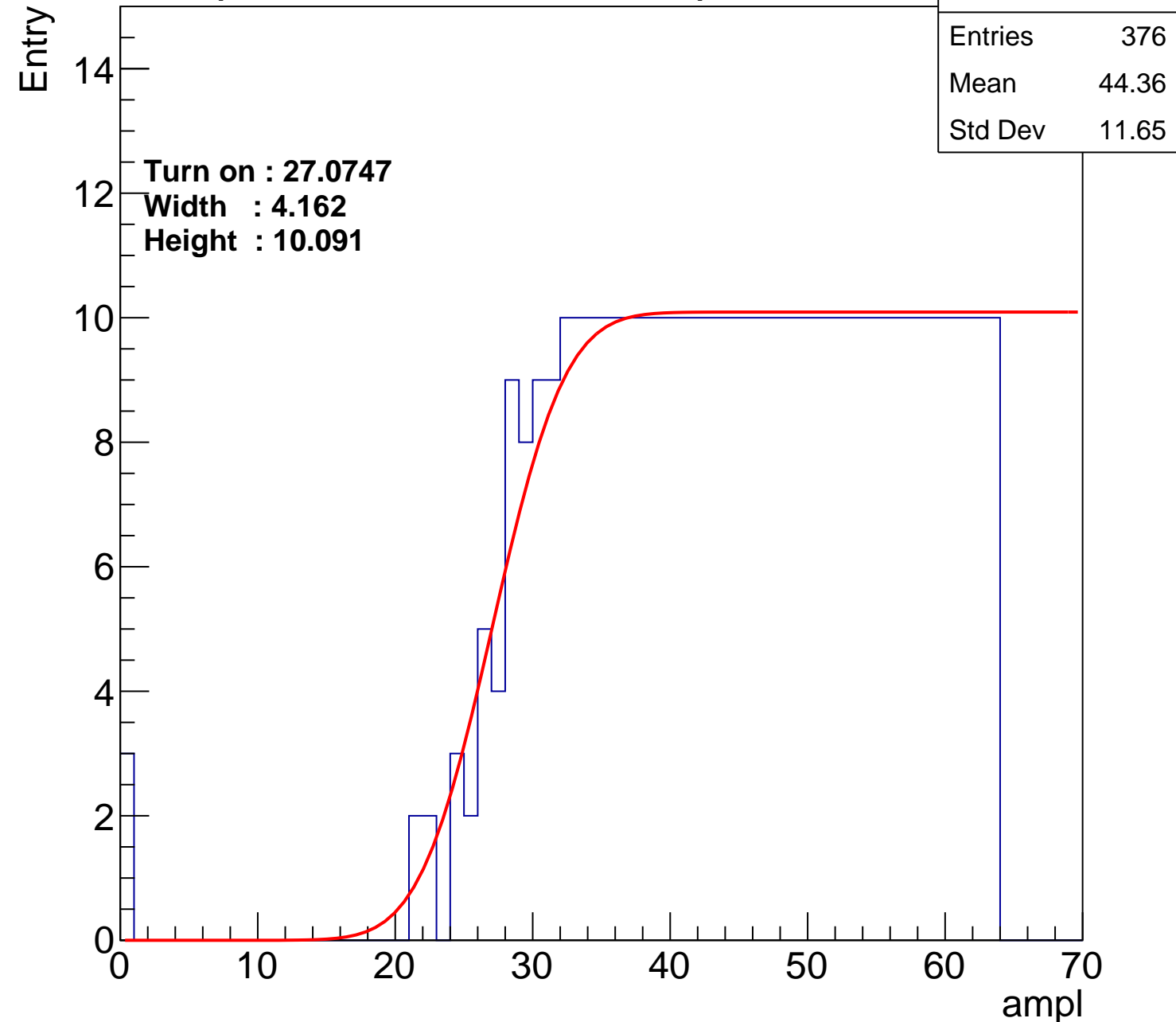
Width : 4.162

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch73

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.63
Std Dev	11.51

Turn on : 27.2825

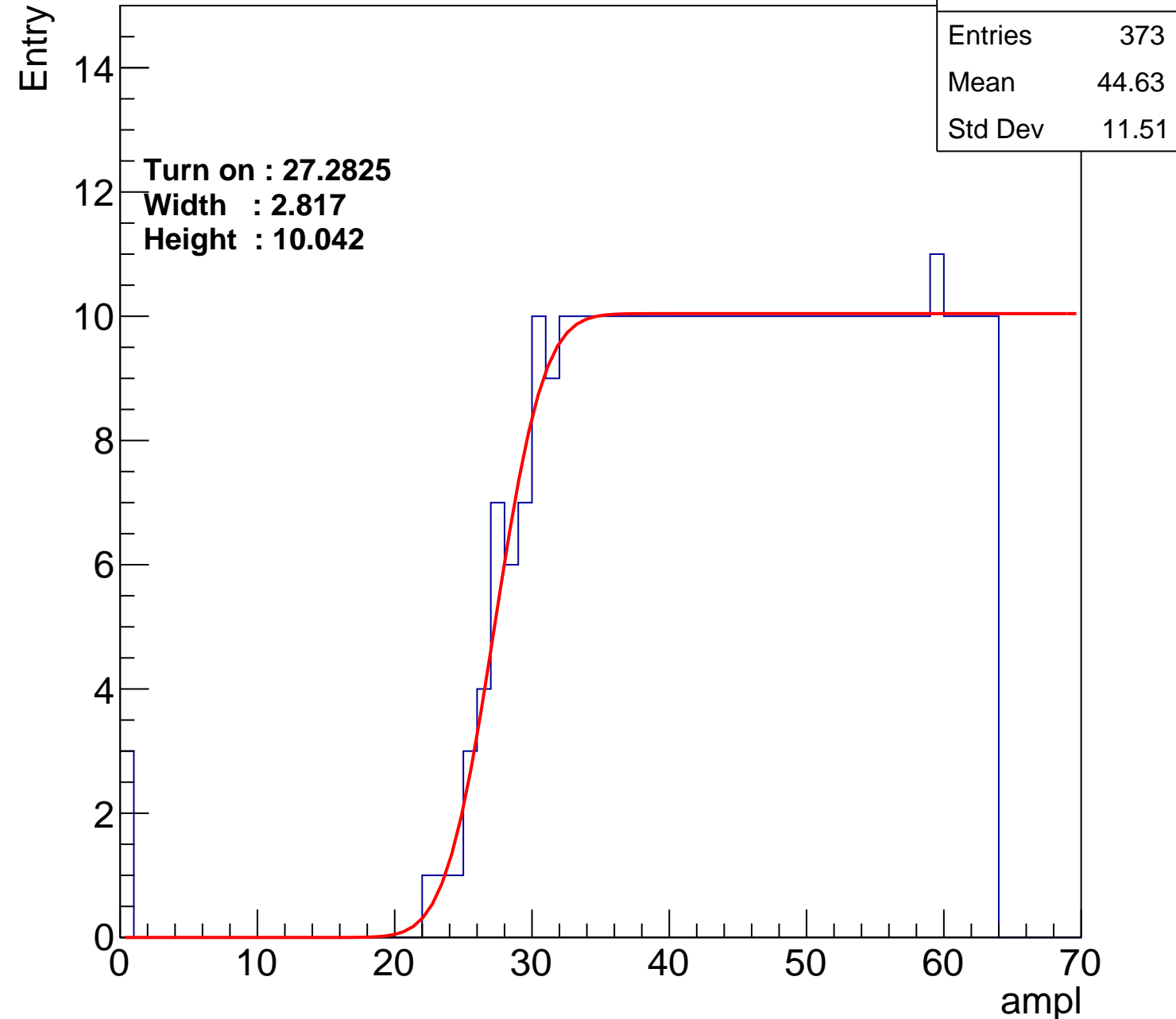
Width : 2.817

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch74

calib_packv5_042523_0143.root, FC#0, port D2

Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 28.3643

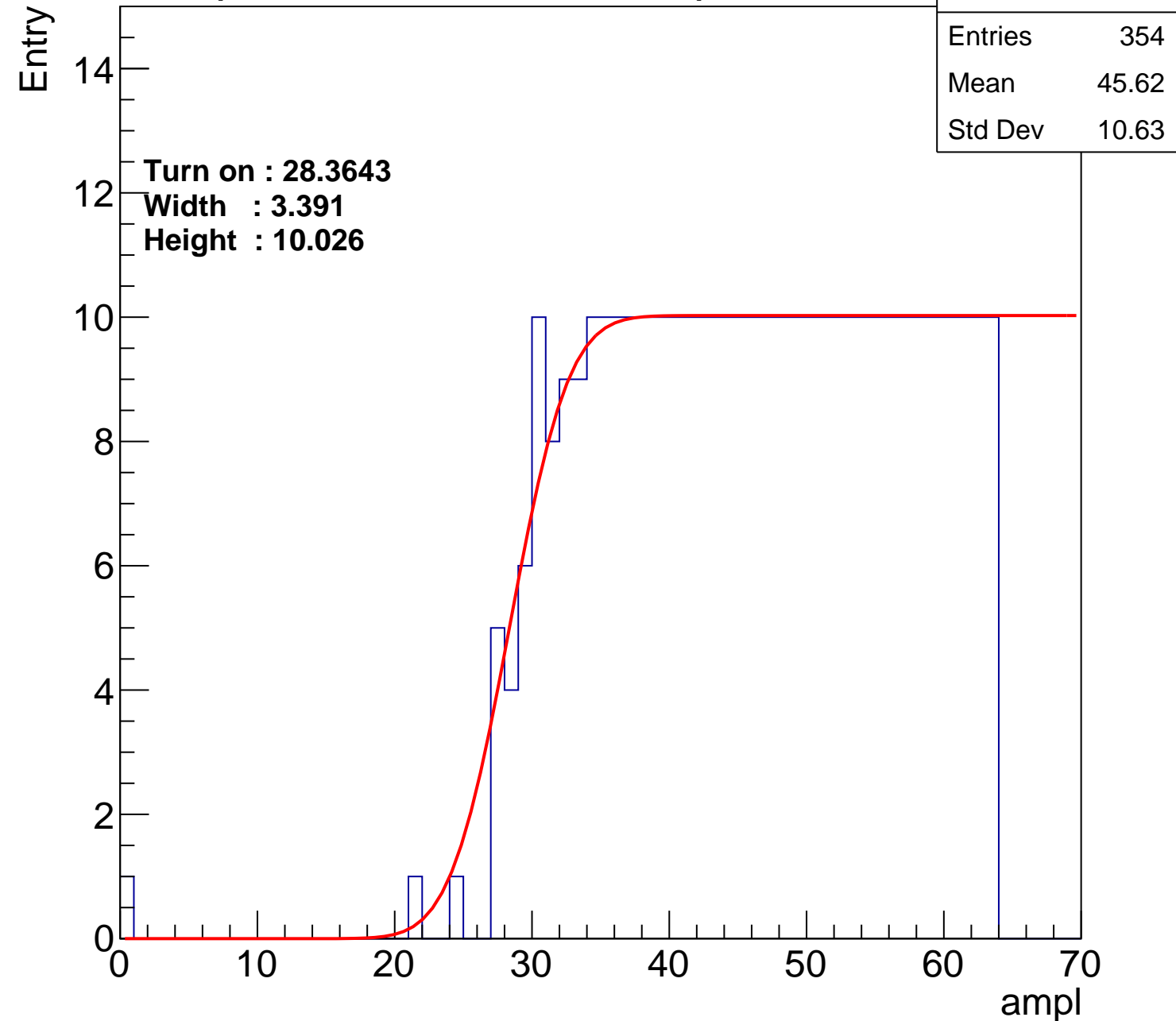
Width : 3.391

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch75

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.89
Std Dev	11.17

Turn on : 27.9390

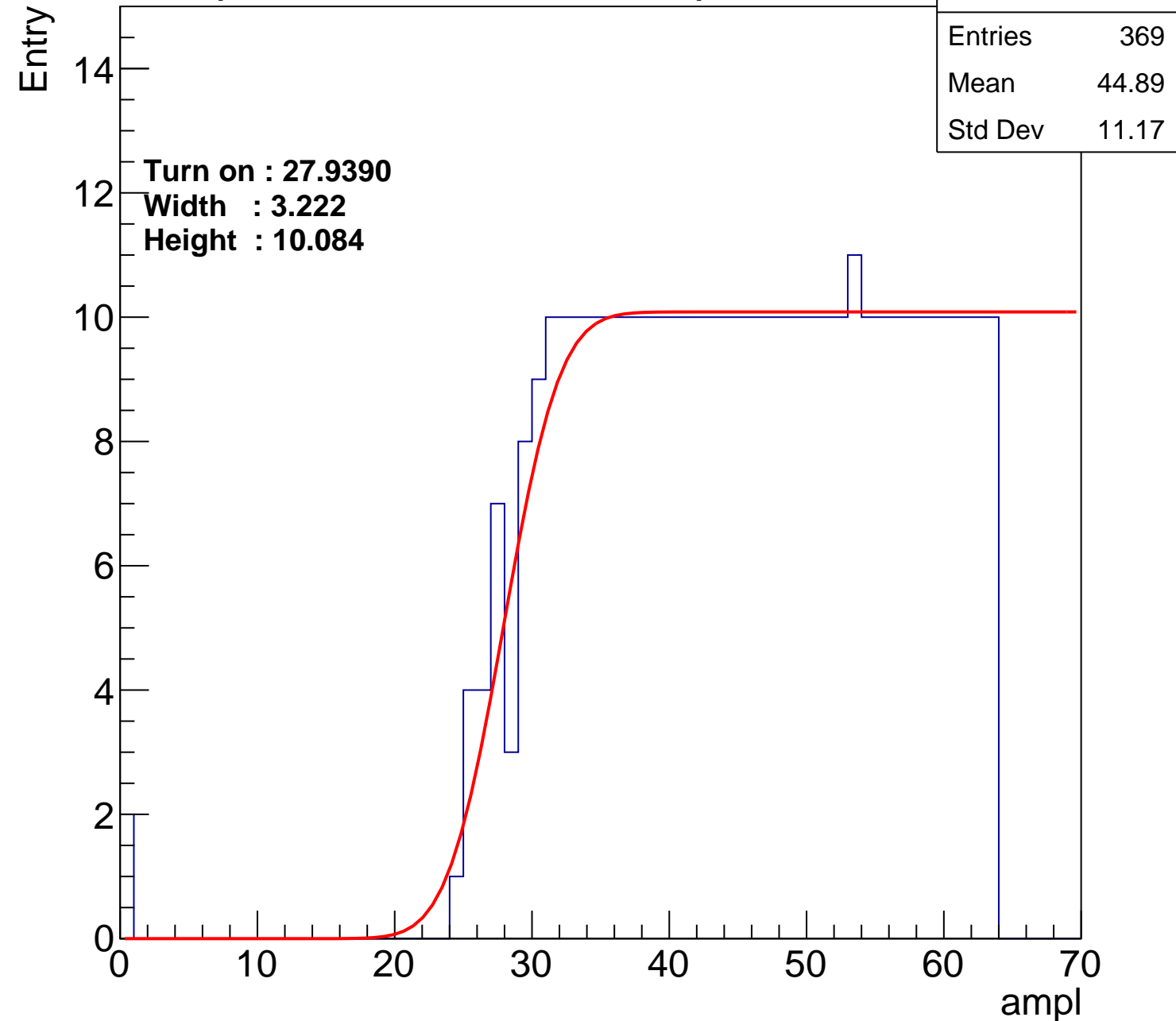
Width : 3.222

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch76

calib_packv5_042523_0143.root, FC#0, port D2

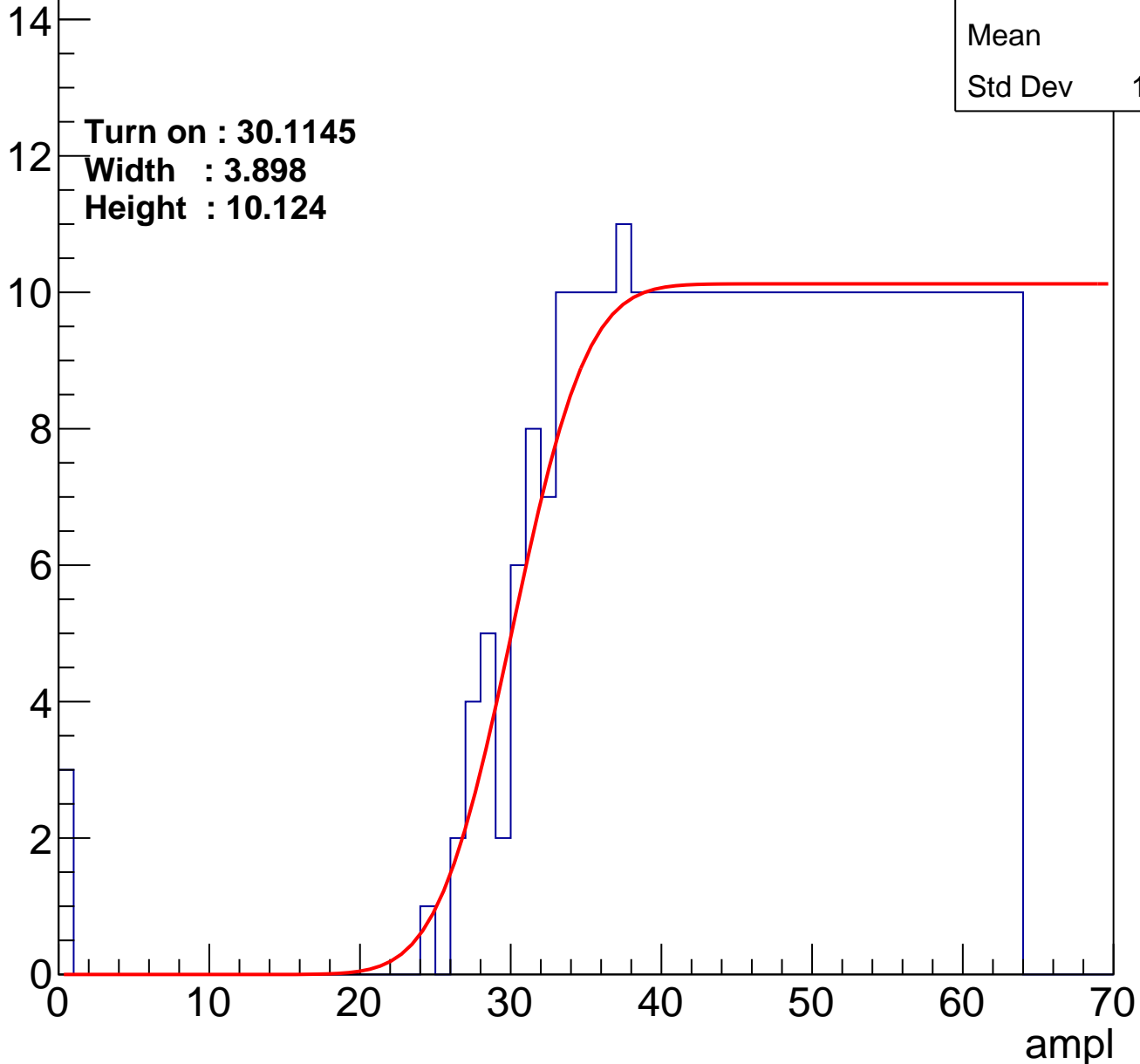
Entries	349
Mean	45.7
Std Dev	10.98

Turn on : 30.1145

Width : 3.898

Height : 10.124

Entry



B1L101S, U22-ch77

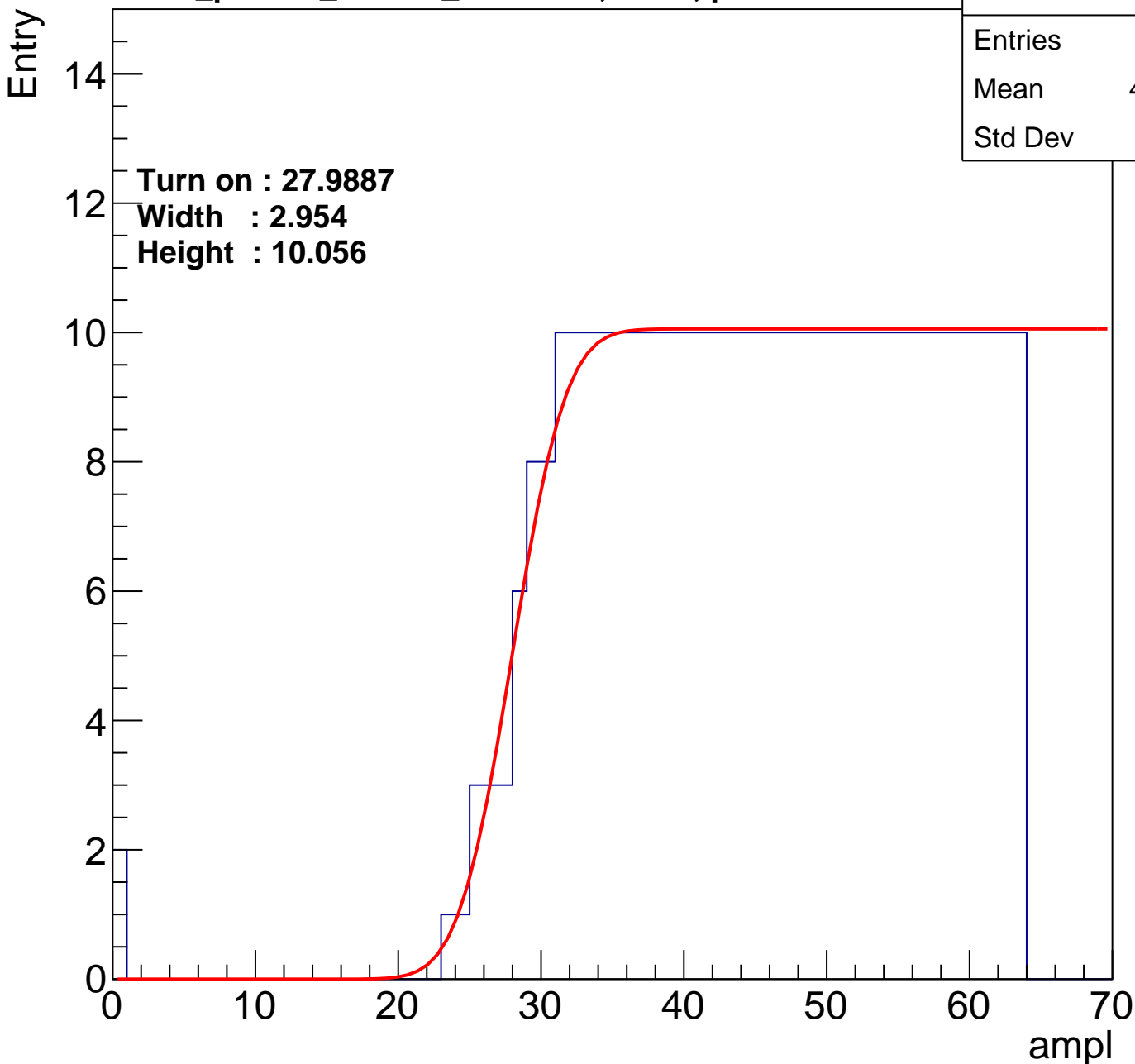
calib_packv5_042523_0143.root, FC#0, port D2

Entries	365
Mean	45.02
Std Dev	11.11

Turn on : 27.9887

Width : 2.954

Height : 10.056



B1L101S, U22-ch78

calib_packv5_042523_0143.root, FC#0, port D2

Entries	381
Mean	44.29
Std Dev	11.34

Turn on : 26.2631

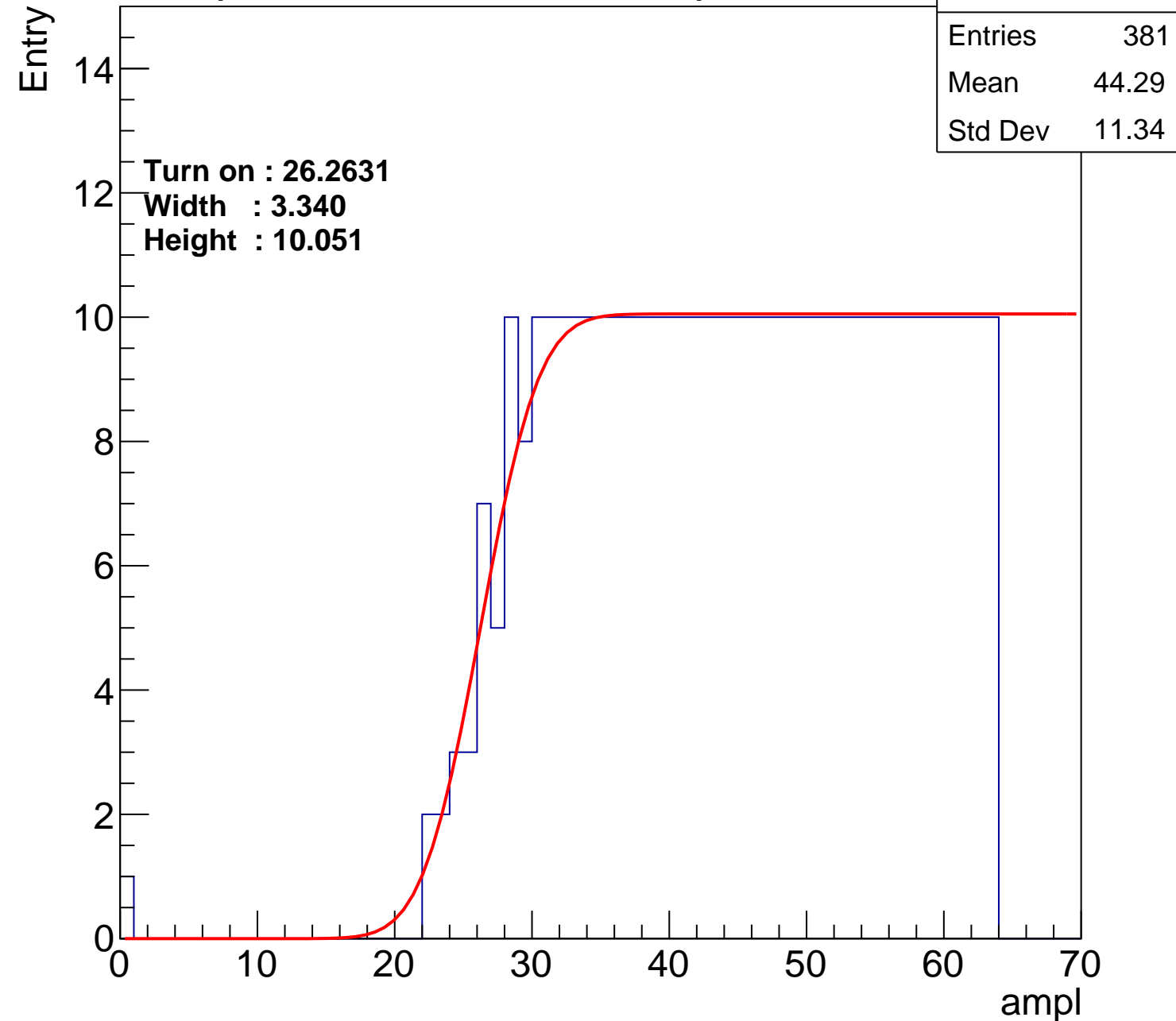
Width : 3.340

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch79

calib_packv5_042523_0143.root, FC#0, port D2

Entries	357
Mean	45.31
Std Dev	11.16

Turn on : 29.0469

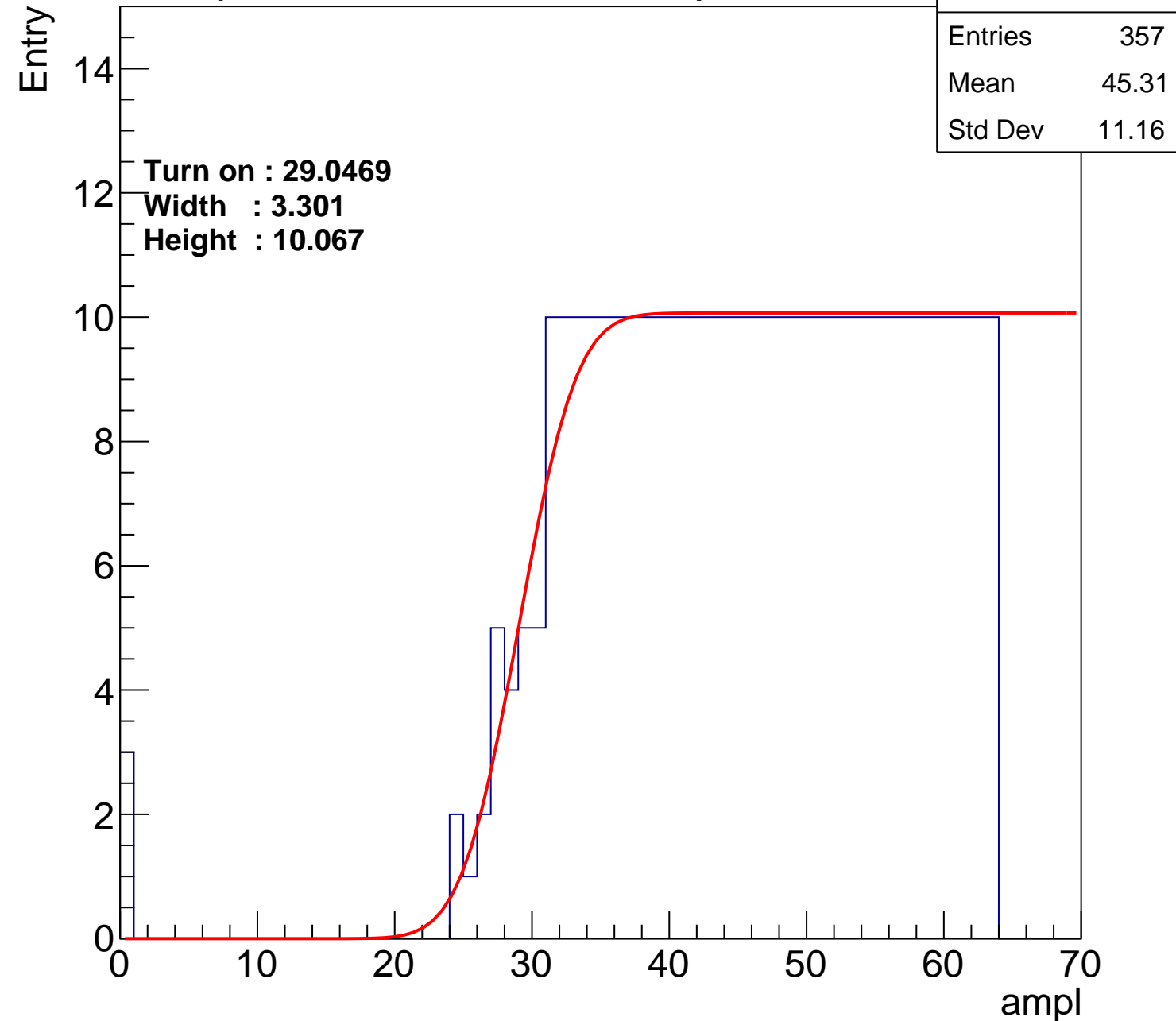
Width : 3.301

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch80

calib_packv5_042523_0143.root, FC#0, port D2

Entries	372
Mean	44.71
Std Dev	11.19

Turn on : 28.0611

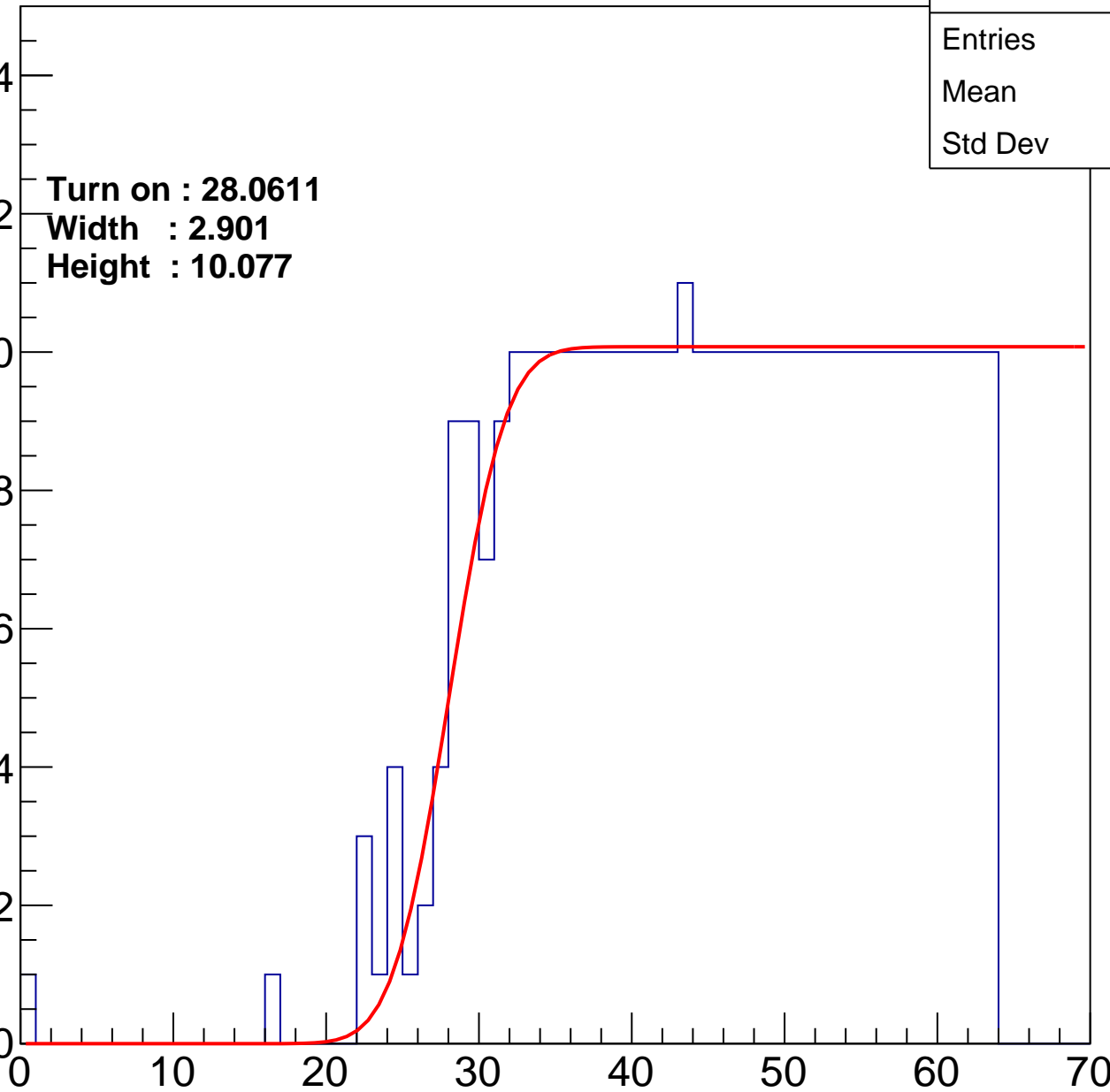
Width : 2.901

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch81

calib_packv5_042523_0143.root, FC#0, port D2

Entries	362
Mean	45.11
Std Dev	11.23

Turn on : 28.6933

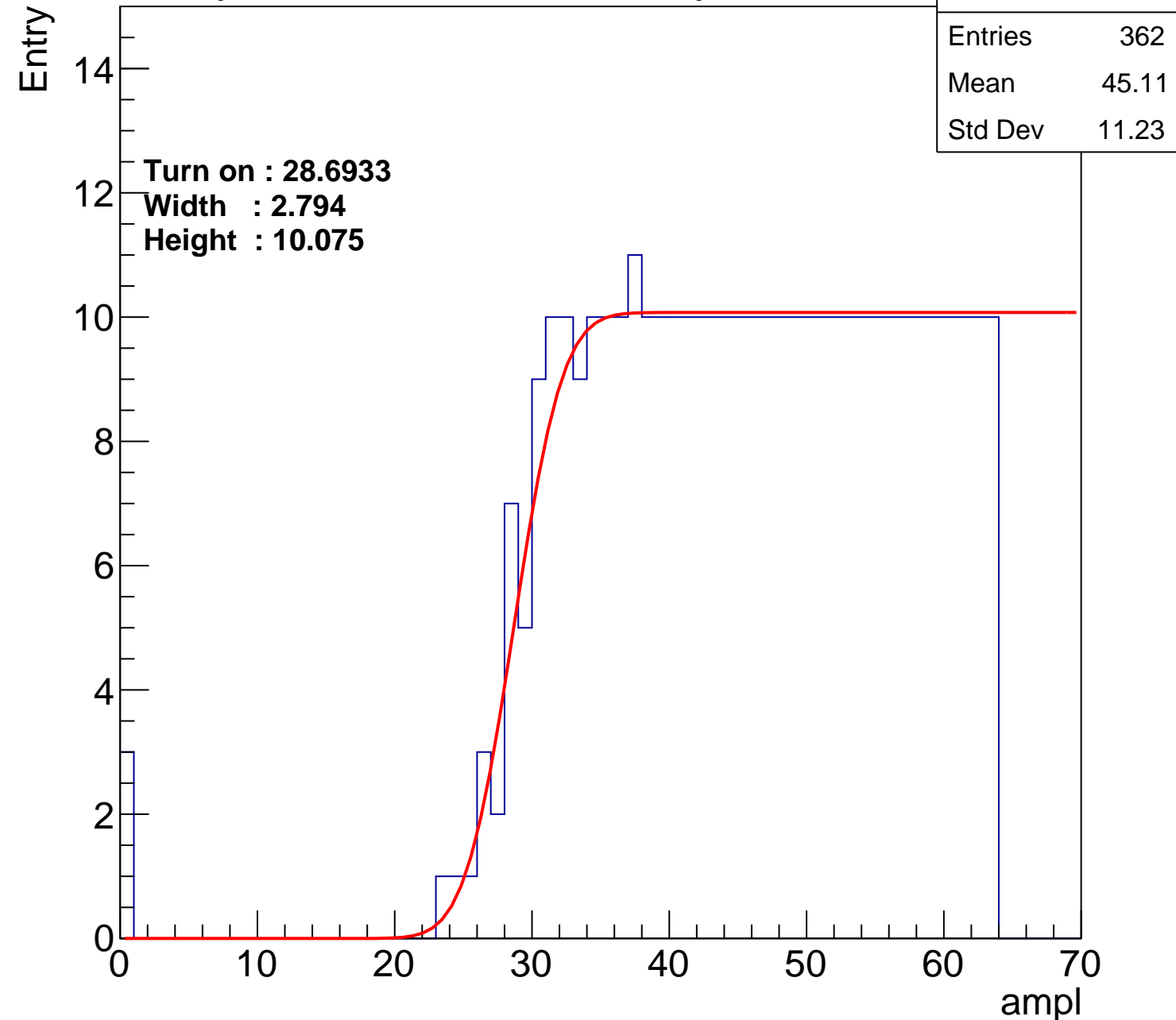
Width : 2.794

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch82

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.21
Std Dev	12.2

Turn on : 27.1594

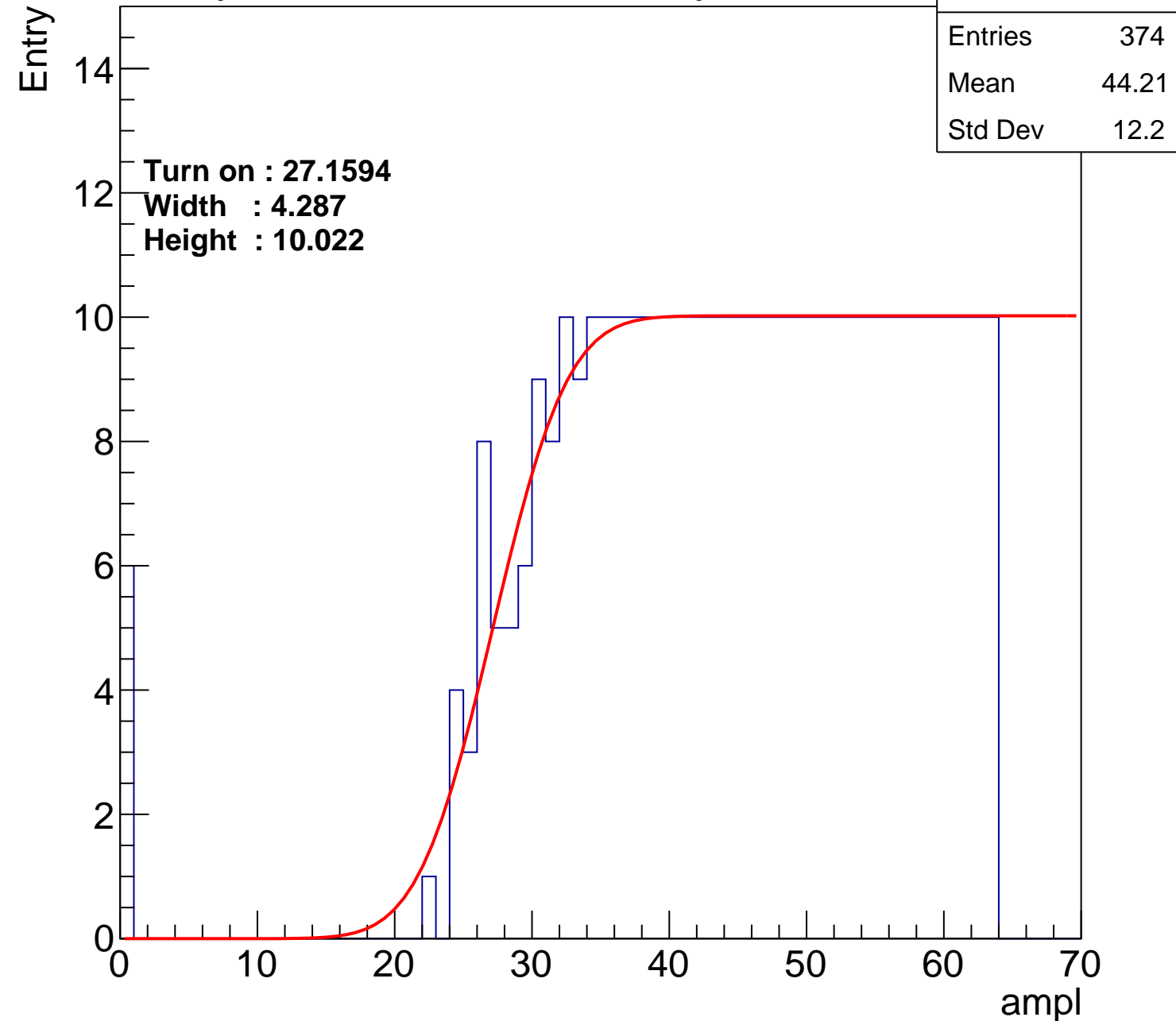
Width : 4.287

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch83

calib_packv5_042523_0143.root, FC#0, port D2

Entries	361
Mean	45.16
Std Dev	11.09

Turn on : 27.3090

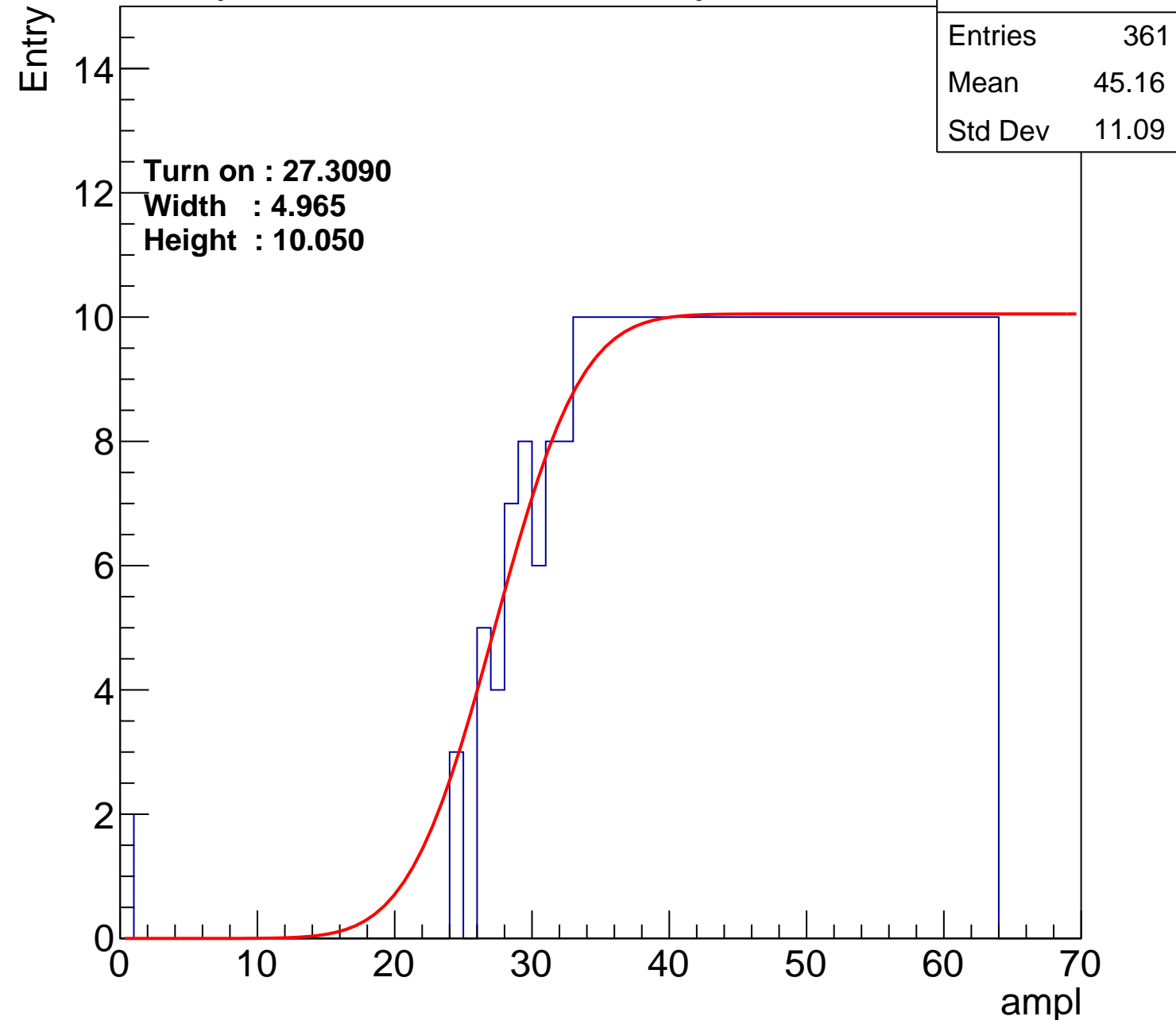
Width : 4.965

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch84

calib_packv5_042523_0143.root, FC#0, port D2

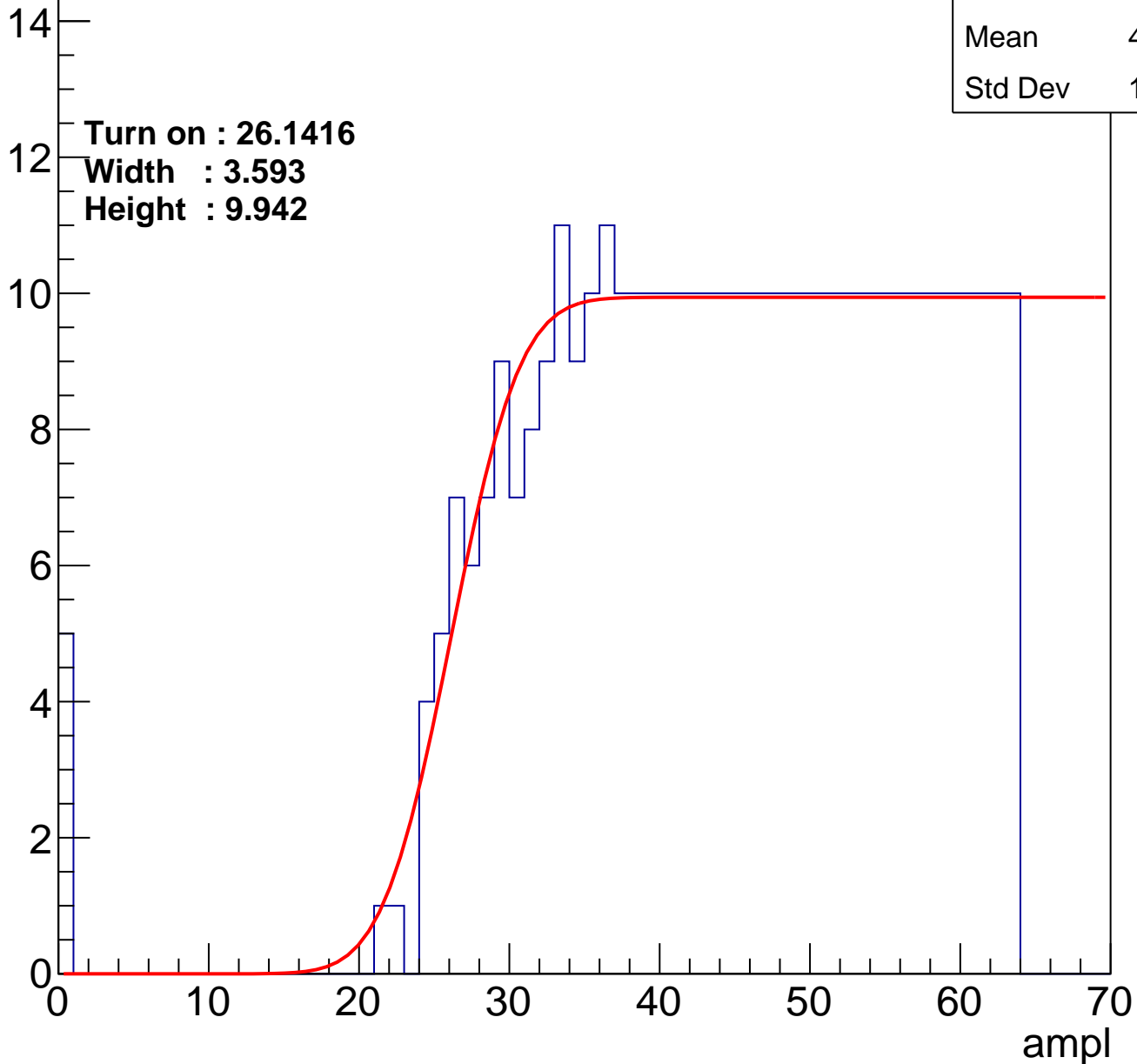
Entries	380
Mean	44.02
Std Dev	12.12

Turn on : 26.1416

Width : 3.593

Height : 9.942

Entry



B1L101S, U22-ch85

calib_packv5_042523_0143.root, FC#0, port D2

Entries	379
Mean	44.28
Std Dev	11.66

Turn on : 26.8278

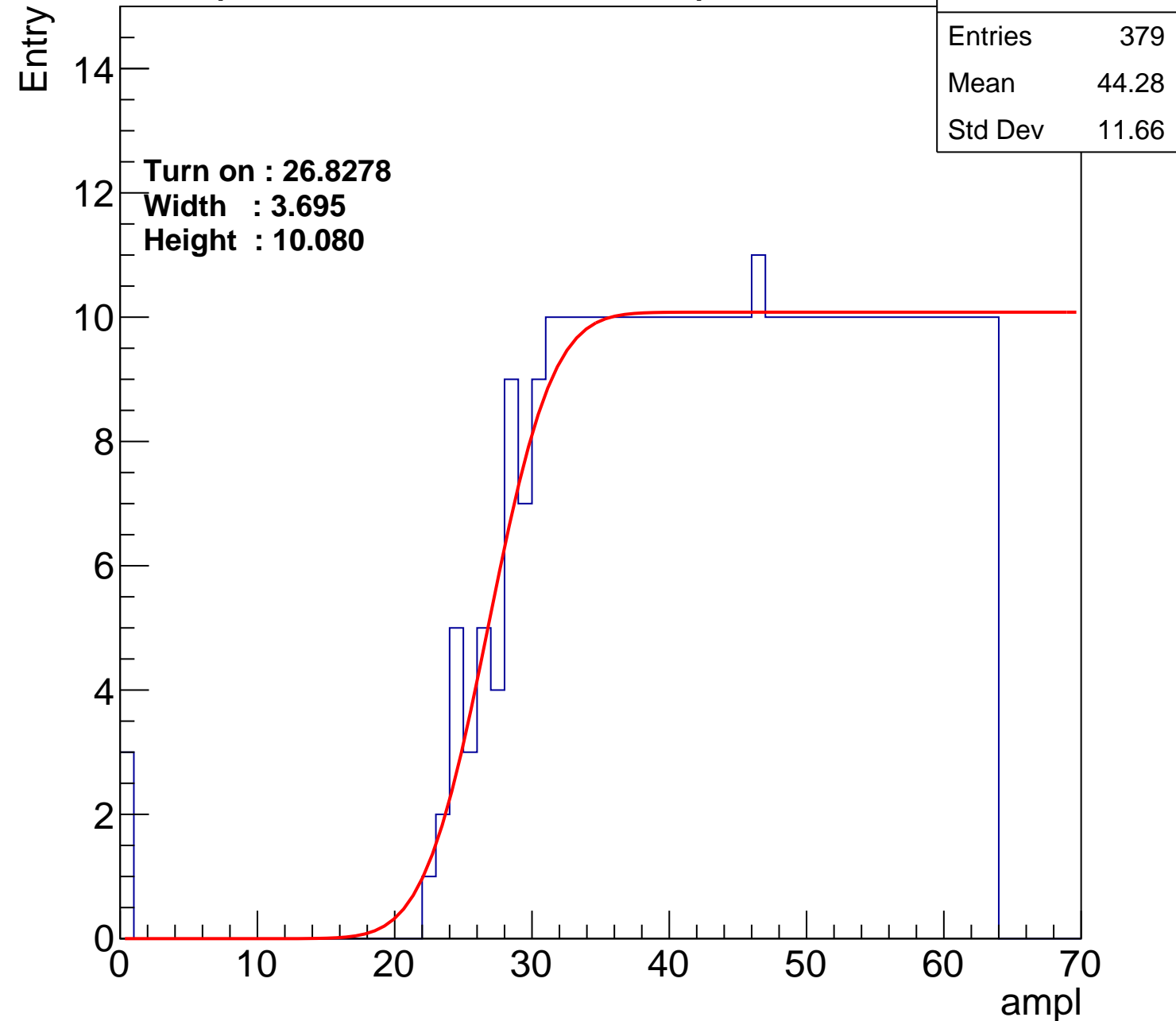
Width : 3.695

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch86

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.59
Std Dev	11.74

Turn on : 27.9340

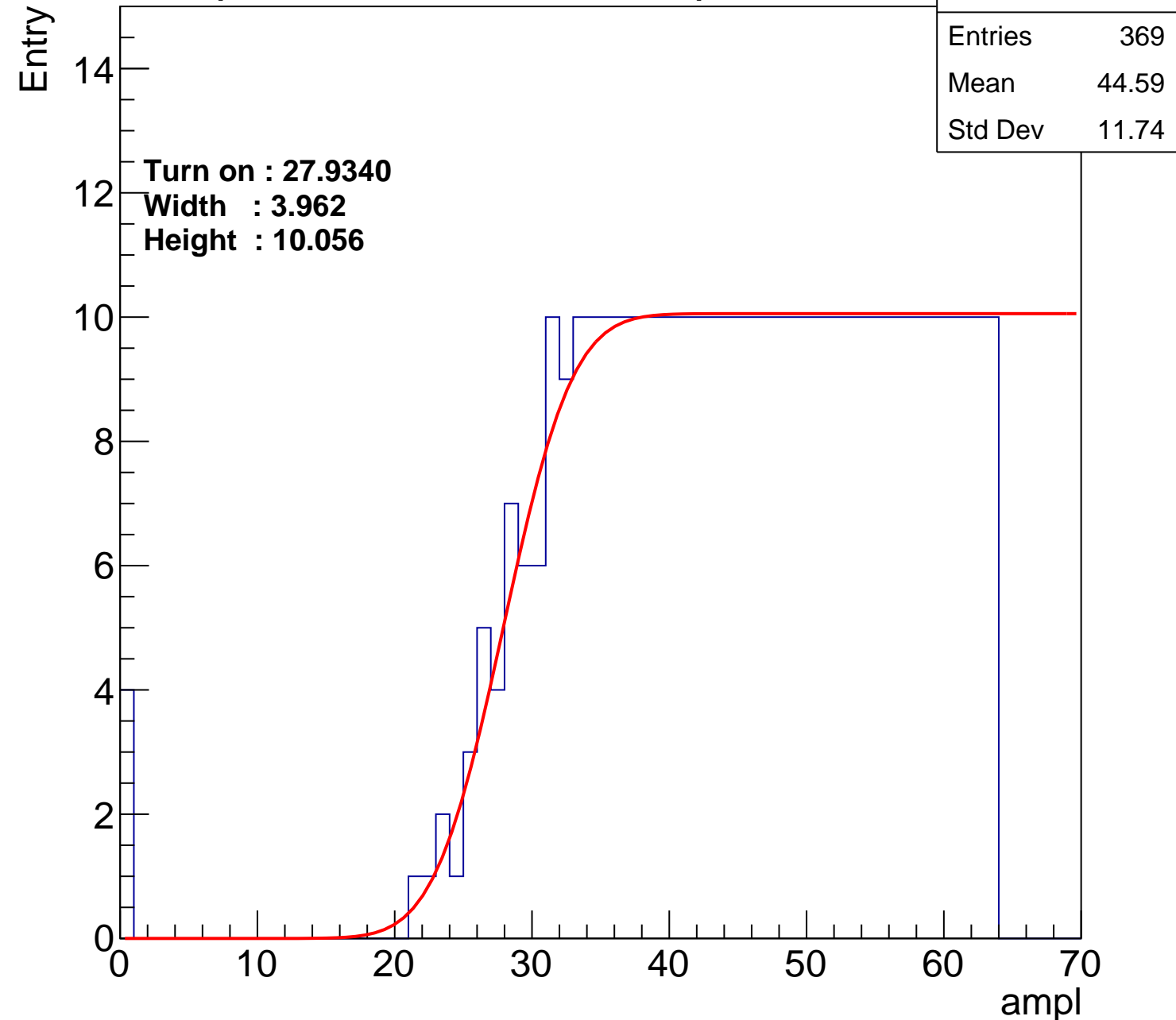
Width : 3.962

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch87

calib_packv5_042523_0143.root, FC#0, port D2

Entries	380
Mean	44.22
Std Dev	11.58

Turn on : 26.0576

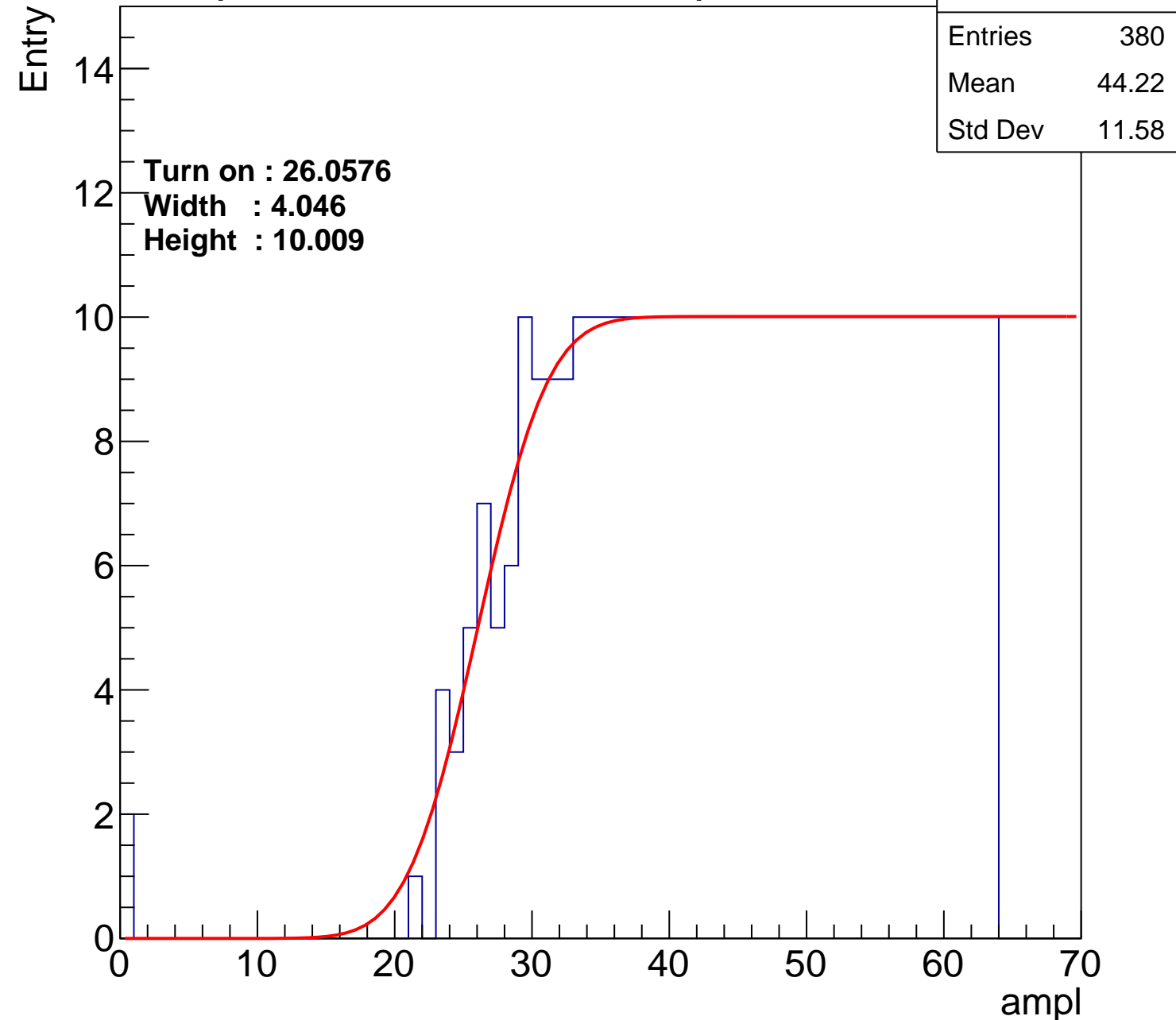
Width : 4.046

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch88

calib_packv5_042523_0143.root, FC#0, port D2

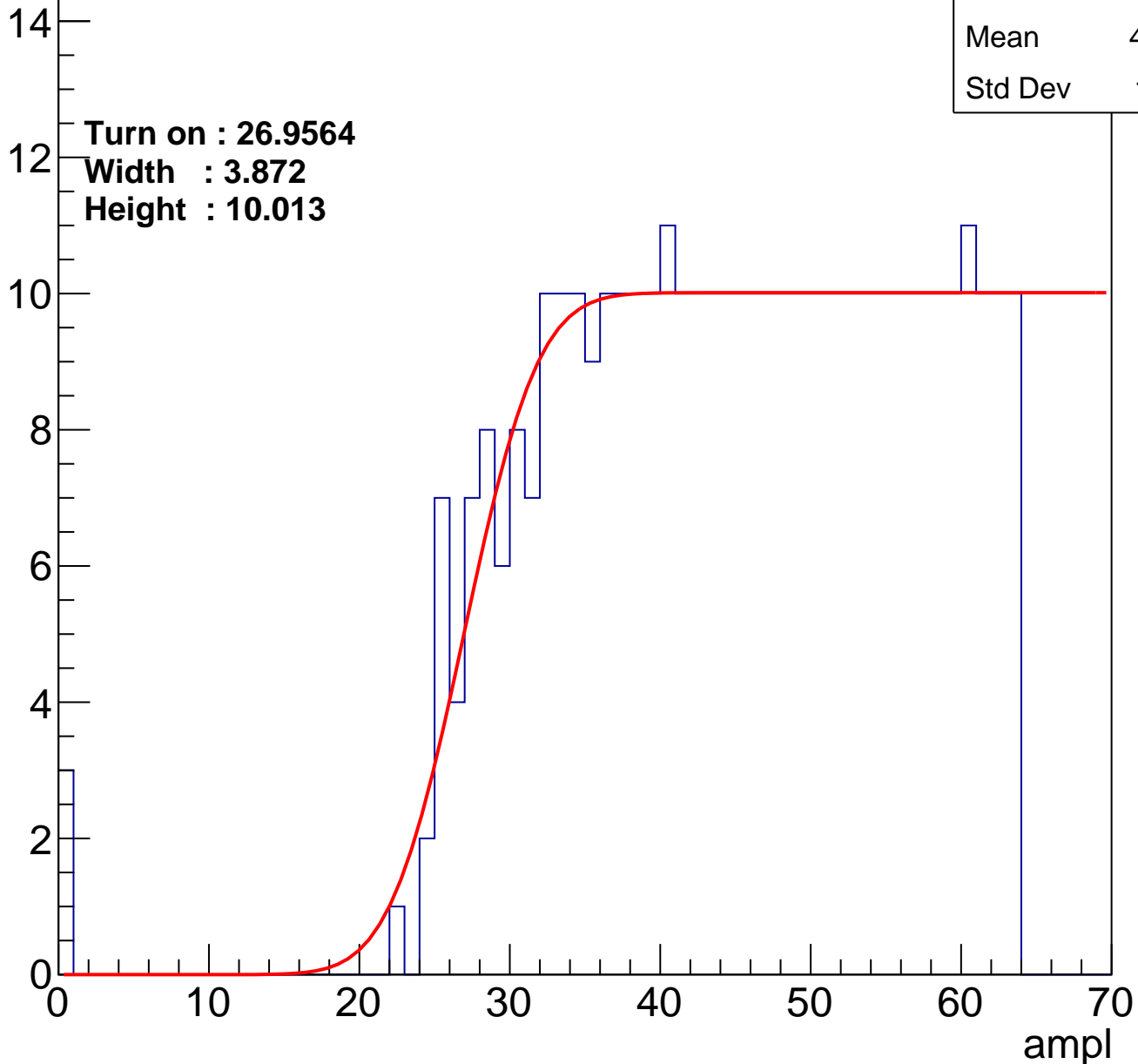
Entries	374
Mean	44.54
Std Dev	11.61

Turn on : 26.9564

Width : 3.872

Height : 10.013

Entry



B1L101S, U22-ch89

calib_packv5_042523_0143.root, FC#0, port D2

Entries	385
Mean	44
Std Dev	11.67

Turn on : 25.8890

Width : 3.791

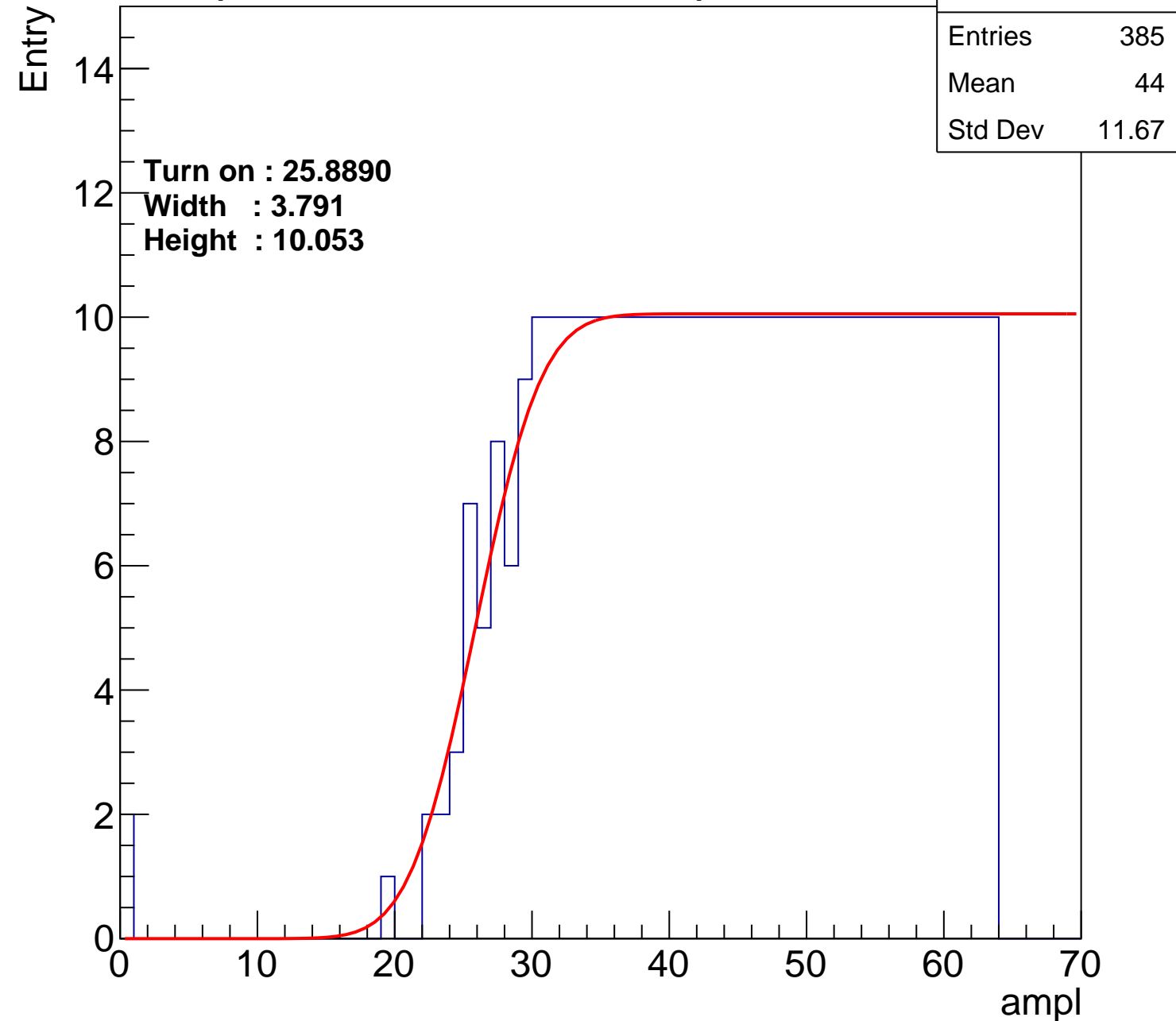
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U22-ch90

calib_packv5_042523_0143.root, FC#0, port D2

Entries	370
Mean	44.58
Std Dev	11.69

Turn on : 27.6011

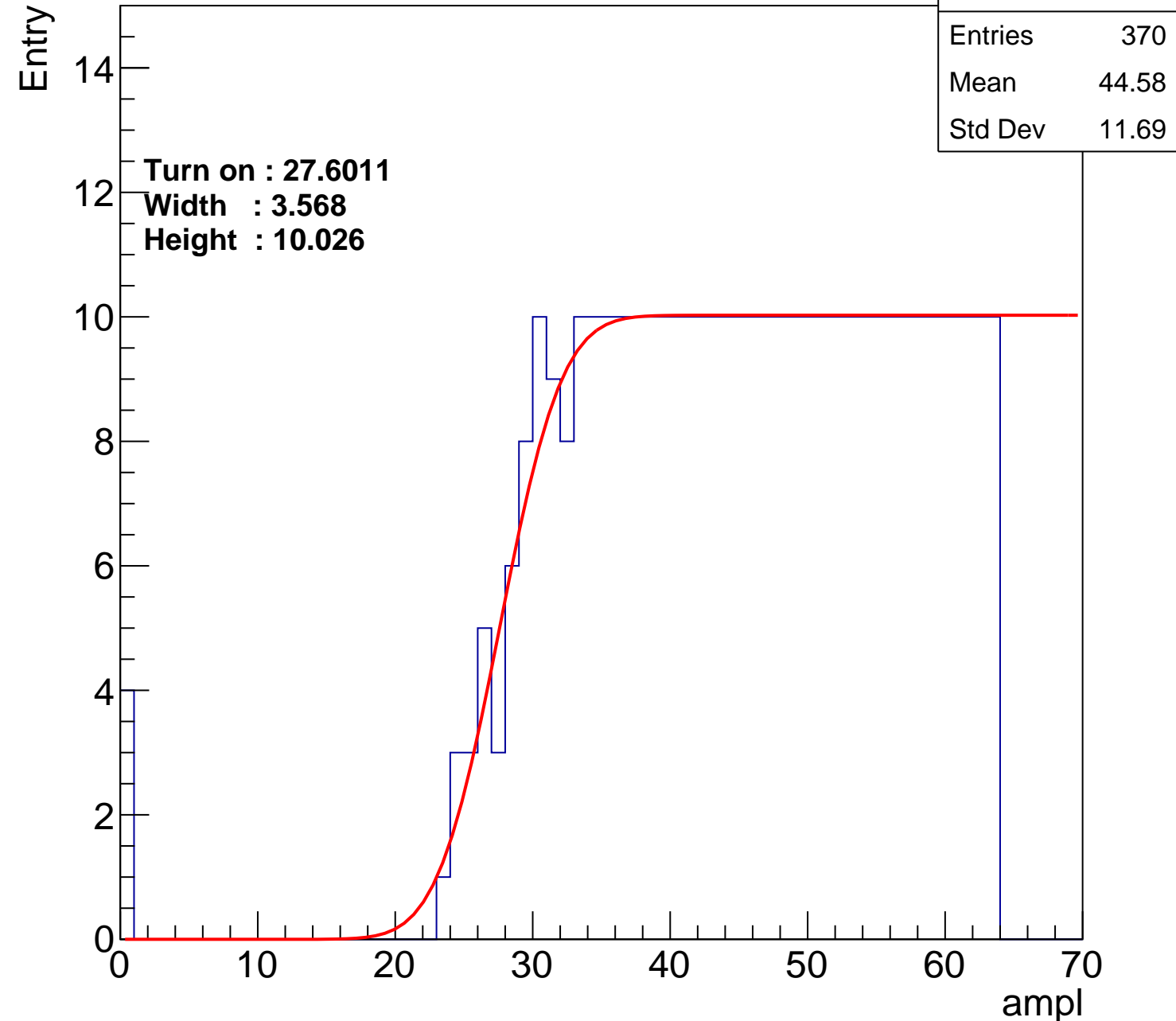
Width : 3.568

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch91

calib_packv5_042523_0143.root, FC#0, port D2

Entries	366
Mean	44.92
Std Dev	11.22

Turn on : 27.9904

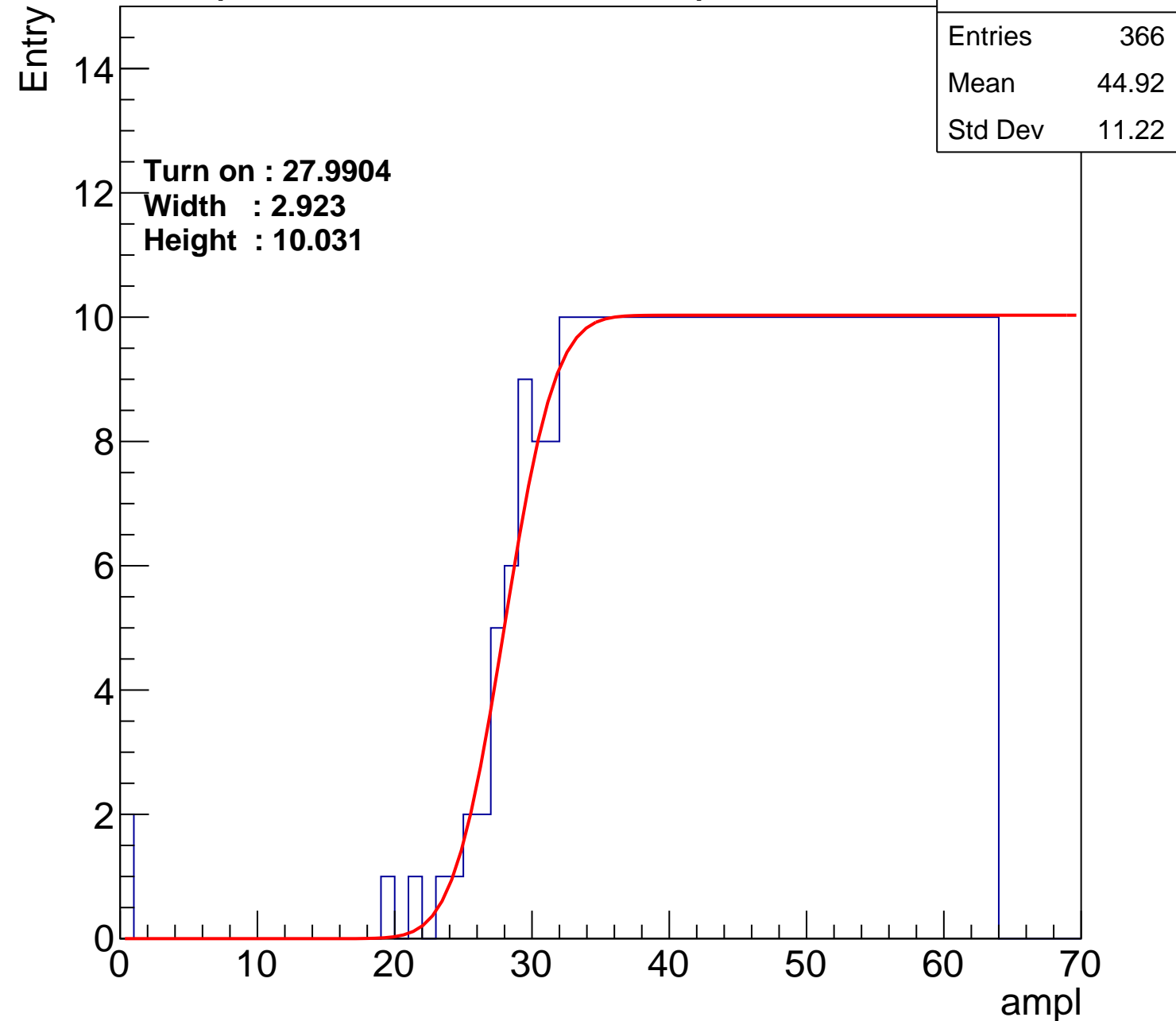
Width : 2.923

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch92

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.56
Std Dev	11.46

Turn on : 27.2543

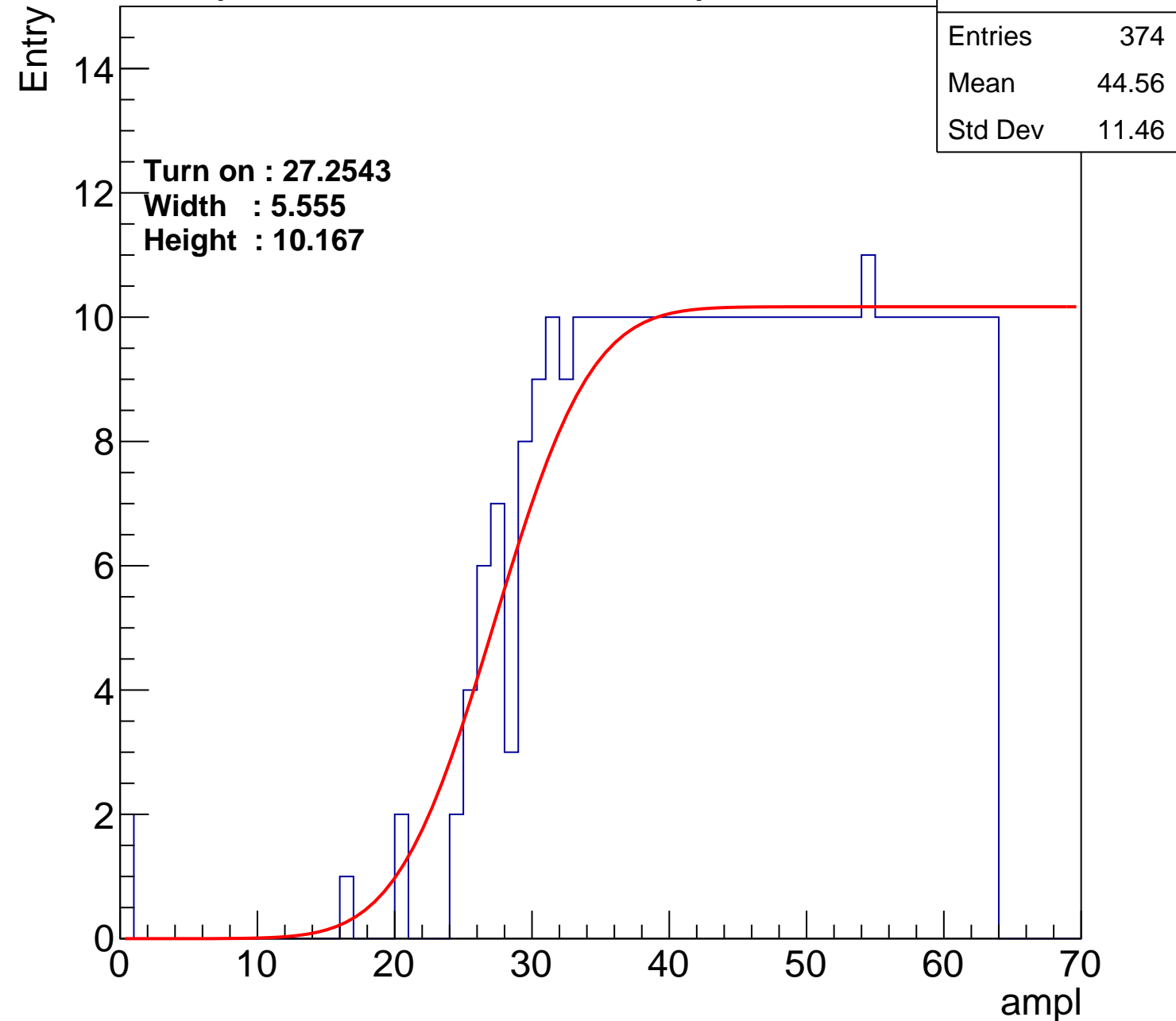
Width : 5.555

Height : 10.167

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch93

calib_packv5_042523_0143.root, FC#0, port D2

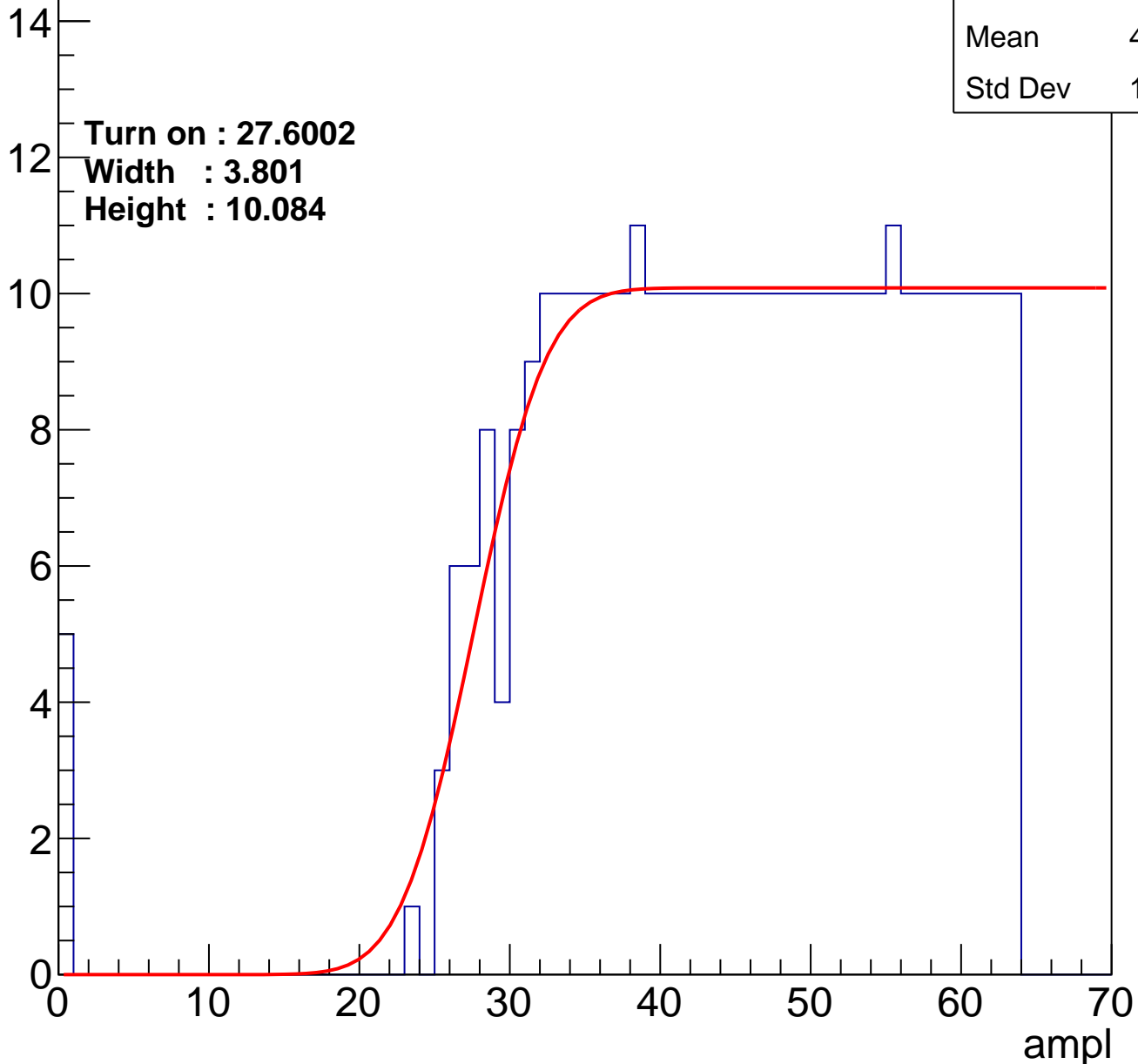
Entries	372
Mean	44.54
Std Dev	11.84

Turn on : 27.6002

Width : 3.801

Height : 10.084

Entry



B1L101S, U22-ch94

calib_packv5_042523_0143.root, FC#0, port D2

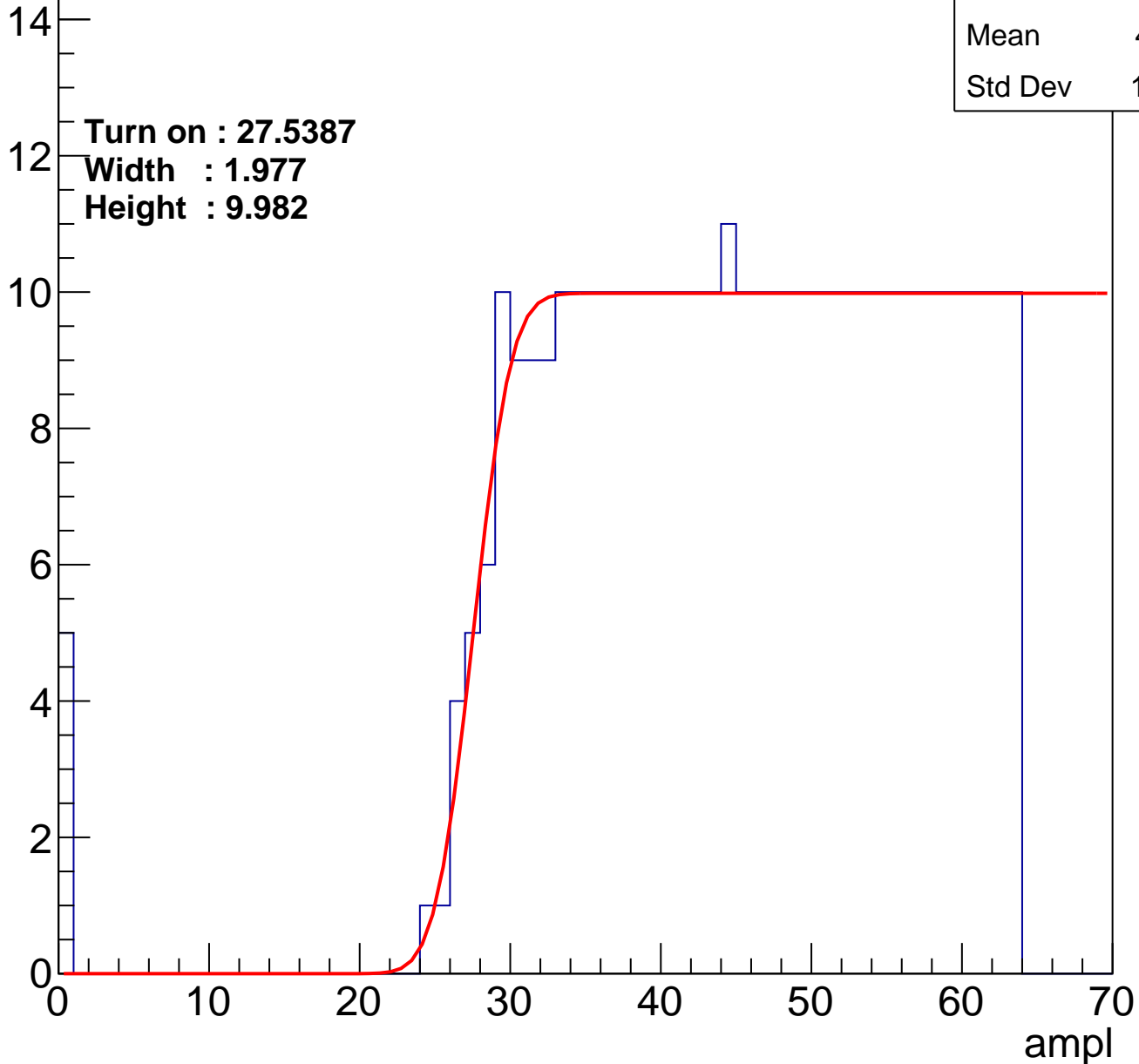
Entries	370
Mean	44.61
Std Dev	11.76

Turn on : 27.5387

Width : 1.977

Height : 9.982

Entry



B1L101S, U22-ch95

calib_packv5_042523_0143.root, FC#0, port D2

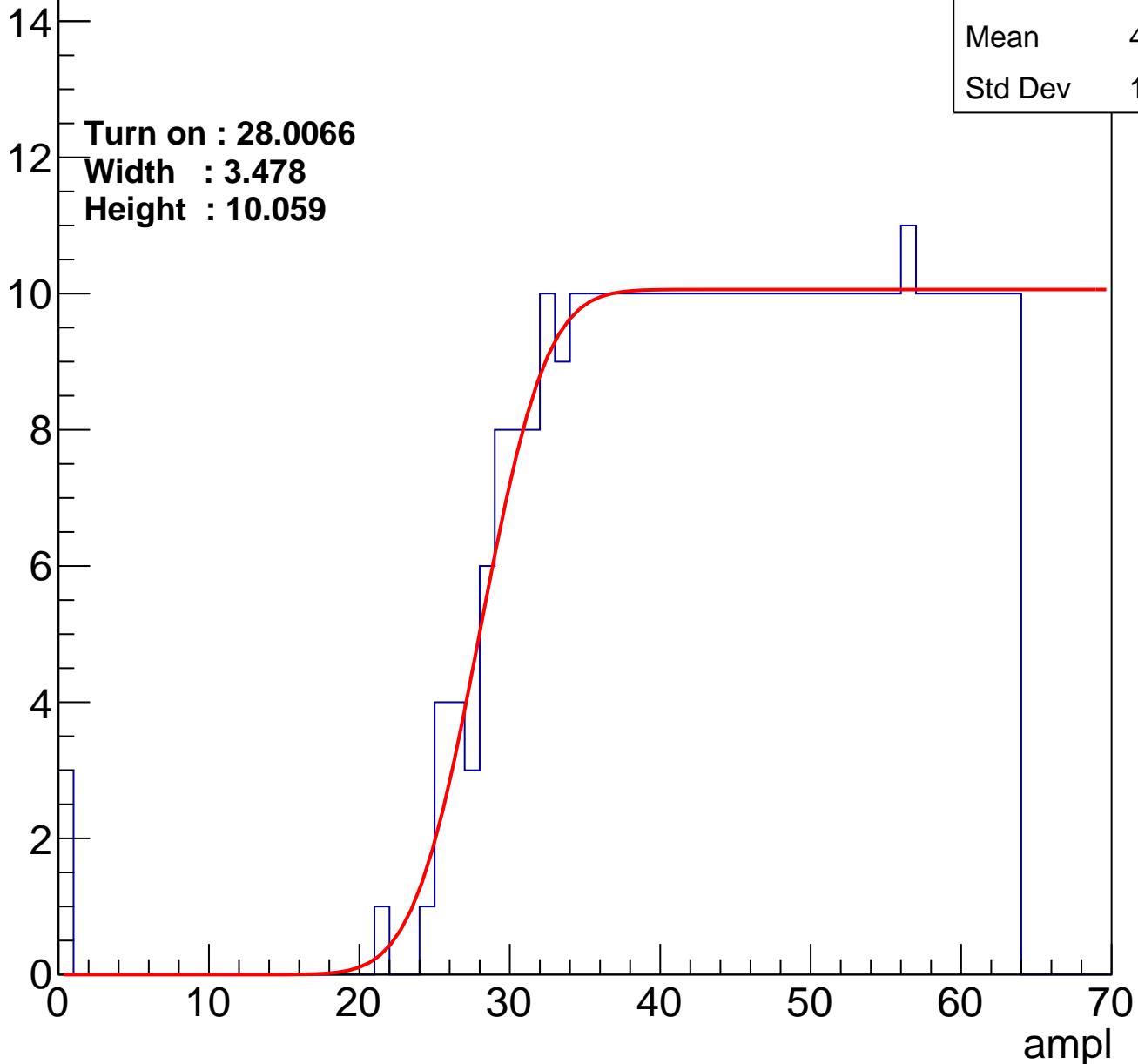
Entries	366
Mean	44.92
Std Dev	11.39

Turn on : 28.0066

Width : 3.478

Height : 10.059

Entry



B1L101S, U22-ch96

calib_packv5_042523_0143.root, FC#0, port D2

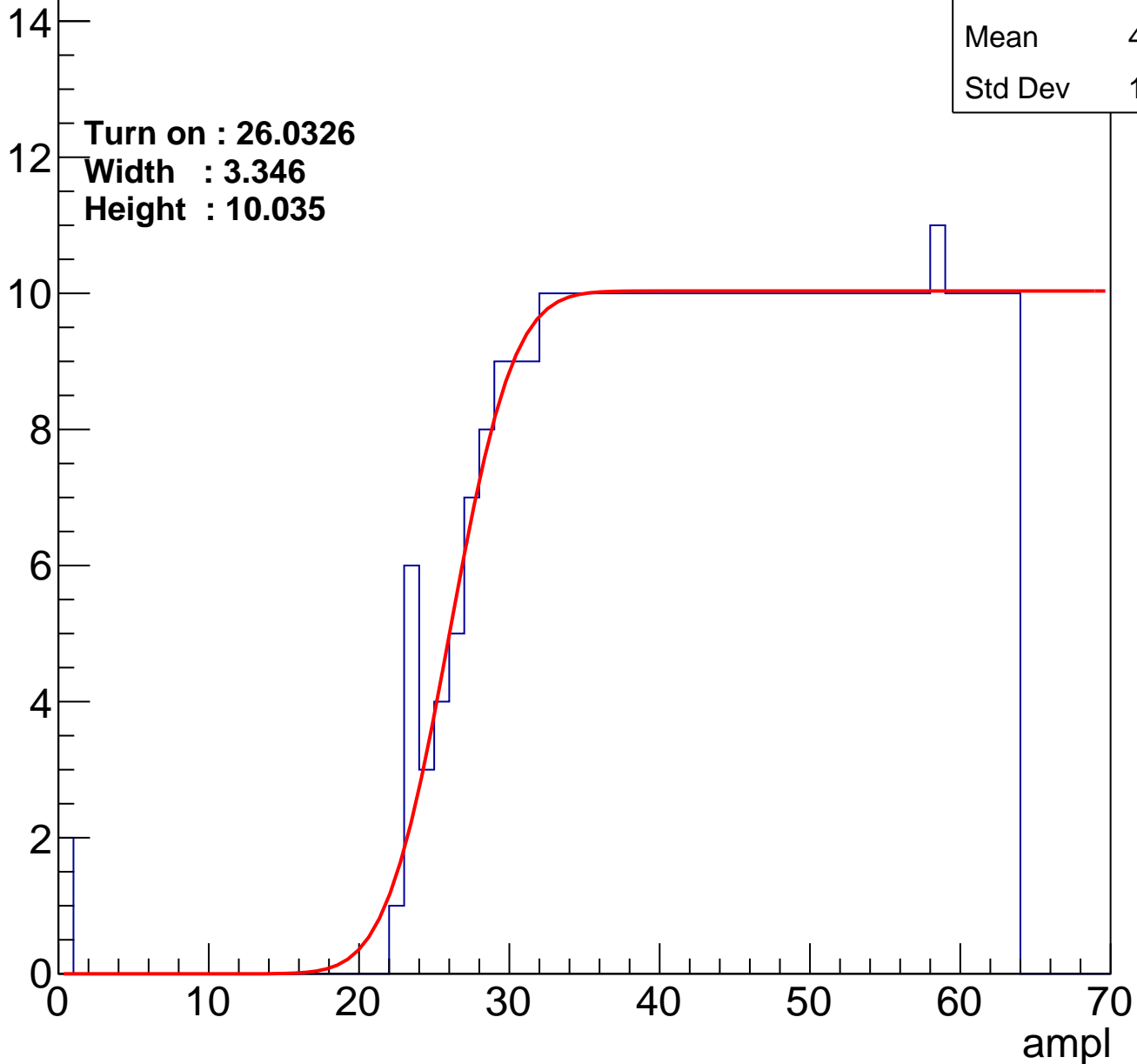
Entries	384
Mean	44.12
Std Dev	11.64

Turn on : 26.0326

Width : 3.346

Height : 10.035

Entry



B1L101S, U22-ch97

calib_packv5_042523_0143.root, FC#0, port D2

Entries	359
Mean	45.23
Std Dev	11.09

Turn on : 28.6669

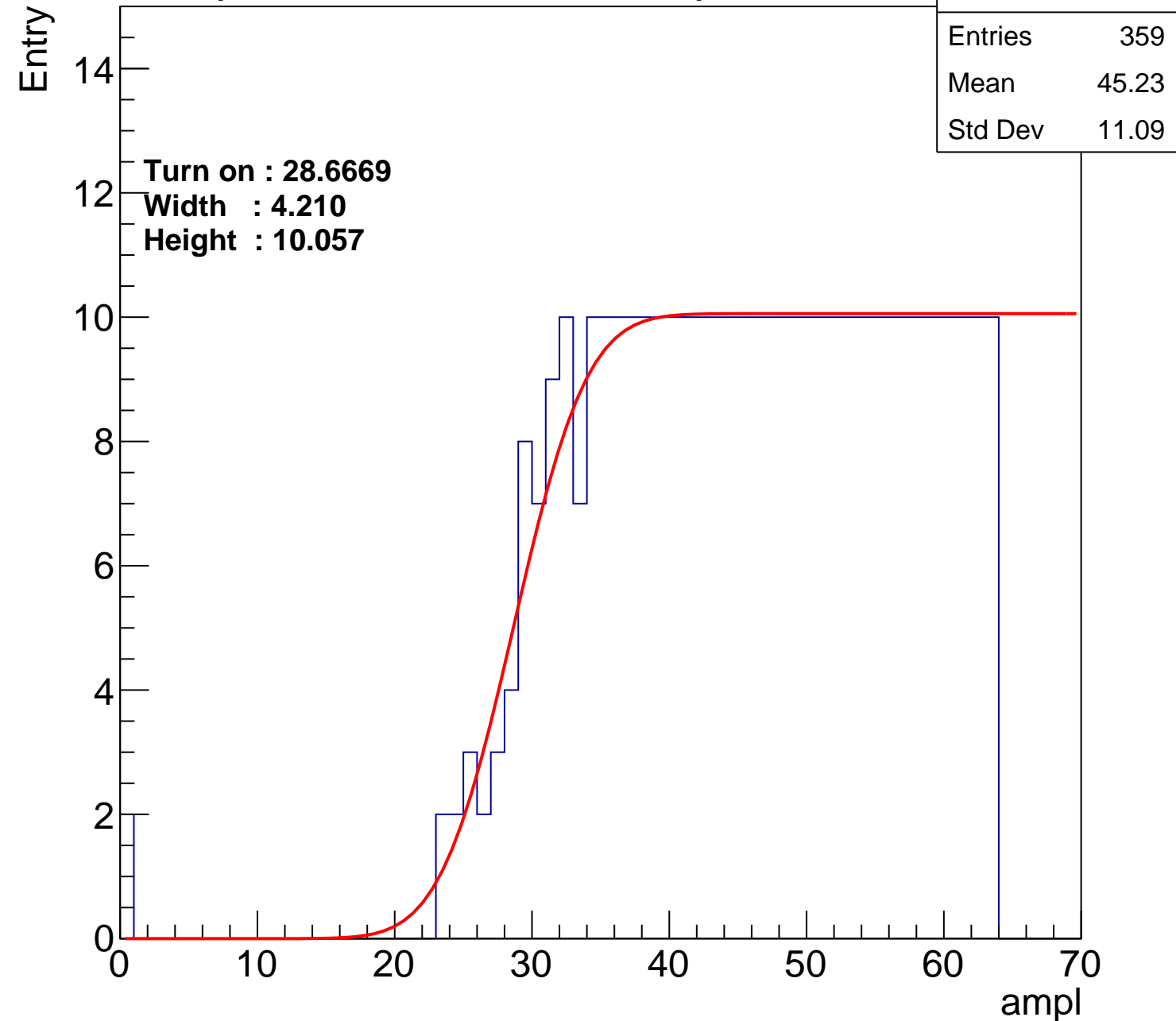
Width : 4.210

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch98

calib_packv5_042523_0143.root, FC#0, port D2

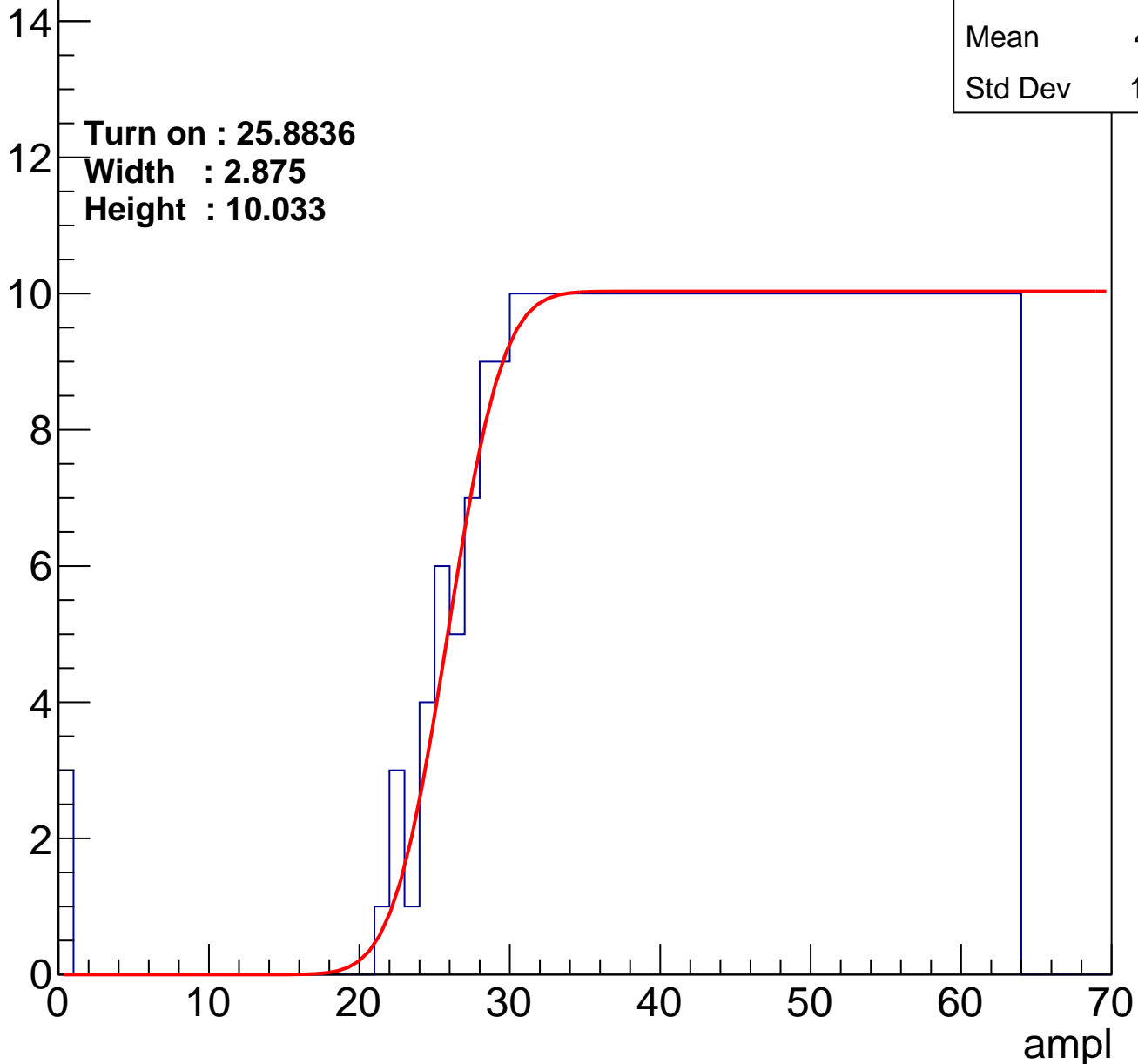
Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.8836

Width : 2.875

Height : 10.033

Entry



B1L101S, U22-ch99

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.45
Std Dev	11.94

Turn on : 27.5993

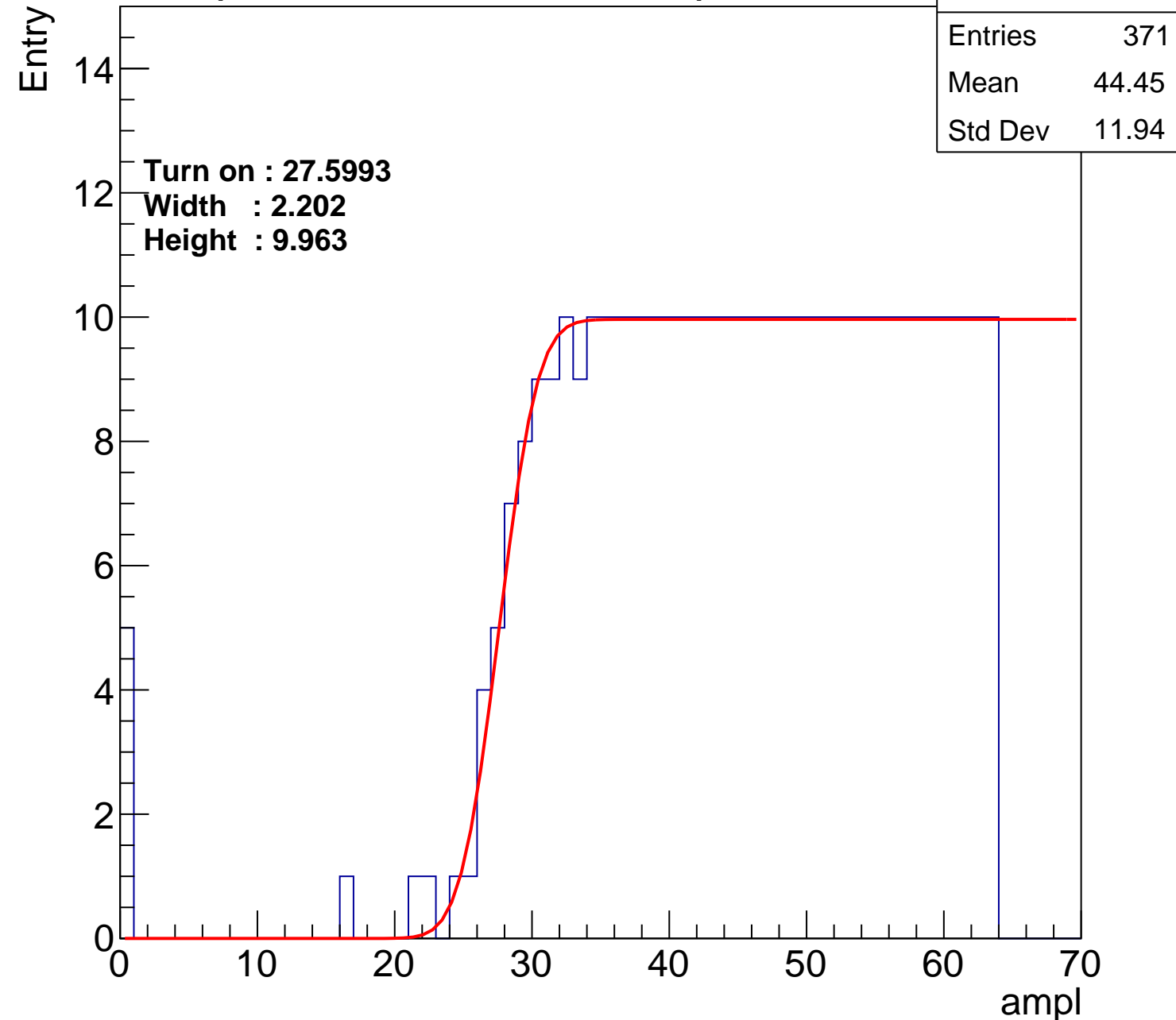
Width : 2.202

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch100

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.85
Std Dev	11.16

Turn on : 27.2205

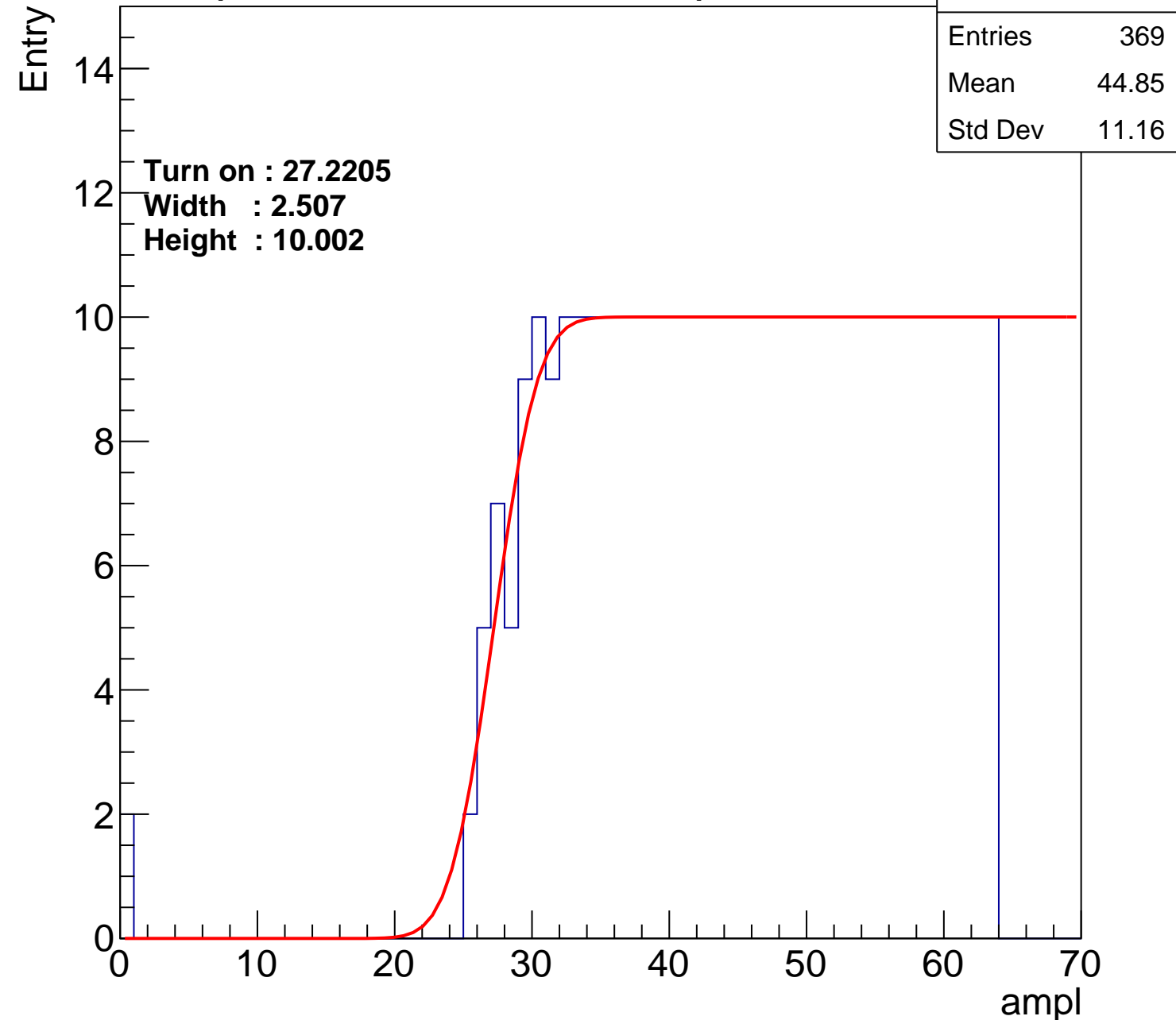
Width : 2.507

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch101

calib_packv5_042523_0143.root, FC#0, port D2

Entries	368
Mean	44.68
Std Dev	11.66

Turn on : 27.6337

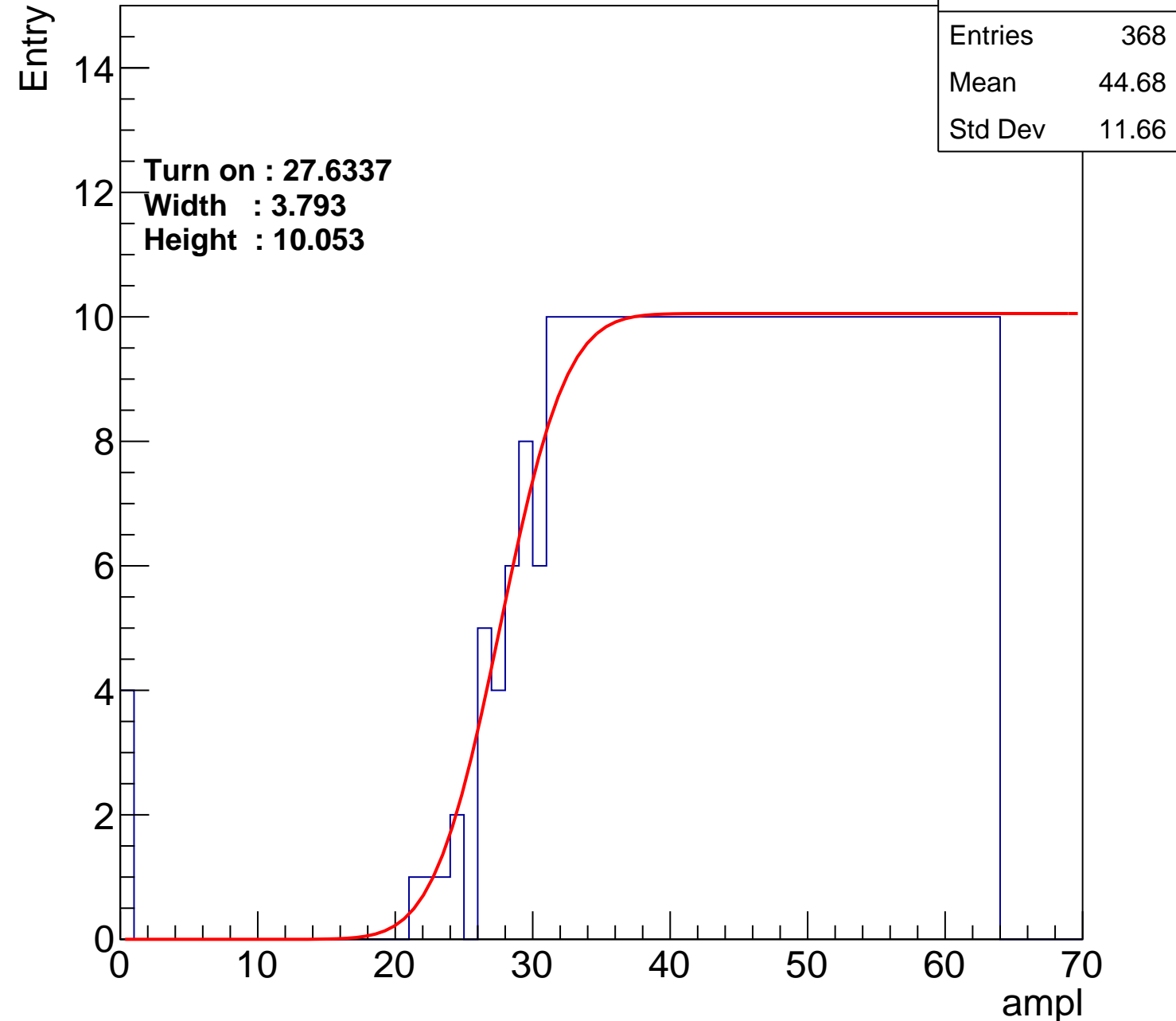
Width : 3.793

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch102

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.53
Std Dev	11.72

Turn on : 26.7226

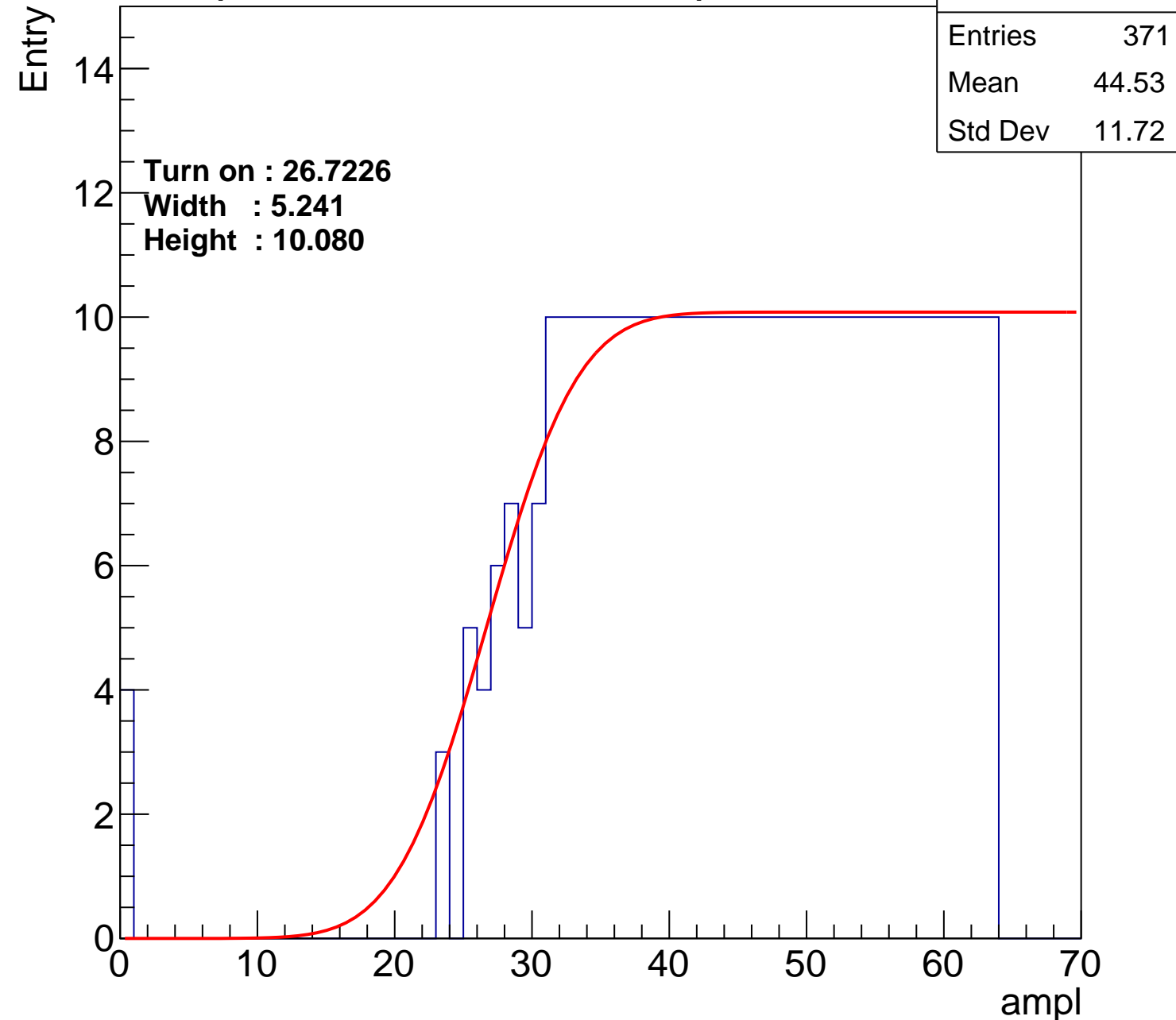
Width : 5.241

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch103

calib_packv5_042523_0143.root, FC#0, port D2

Entries	356
Mean	45.44
Std Dev	10.91

Turn on : 28.6952

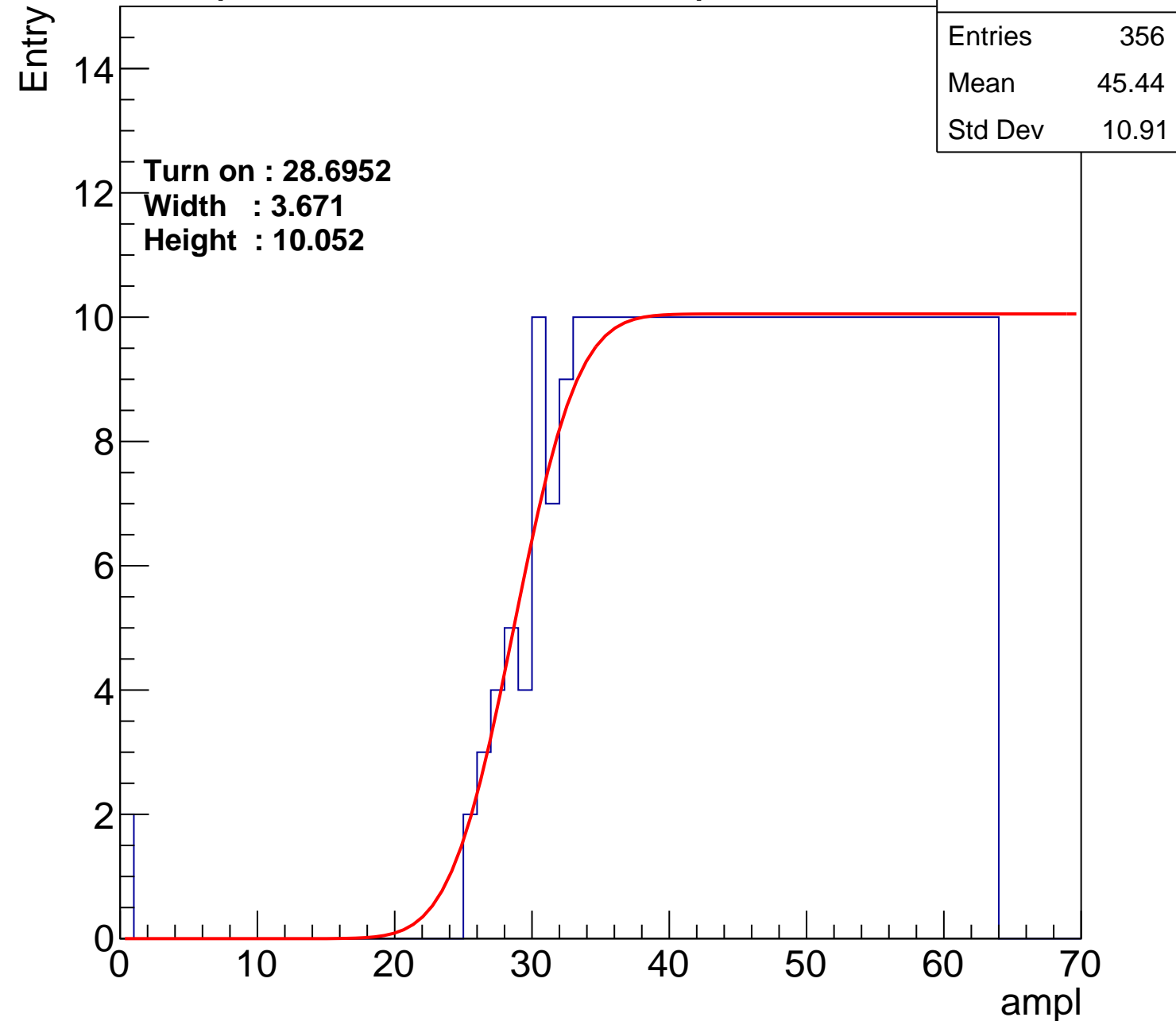
Width : 3.671

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch104

calib_packv5_042523_0143.root, FC#0, port D2

Entries	378
Mean	44.21
Std Dev	11.86

Turn on : 26.8782

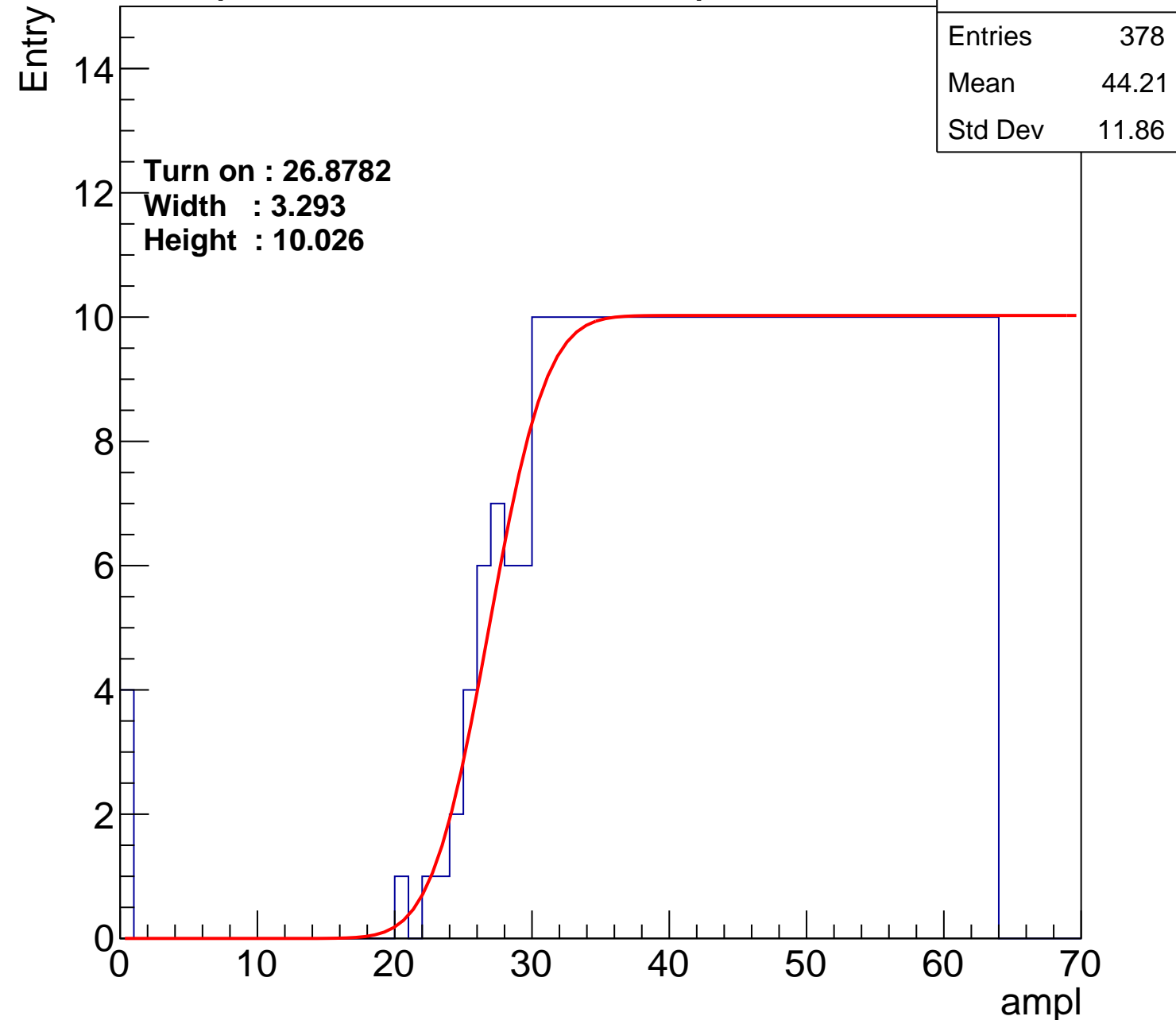
Width : 3.293

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch105

calib_packv5_042523_0143.root, FC#0, port D2

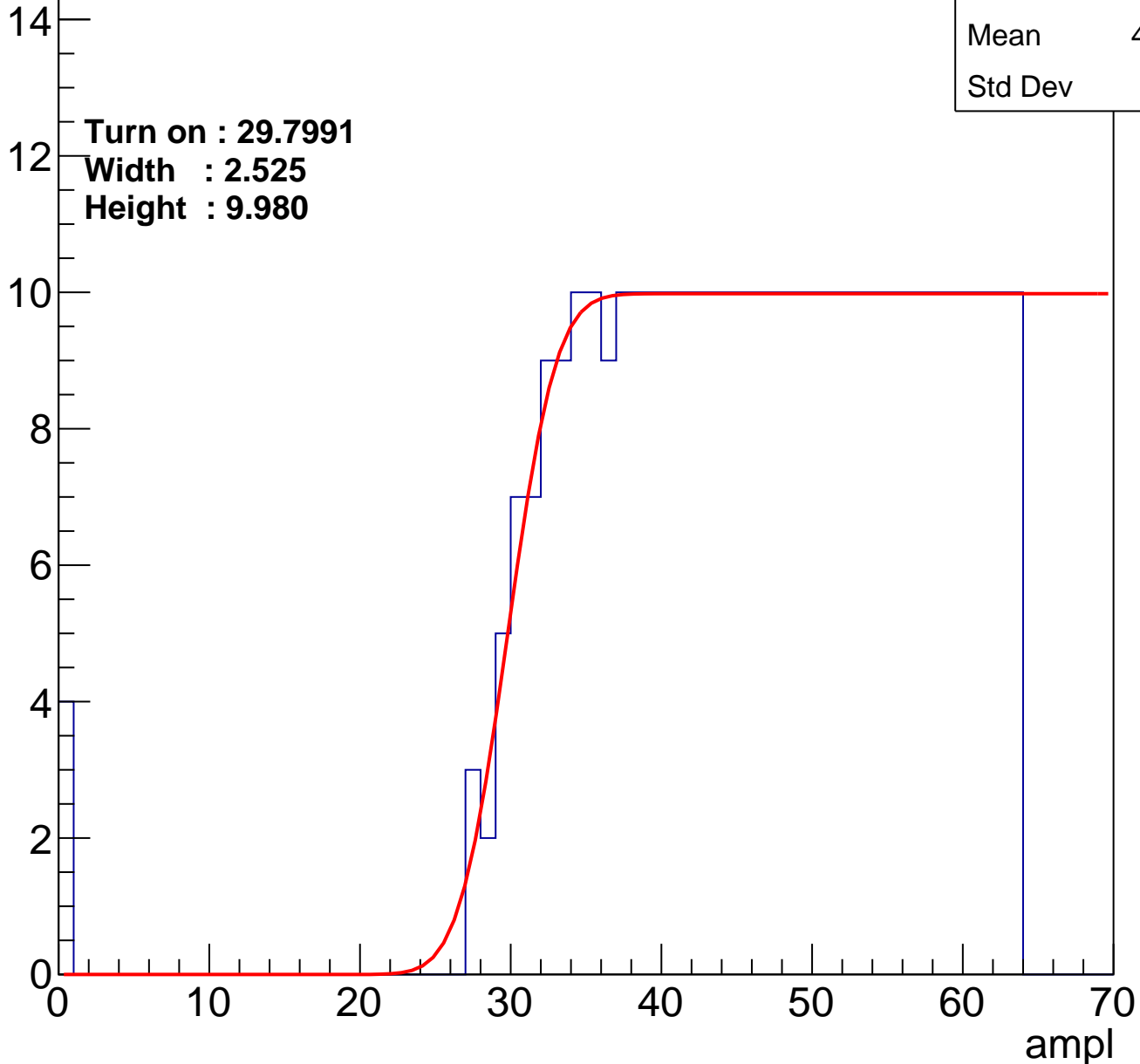
Entries	345
Mean	45.82
Std Dev	11.1

Turn on : 29.7991

Width : 2.525

Height : 9.980

Entry



B1L101S, U22-ch106

calib_packv5_042523_0143.root, FC#0, port D2

Entries	387
Mean	44.01
Std Dev	11.47

Turn on : 25.5053

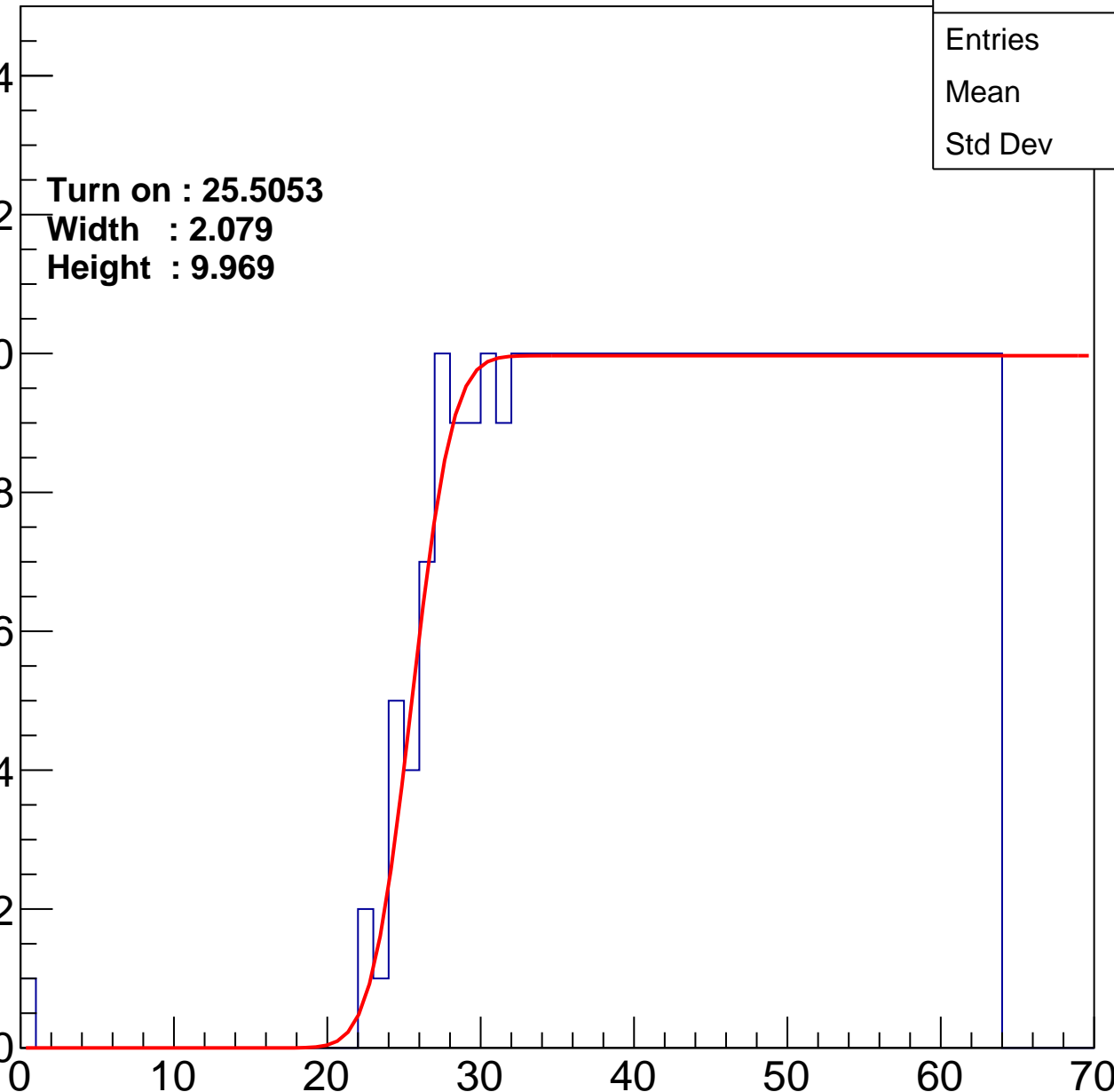
Width : 2.079

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch107

calib_packv5_042523_0143.root, FC#0, port D2

Entries	360
Mean	45.23
Std Dev	11.04

Turn on : 28.4662

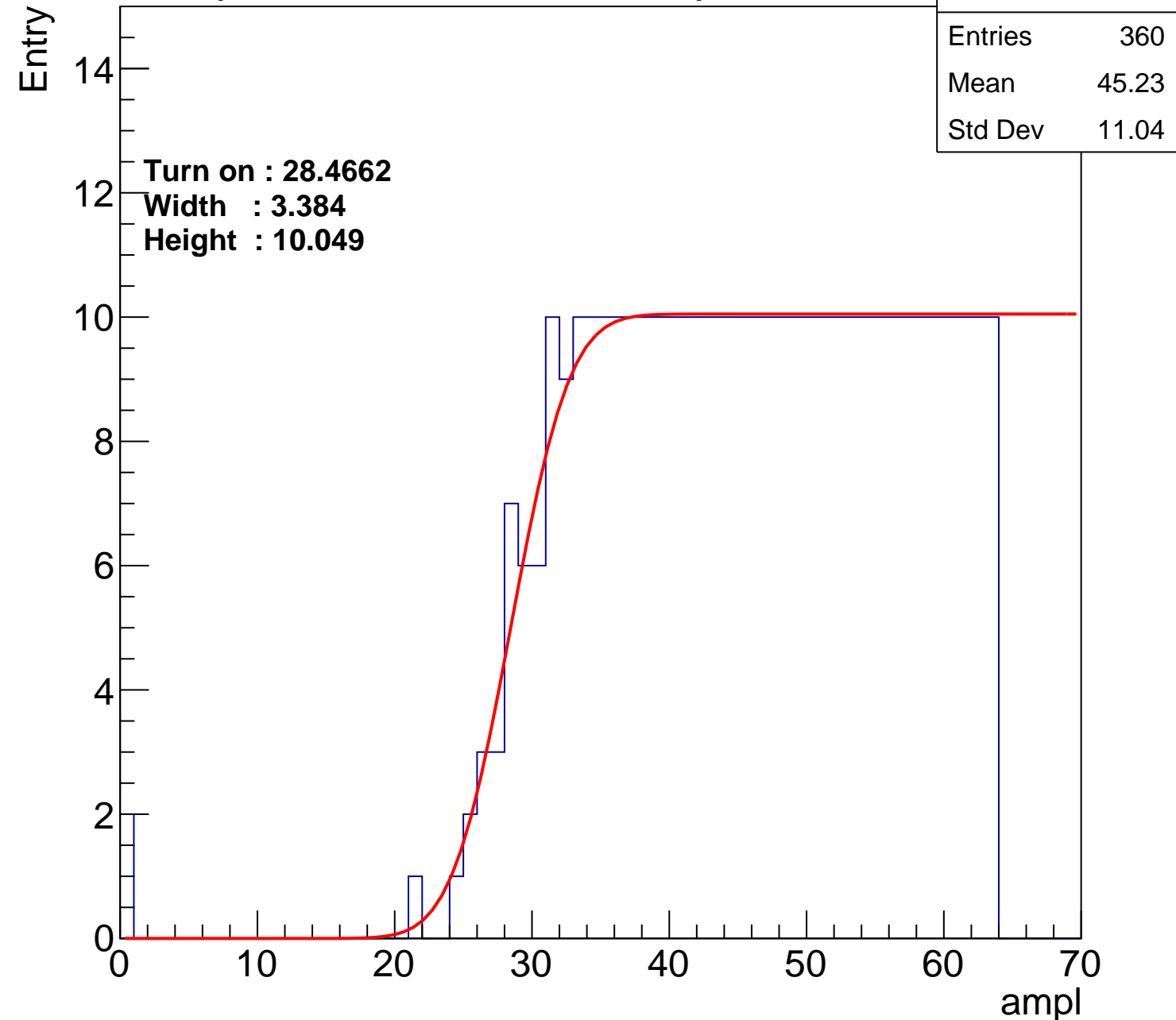
Width : 3.384

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch108

calib_packv5_042523_0143.root, FC#0, port D2

Entries	358
Mean	45.3
Std Dev	11.03

Turn on : 28.7599

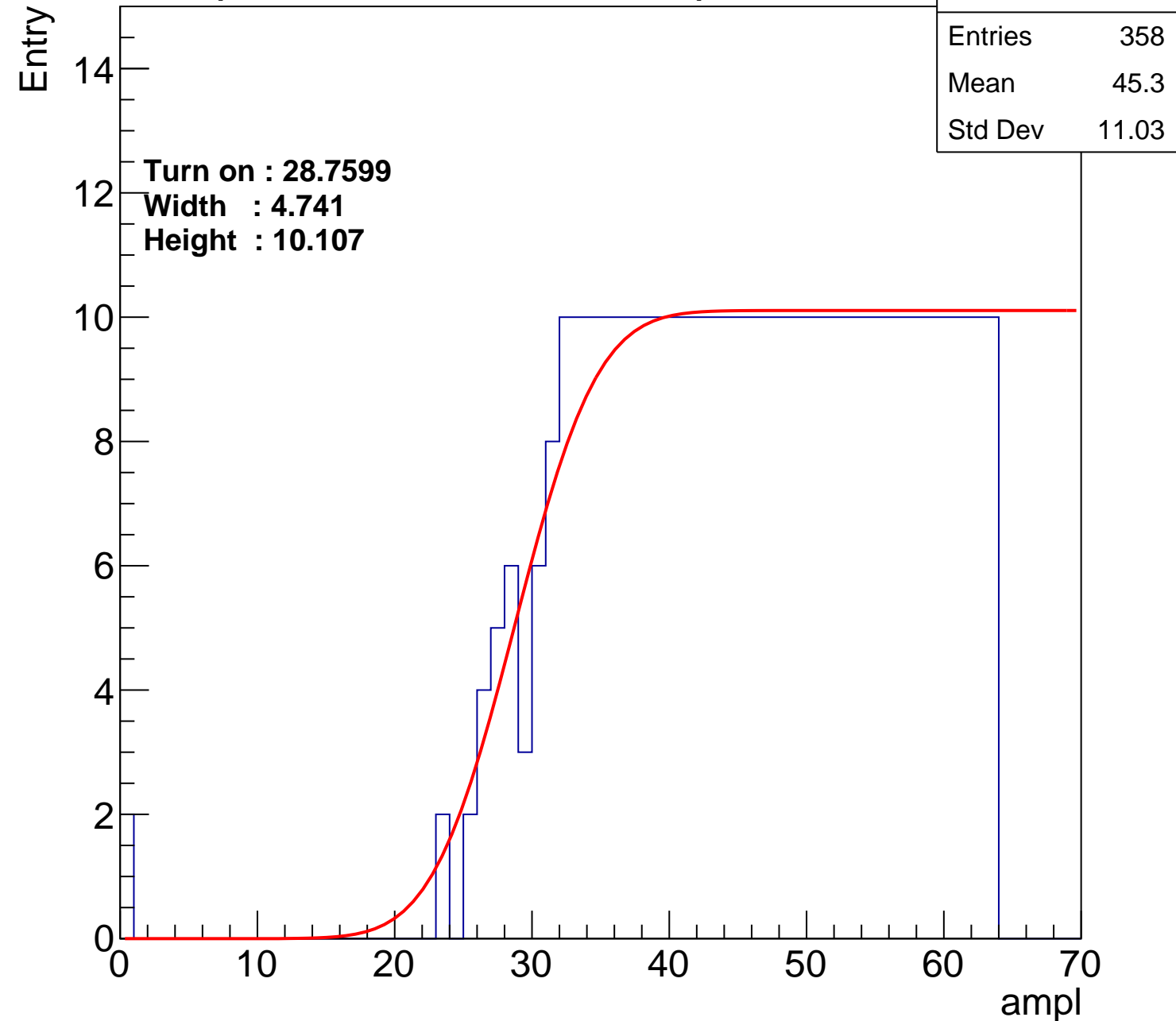
Width : 4.741

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch109

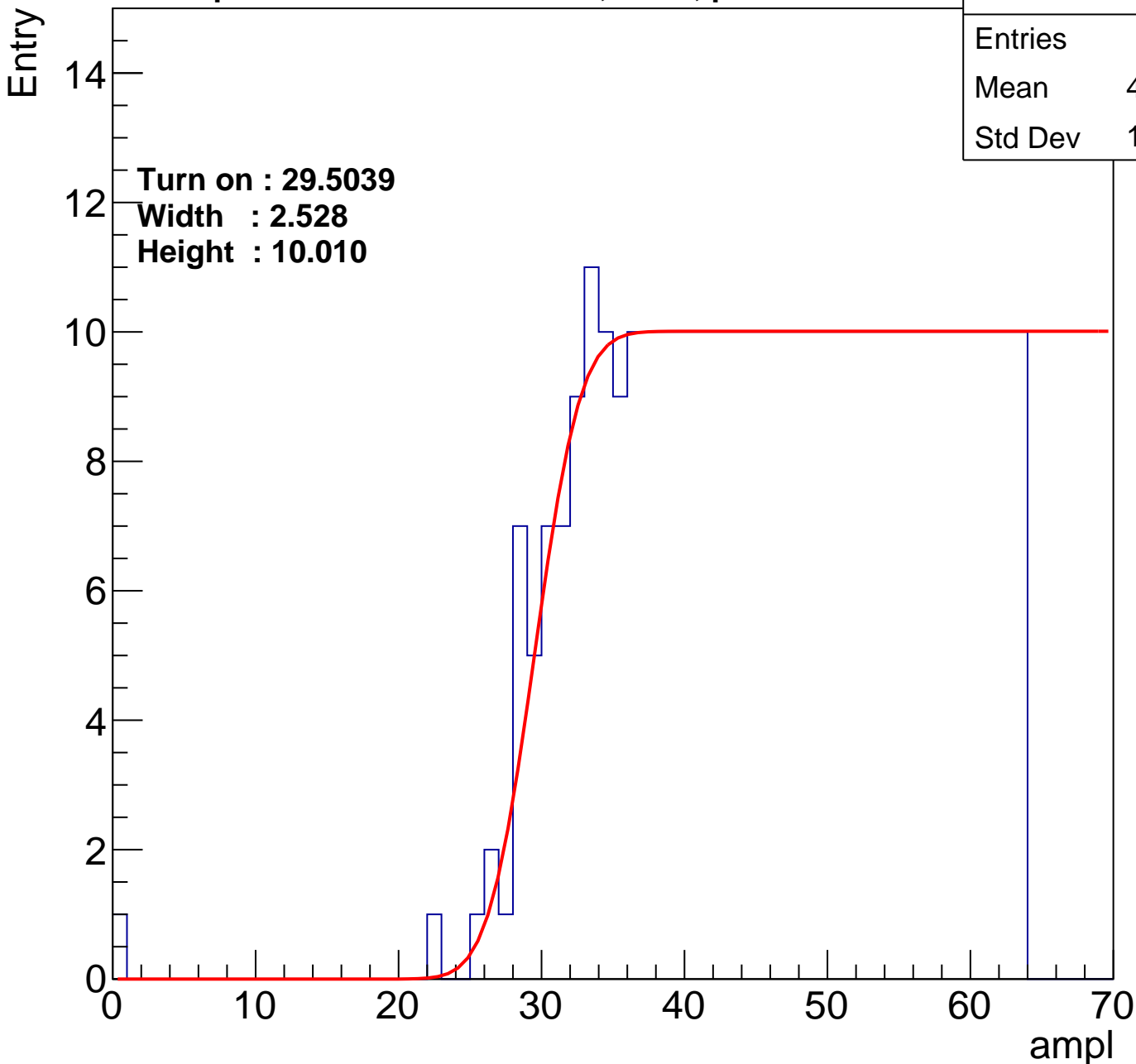
calib_packv5_042523_0143.root, FC#0, port D2

Entries	351
Mean	45.75
Std Dev	10.57

Turn on : 29.5039

Width : 2.528

Height : 10.010



B1L101S, U22-ch110

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.41
Std Dev	11.76

Turn on : 27.5089

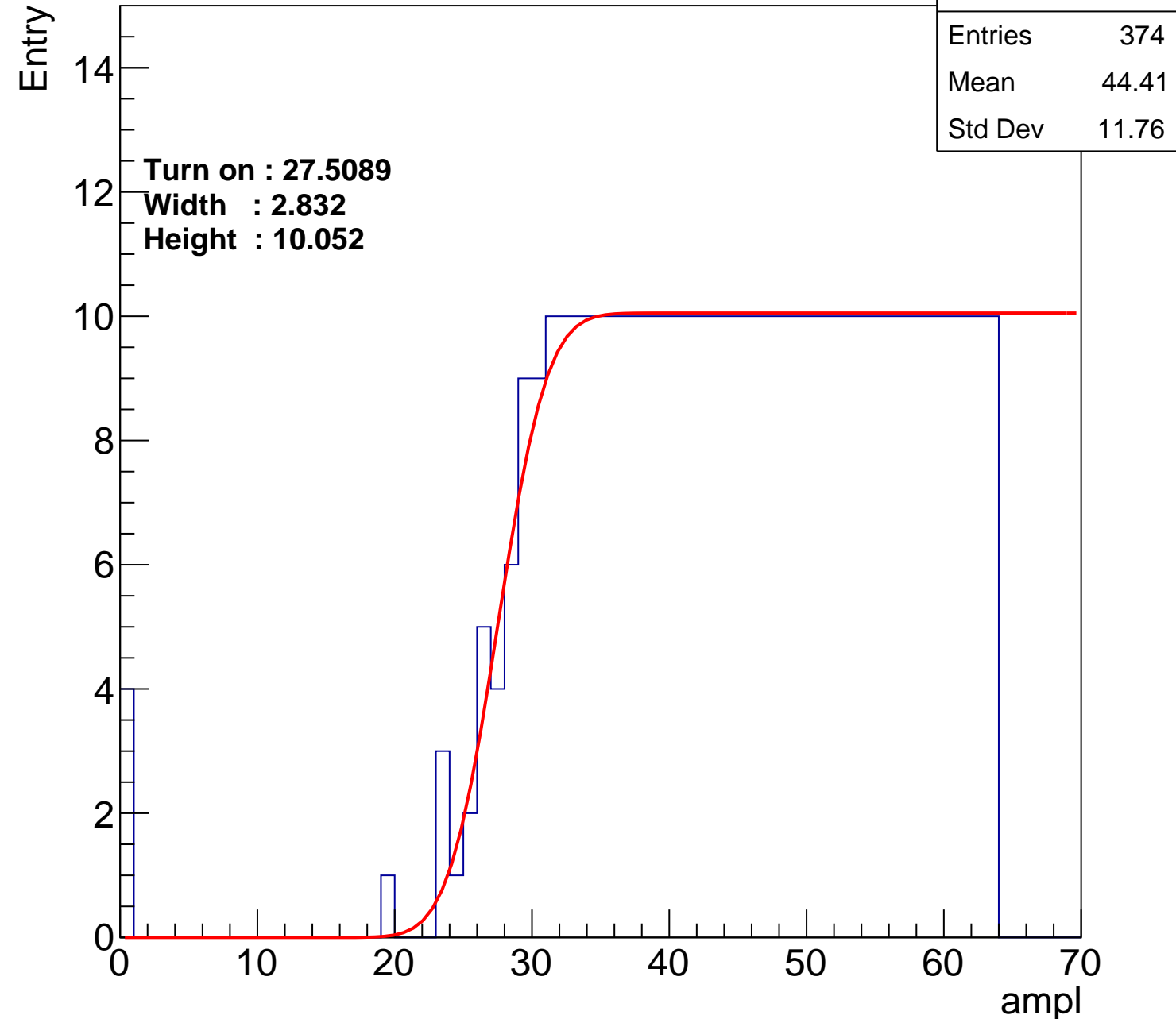
Width : 2.832

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch111

calib_packv5_042523_0143.root, FC#0, port D2

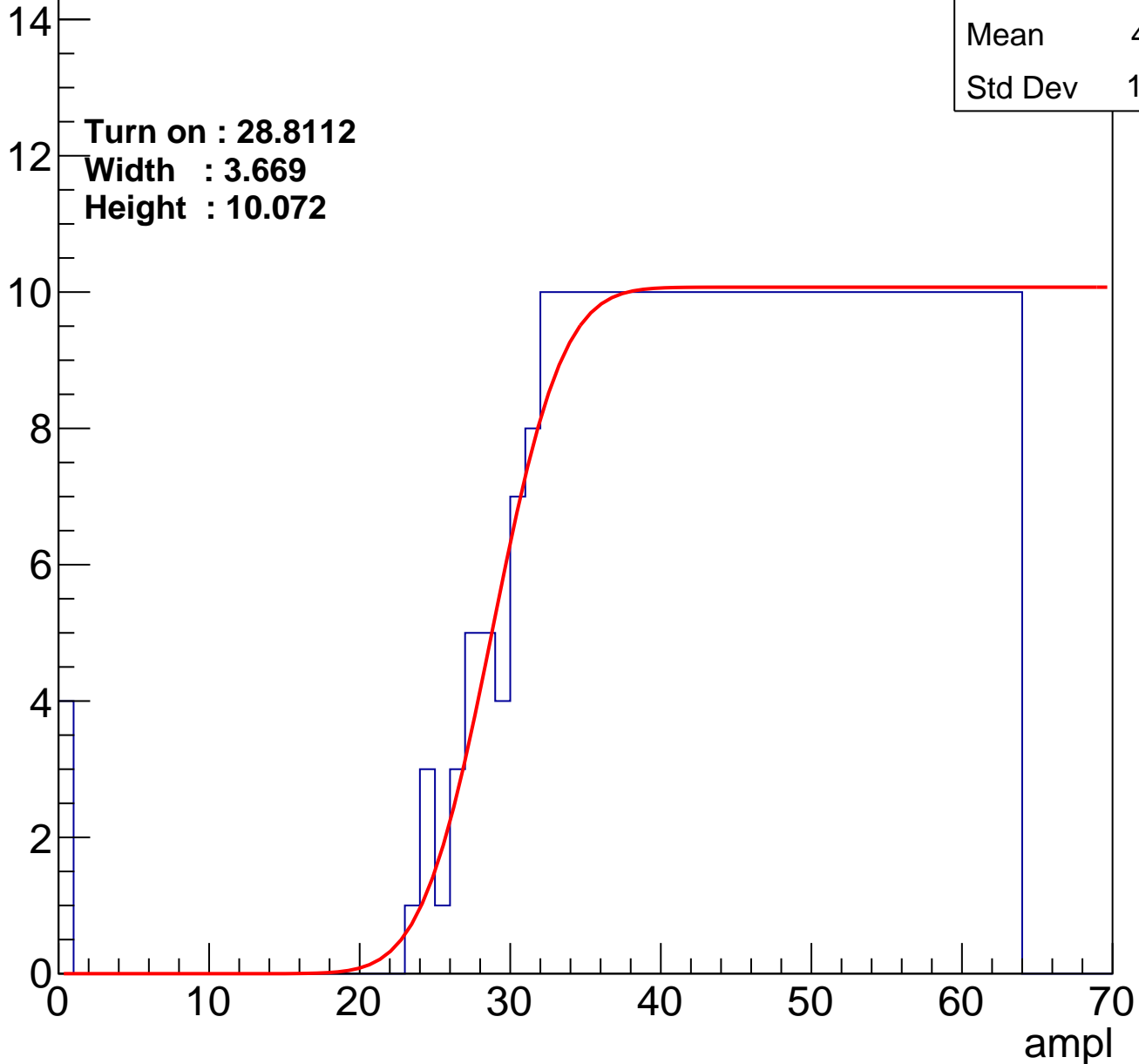
Entries	361
Mean	45.01
Std Dev	11.52

Turn on : 28.8112

Width : 3.669

Height : 10.072

Entry



B1L101S, U22-ch112

calib_packv5_042523_0143.root, FC#0, port D2

Entries	360
Mean	45.09
Std Dev	11.36

Turn on : 28.6725

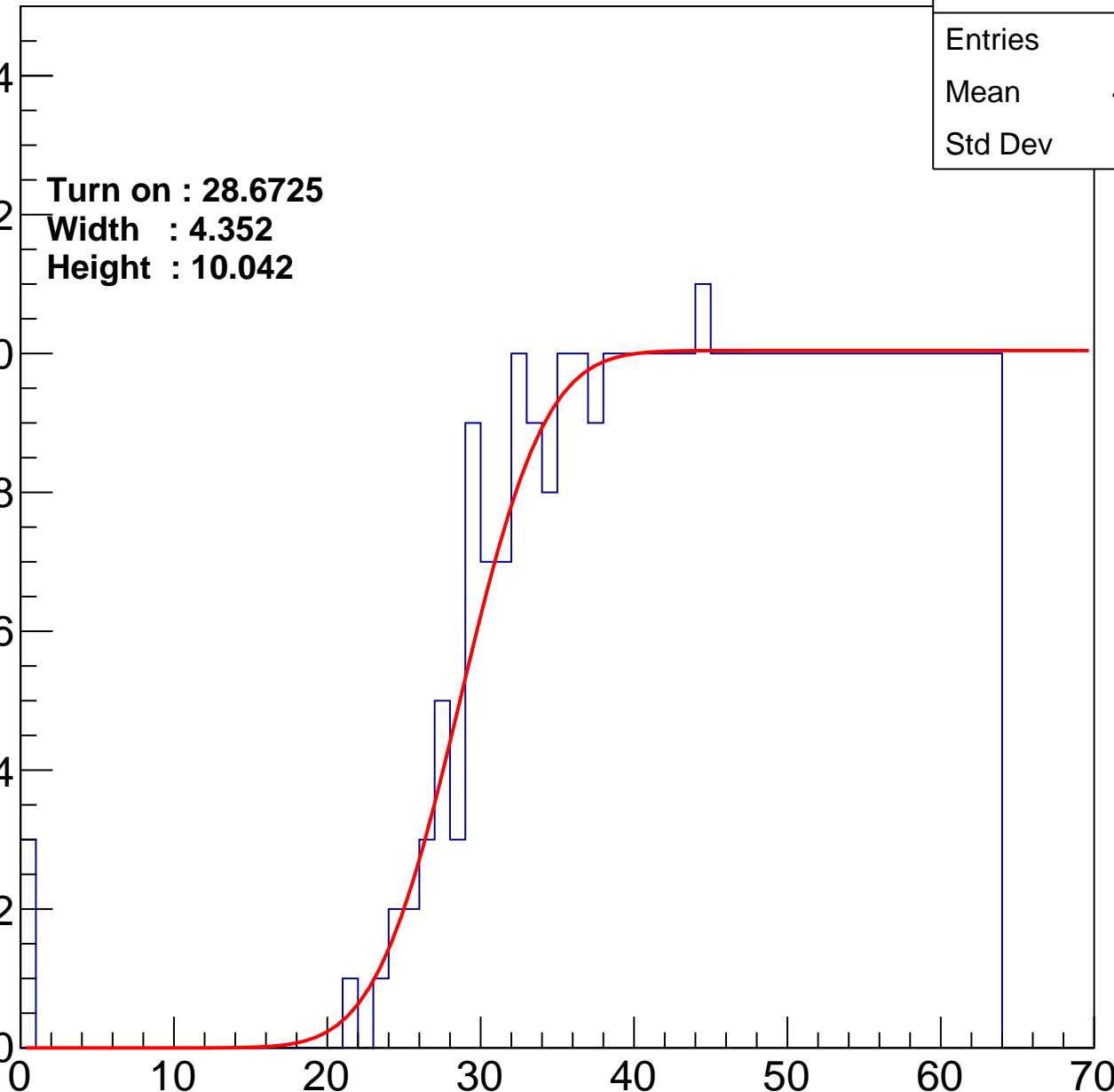
Width : 4.352

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch113

calib_packv5_042523_0143.root, FC#0, port D2

Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.7494

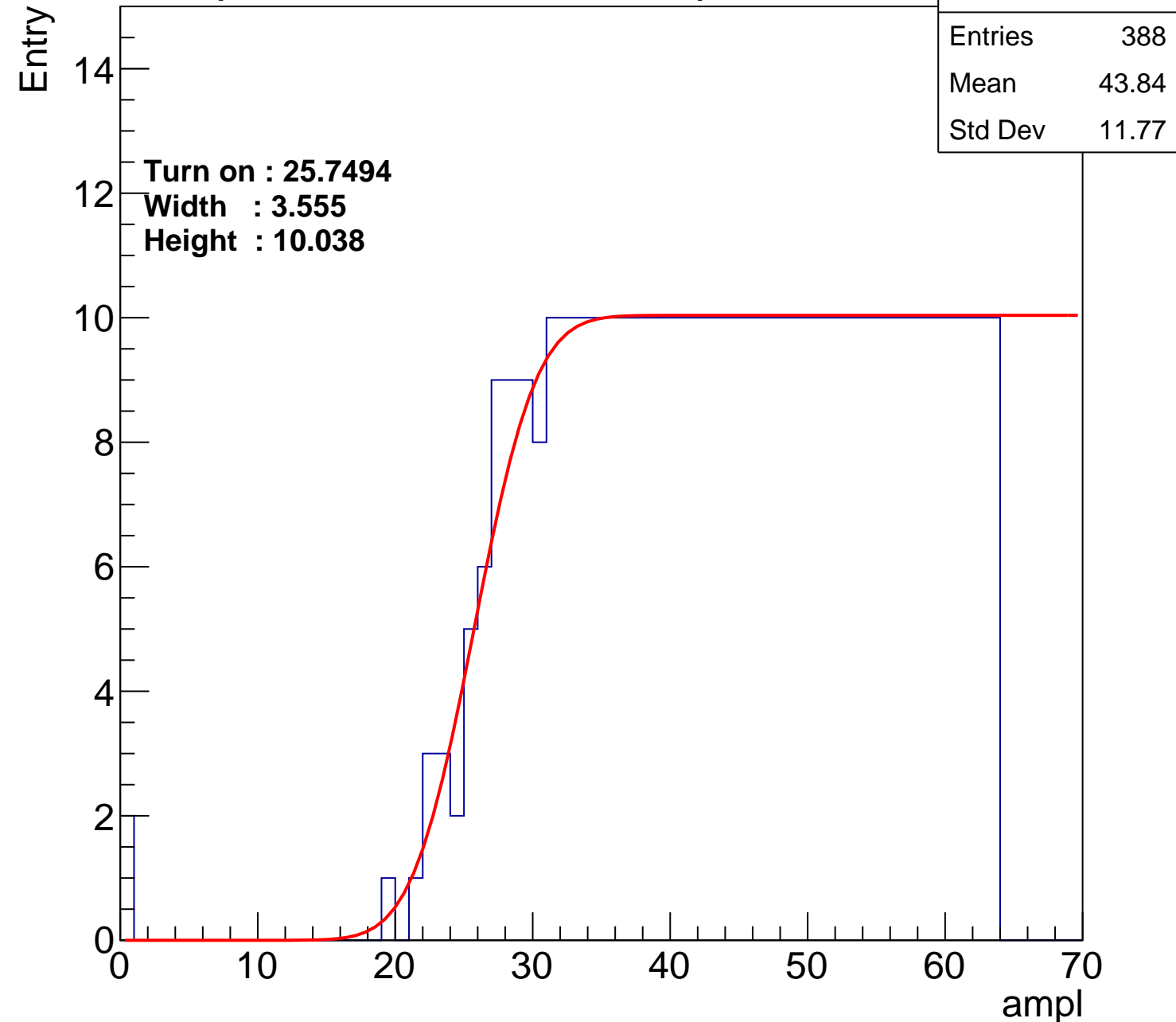
Width : 3.555

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch114

calib_packv5_042523_0143.root, FC#0, port D2

Entries	384
Mean	43.91
Std Dev	12.15

Turn on : 25.9976

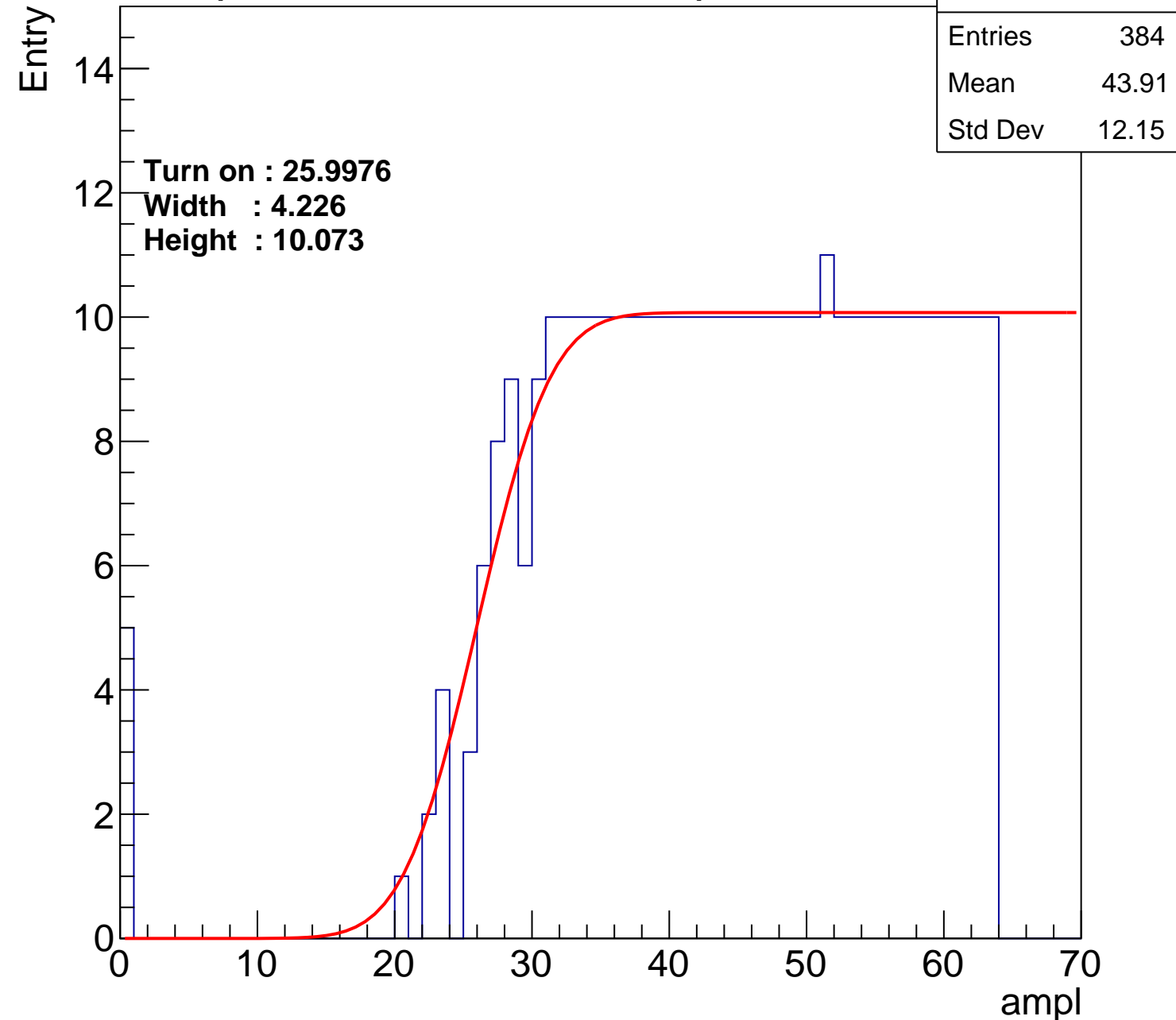
Width : 4.226

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch115

calib_packv5_042523_0143.root, FC#0, port D2

Entries	376
Mean	44.68
Std Dev	11.18

Turn on : 26.9223

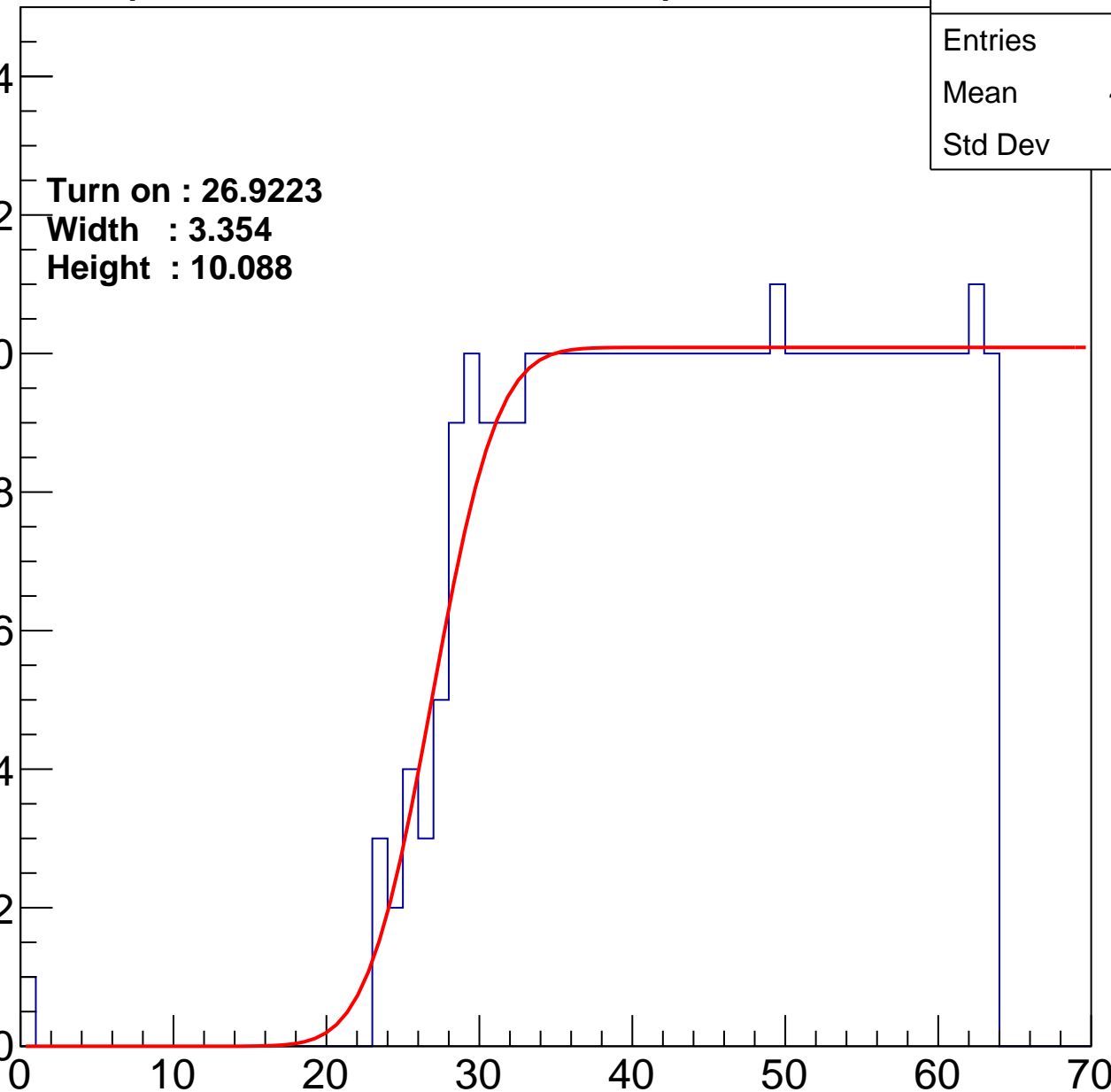
Width : 3.354

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch116

calib_packv5_042523_0143.root, FC#0, port D2

Entries	395
Mean	43.24
Std Dev	12.65

Turn on : 25.3448

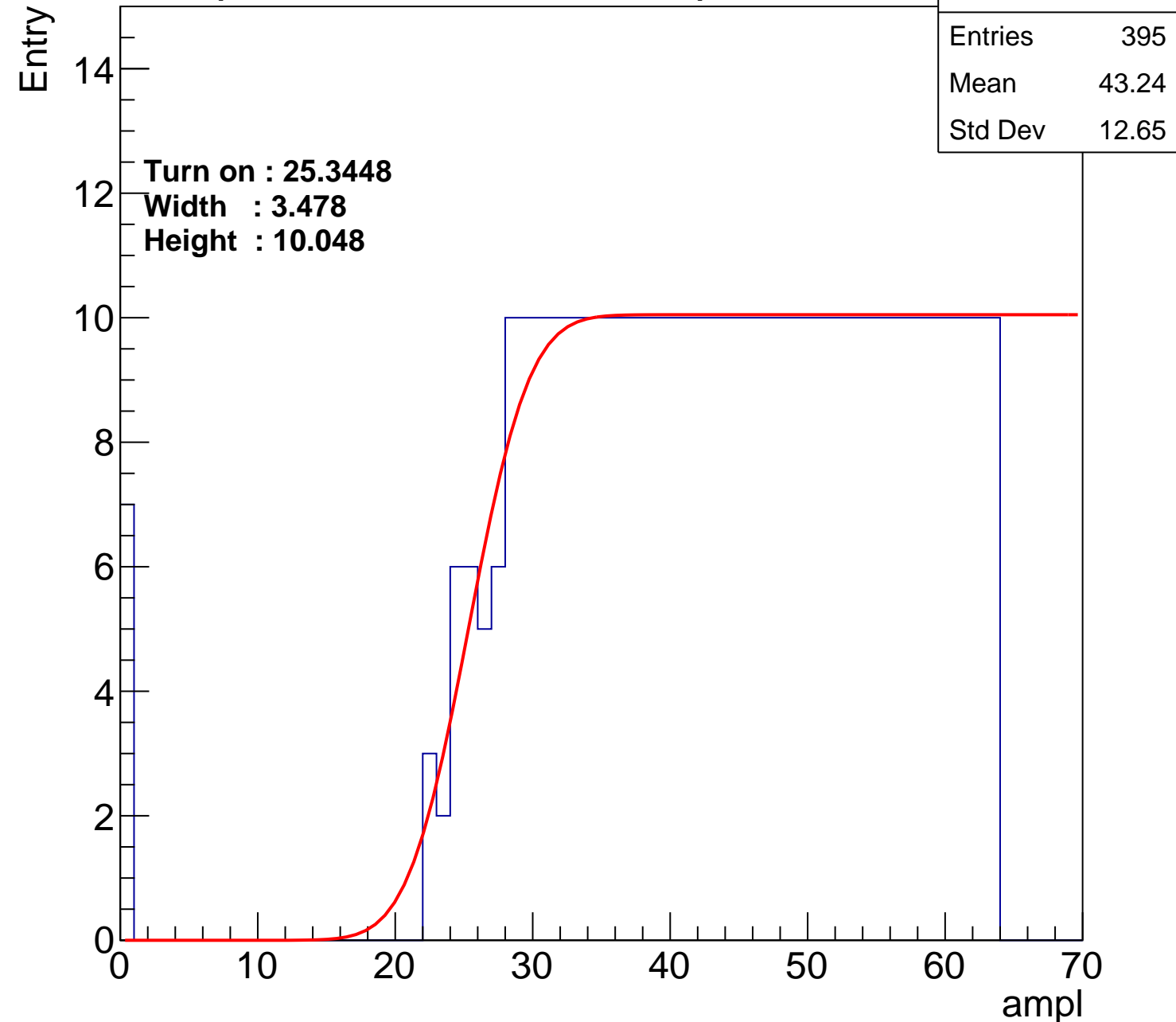
Width : 3.478

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch117

calib_packv5_042523_0143.root, FC#0, port D2

Entries	365
Mean	44.61
Std Dev	12.08

Turn on : 29.3802

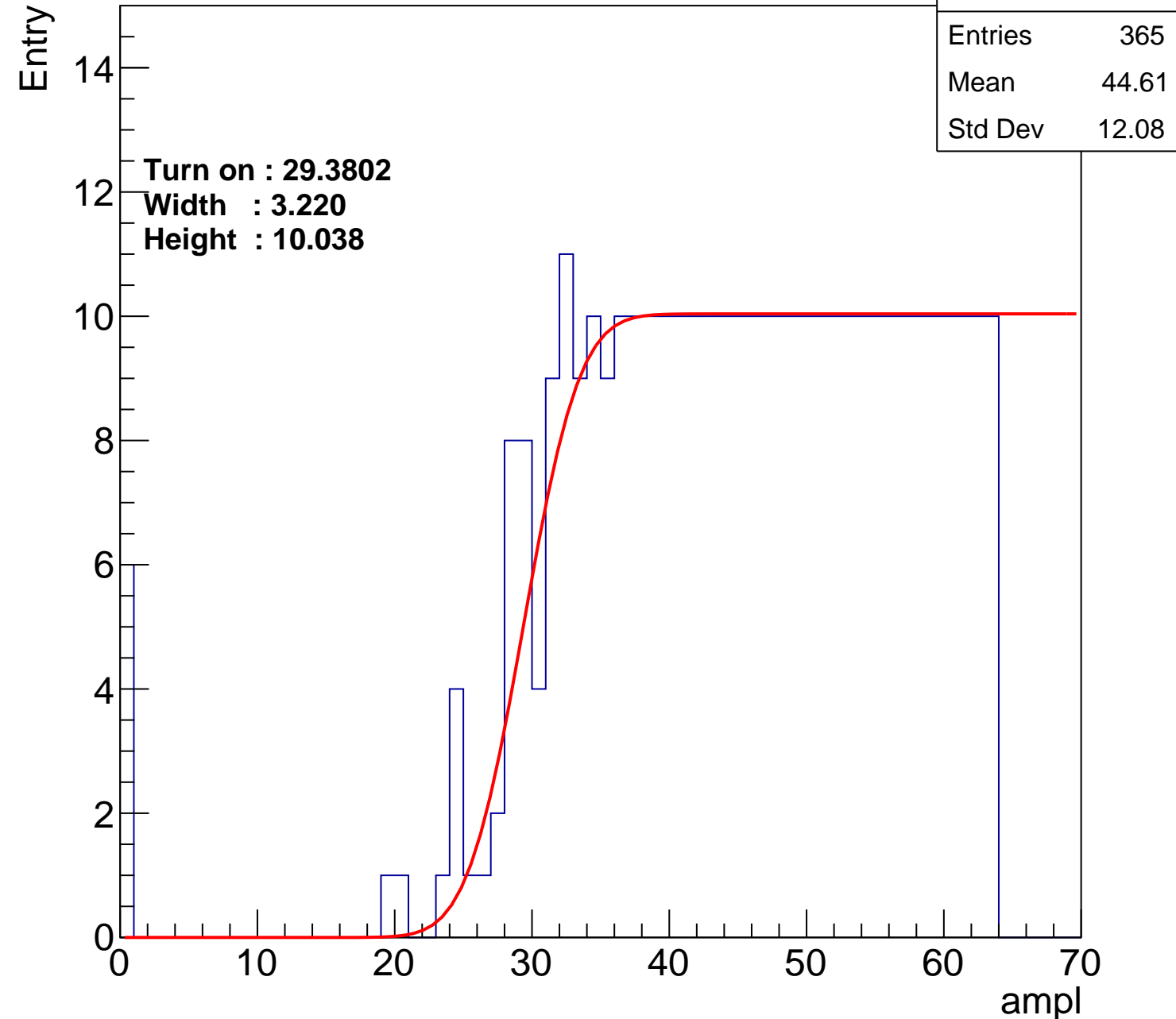
Width : 3.220

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch118

calib_packv5_042523_0143.root, FC#0, port D2

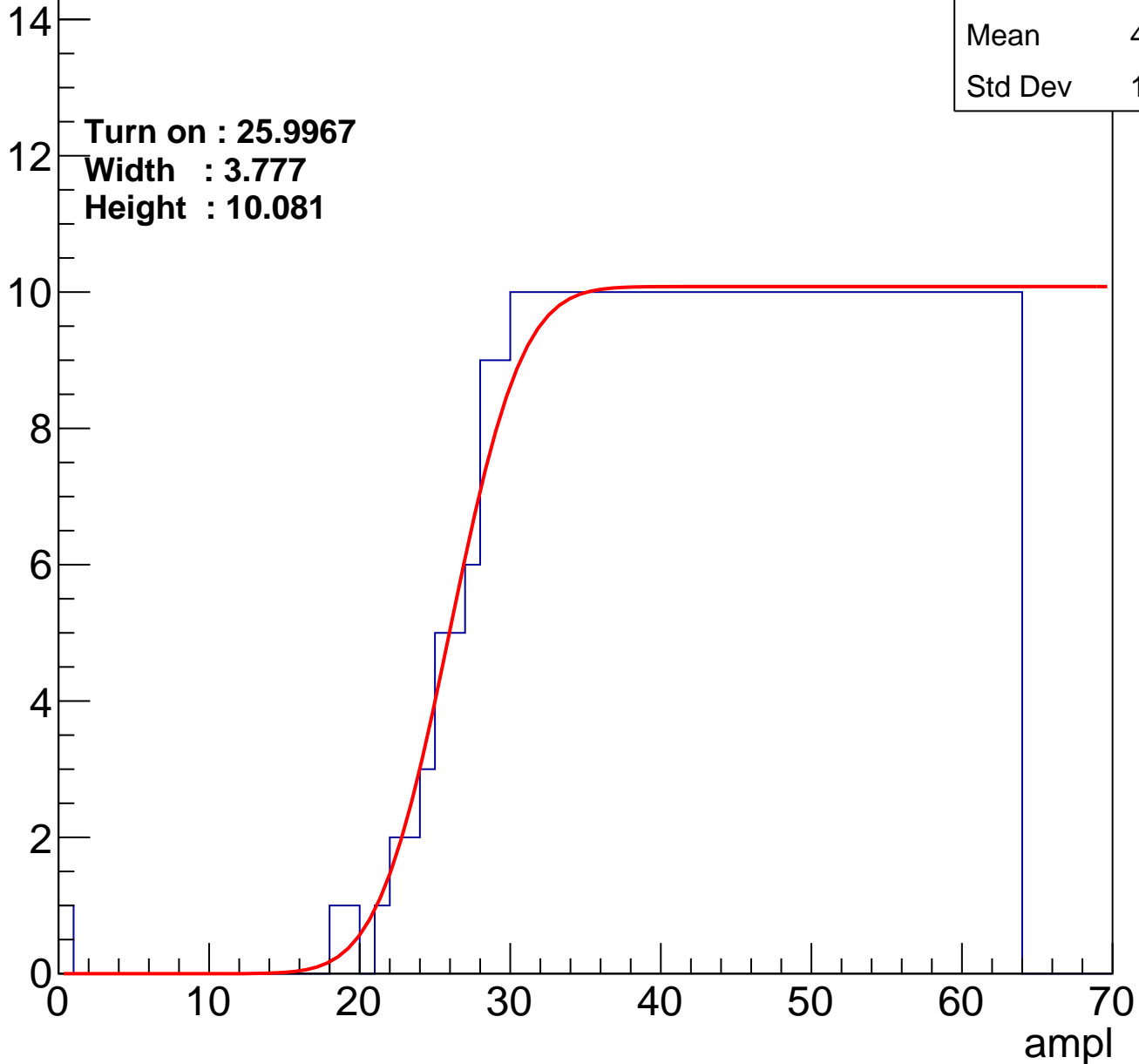
Entries	385
Mean	44.05
Std Dev	11.53

Turn on : 25.9967

Width : 3.777

Height : 10.081

Entry



B1L101S, U22-ch119

calib_packv5_042523_0143.root, FC#0, port D2

Entries	369
Mean	44.63
Std Dev	11.67

Turn on : 28.1727

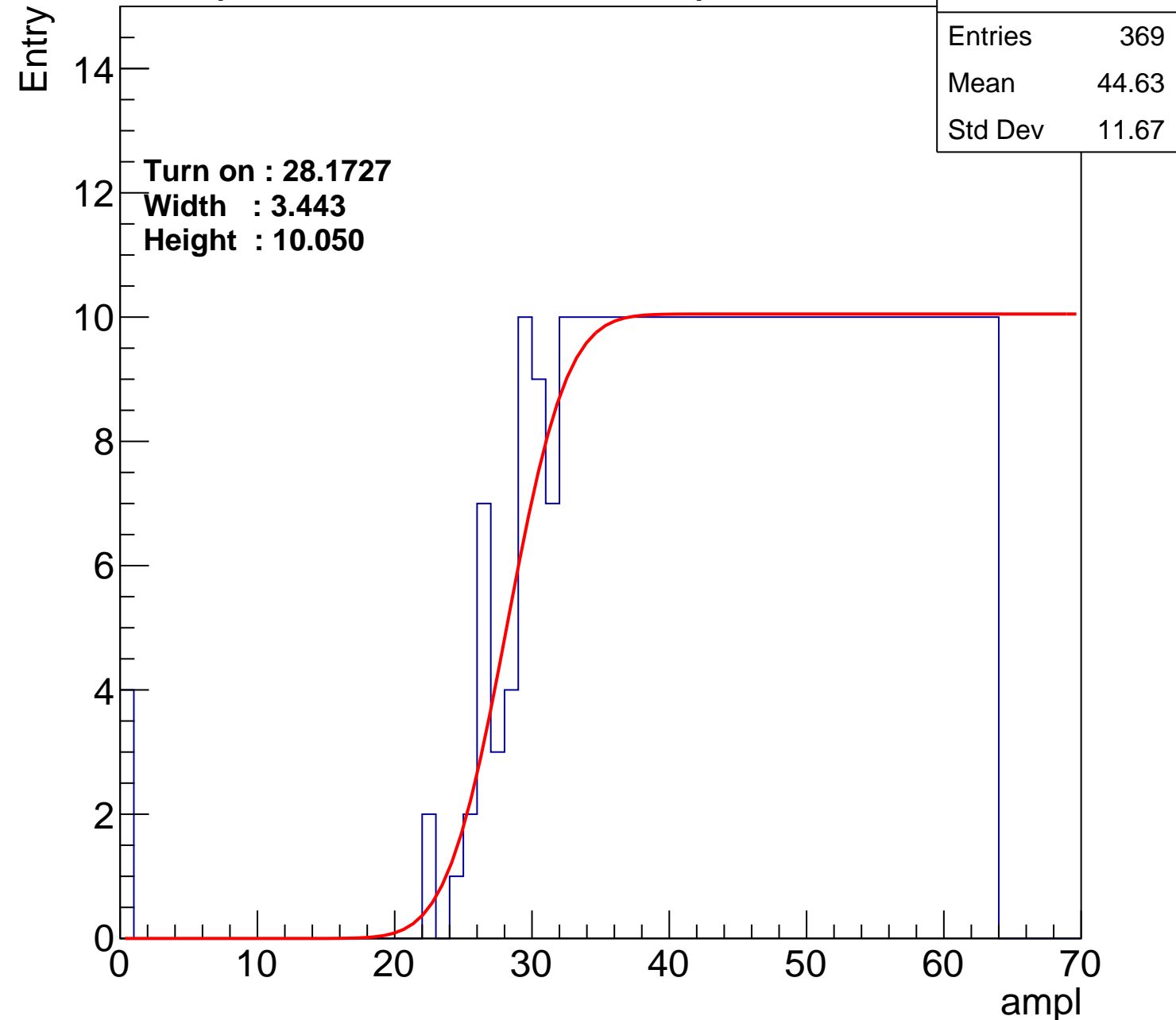
Width : 3.443

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch120

calib_packv5_042523_0143.root, FC#0, port D2

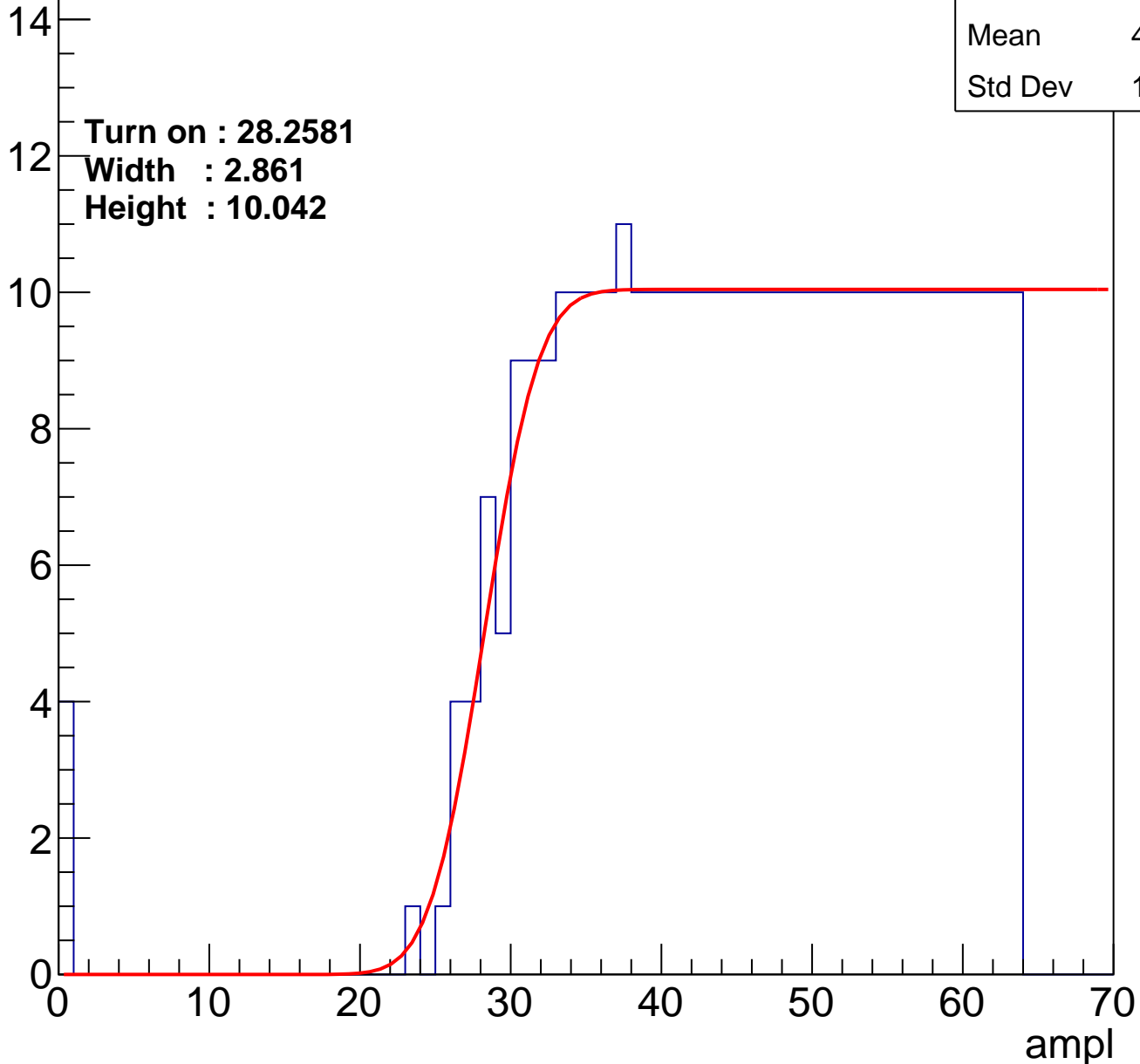
Entries	364
Mean	44.93
Std Dev	11.48

Turn on : 28.2581

Width : 2.861

Height : 10.042

Entry



B1L101S, U22-ch121

calib_packv5_042523_0143.root, FC#0, port D2

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.4363

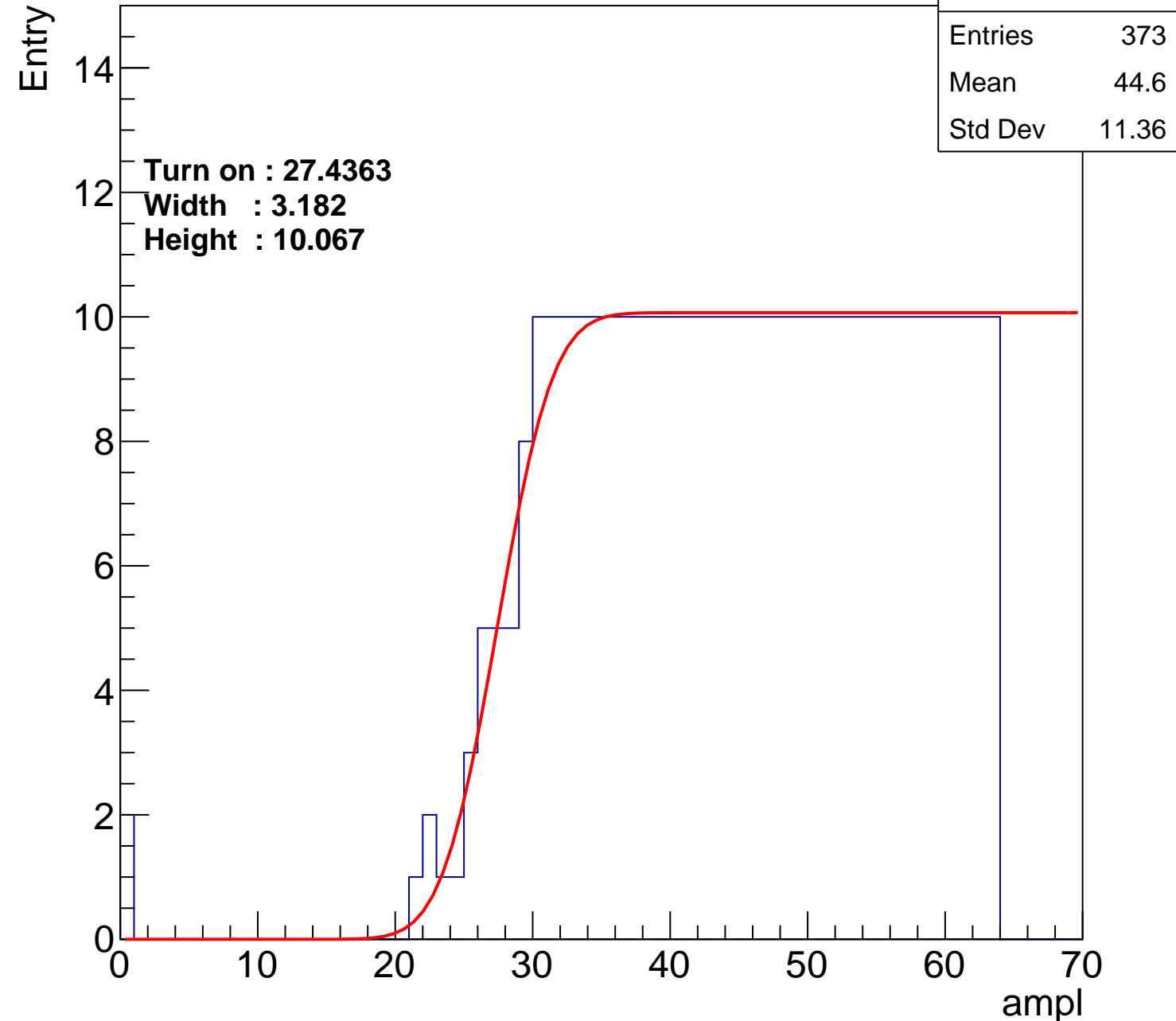
Width : 3.182

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch122

calib_packv5_042523_0143.root, FC#0, port D2

Entries	377
Mean	44.22
Std Dev	11.96

Turn on : 26.8932

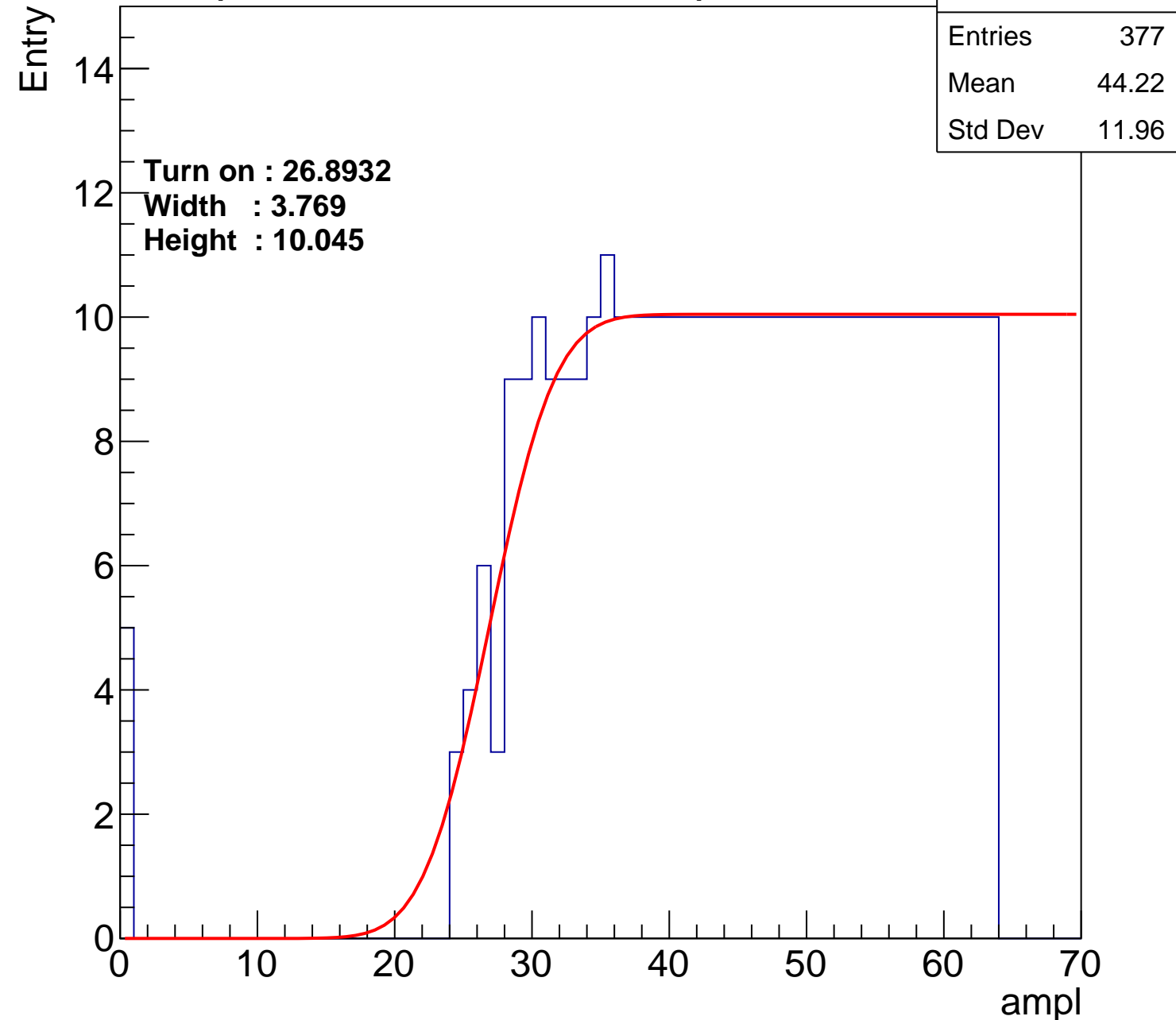
Width : 3.769

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch123

calib_packv5_042523_0143.root, FC#0, port D2

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.6646

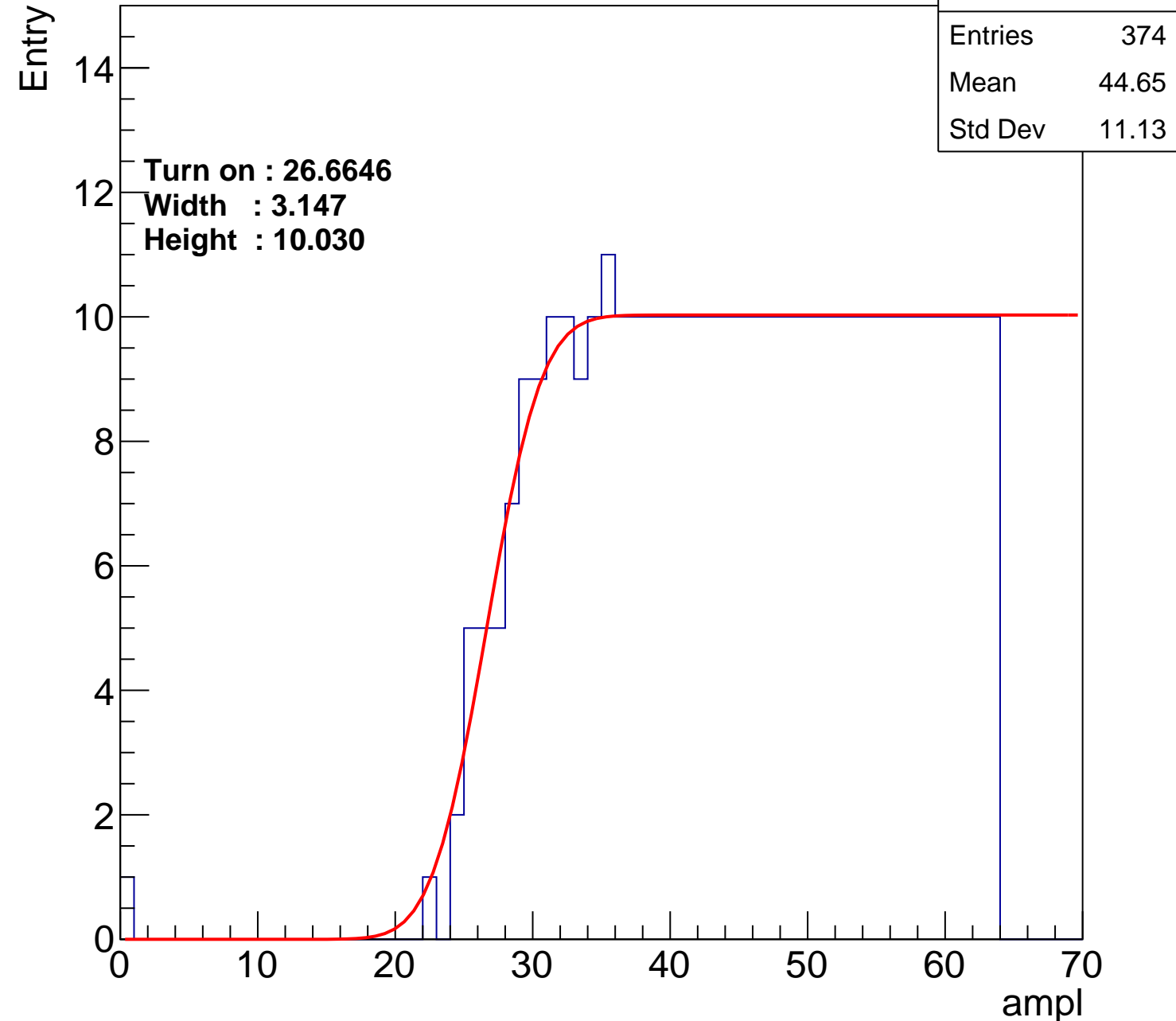
Width : 3.147

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch124

calib_packv5_042523_0143.root, FC#0, port D2

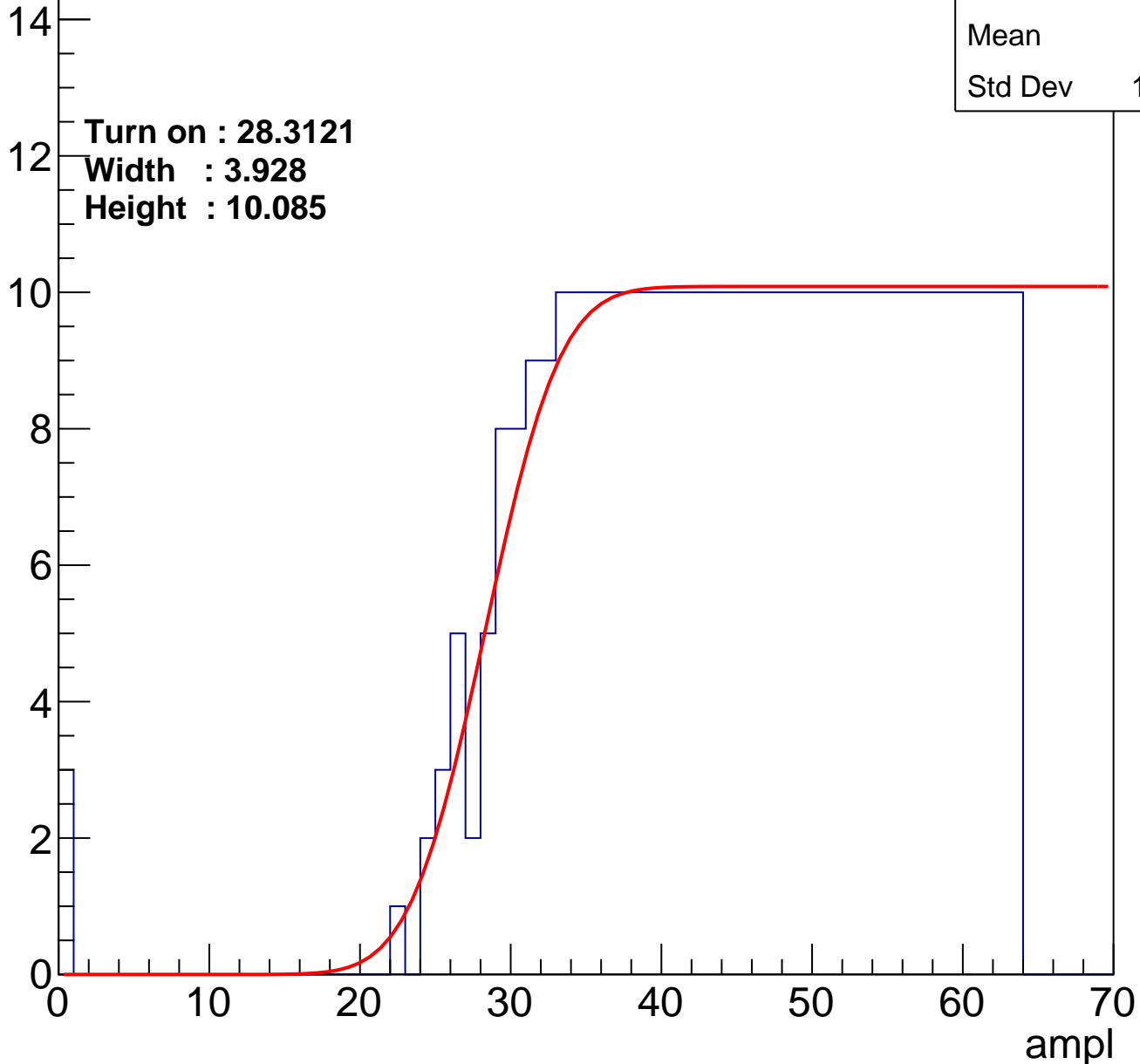
Entries	365
Mean	44.9
Std Dev	11.38

Turn on : 28.3121

Width : 3.928

Height : 10.085

Entry



B1L101S, U22-ch125

calib_packv5_042523_0143.root, FC#0, port D2

Entries	358
Mean	45.42
Std Dev	10.75

Turn on : 28.9874

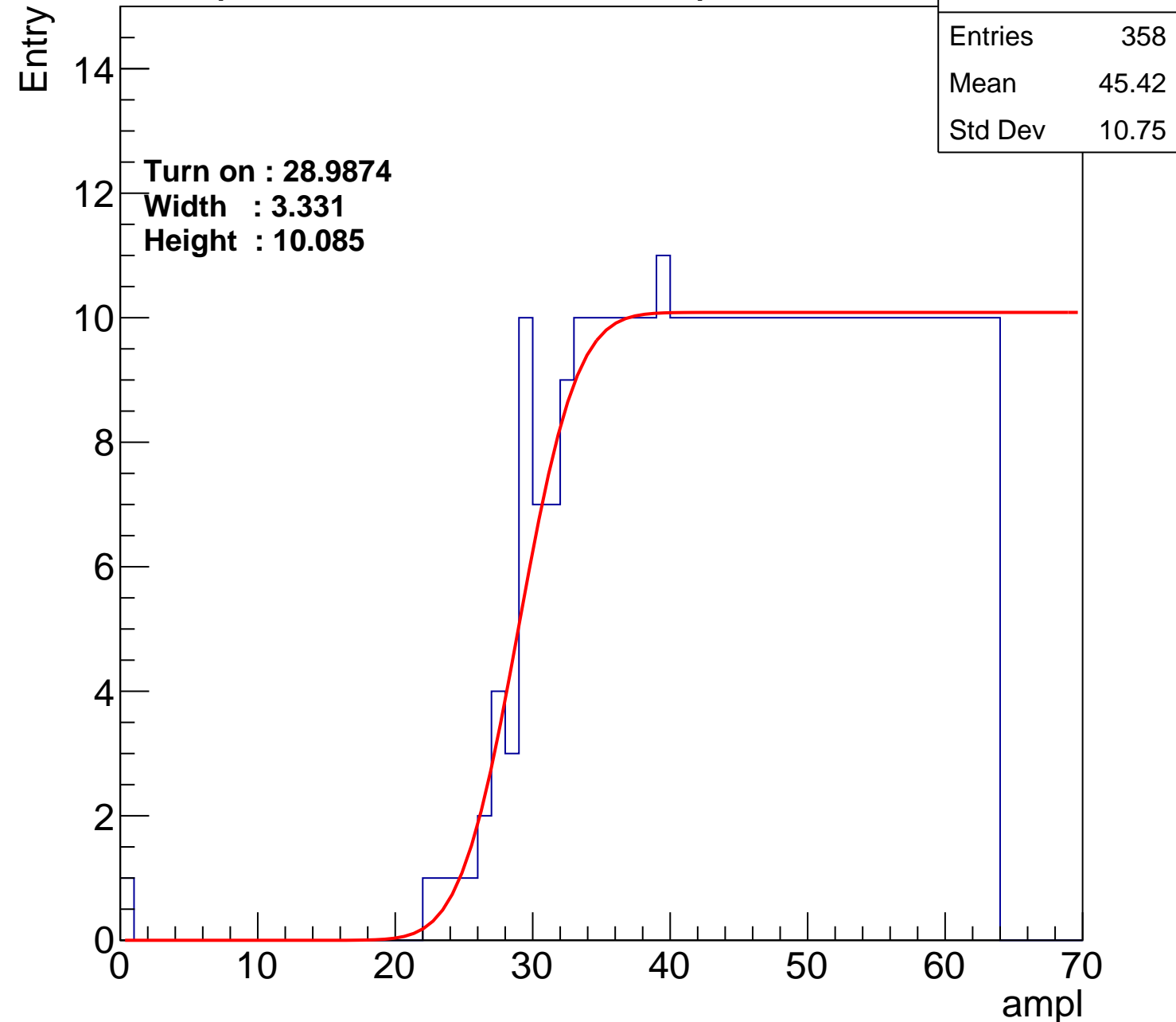
Width : 3.331

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch126

calib_packv5_042523_0143.root, FC#0, port D2

Entries	375
Mean	44.21
Std Dev	12.08

Turn on : 27.4492

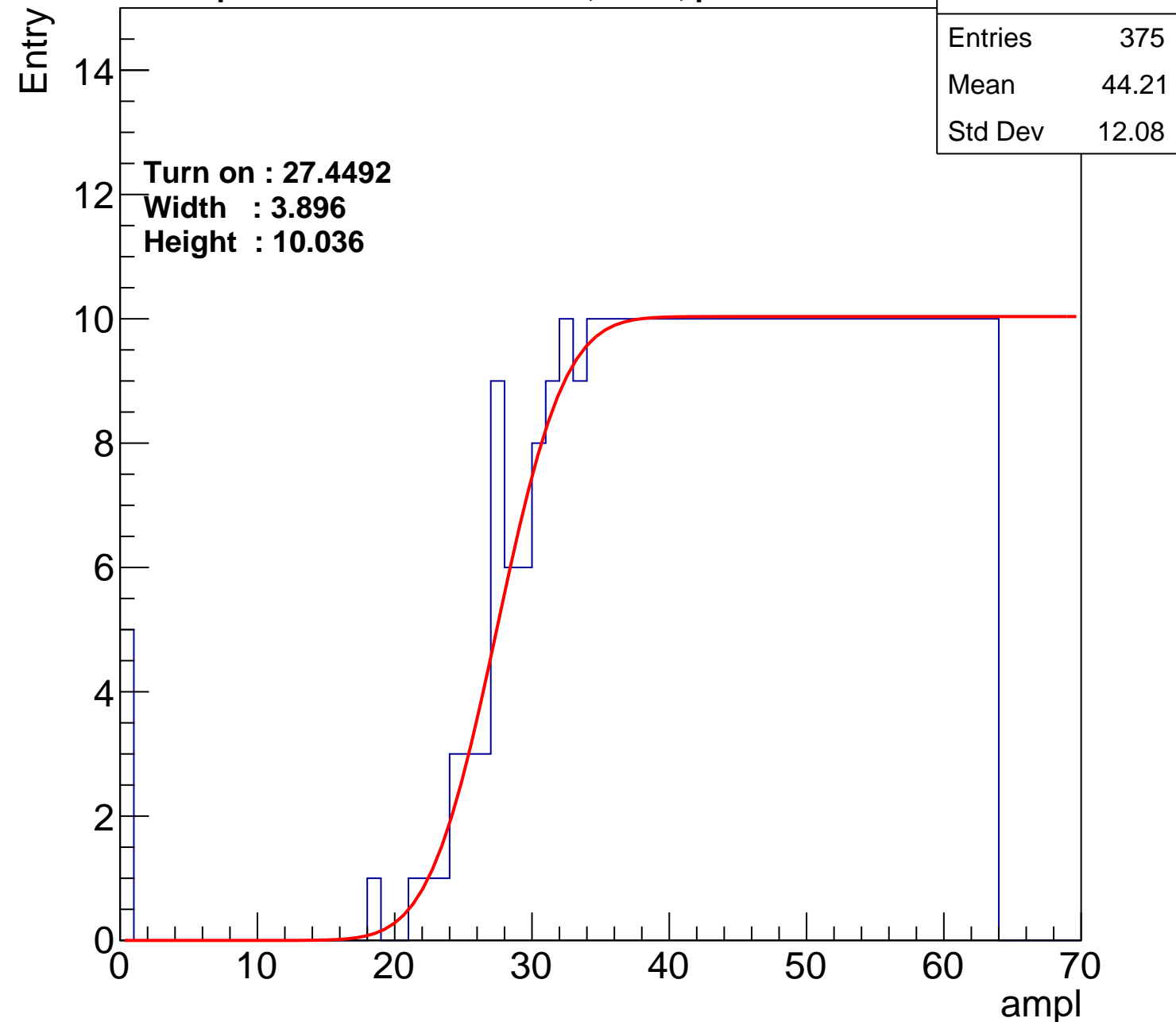
Width : 3.896

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch127

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.67
Std Dev	11.44

Turn on : 27.5522

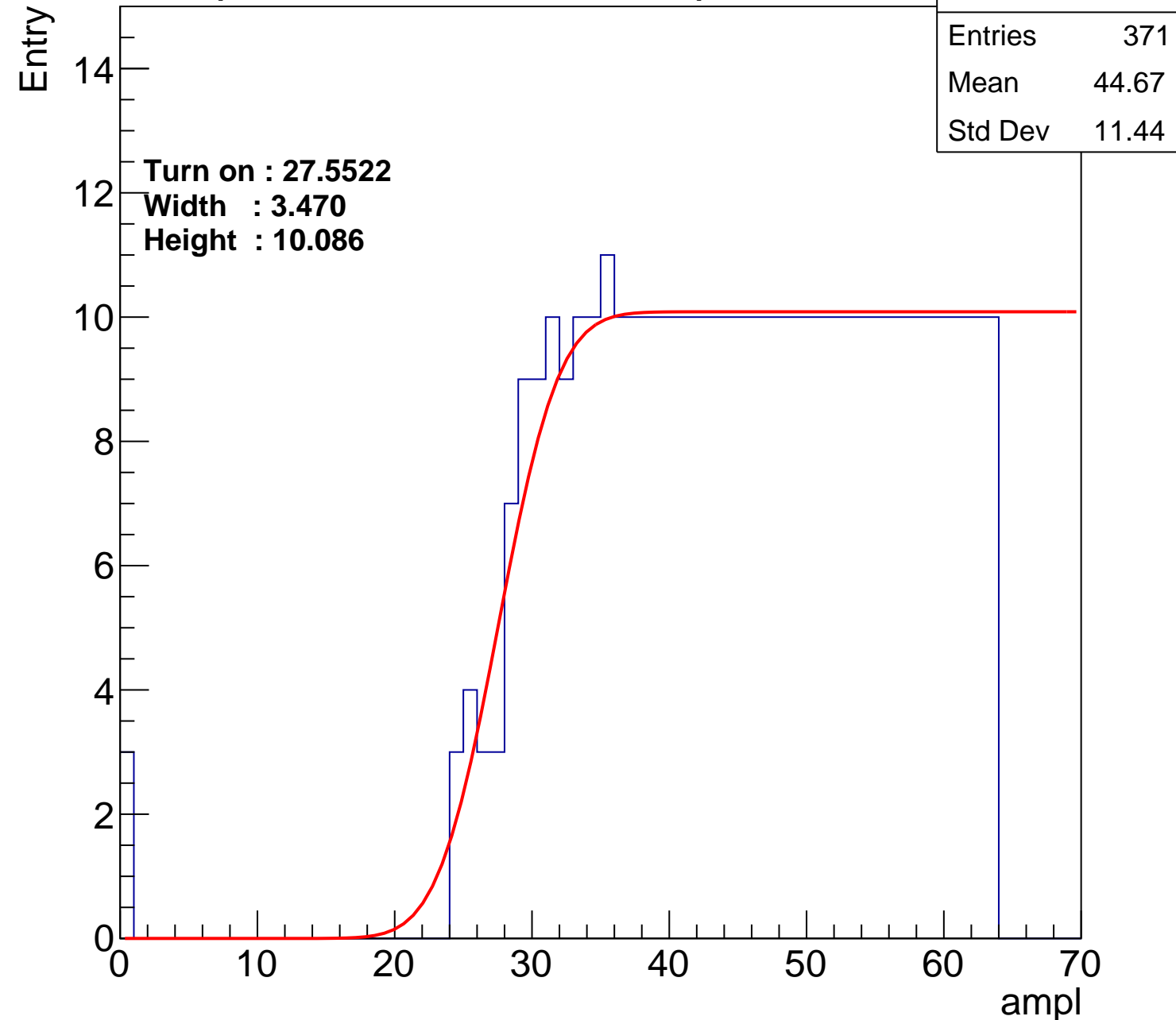
Width : 3.470

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L101S, U22-ch127

calib_packv5_042523_0143.root, FC#0, port D2

Entries	371
Mean	44.67
Std Dev	11.44

Turn on : 27.5522

Width : 3.470

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl

