

B0L002S, U10-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	44.03
Std Dev	11.49

Turn on : 25.6351

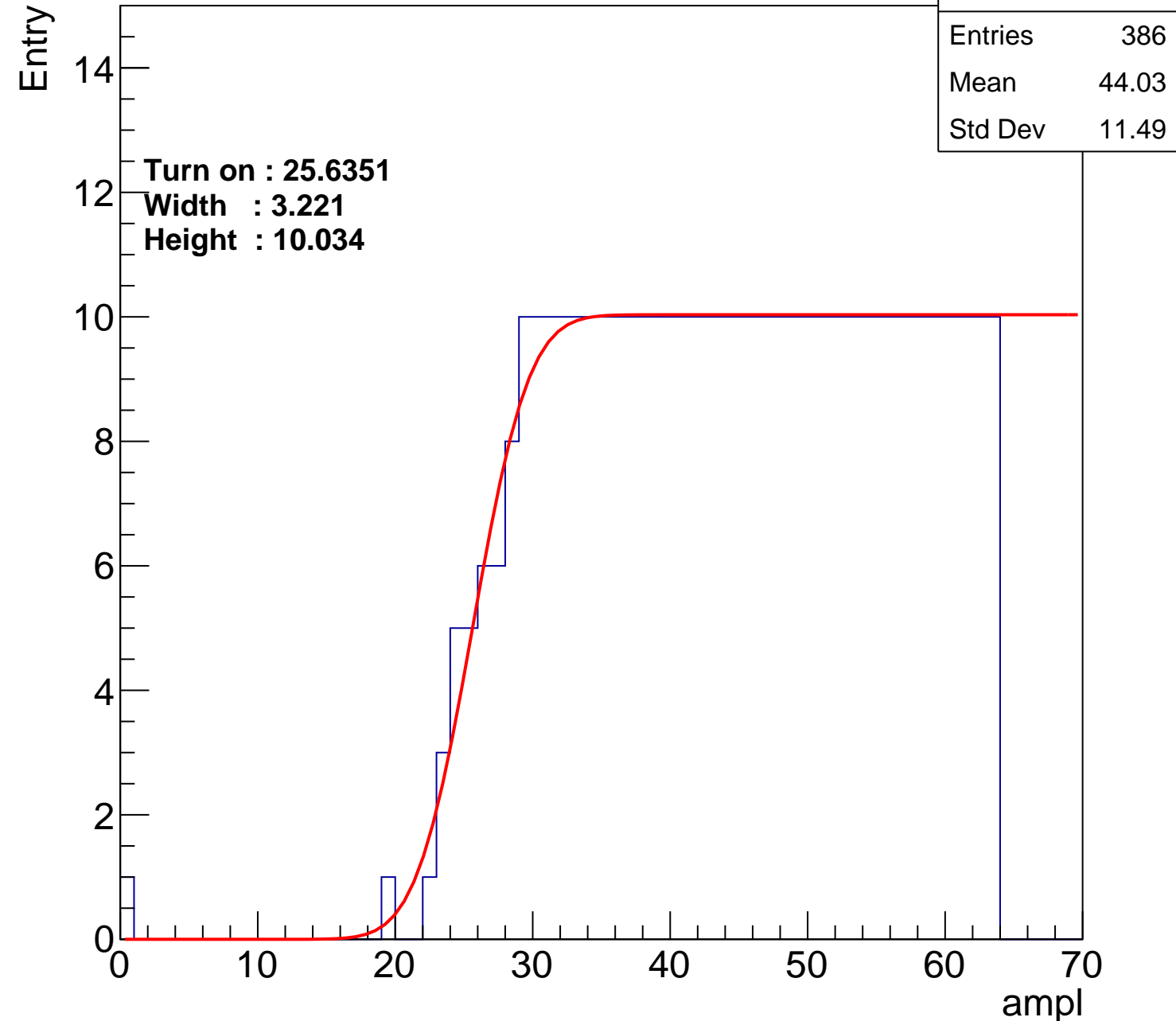
Width : 3.221

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.02
Std Dev	11.3

Turn on : 28.2472

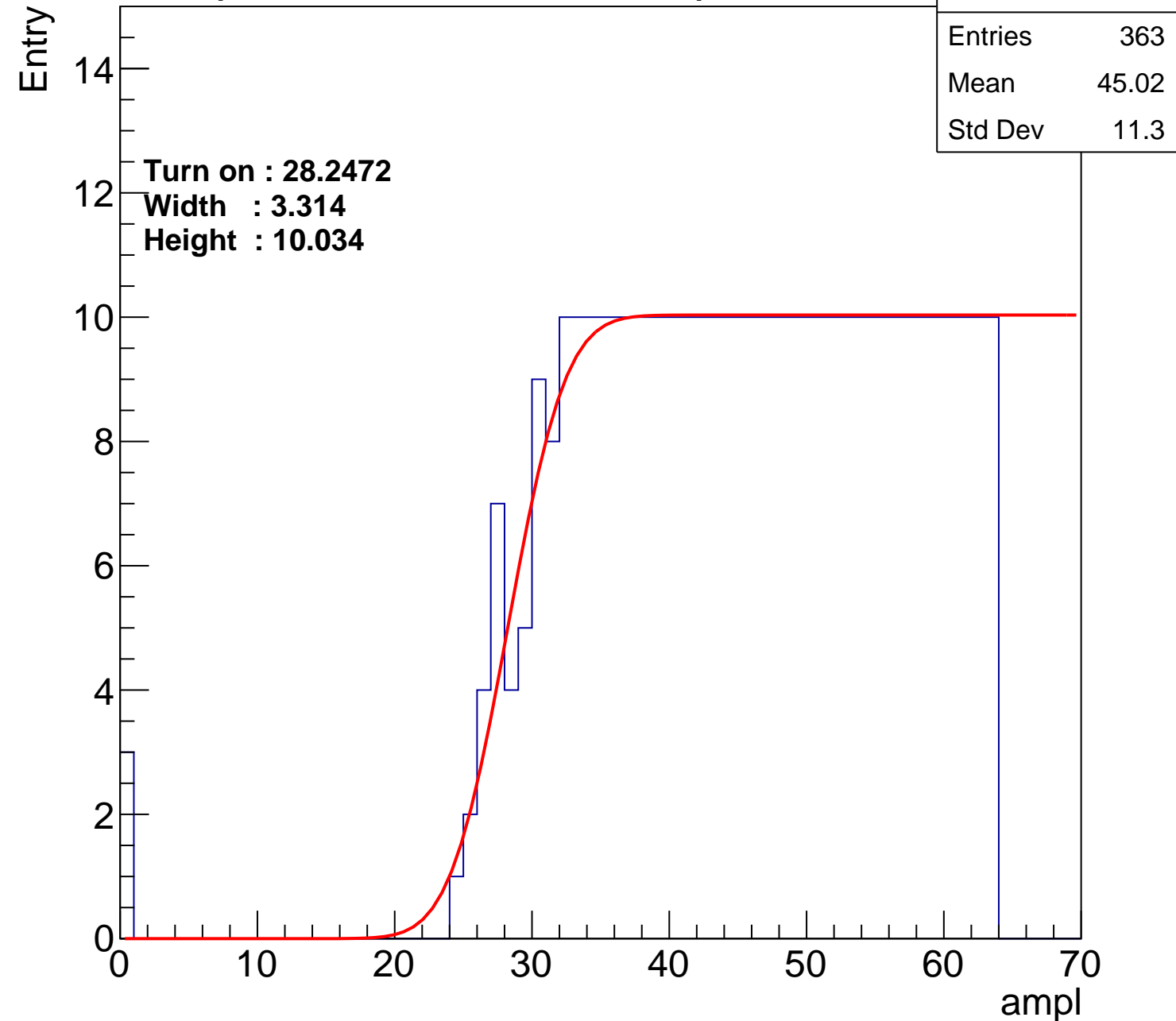
Width : 3.314

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 26.7276

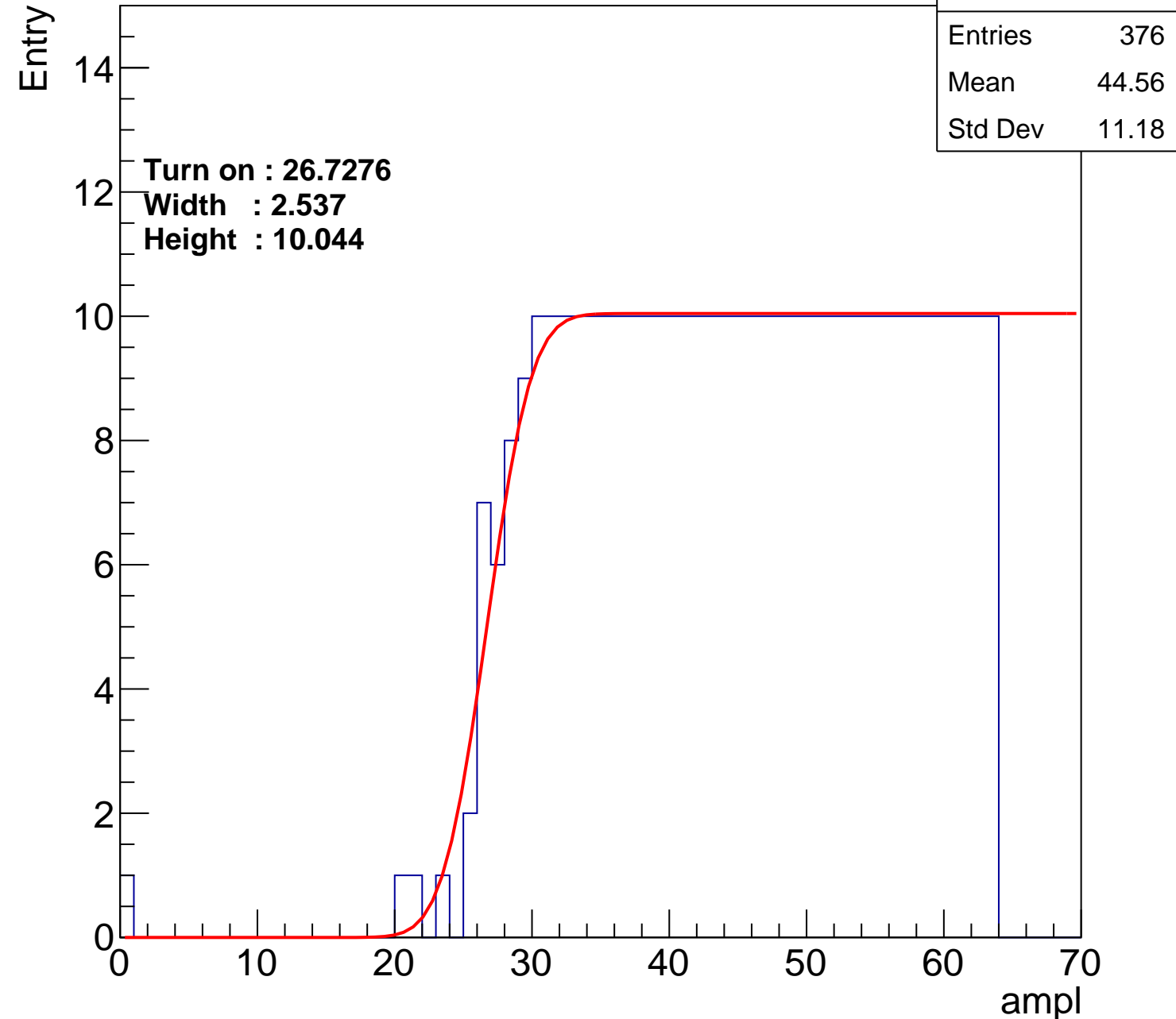
Width : 2.537

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.55
Std Dev	11.52

Turn on : 27.5046

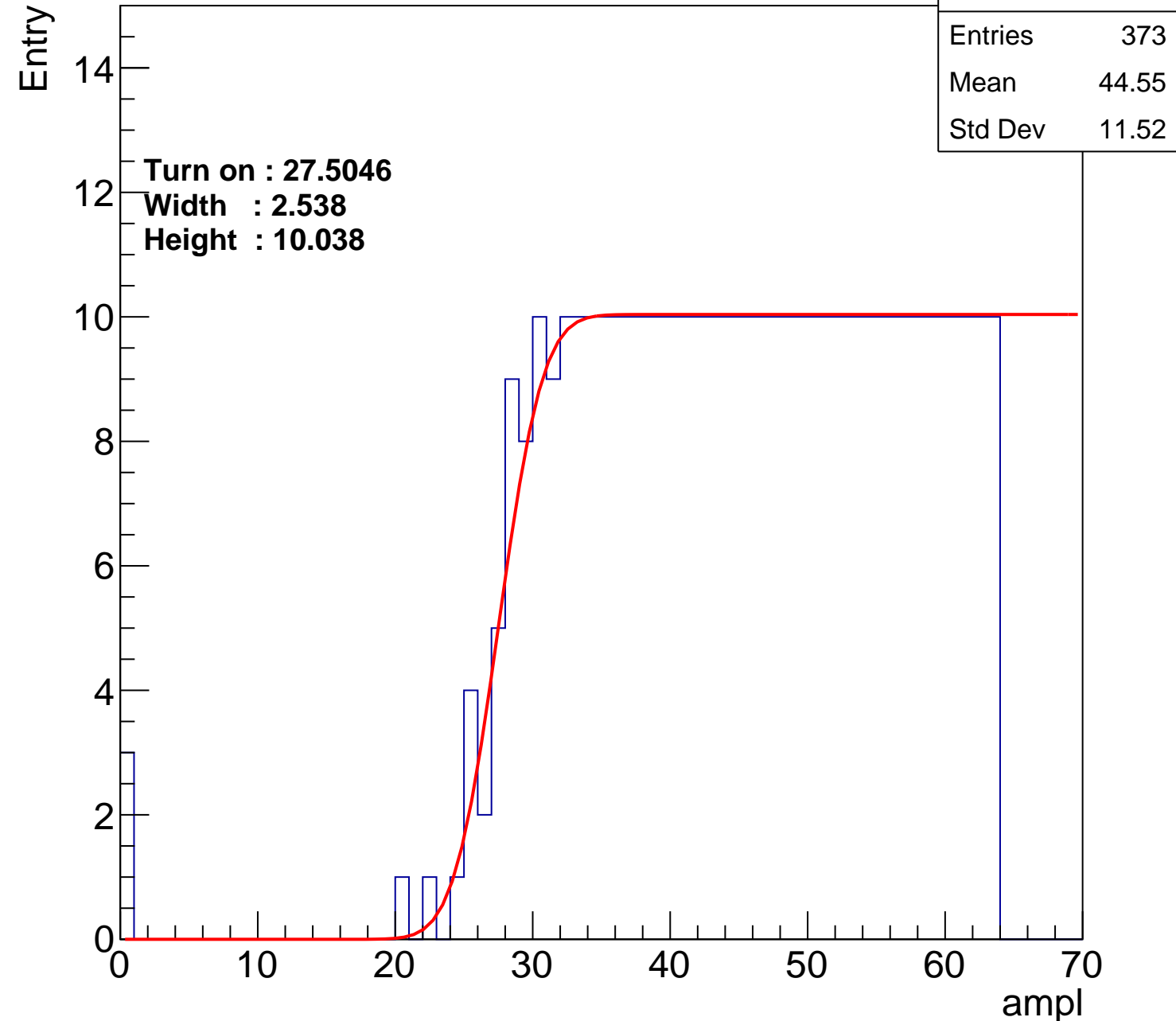
Width : 2.538

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.38
Std Dev	10.75

Turn on : 28.5418

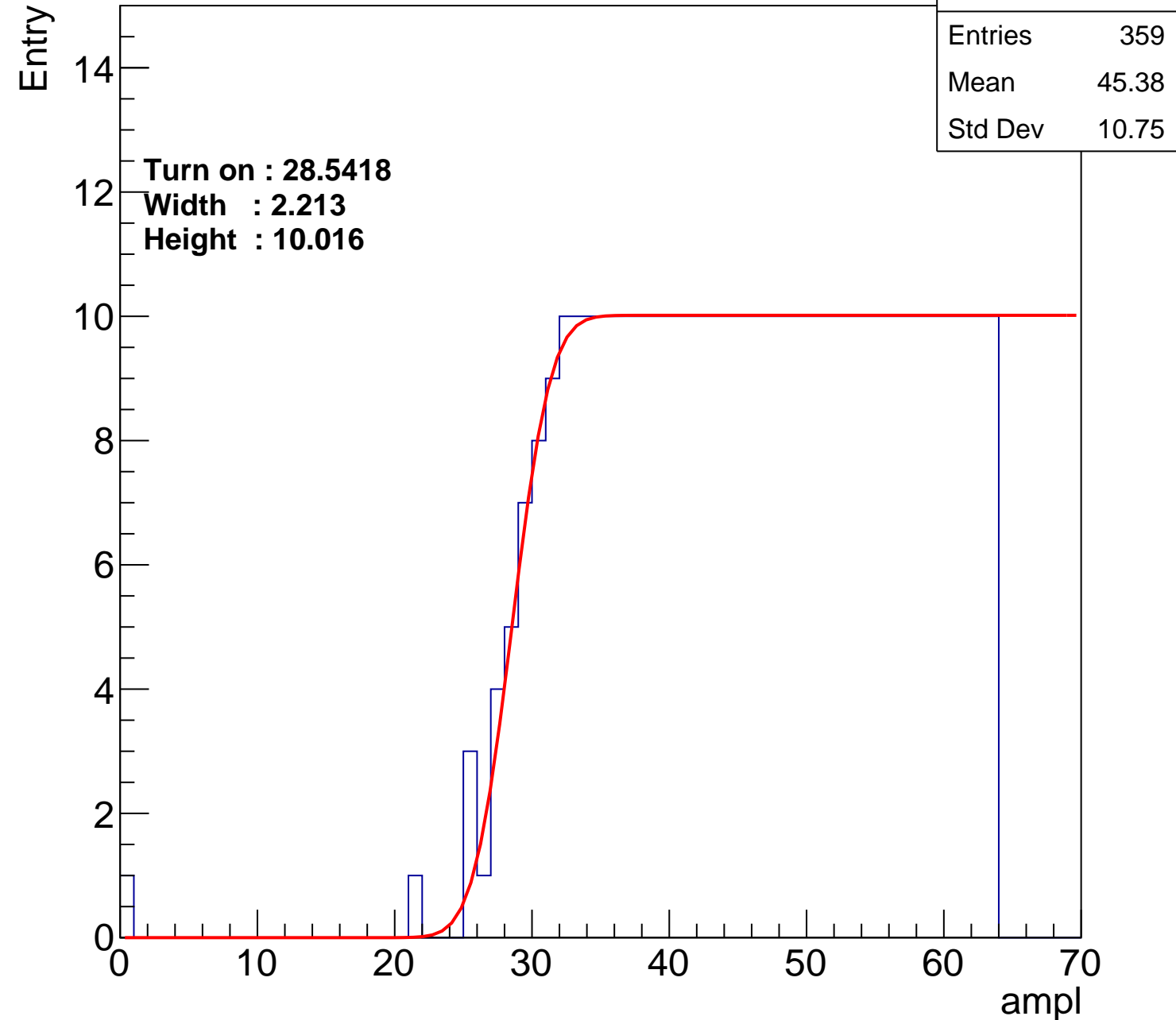
Width : 2.213

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch5

calib_packv5_042523_0143.root, FC#8, port C1

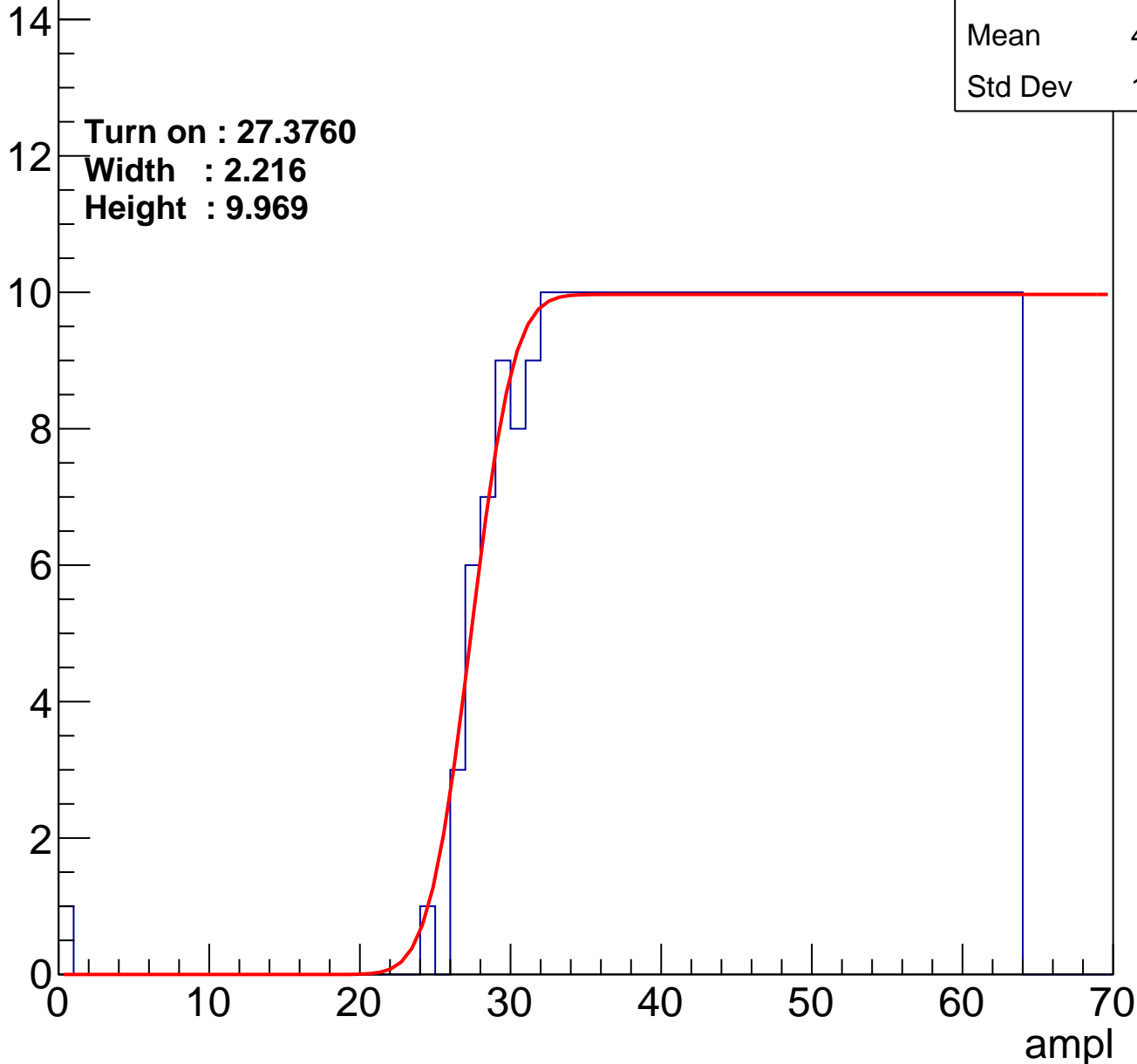
Entry

Entries	364
Mean	45.16
Std Dev	10.83

Turn on : 27.3760

Width : 2.216

Height : 9.969



B0L002S, U10-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.4
Std Dev	10.71

Turn on : 28.3545

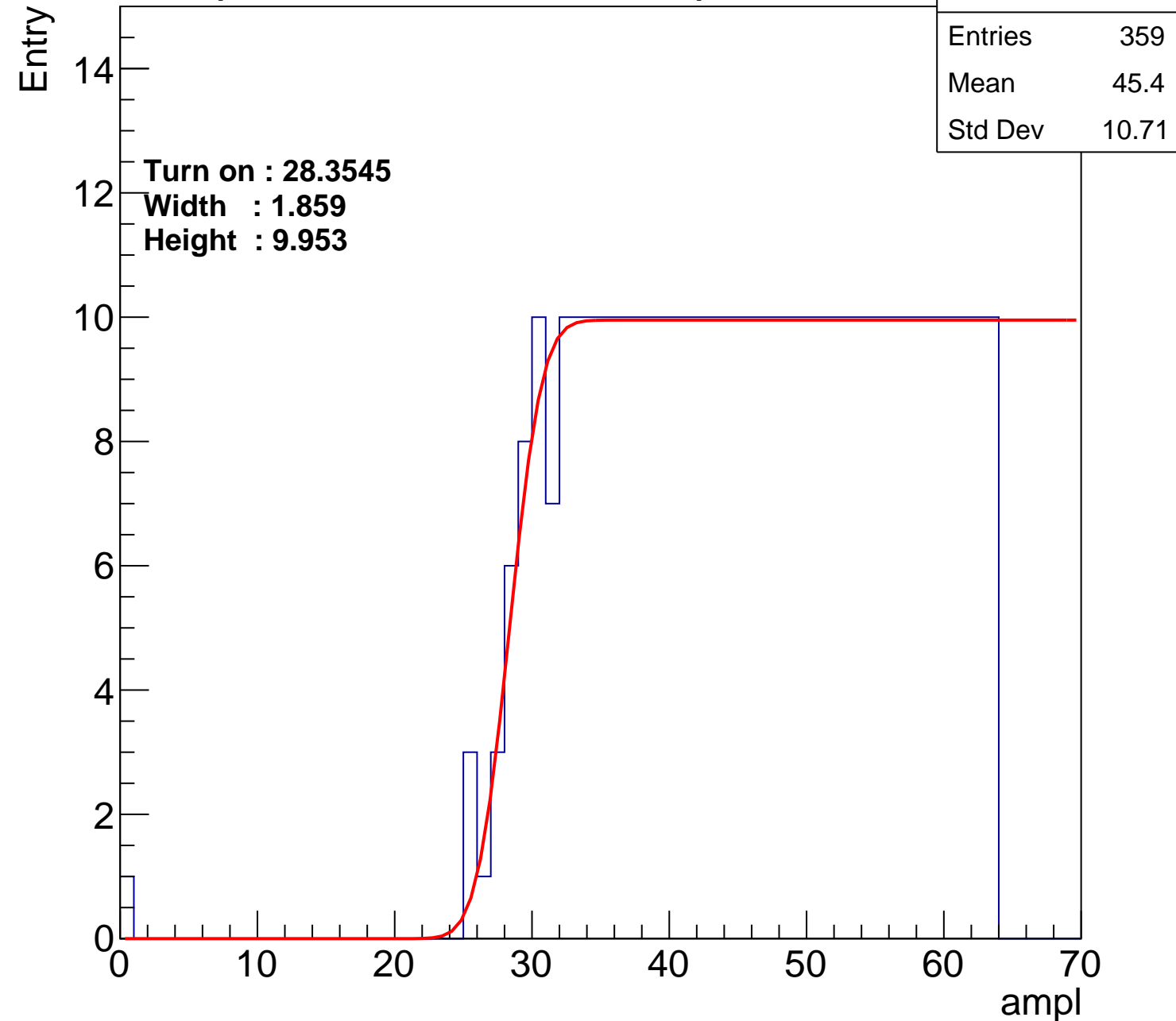
Width : 1.859

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.55
Std Dev	11.71

Turn on : 27.8054

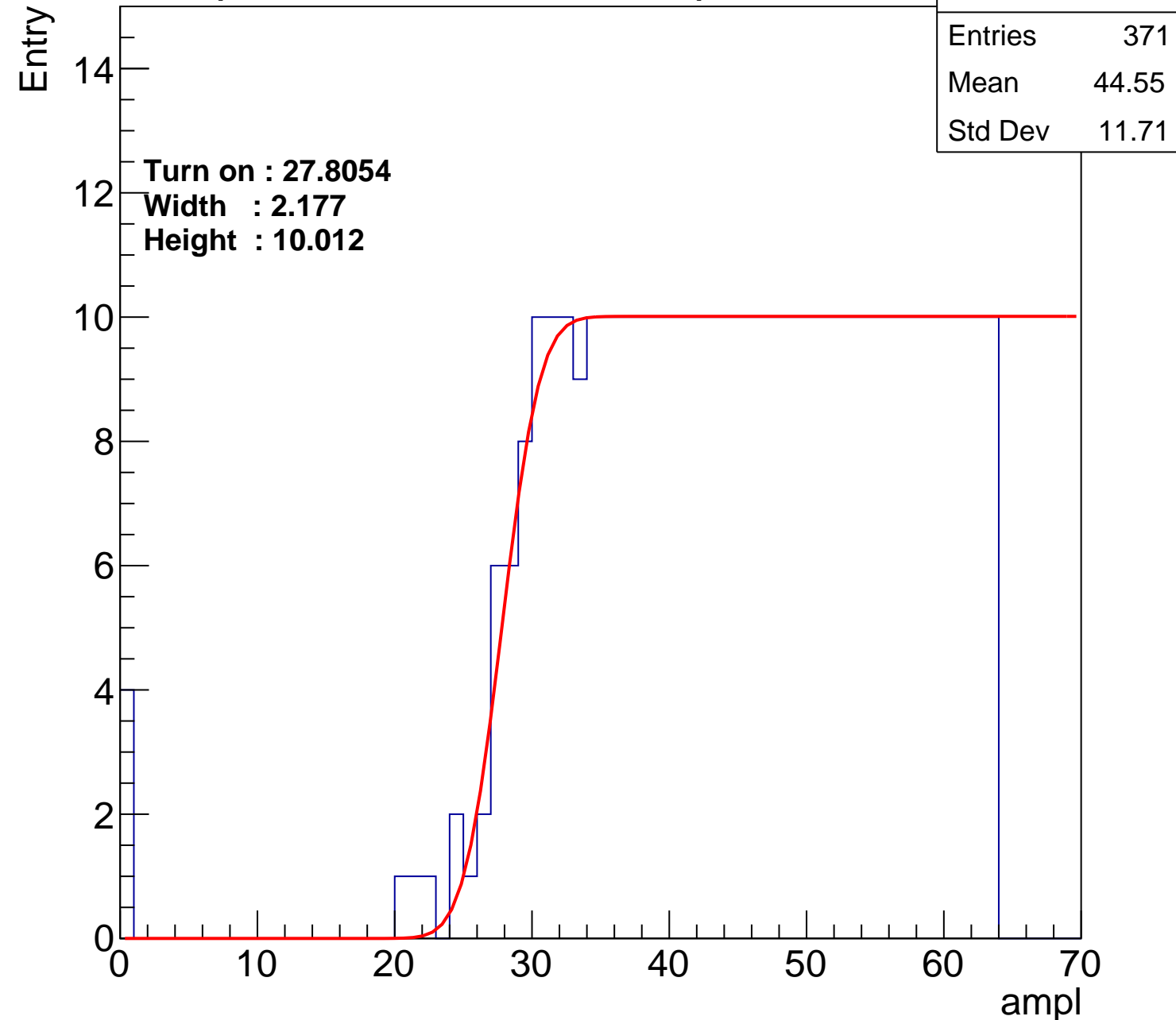
Width : 2.177

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.09
Std Dev	10.79

Turn on : 30.5919

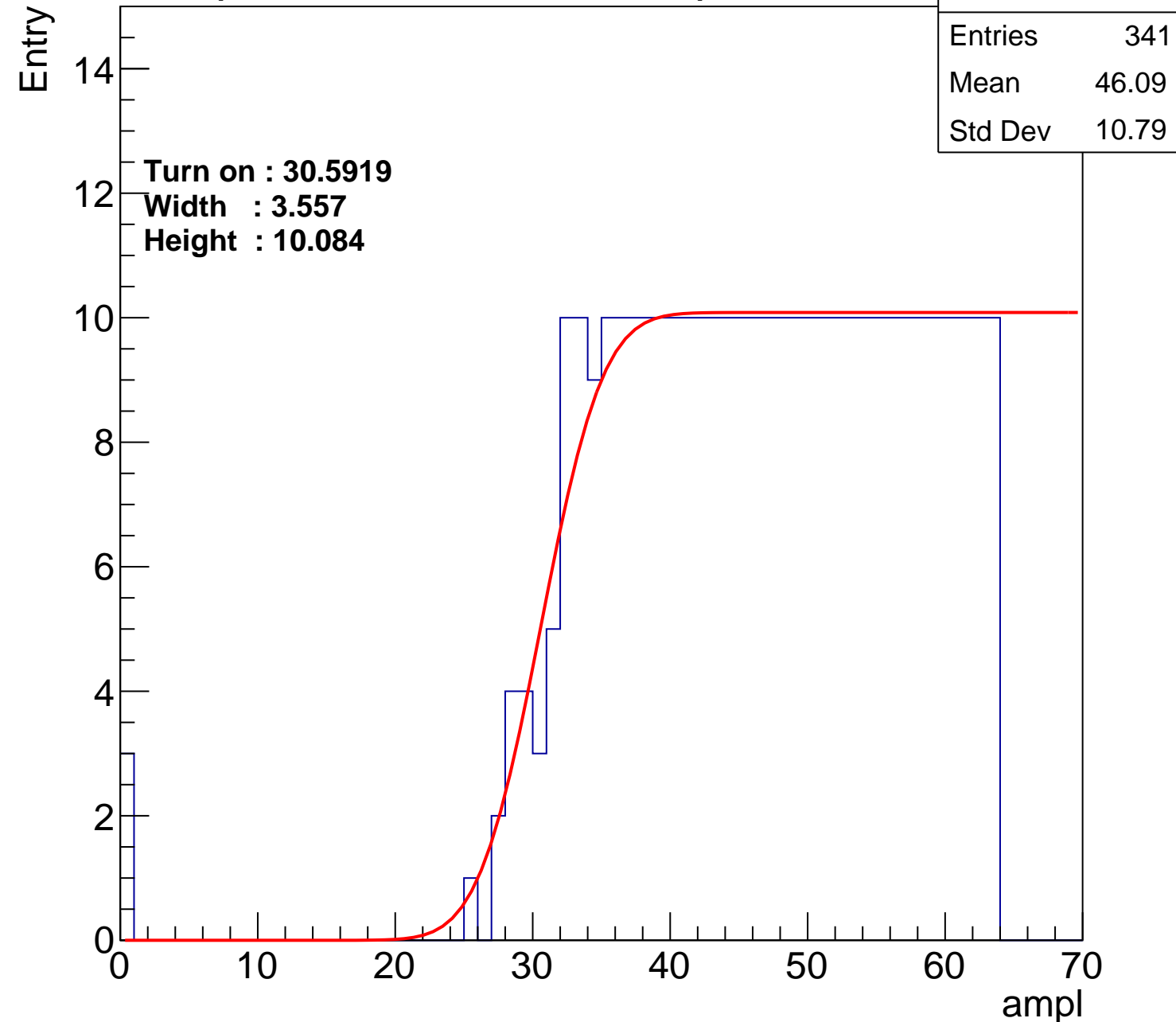
Width : 3.557

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.79
Std Dev	11.07

Turn on : 27.5277

Width : 3.188

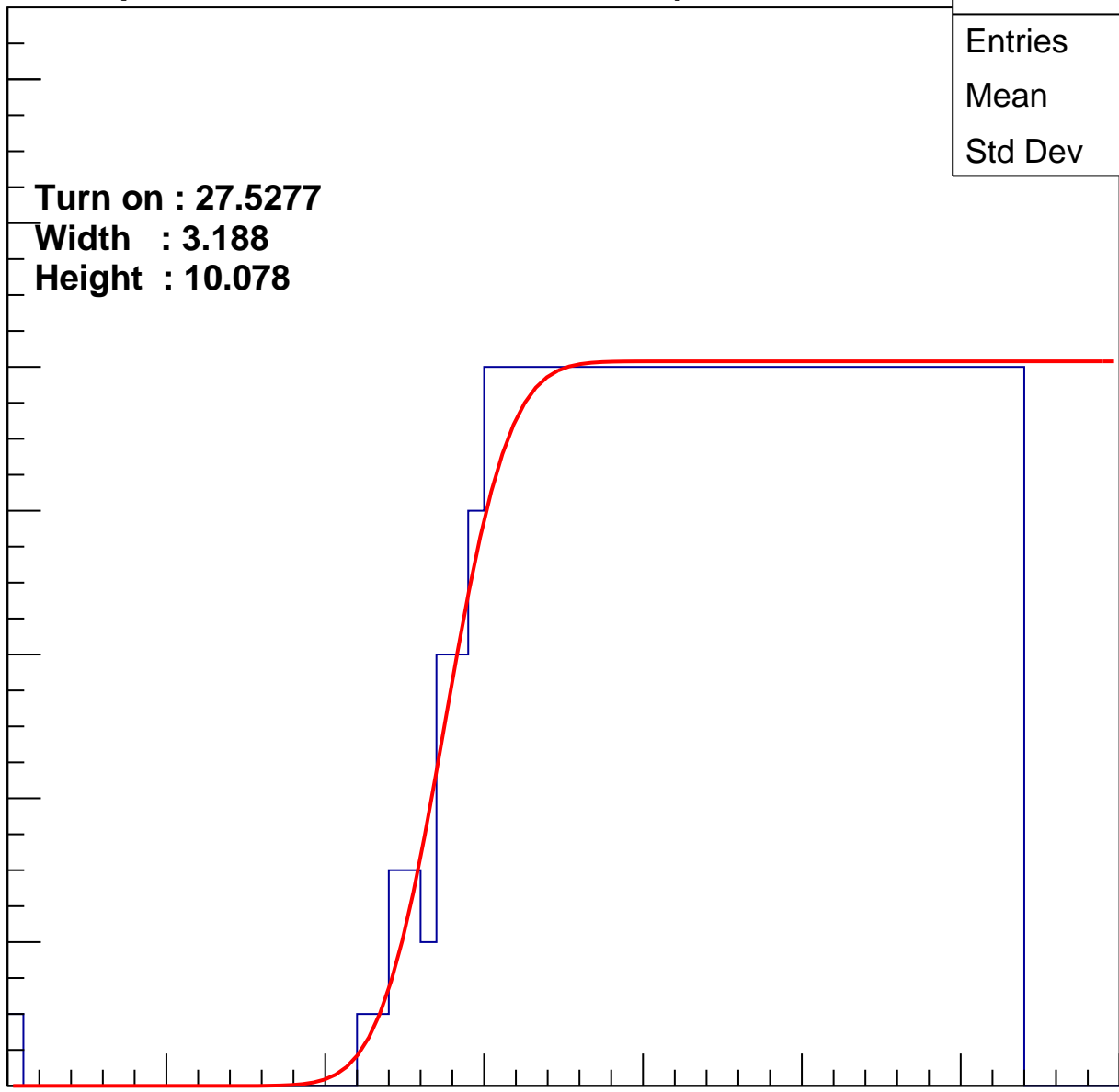
Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U10-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.51
Std Dev	11.44

Turn on : 27.2867

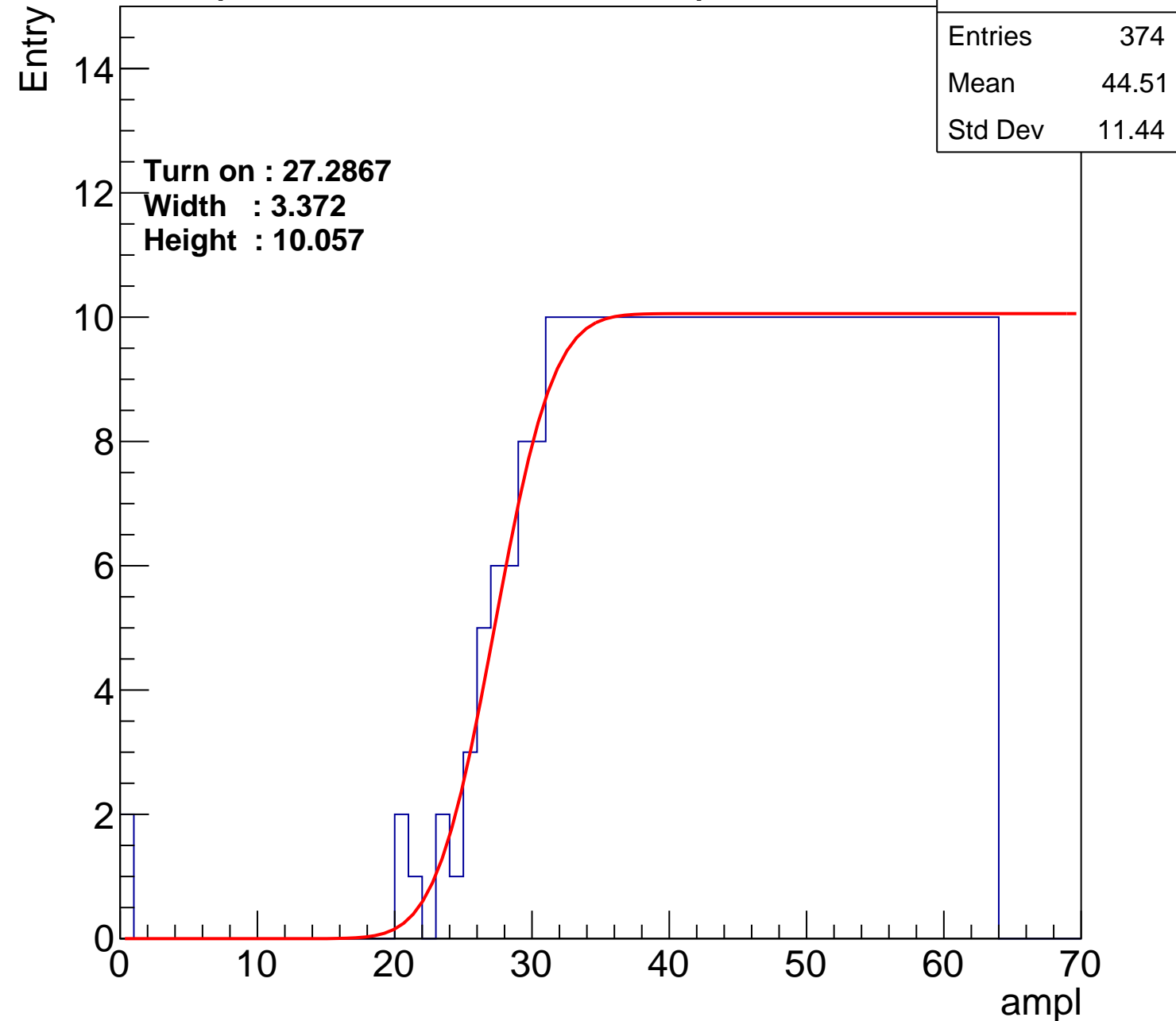
Width : 3.372

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.03
Std Dev	11.29

Turn on : 28.5961

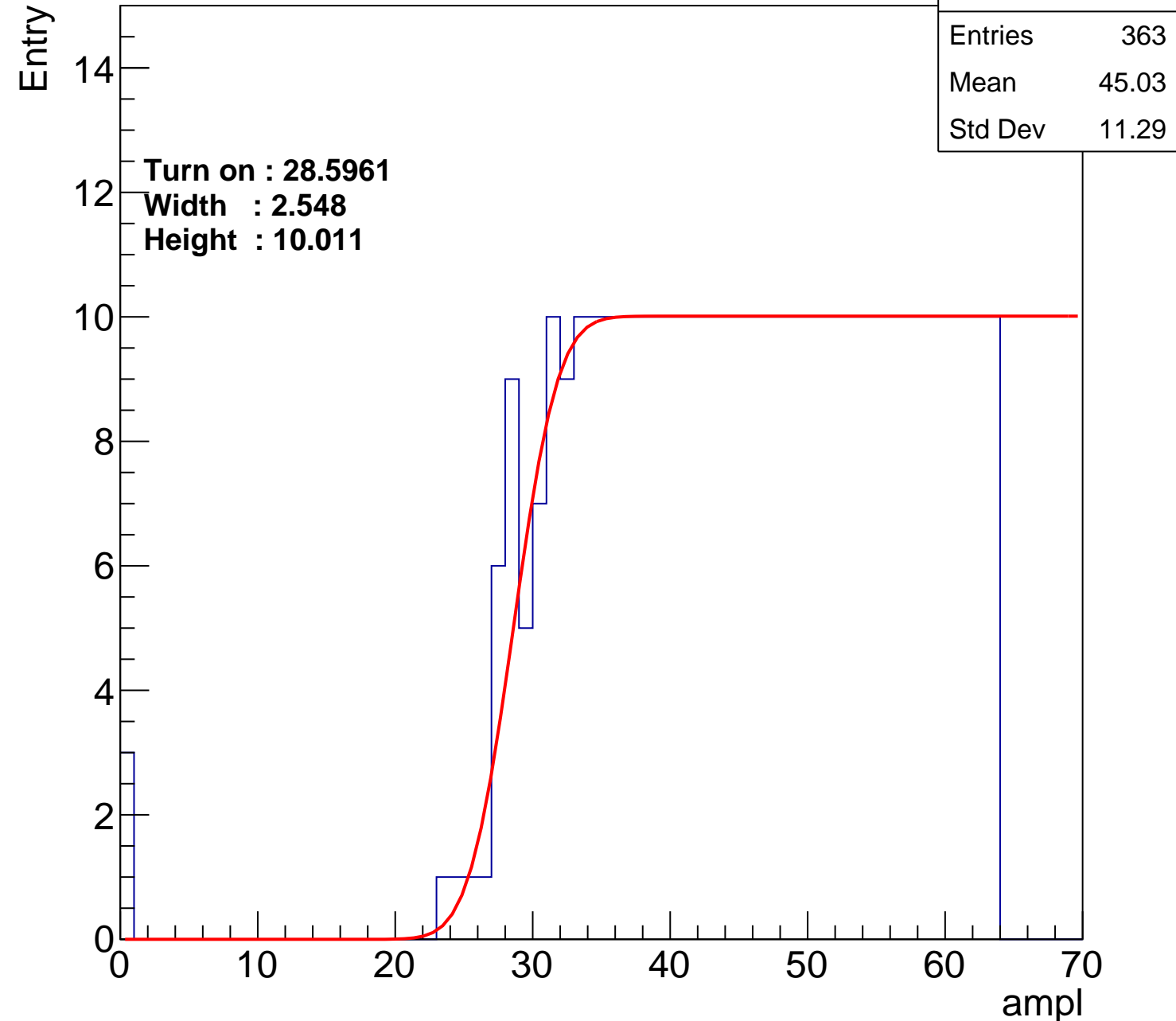
Width : 2.548

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.54
Std Dev	11.52

Turn on : 27.3169

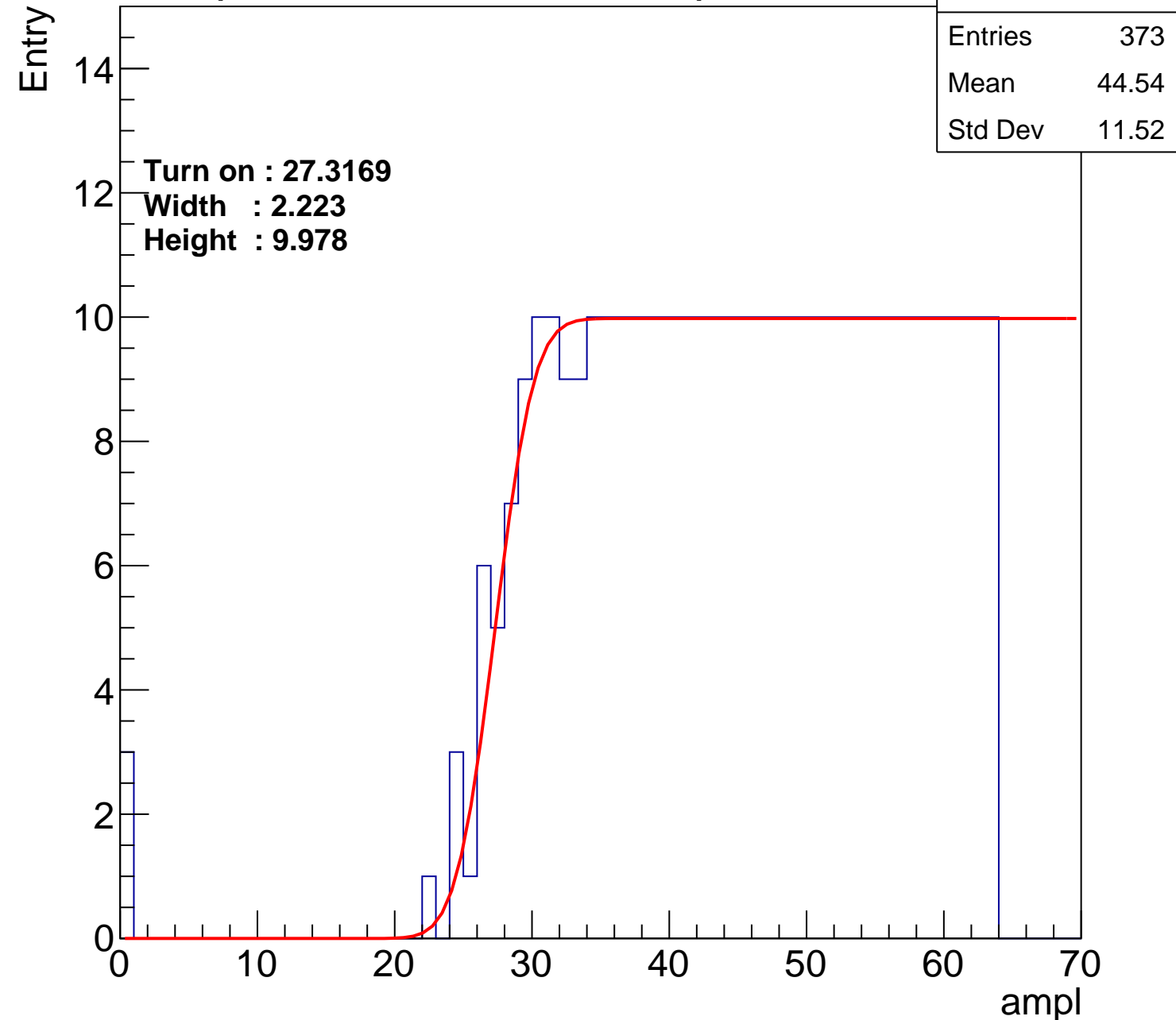
Width : 2.223

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.1461
Width : 2.694
Height : 9.977

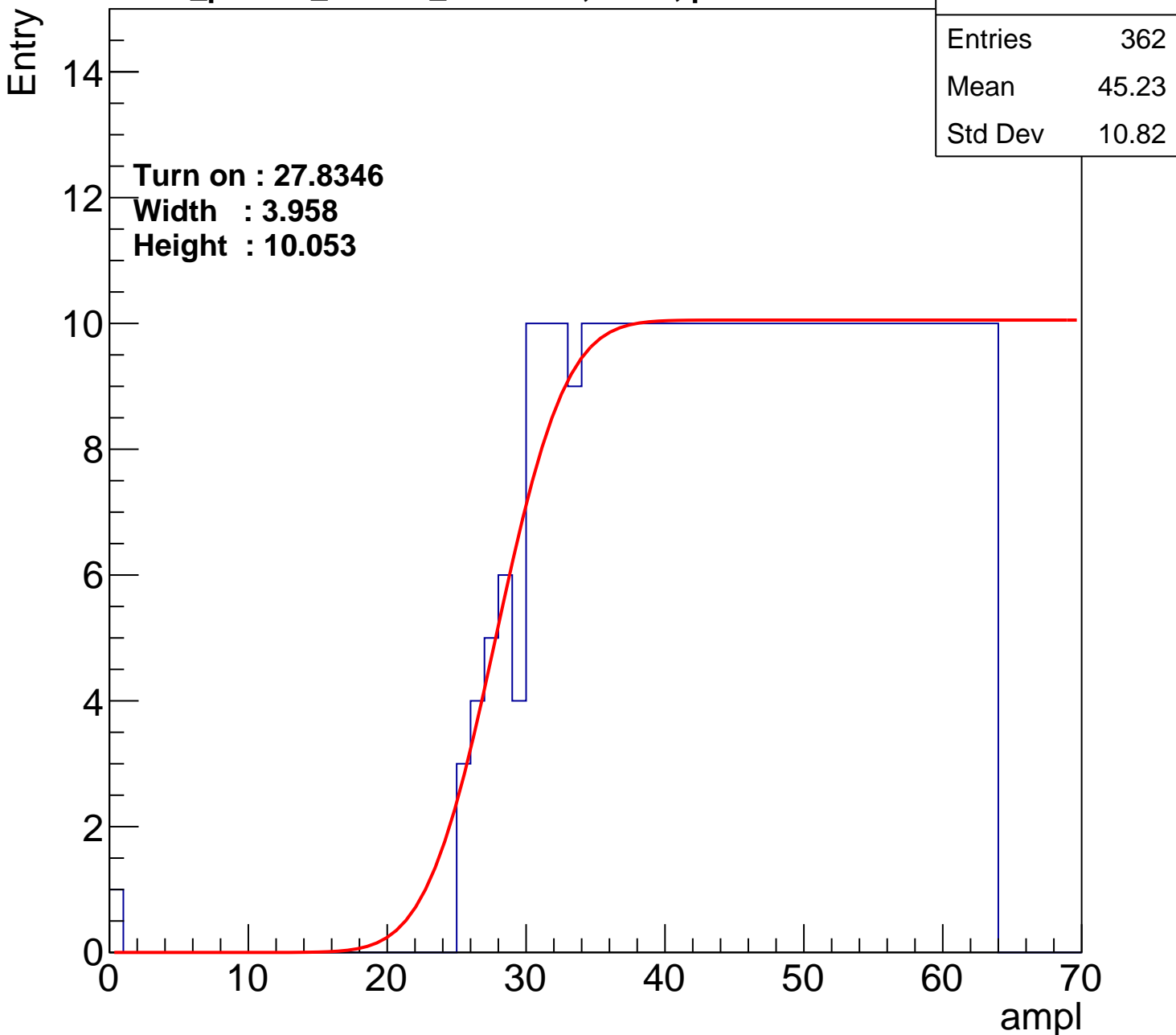


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.82

Height : 10.053



B0L002S, U10-ch15

calib_packv5_042523_0143.root, FC#8, port C1

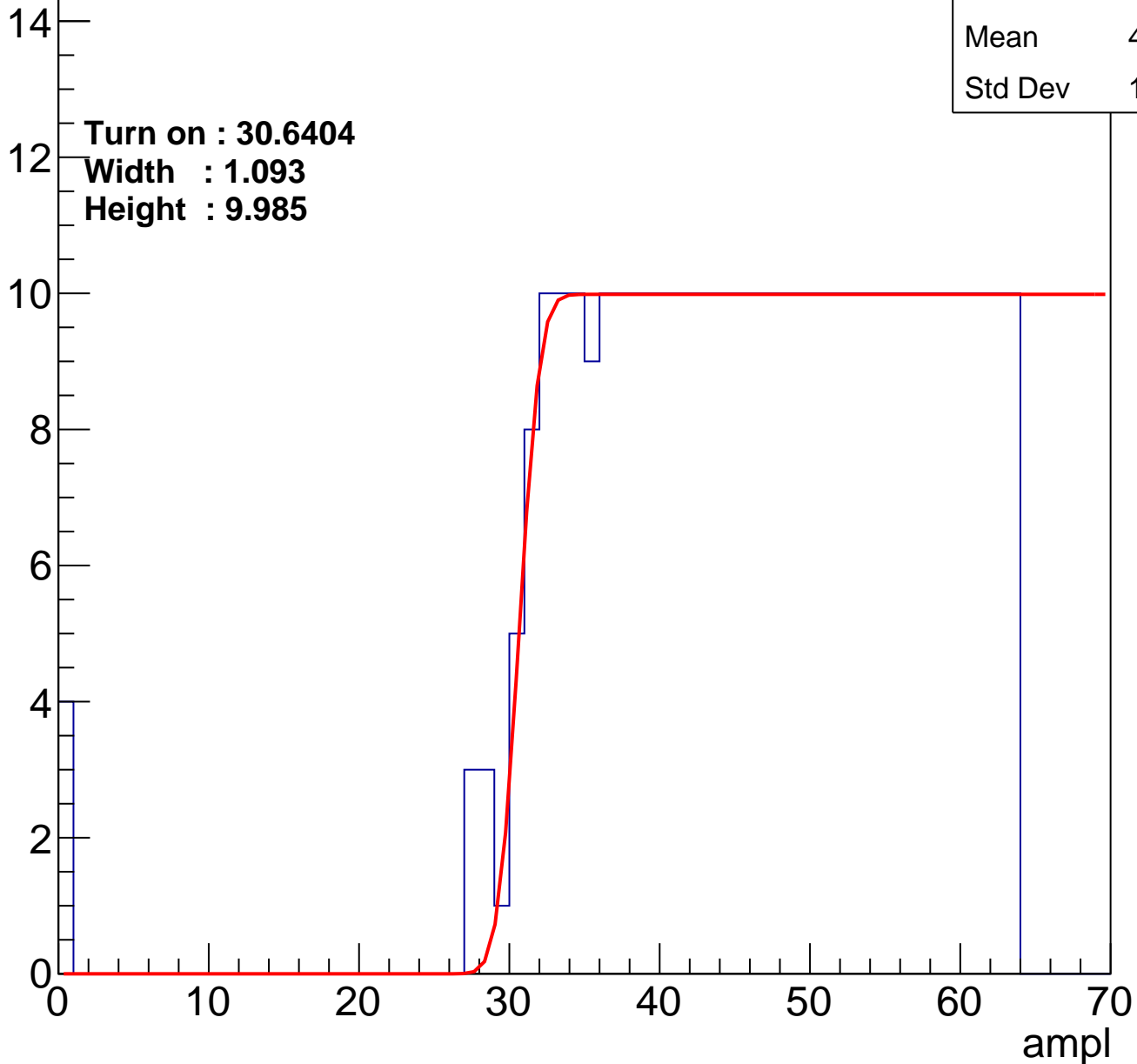
Entries	343
Mean	45.94
Std Dev	11.03

Turn on : 30.6404

Width : 1.093

Height : 9.985

Entry



B0L002S, U10-ch16

calib_packv5_042523_0143.root, FC#8, port C1

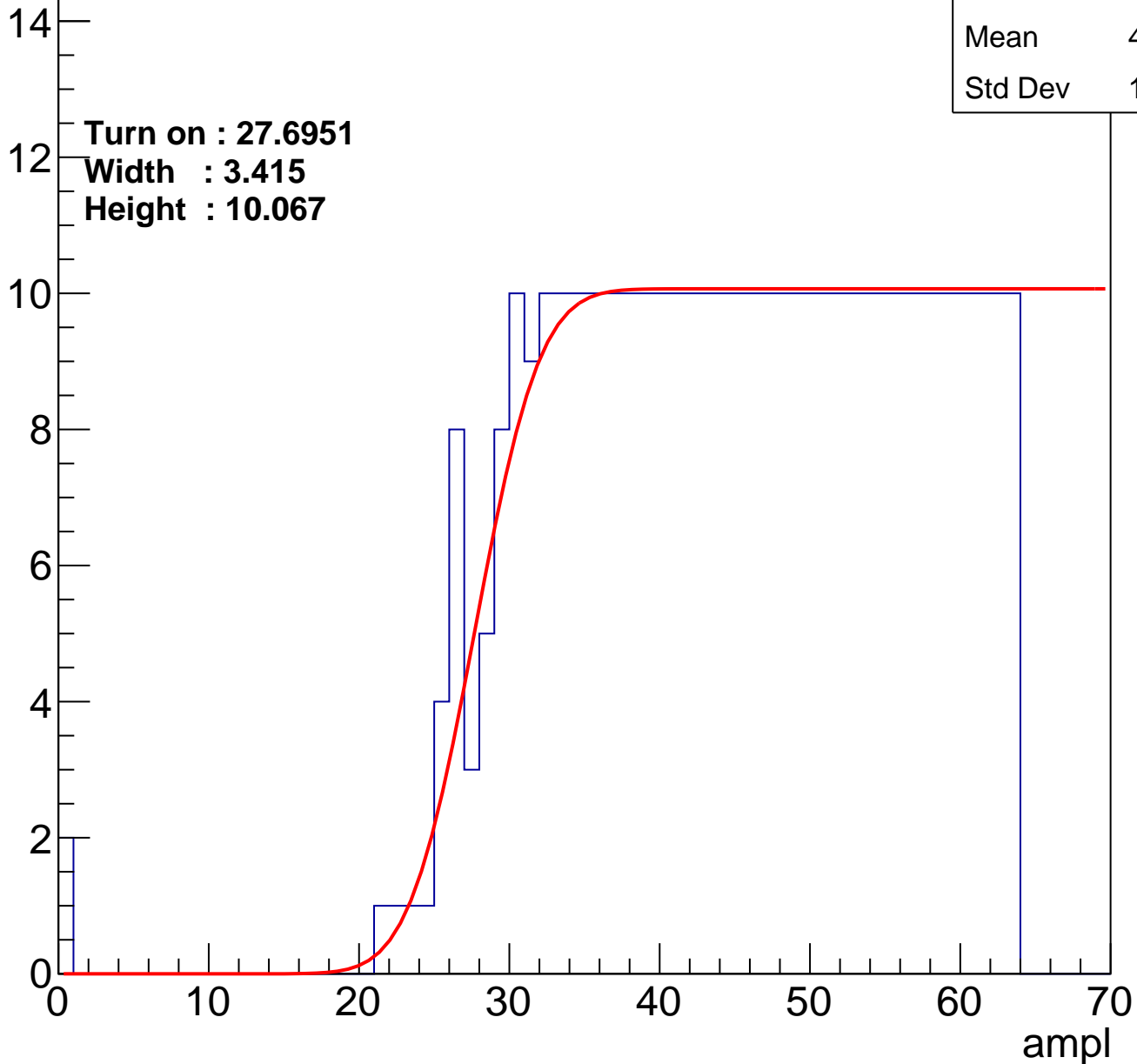
Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 27.6951

Width : 3.415

Height : 10.067

Entry



B0L002S, U10-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.1828

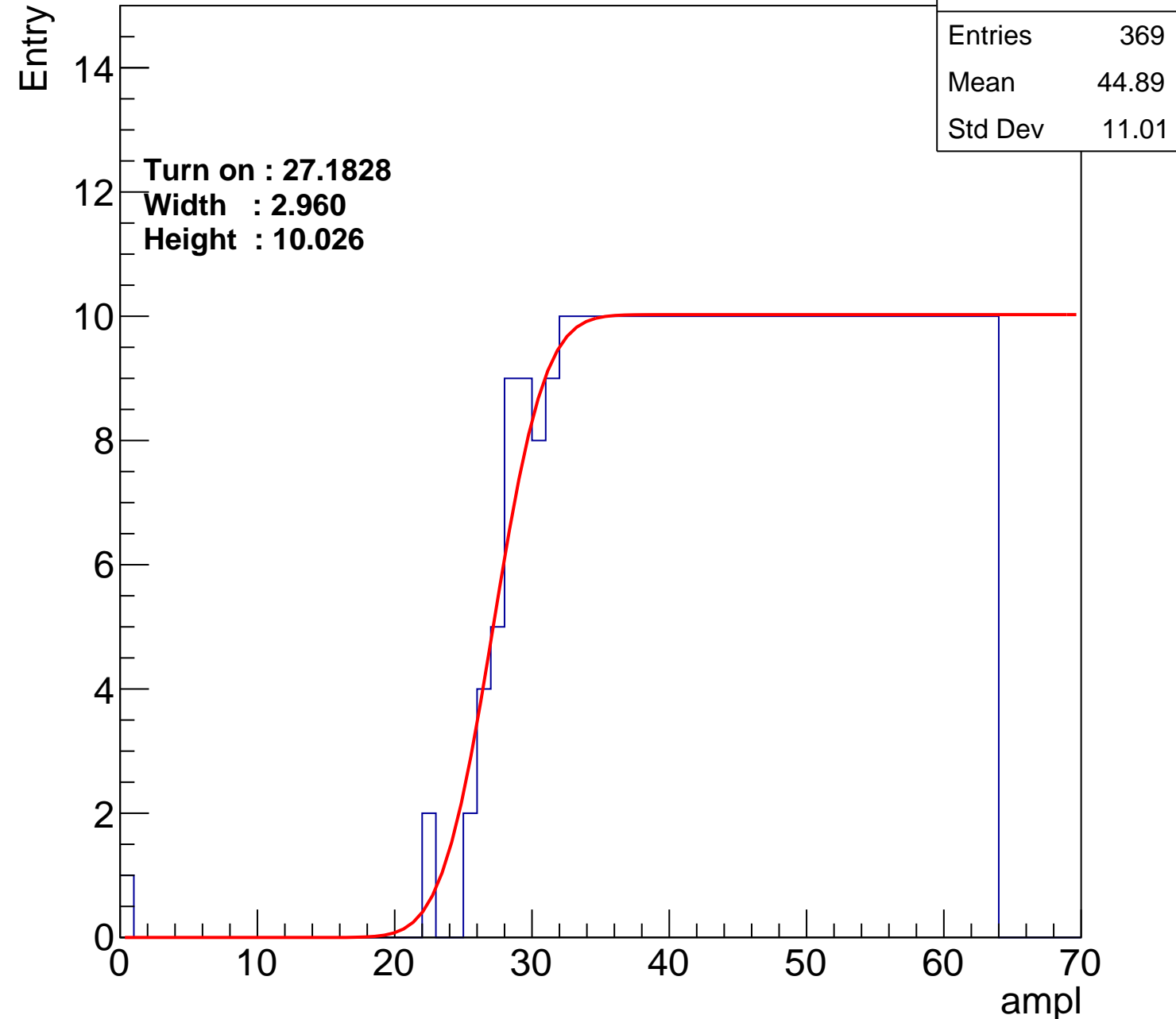
Width : 2.960

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.22
Std Dev	11.05

Turn on : 28.5737

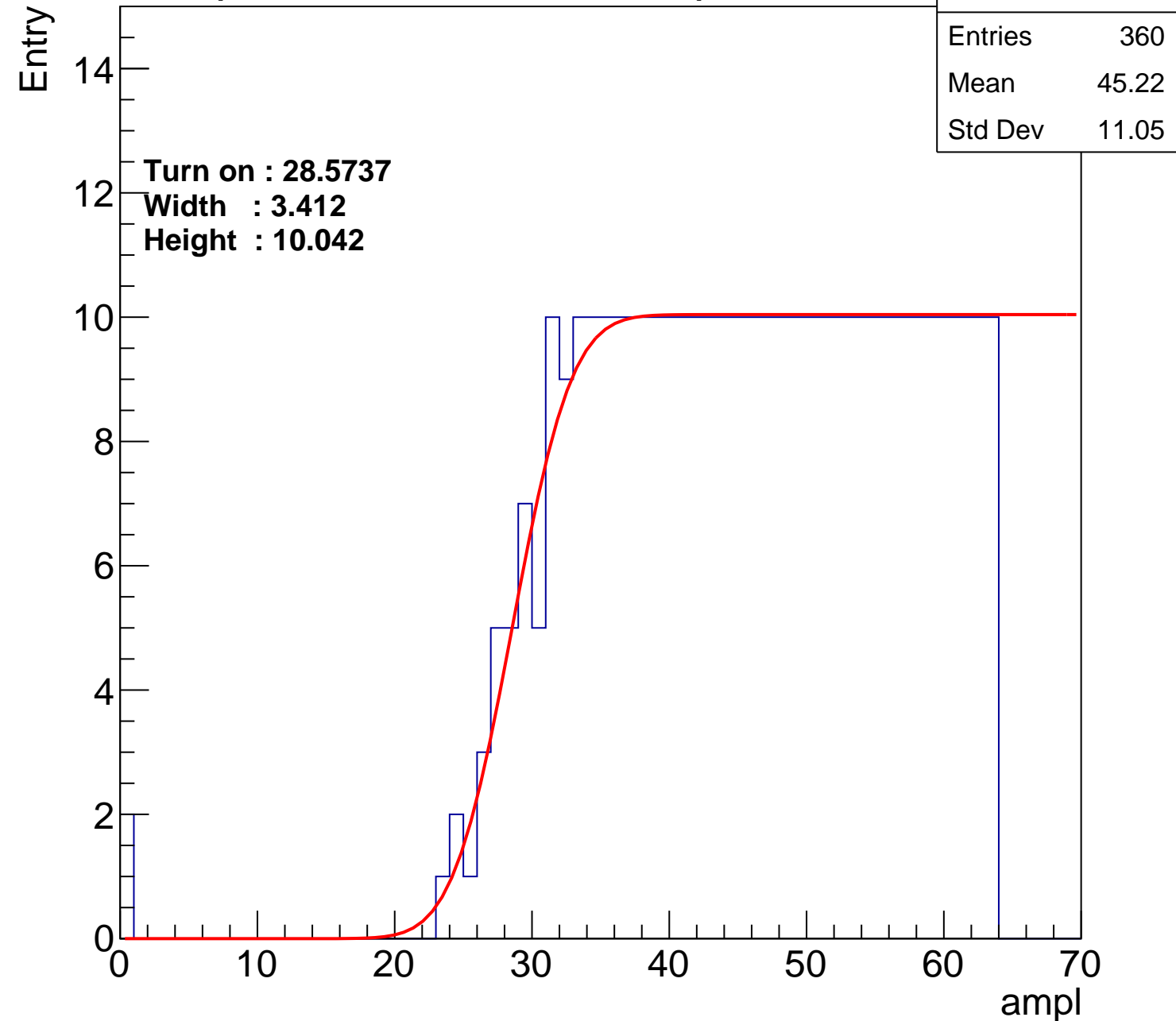
Width : 3.412

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.84
Std Dev	10.48

Turn on : 29.5214

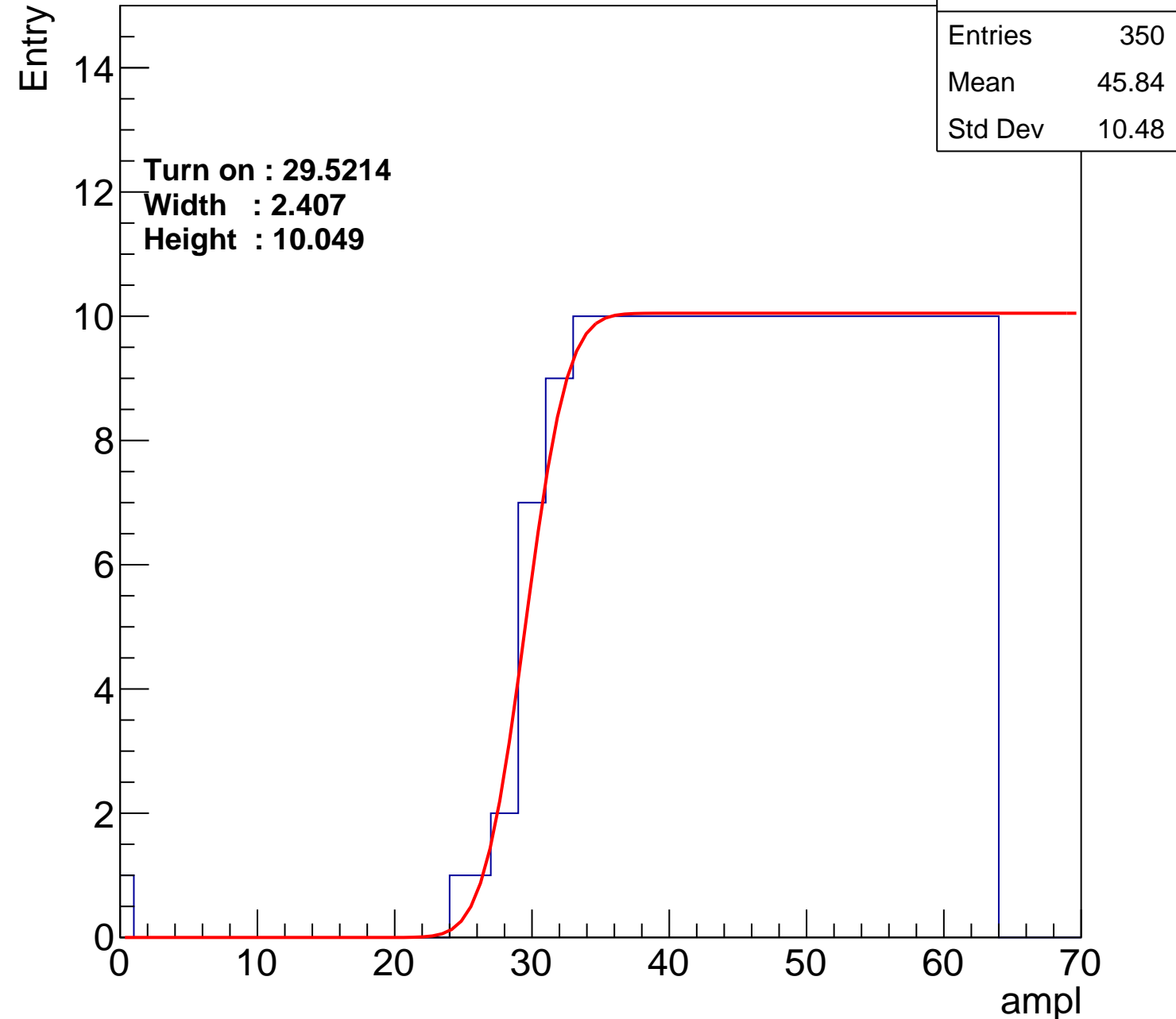
Width : 2.407

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.58
Std Dev	10.79

Turn on : 28.7279

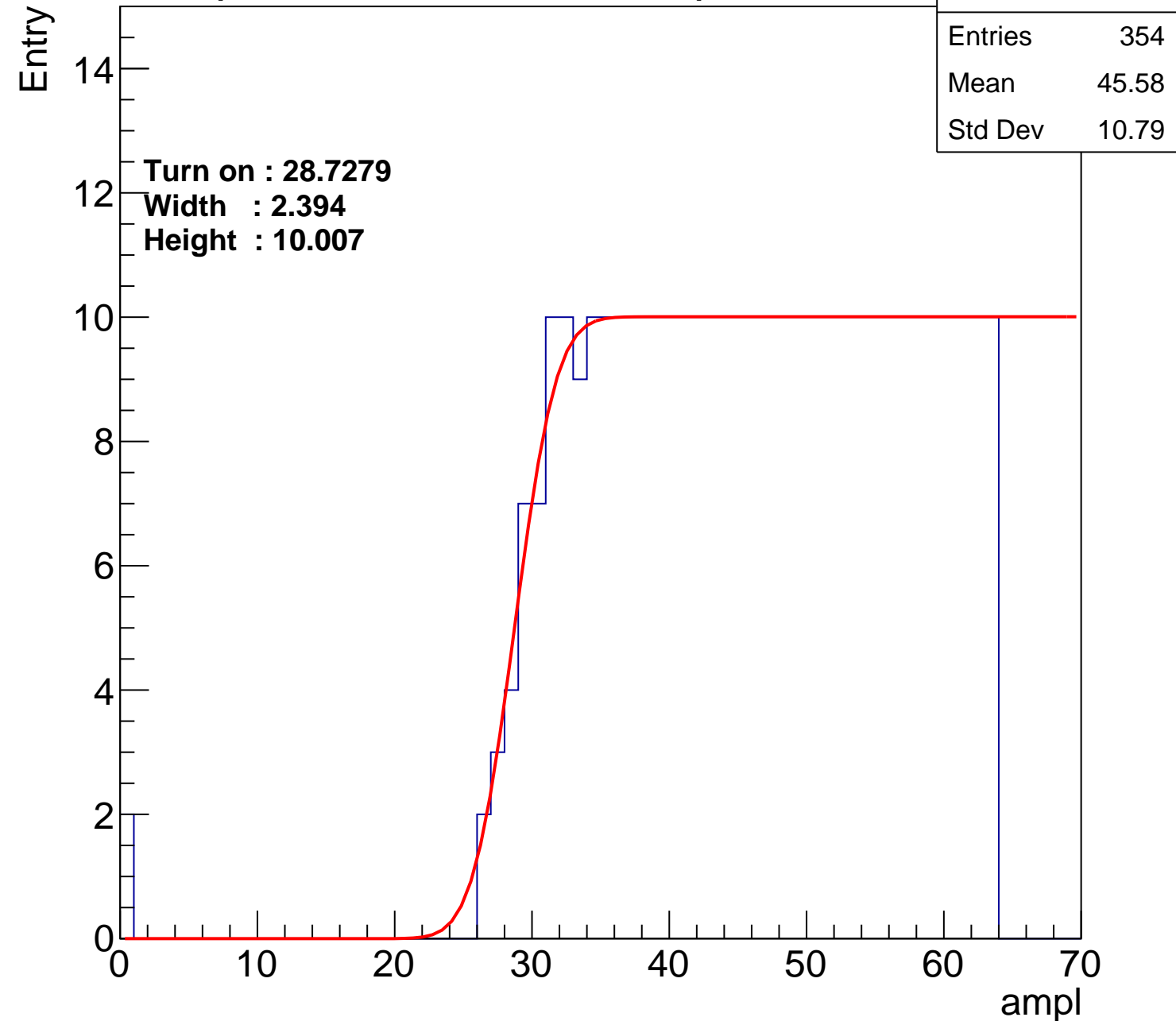
Width : 2.394

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 27.0965

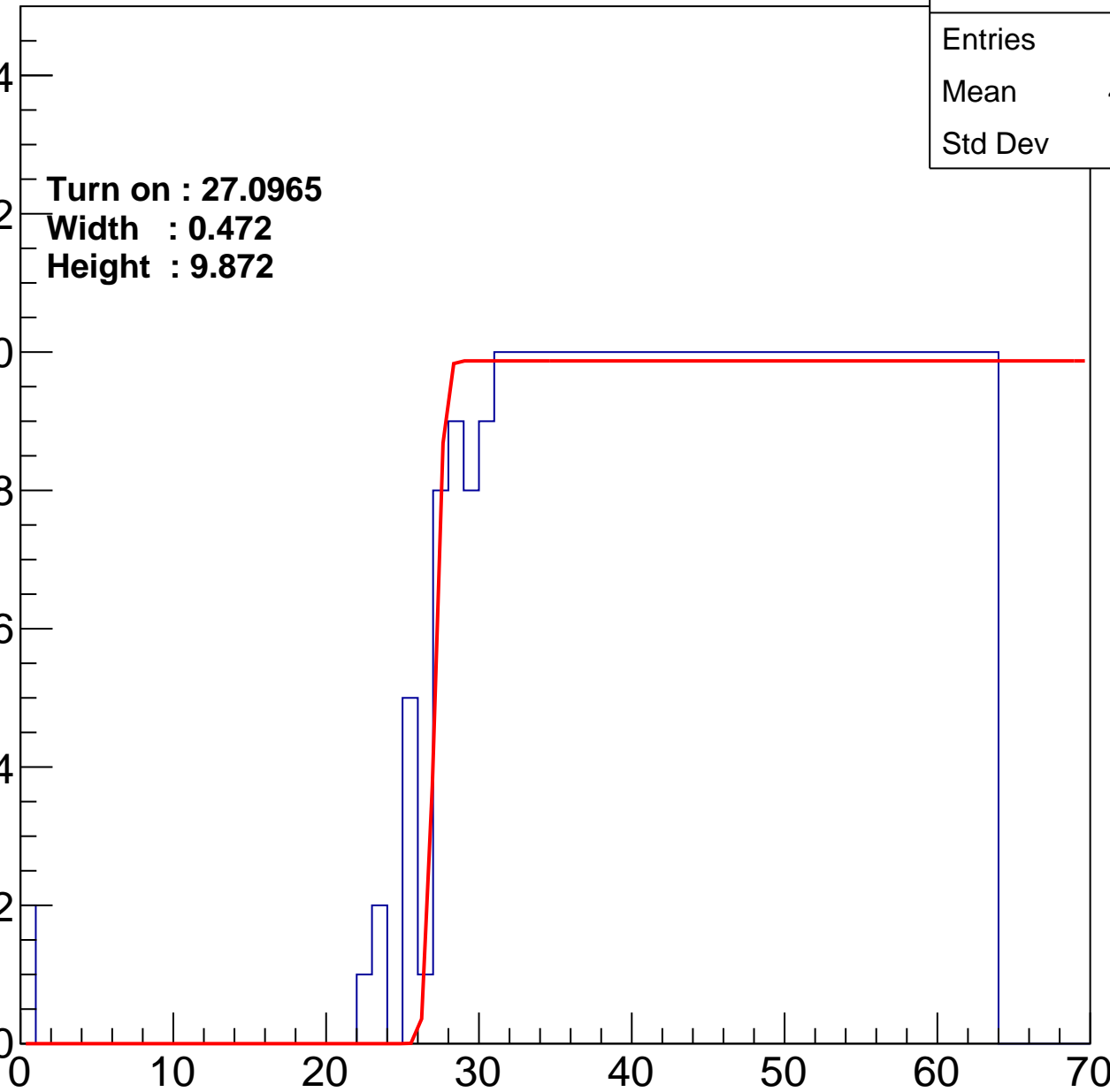
Width : 0.472

Height : 9.872

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.14
Std Dev	11.01

Turn on : 28.1229

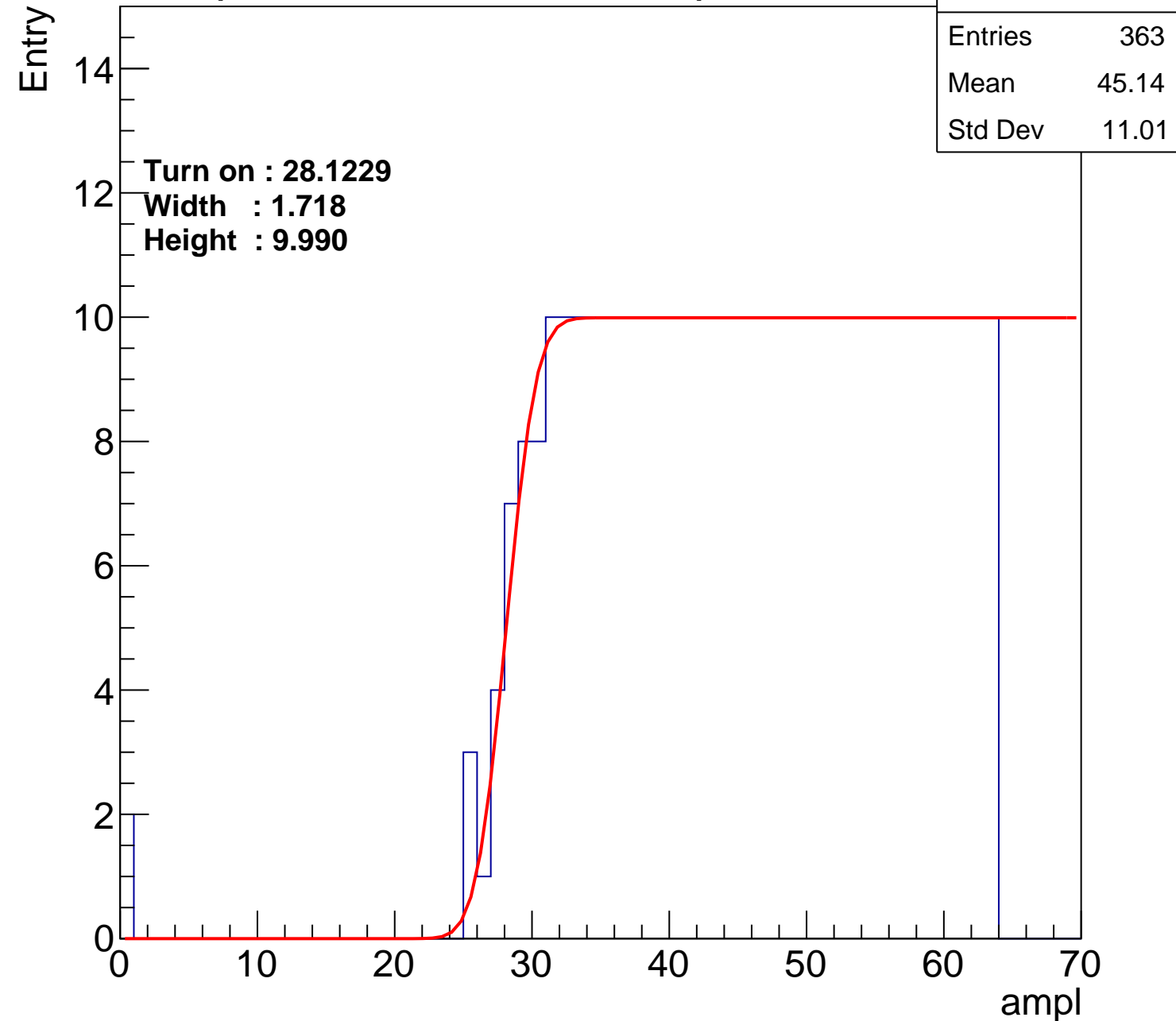
Width : 1.718

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.13
Std Dev	10.34

Turn on : 29.7294

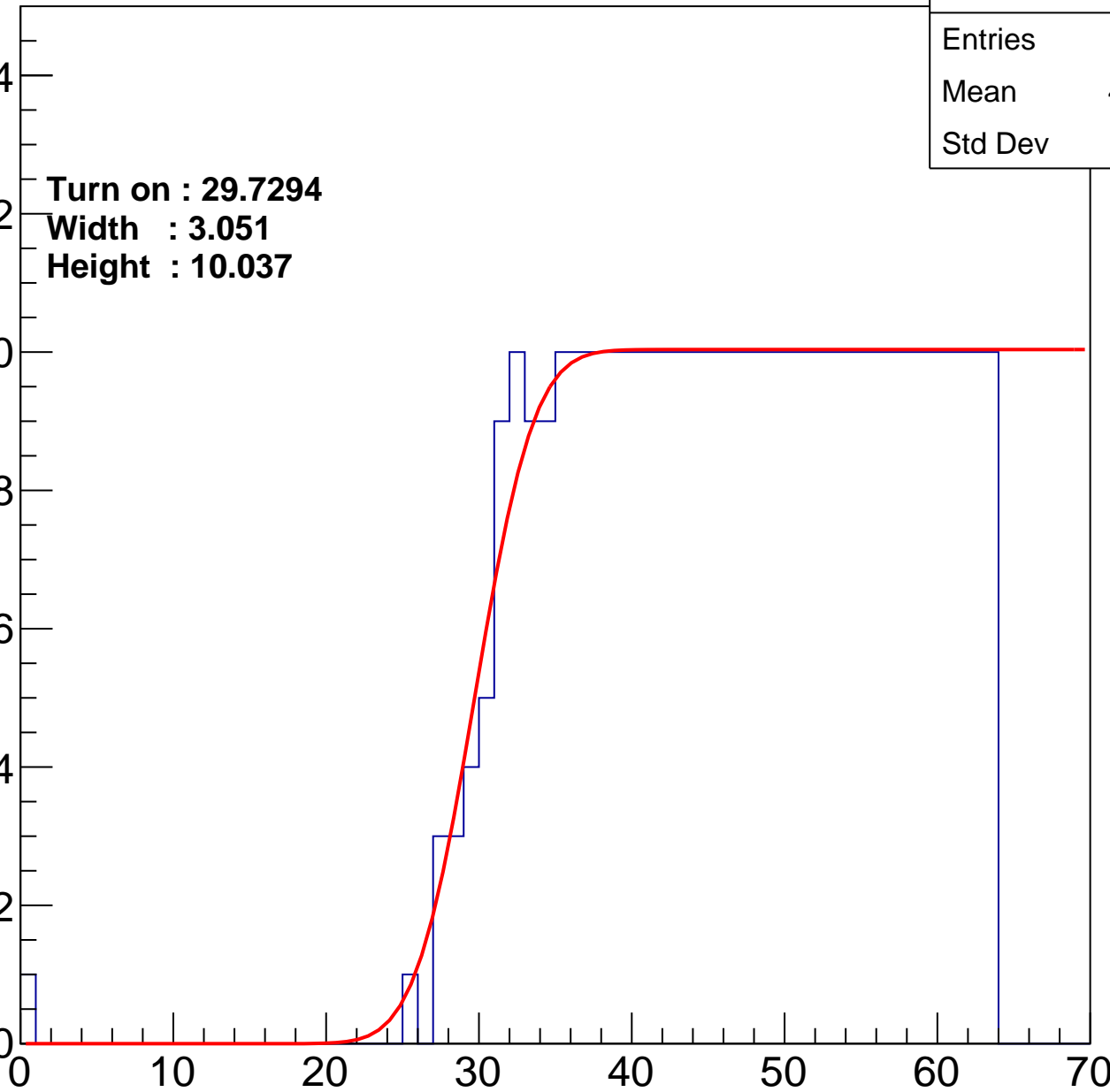
Width : 3.051

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.2
Std Dev	11.37

Turn on : 28.9258

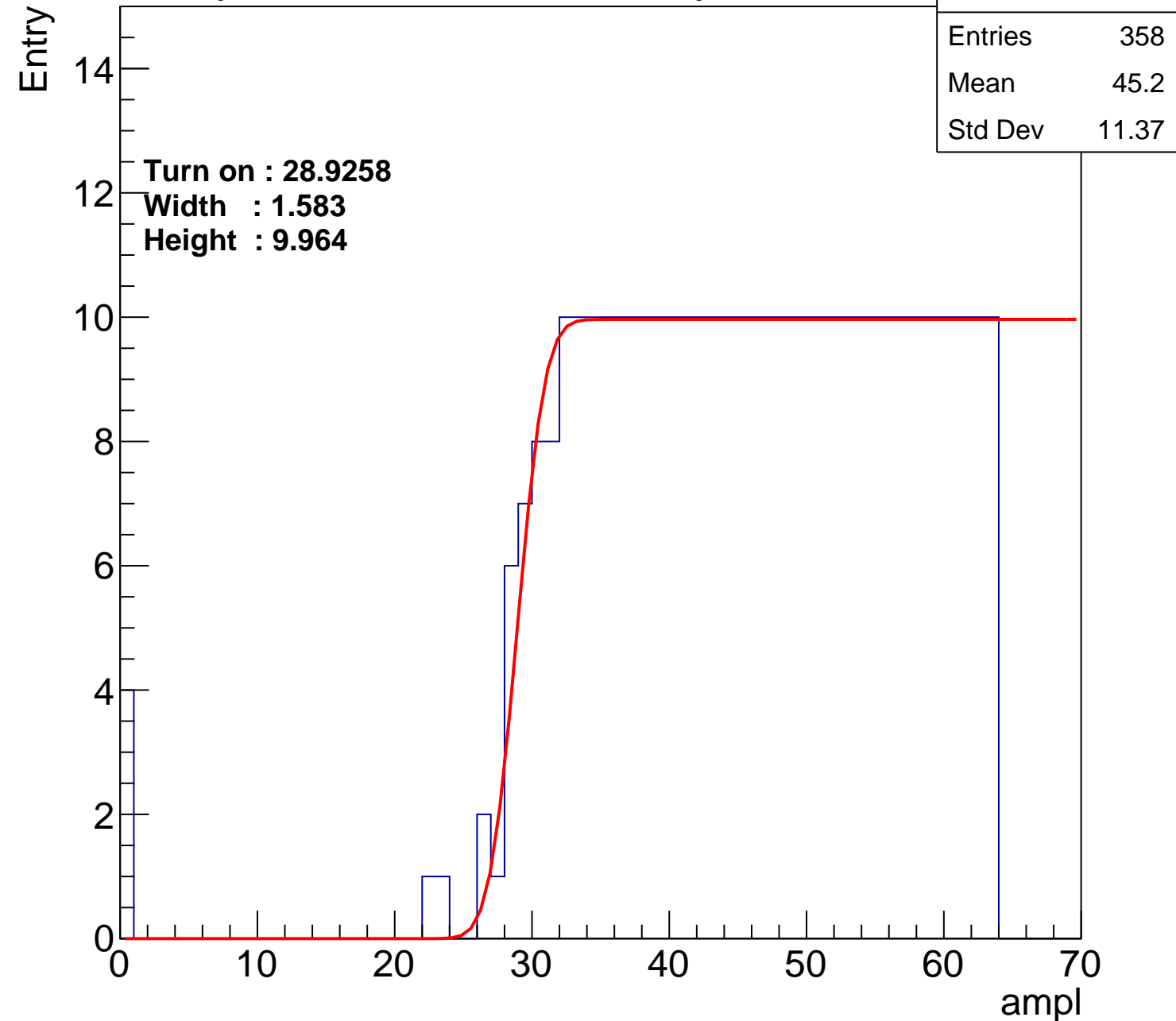
Width : 1.583

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.5173

Width : 2.380

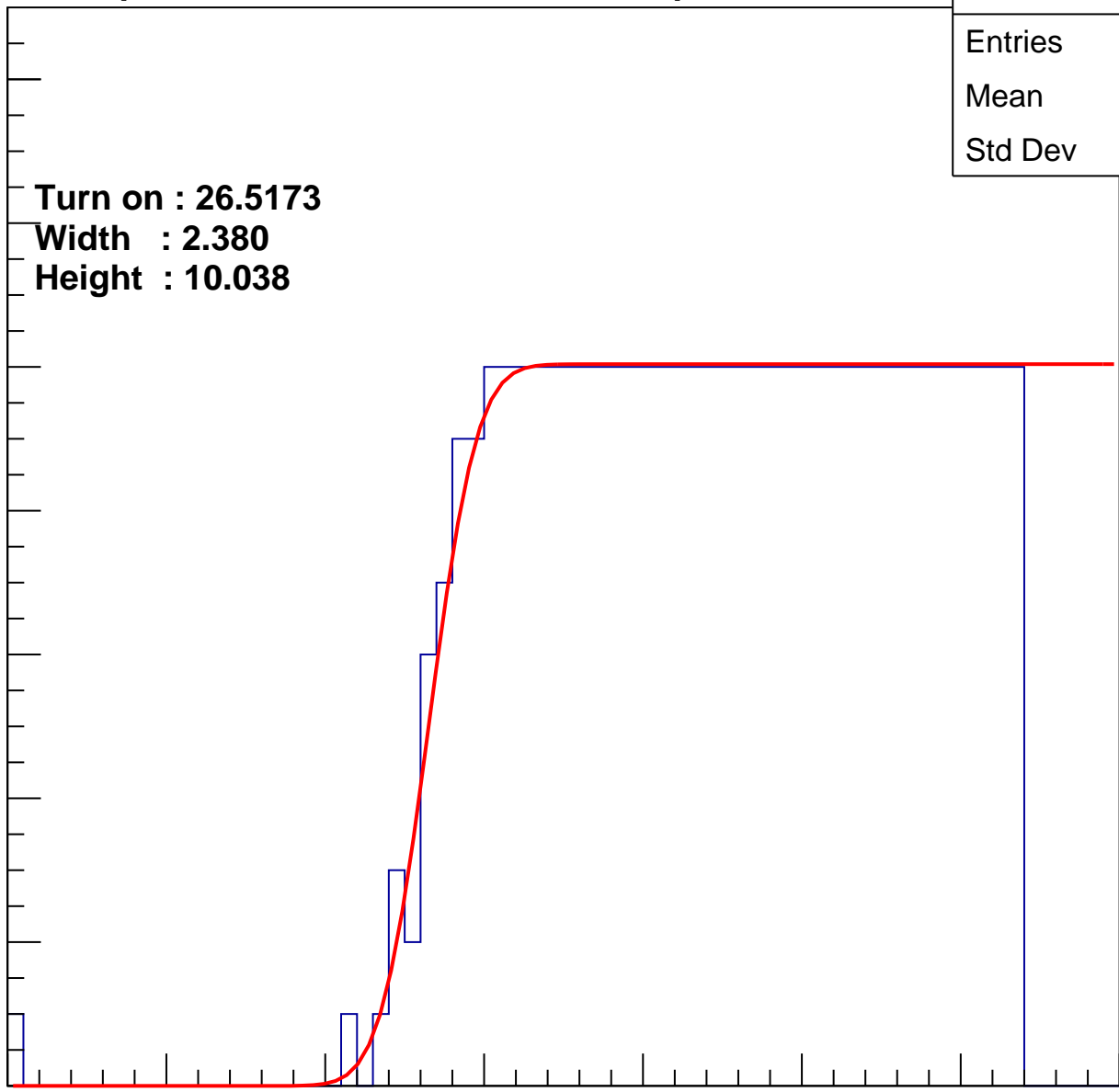
Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U10-ch26

calib_packv5_042523_0143.root, FC#8, port C1

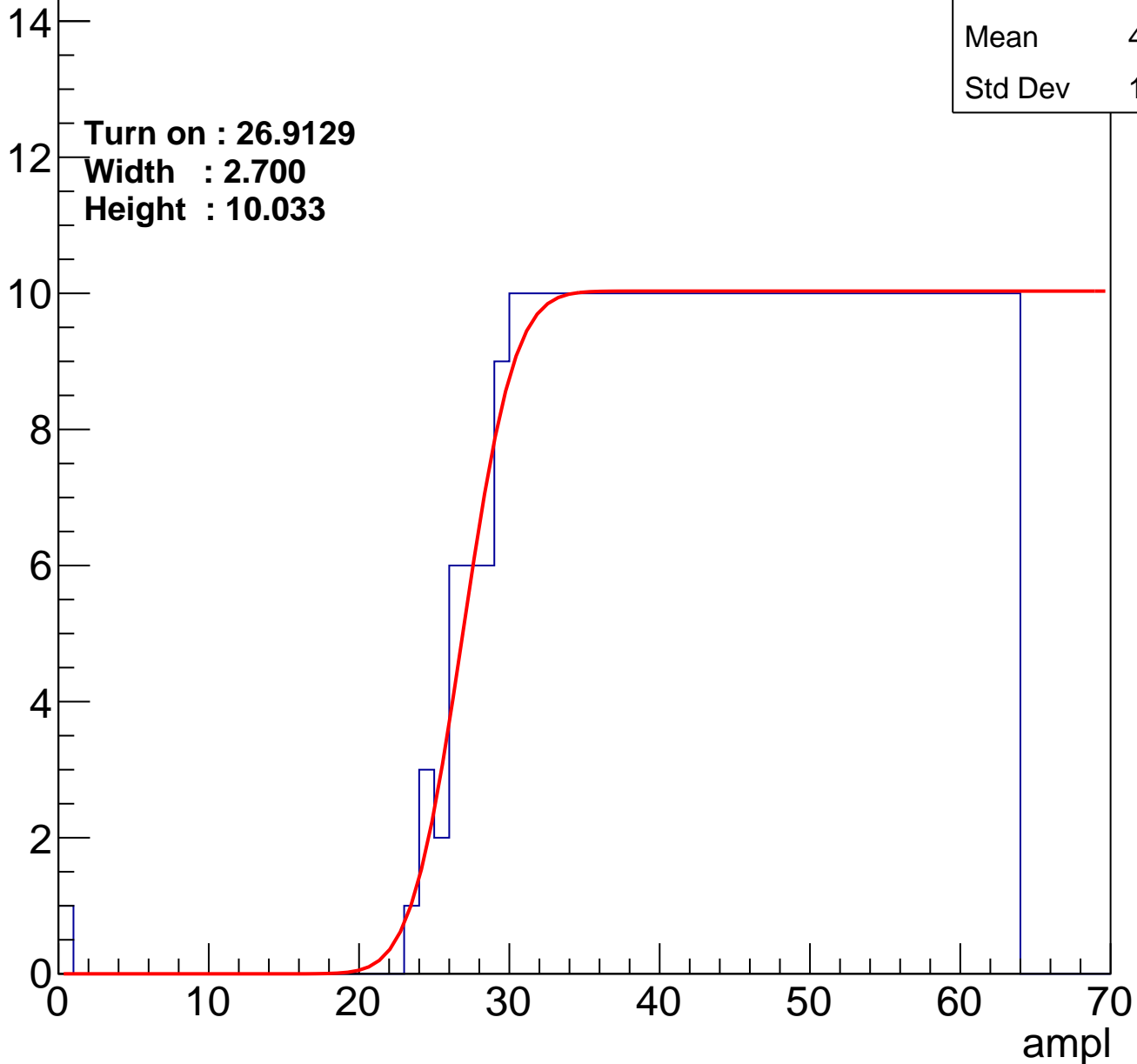
Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 26.9129

Width : 2.700

Height : 10.033

Entry



B0L002S, U10-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.45
Std Dev	10.84

Turn on : 28.5910

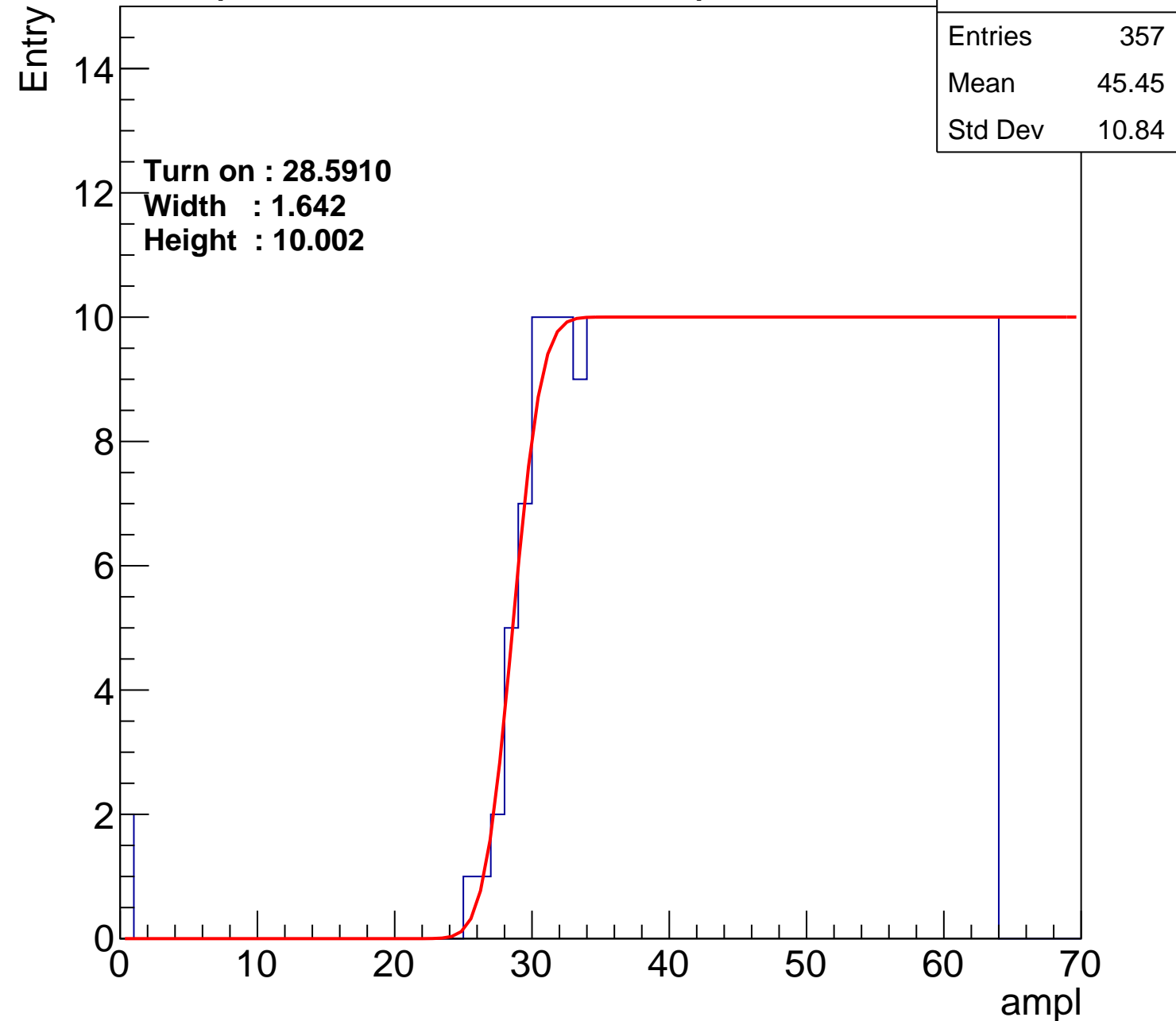
Width : 1.642

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.31
Std Dev	11.18

Turn on : 29.3765

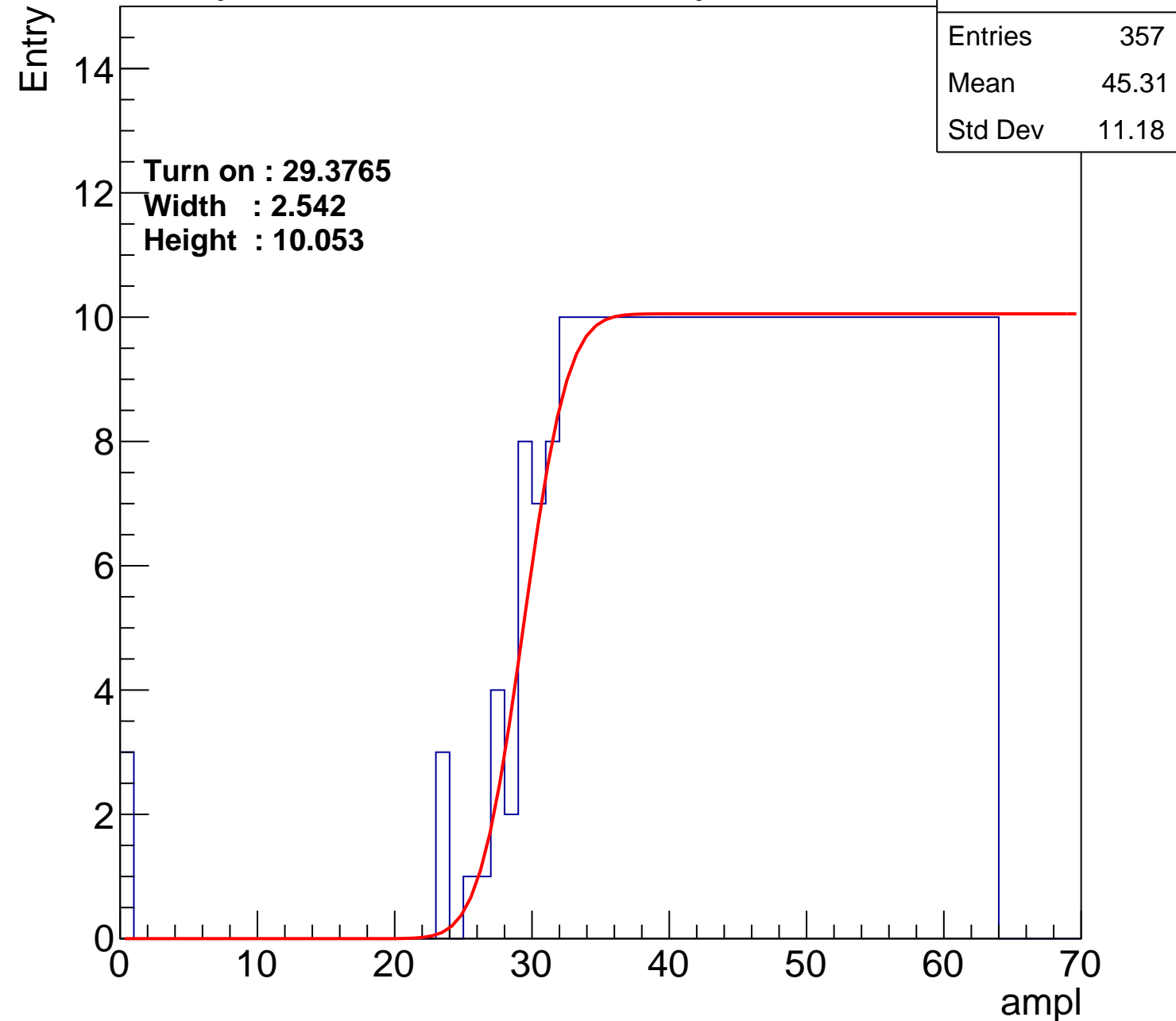
Width : 2.542

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.35
Std Dev	10.74

Turn on : 28.2494

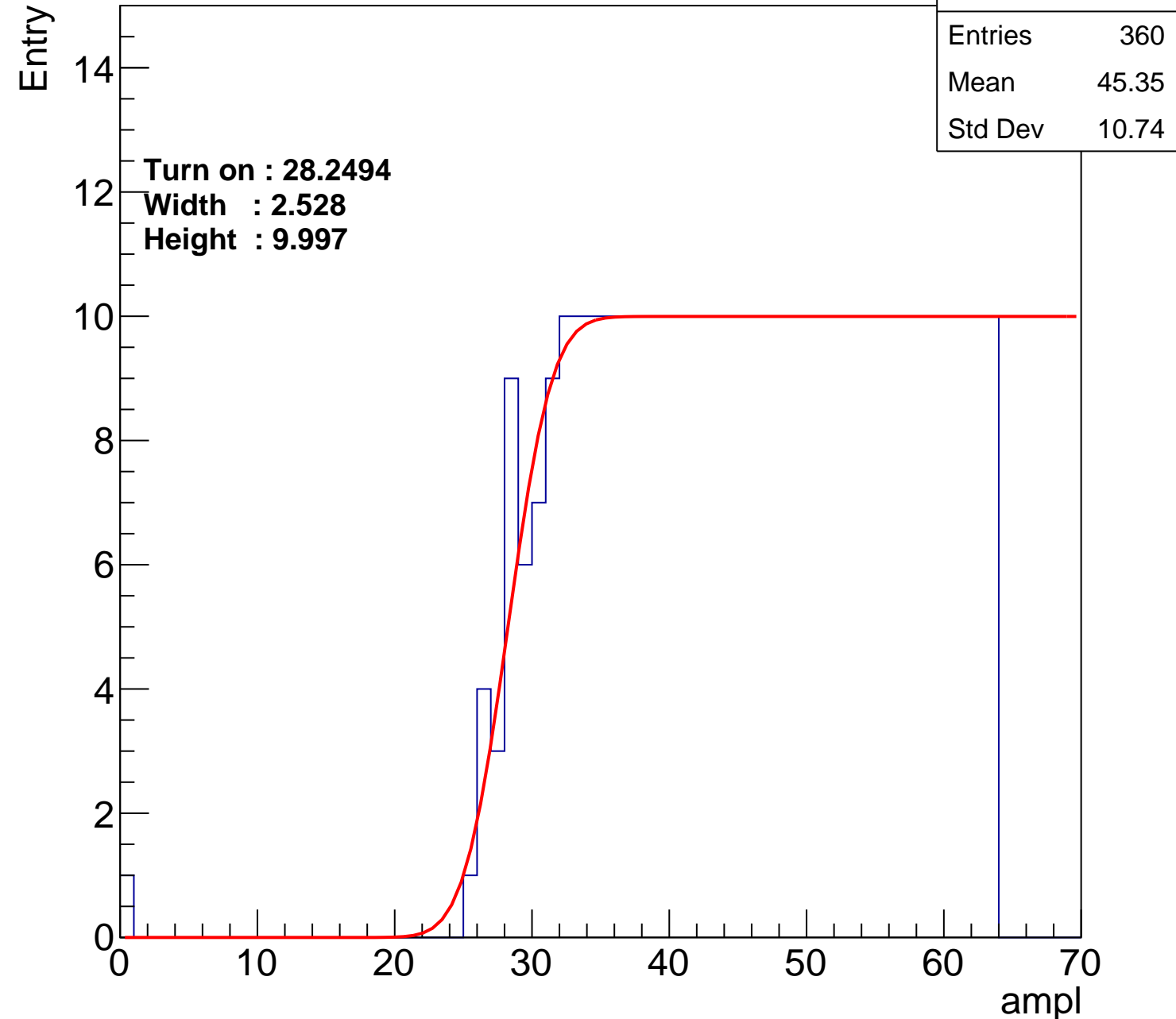
Width : 2.528

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch30

calib_packv5_042523_0143.root, FC#8, port C1

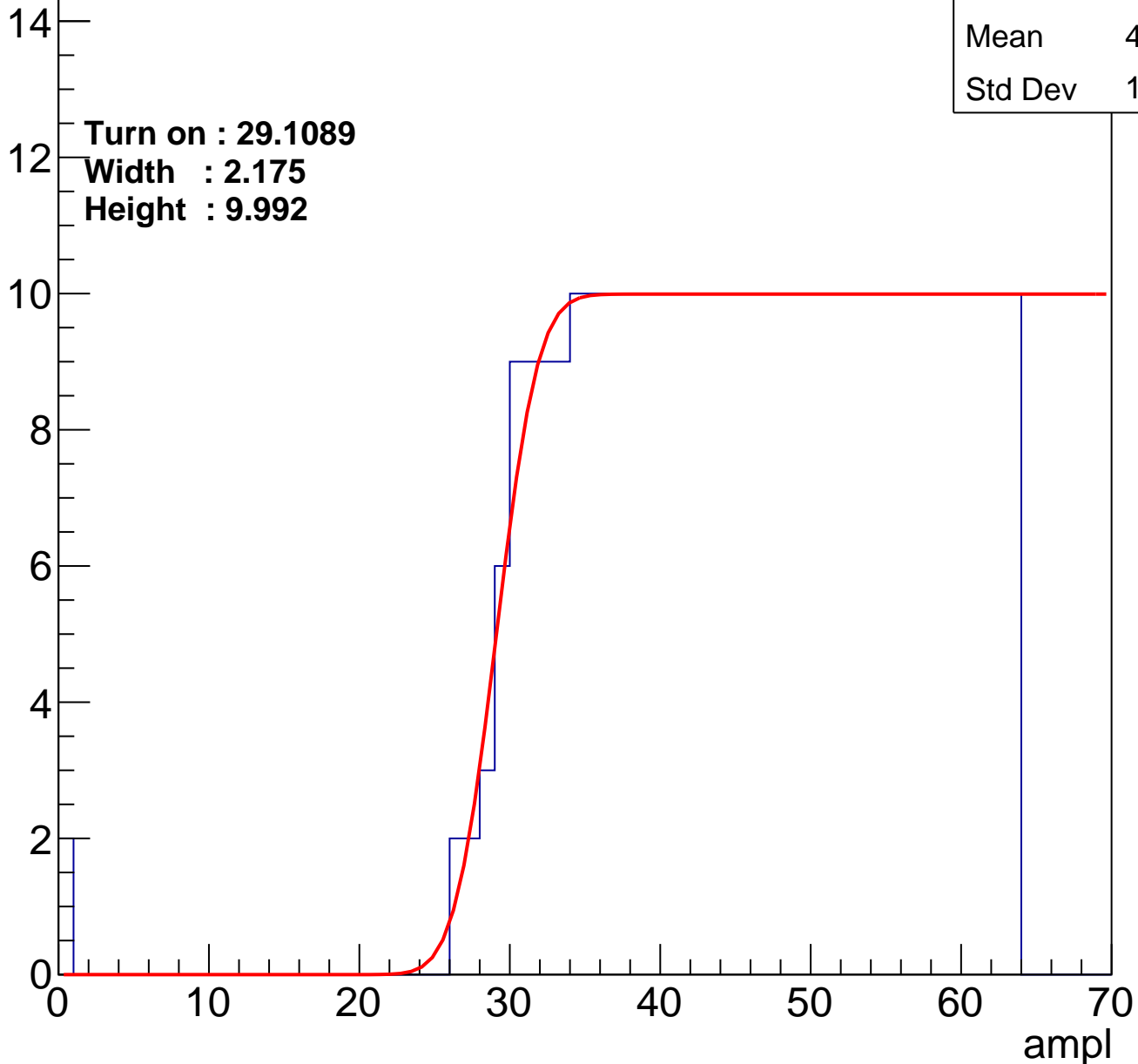
Entries	351
Mean	45.72
Std Dev	10.73

Turn on : 29.1089

Width : 2.175

Height : 9.992

Entry



B0L002S, U10-ch31

calib_packv5_042523_0143.root, FC#8, port C1

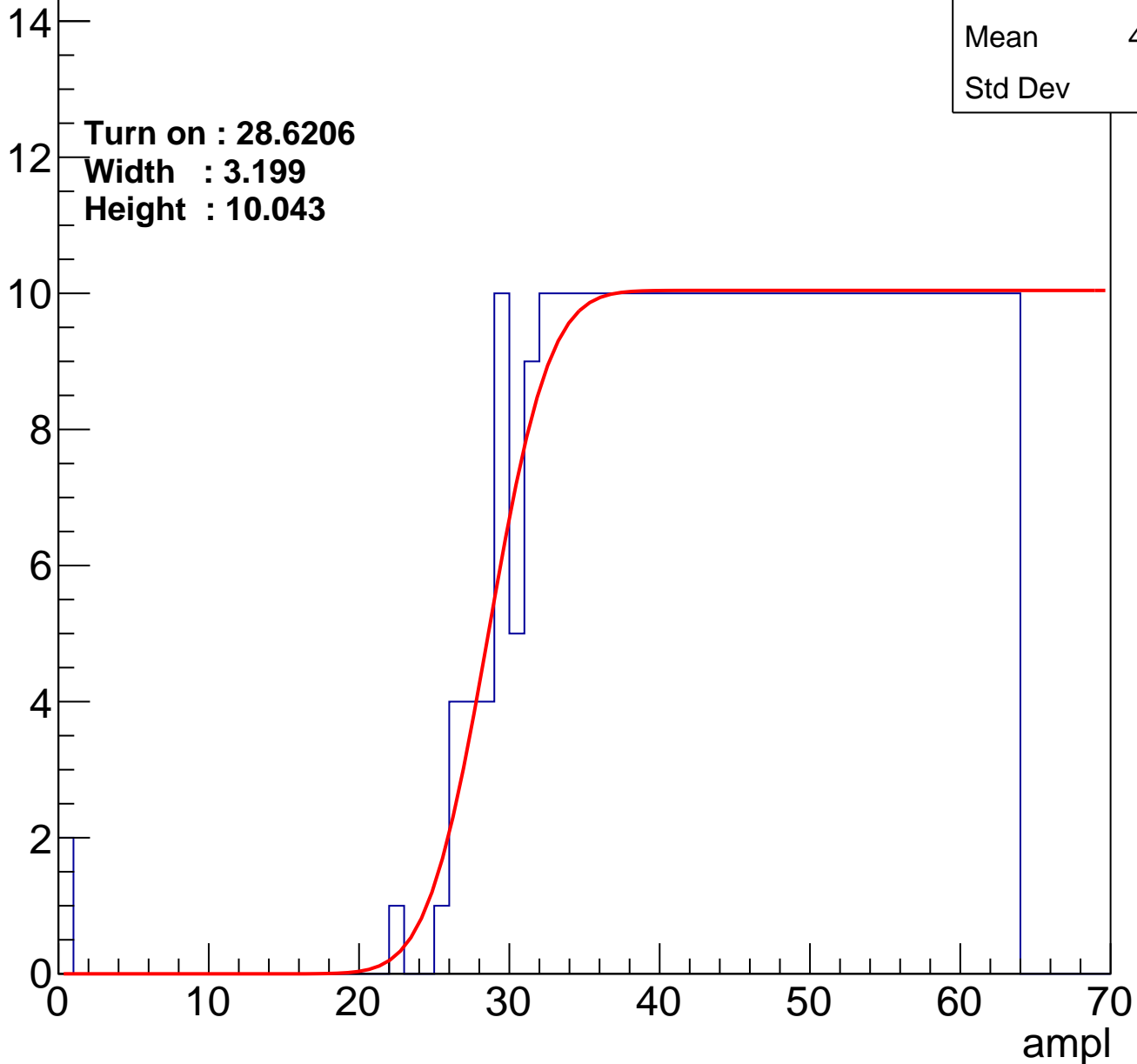
Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.6206

Width : 3.199

Height : 10.043

Entry



B0L002S, U10-ch32

calib_packv5_042523_0143.root, FC#8, port C1

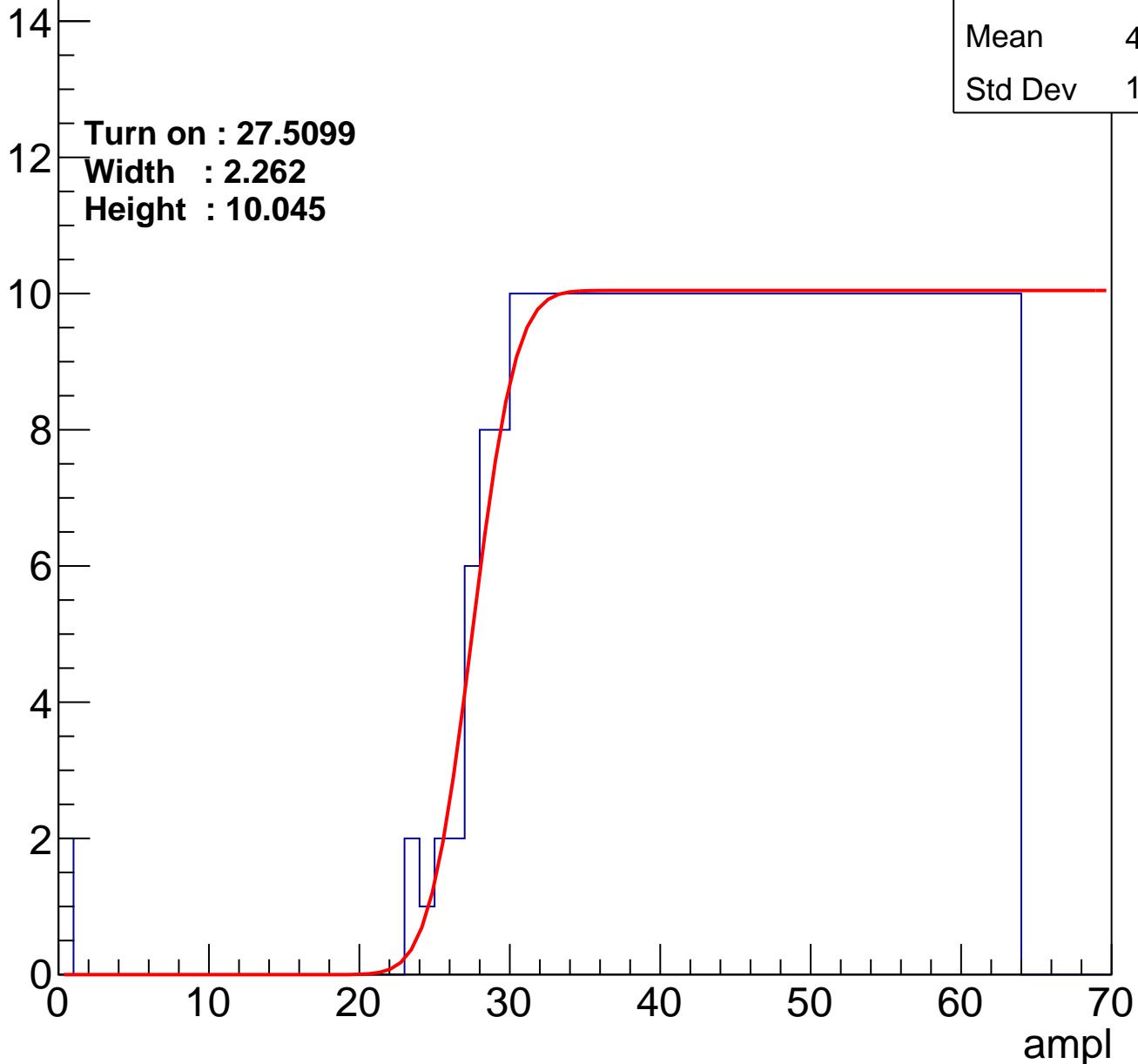
Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.5099

Width : 2.262

Height : 10.045

Entry



B0L002S, U10-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.72
Std Dev	10.73

Turn on : 29.8793

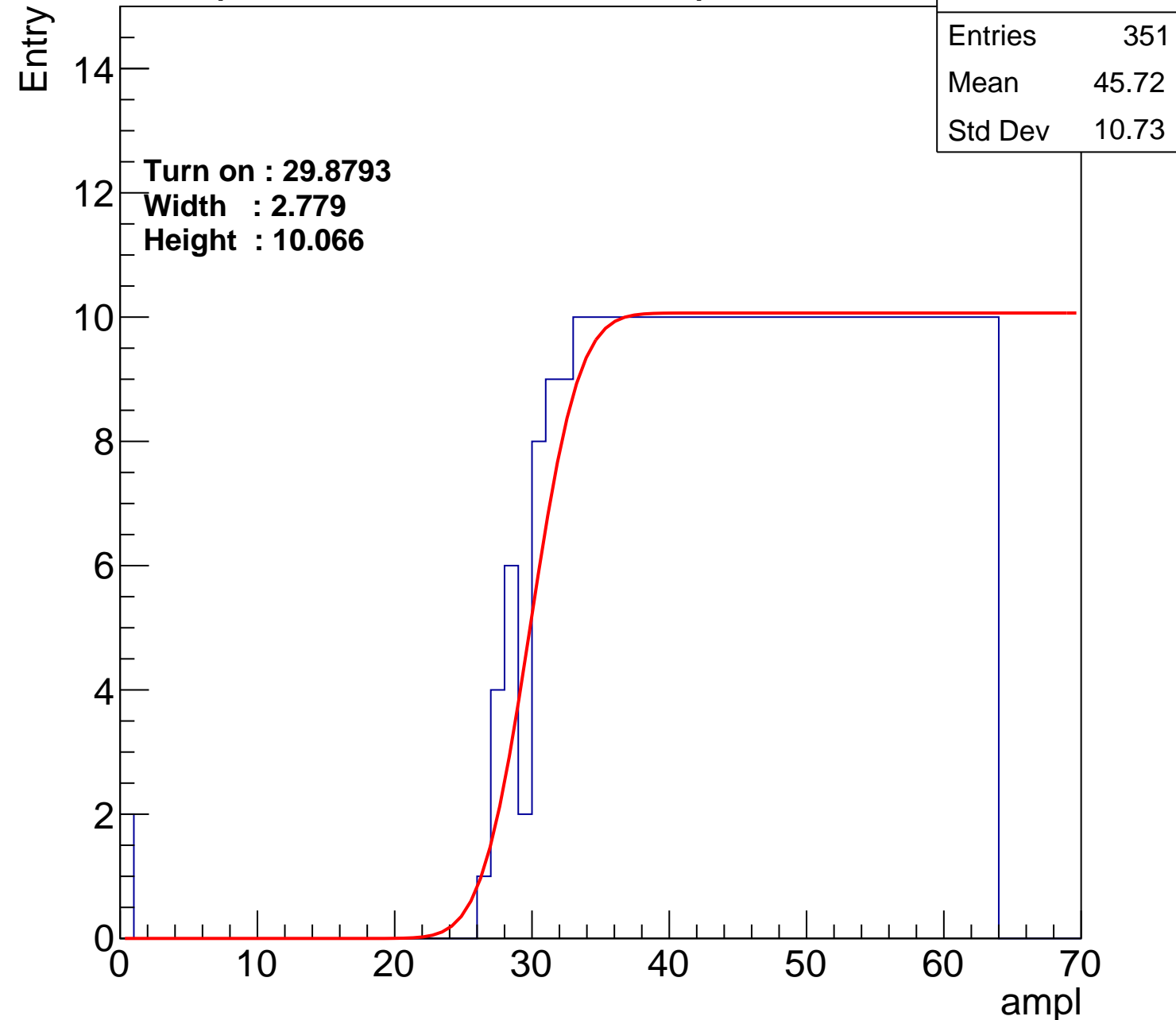
Width : 2.779

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.81
Std Dev	10.54

Turn on : 29.0755

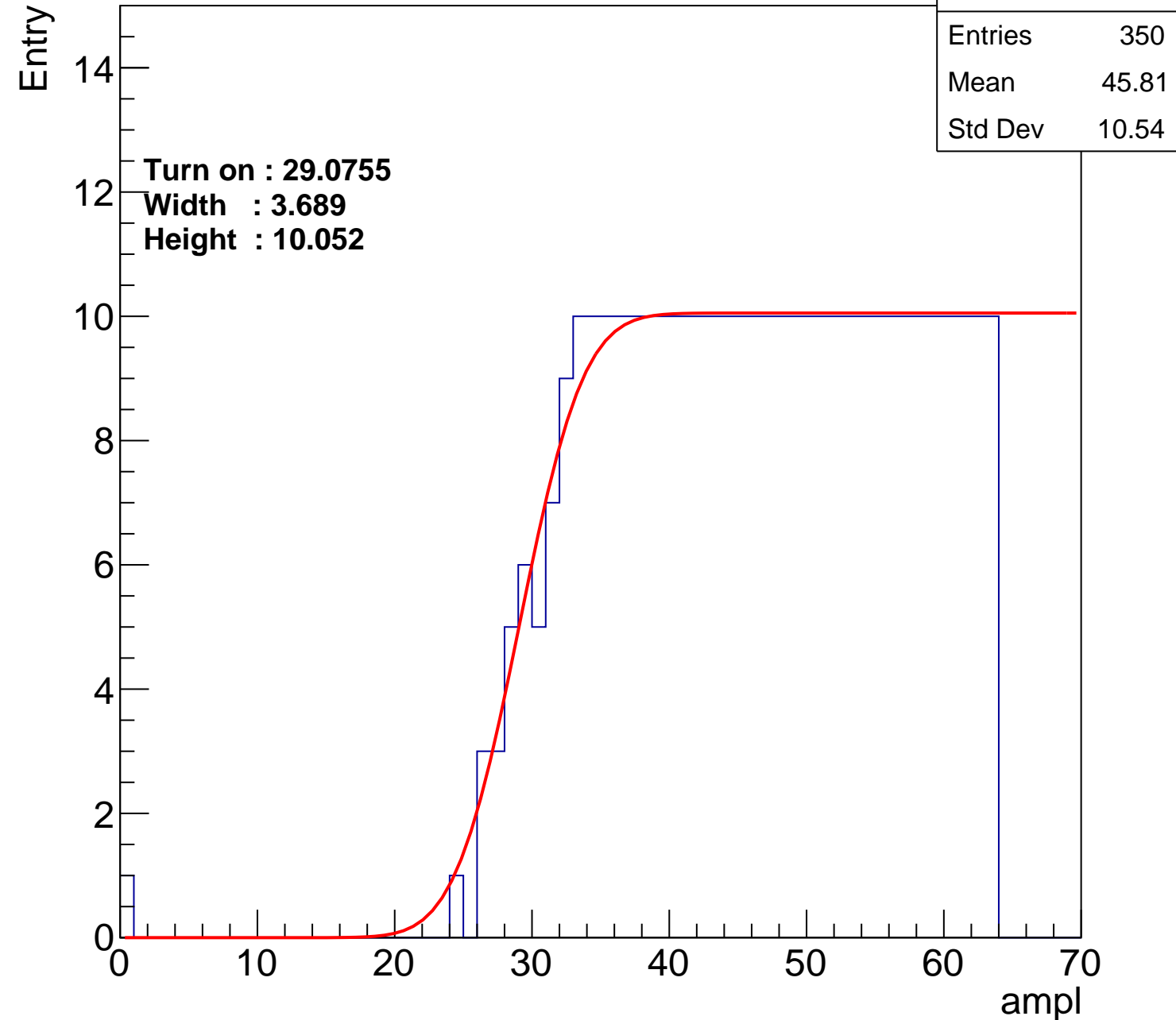
Width : 3.689

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.9470
Width : 2.740
Height : 10.009



B0L002S, U10-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.5261

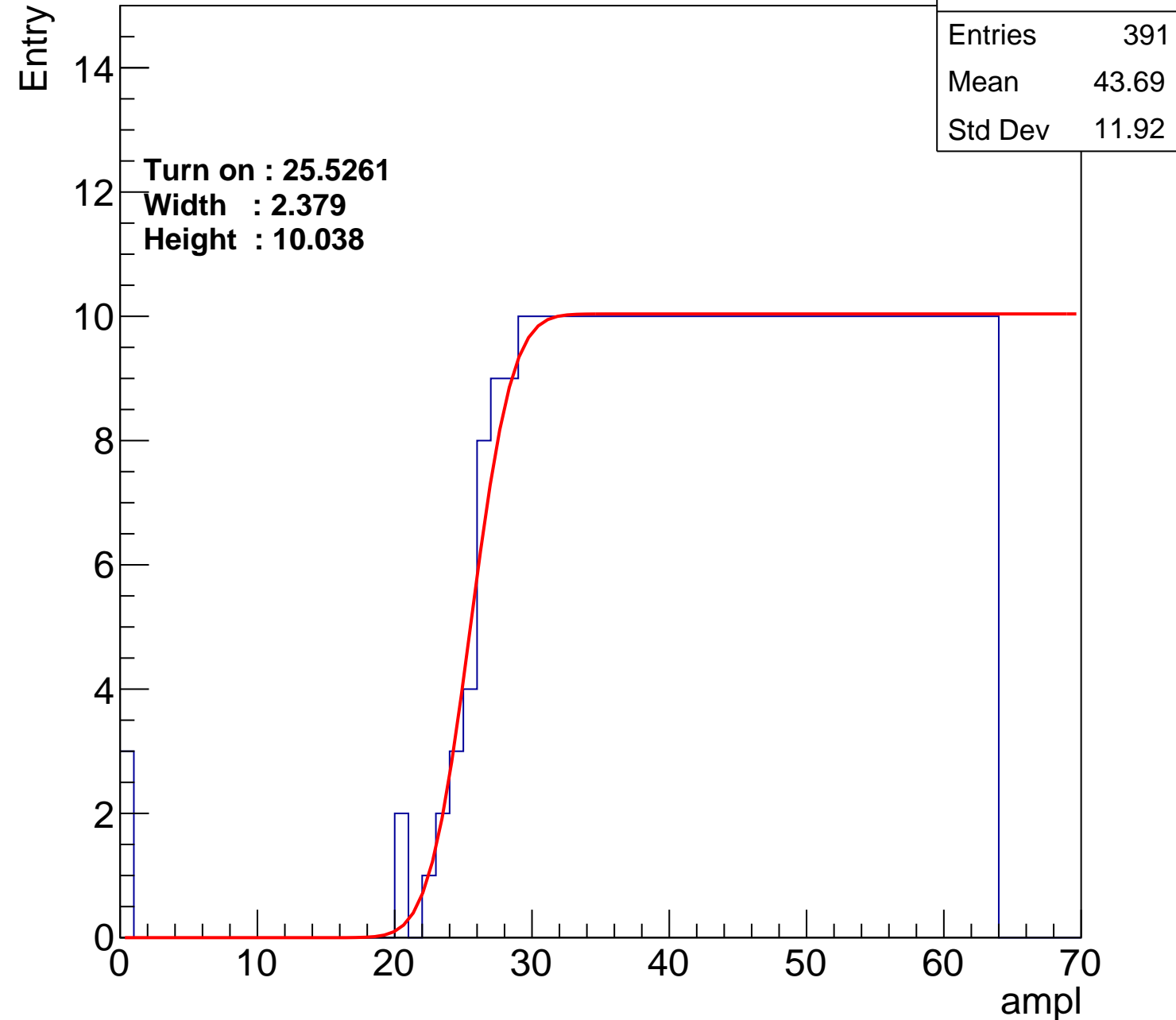
Width : 2.379

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch37

calib_packv5_042523_0143.root, FC#8, port C1

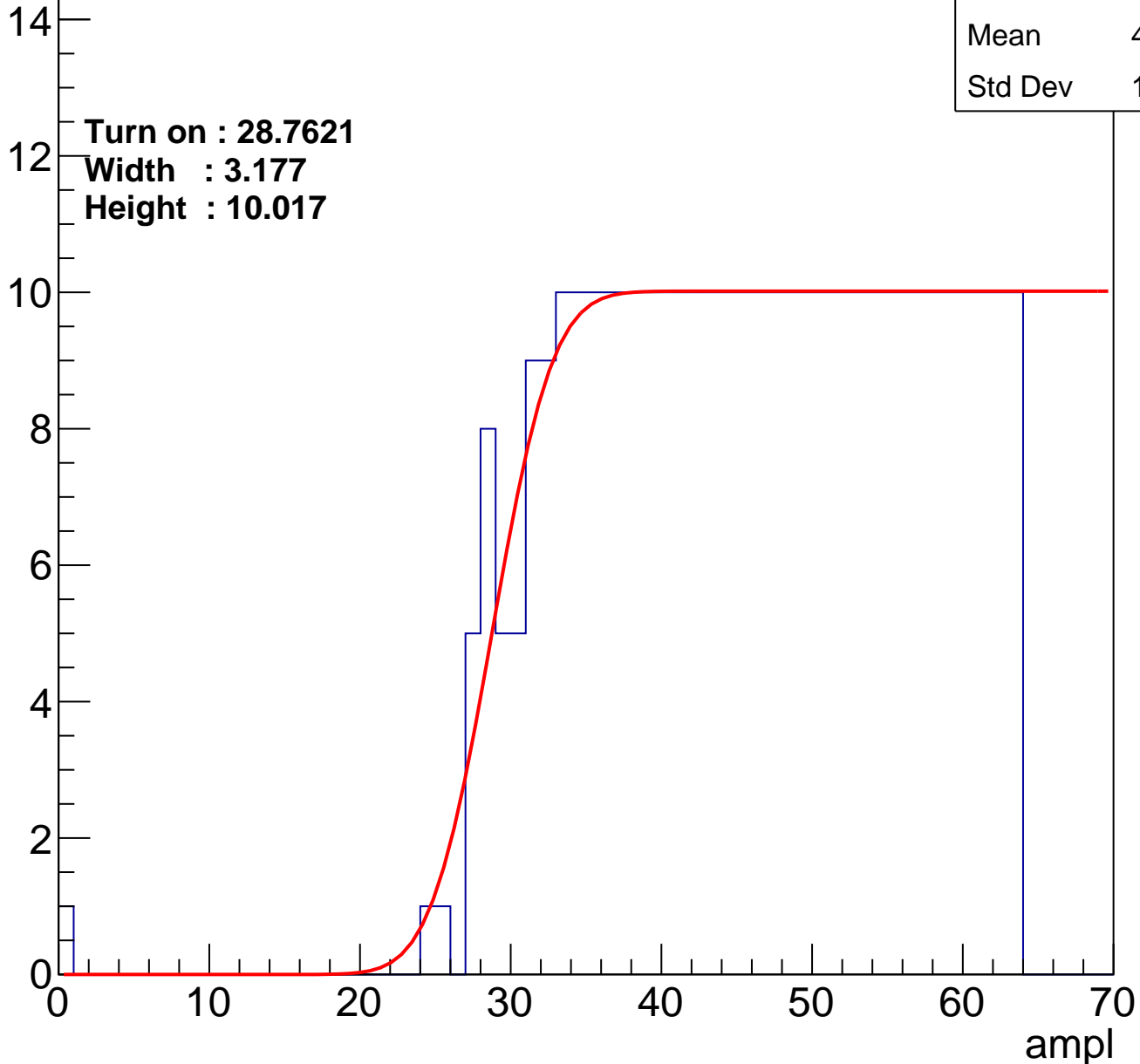
Entries	354
Mean	45.62
Std Dev	10.62

Turn on : 28.7621

Width : 3.177

Height : 10.017

Entry



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 30.5326
Width : 2.760
Height : 10.056



B0L002S, U10-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.92
Std Dev	10.46

Turn on : 29.2727

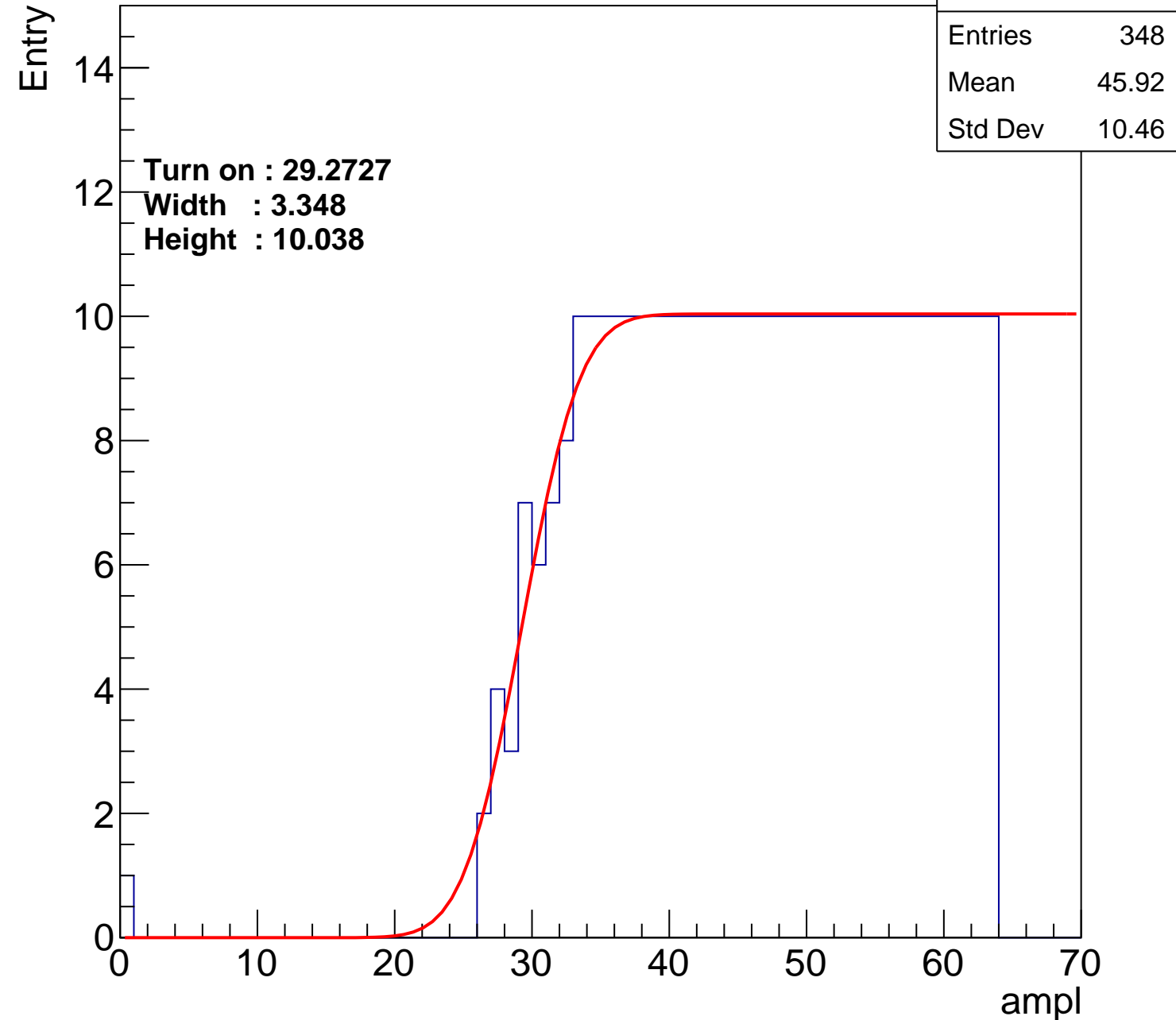
Width : 3.348

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.5791

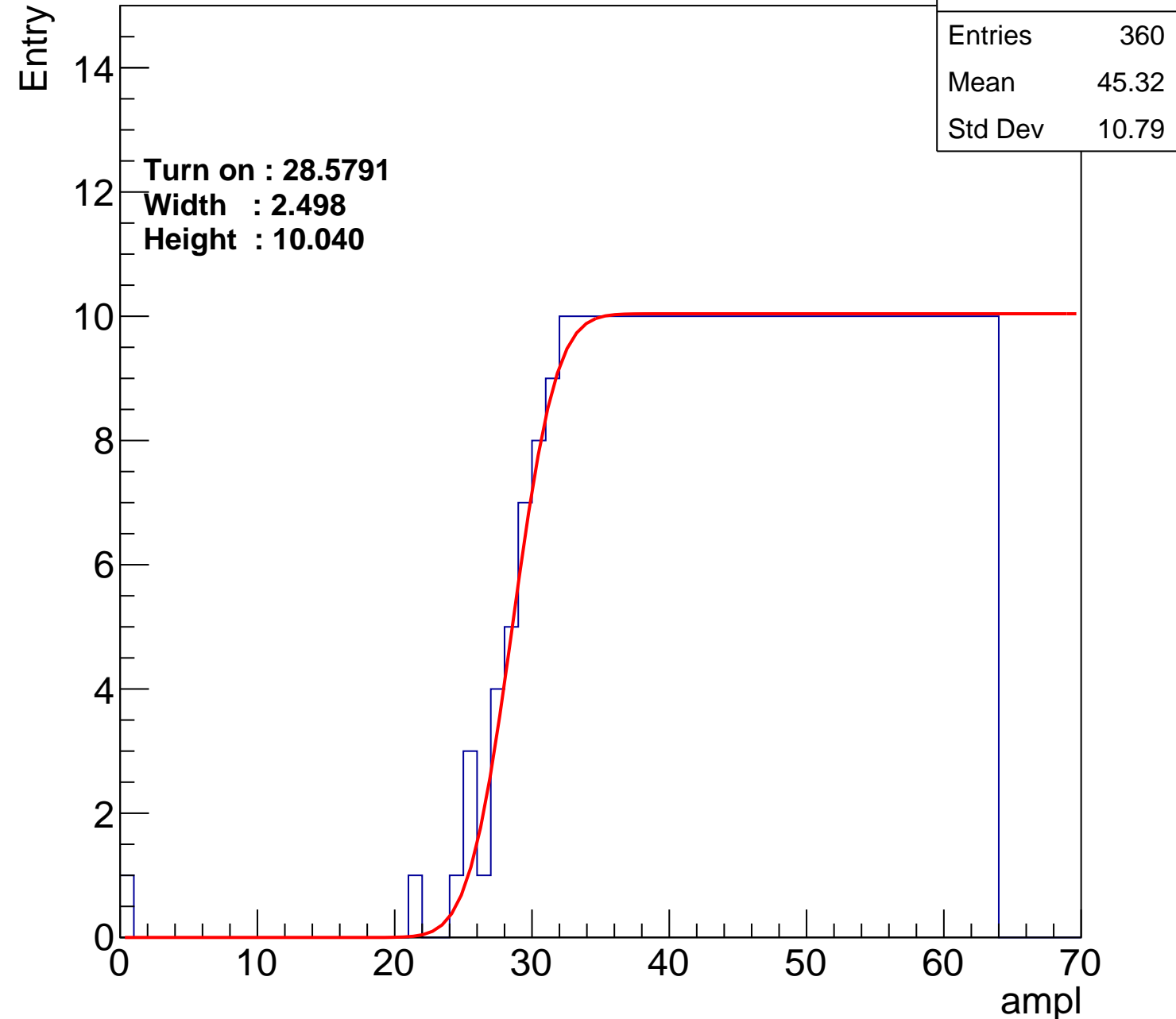
Width : 2.498

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.09
Std Dev	11.03

Turn on : 27.6829

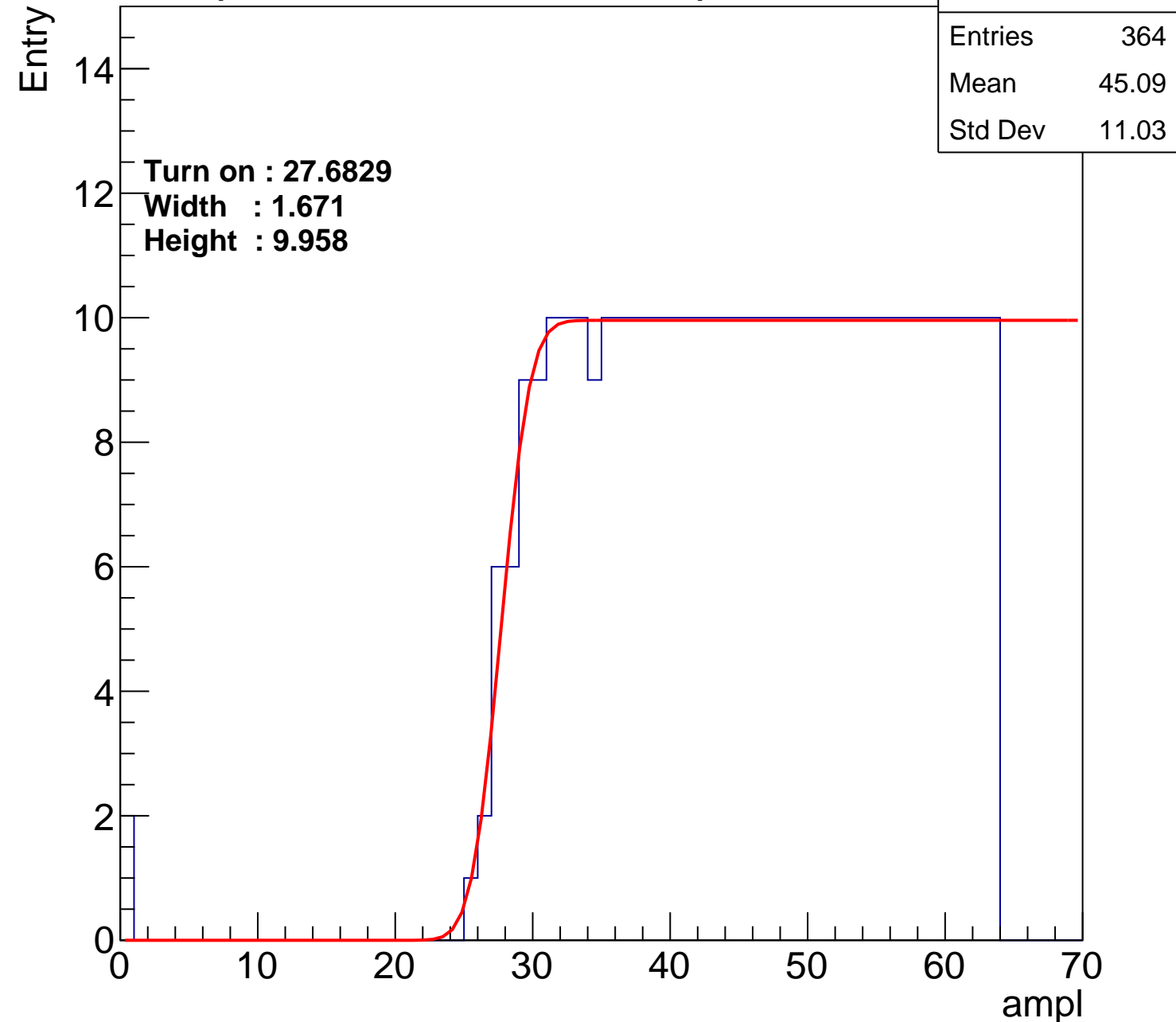
Width : 1.671

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.81
Std Dev	10.53

Turn on : 29.4049

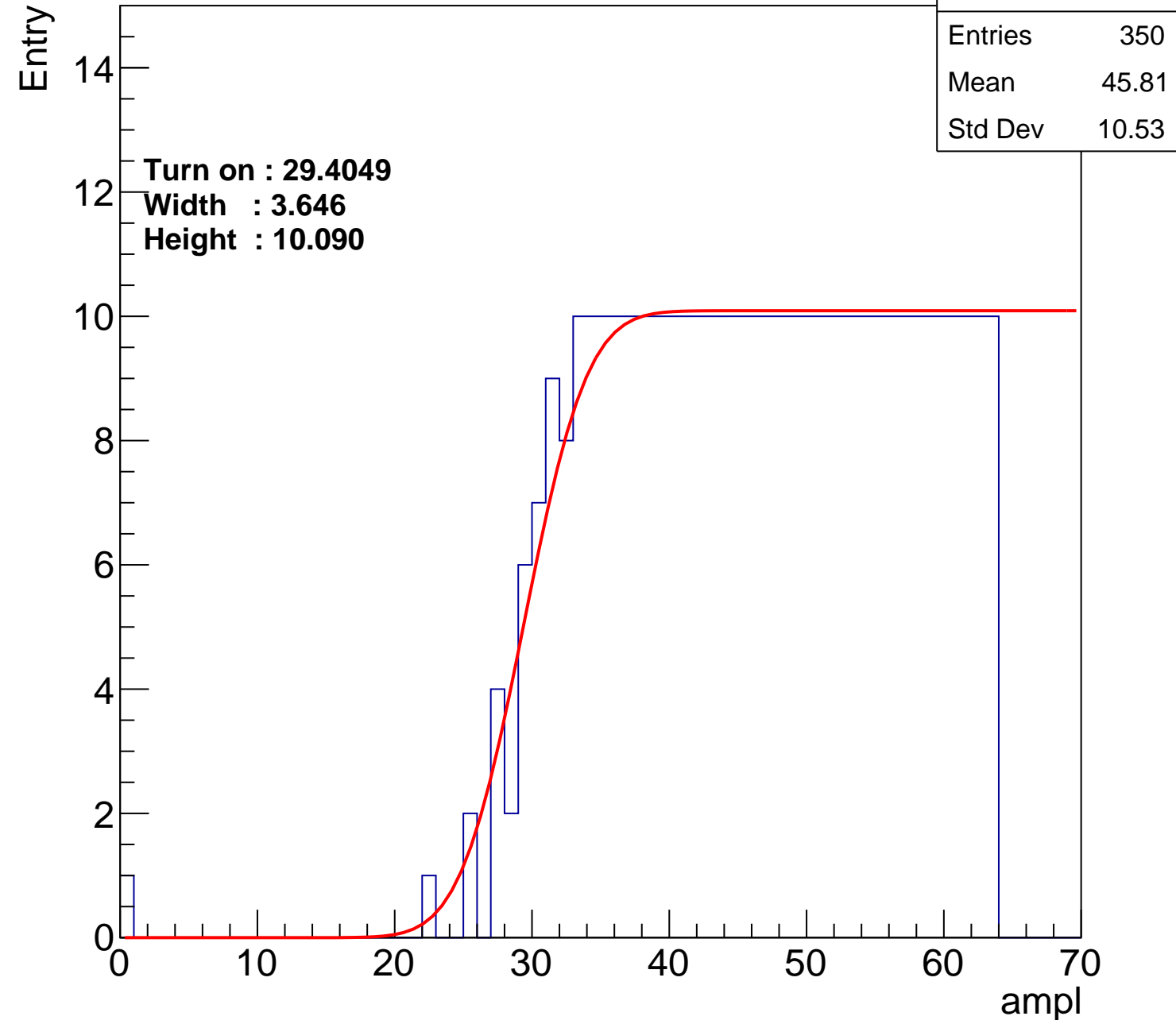
Width : 3.646

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch43

calib_packv5_042523_0143.root, FC#8, port C1

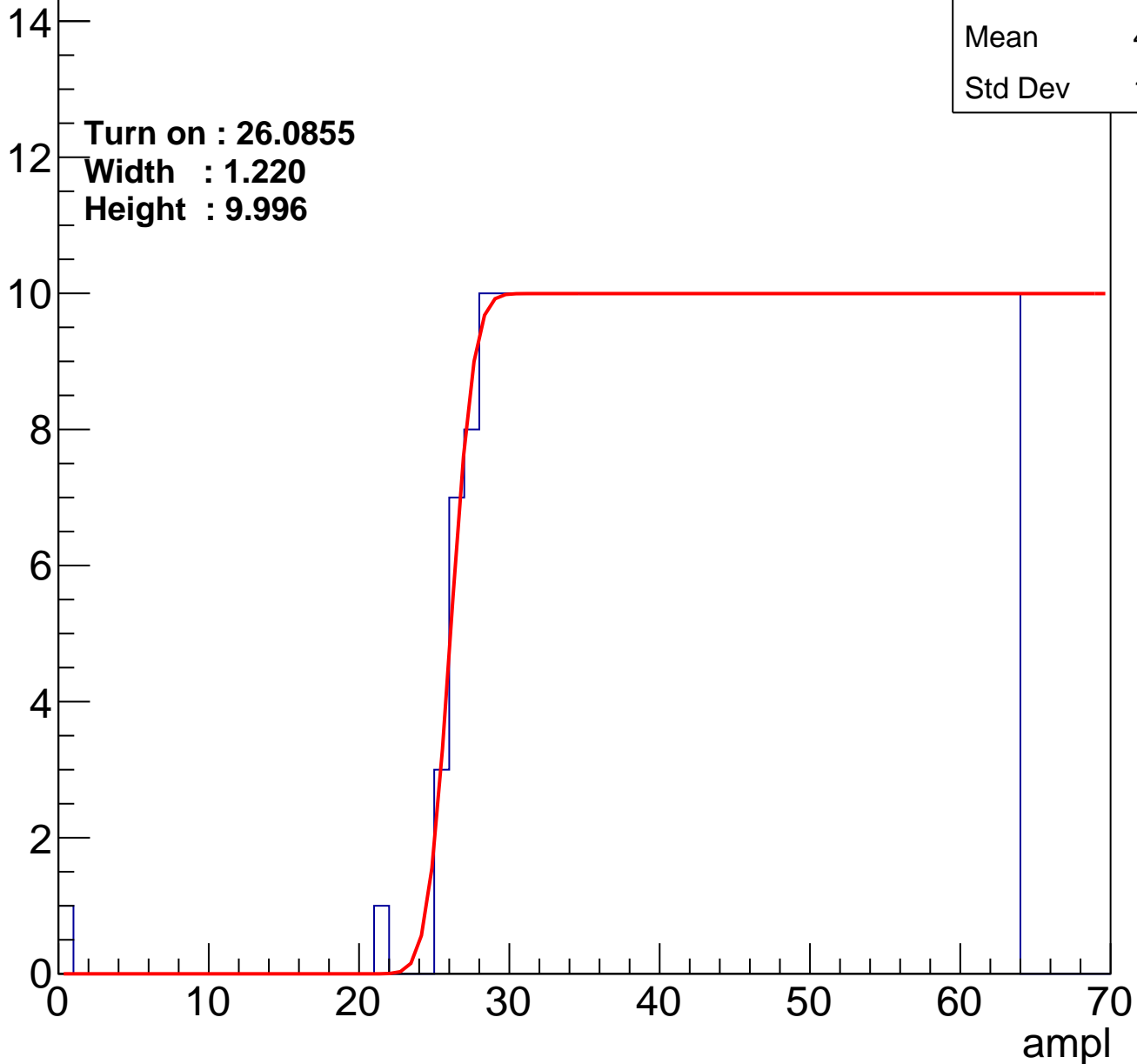
Entries	380
Mean	44.41
Std Dev	11.21

Turn on : 26.0855

Width : 1.220

Height : 9.996

Entry



B0L002S, U10-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.4736

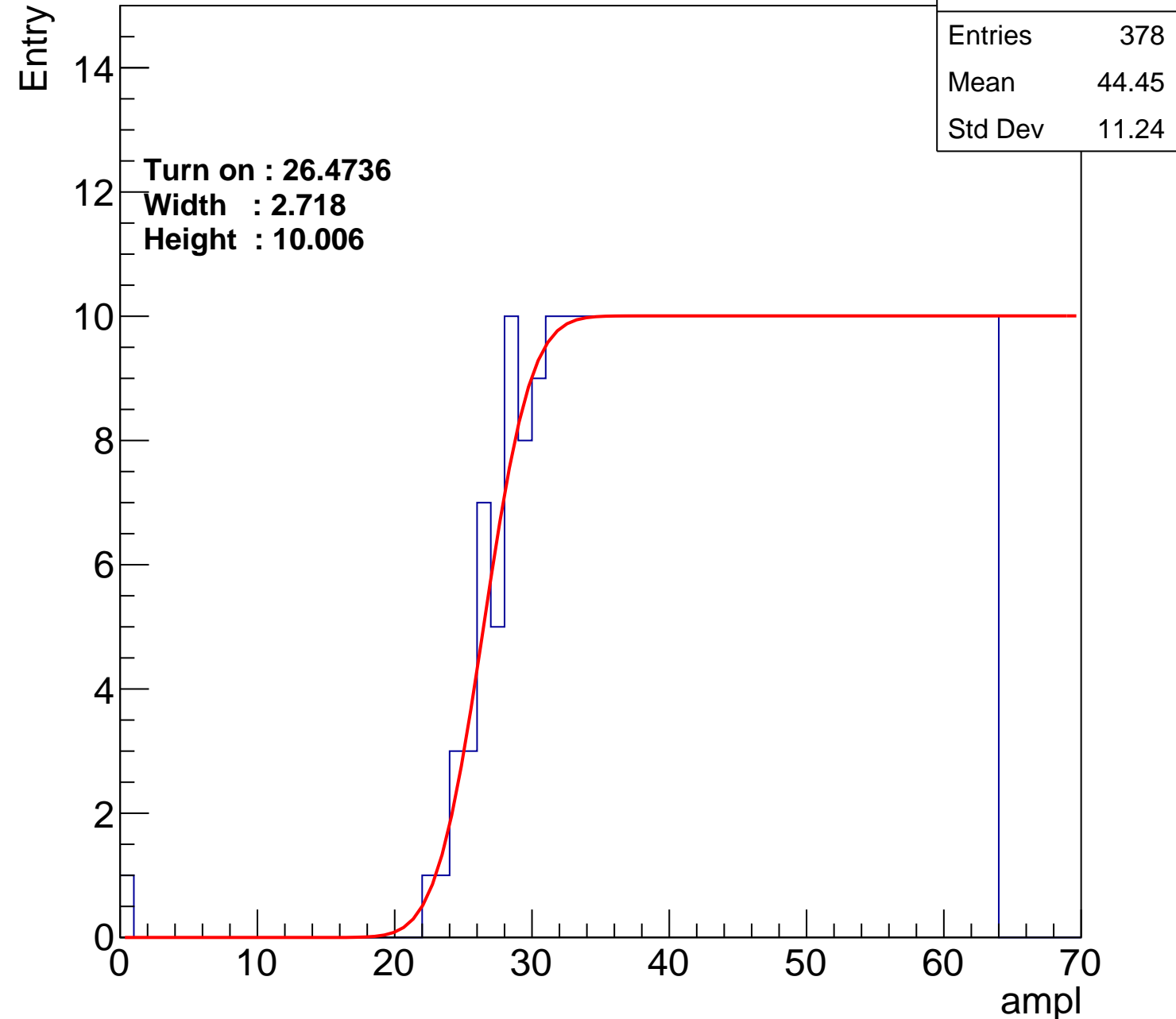
Width : 2.718

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.47
Std Dev	10.71

Turn on : 28.5299

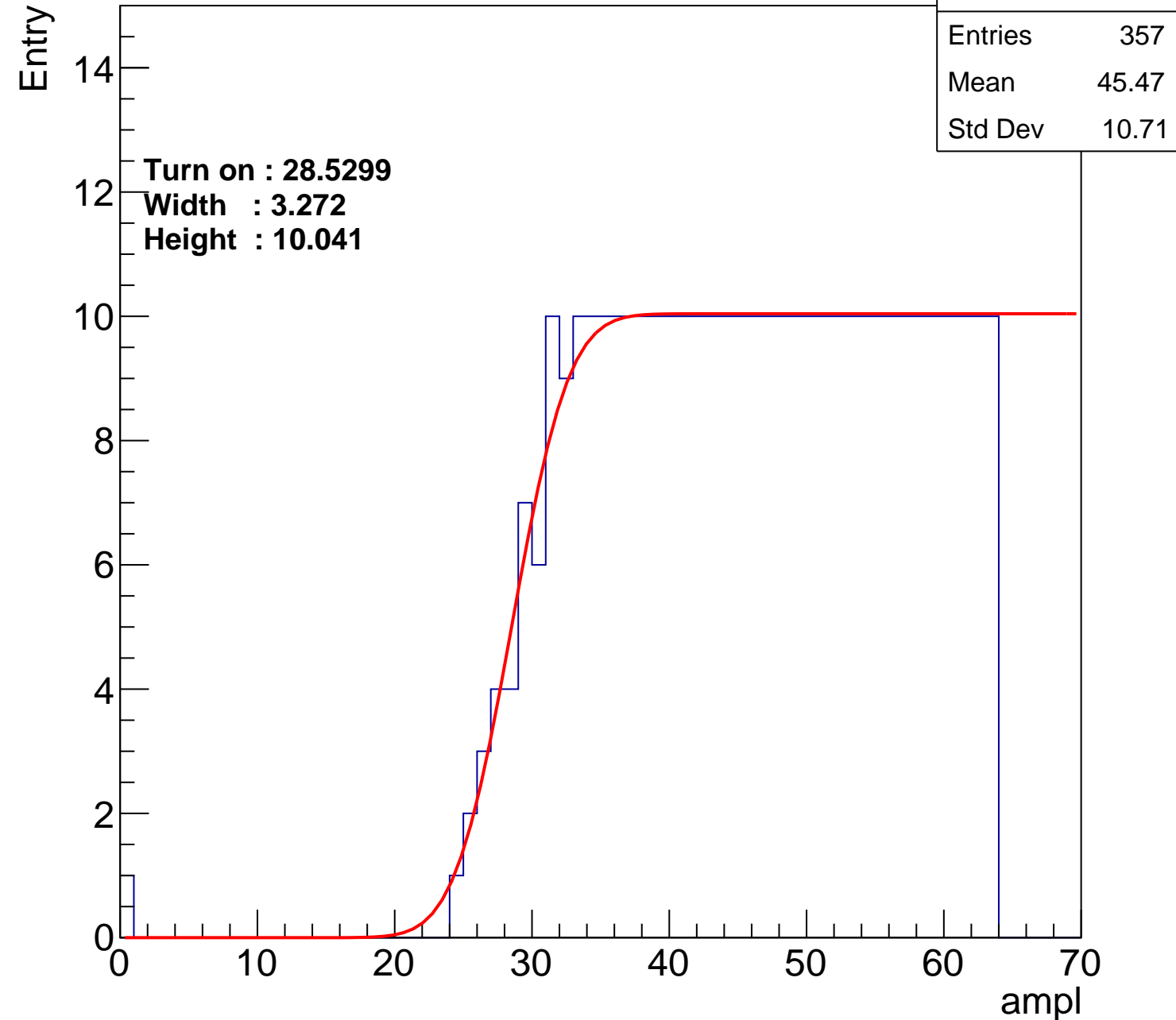
Width : 3.272

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.72
Std Dev	10.52

Turn on : 28.7139

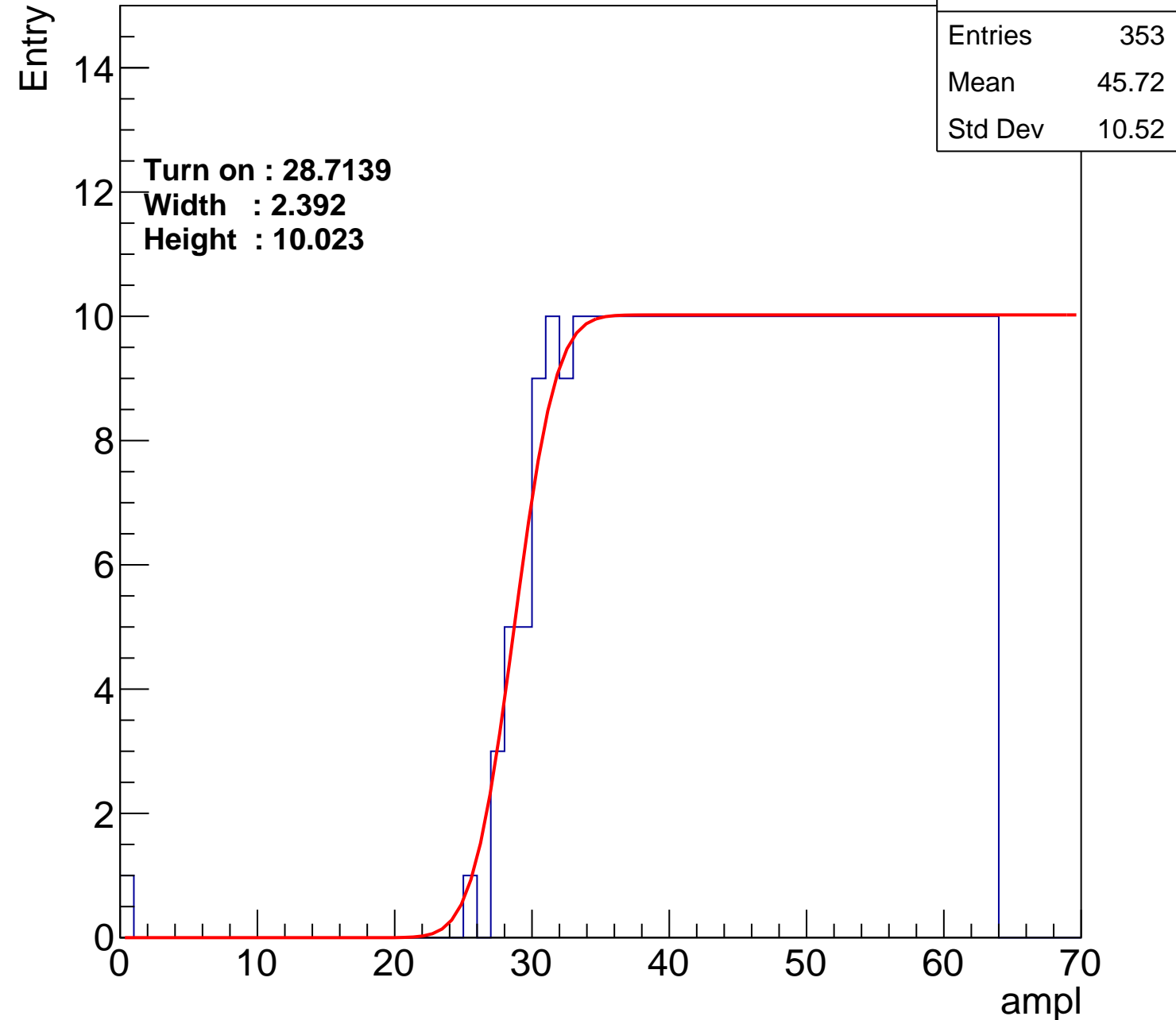
Width : 2.392

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	46.13
Std Dev	10.28

Turn on : 29.6648

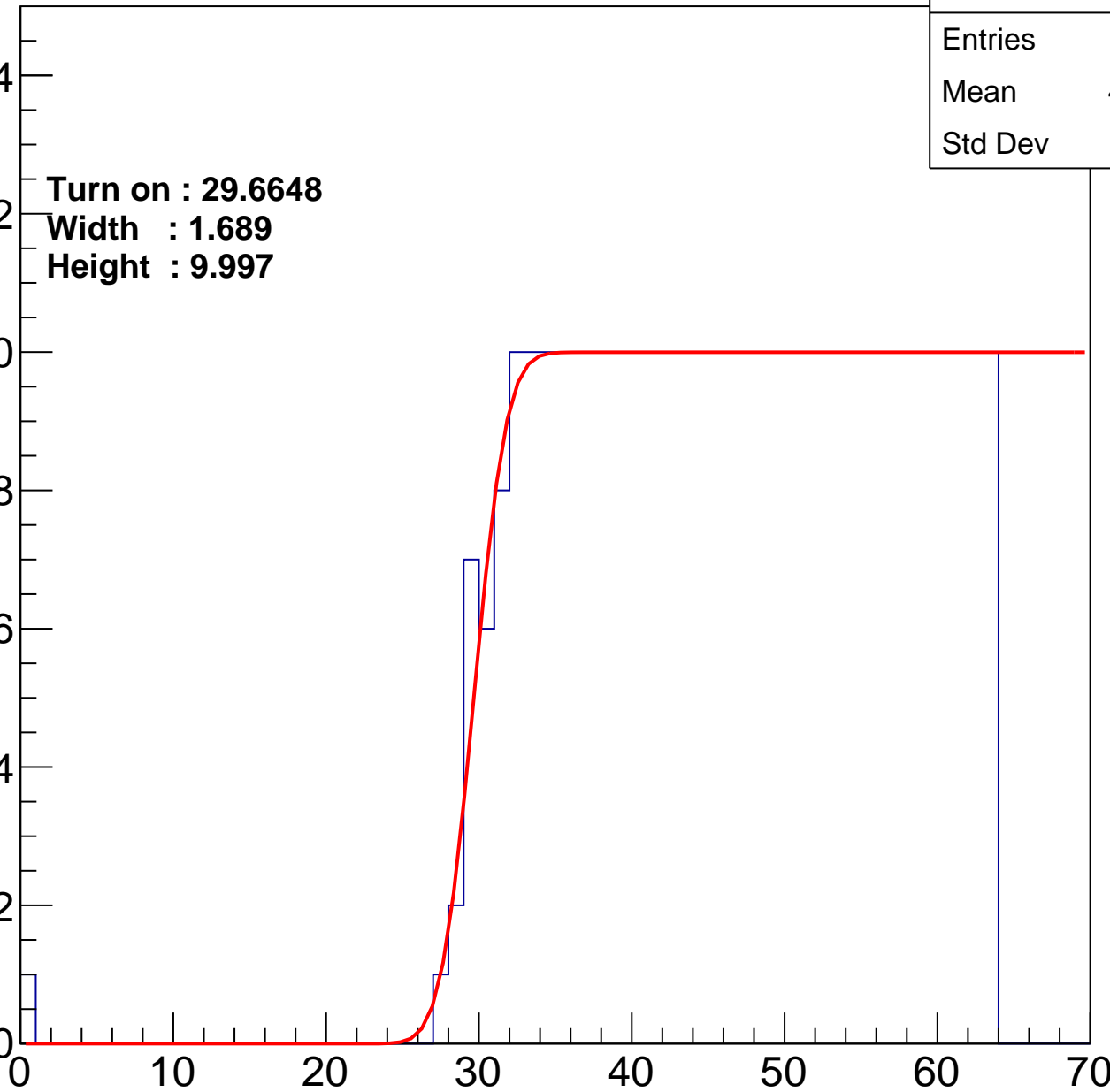
Width : 1.689

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.37
Std Dev	10.71

Turn on : 28.1143

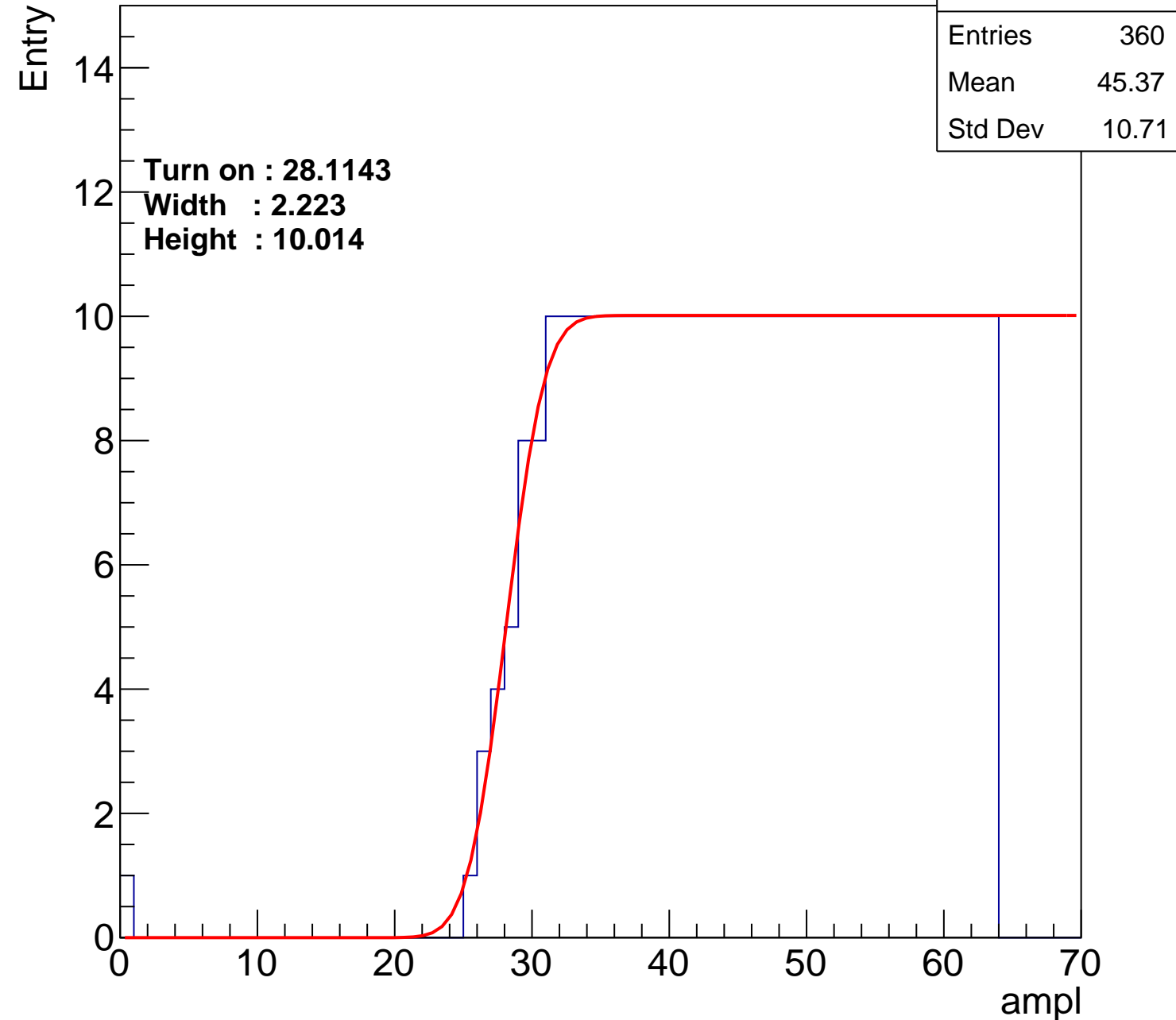
Width : 2.223

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch49

calib_packv5_042523_0143.root, FC#8, port C1

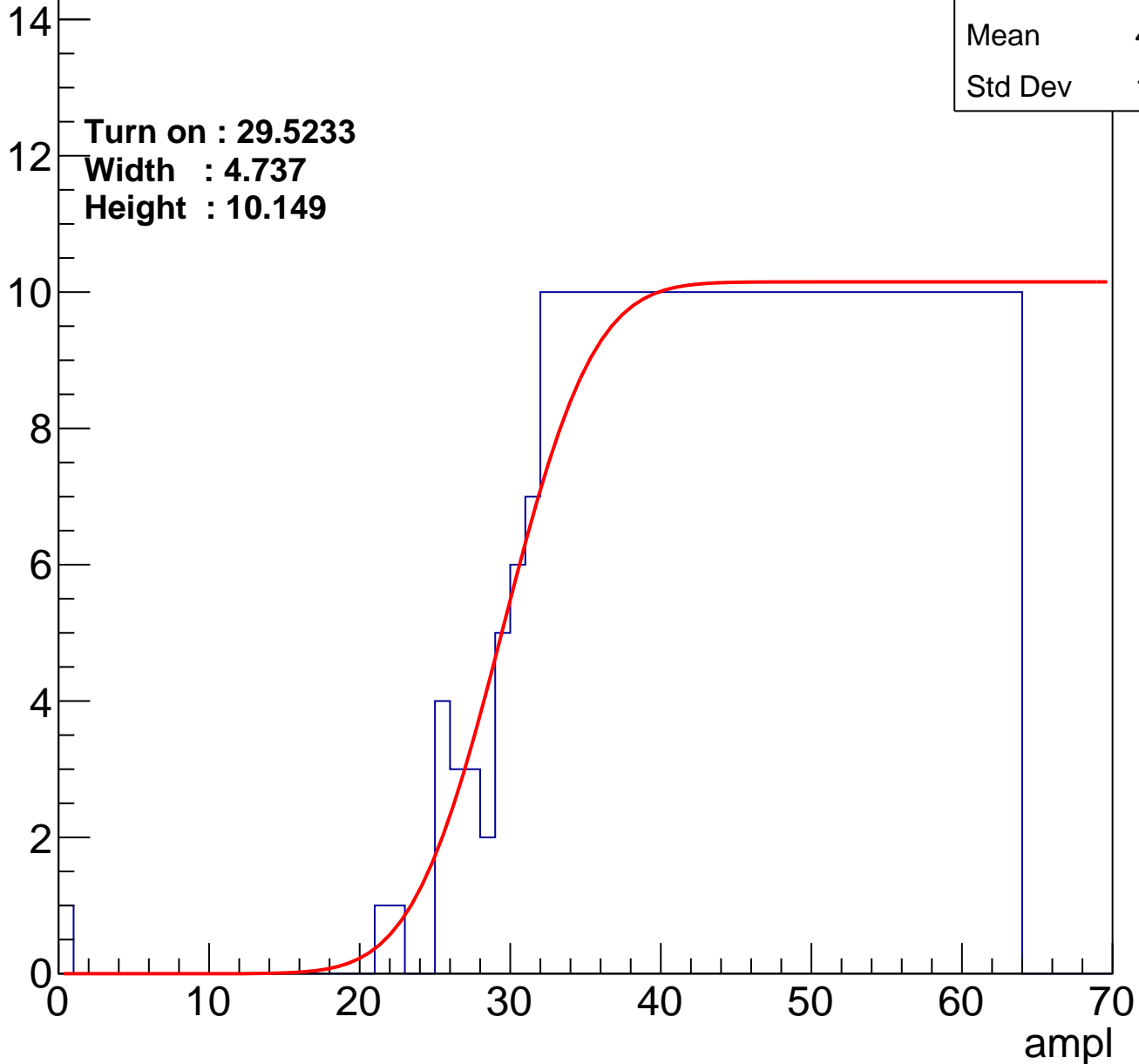
Entries	353
Mean	45.61
Std Dev	10.71

Turn on : 29.5233

Width : 4.737

Height : 10.149

Entry



B0L002S, U10-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.75
Std Dev	10.53

Turn on : 29.1269

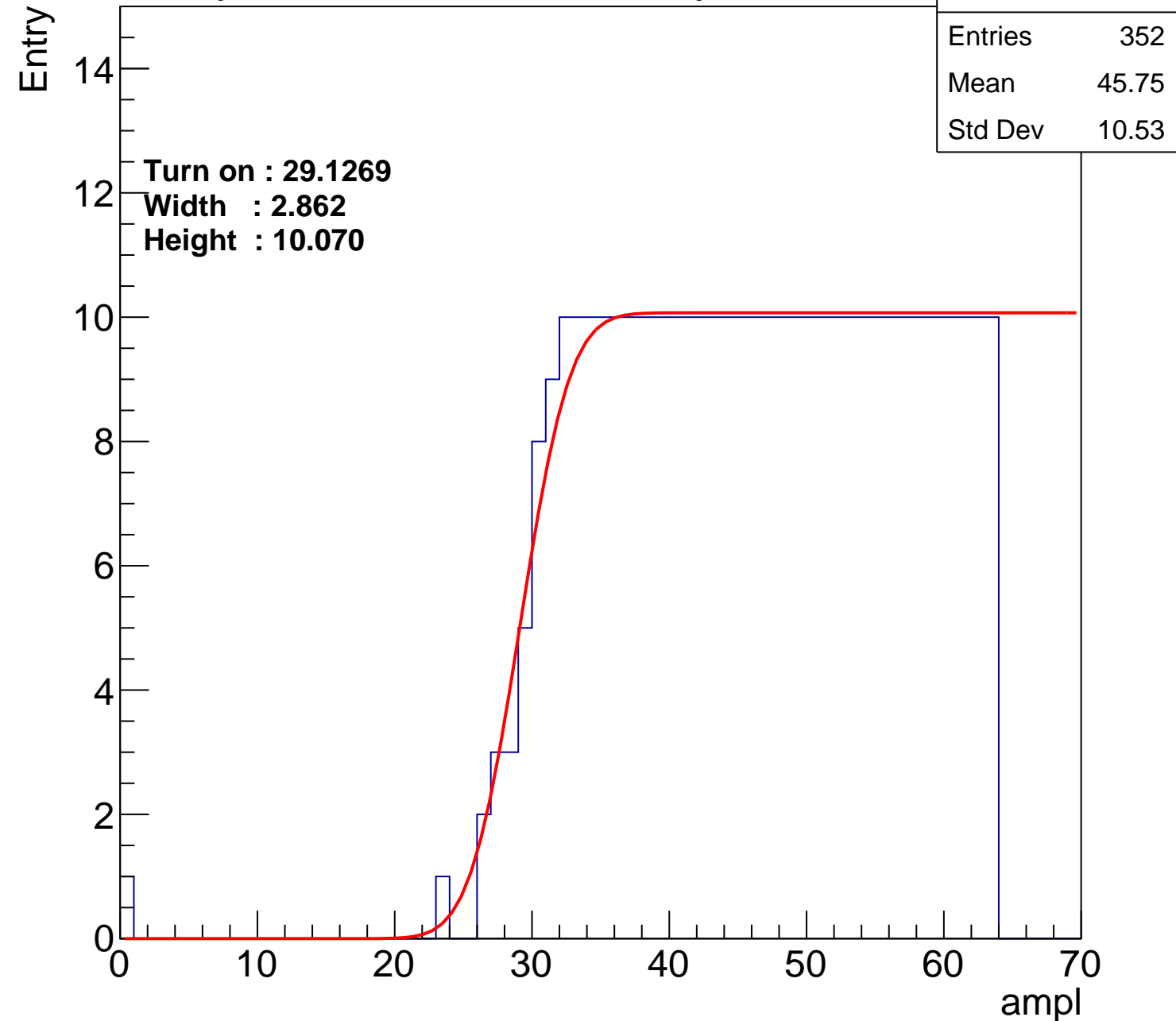
Width : 2.862

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.4957

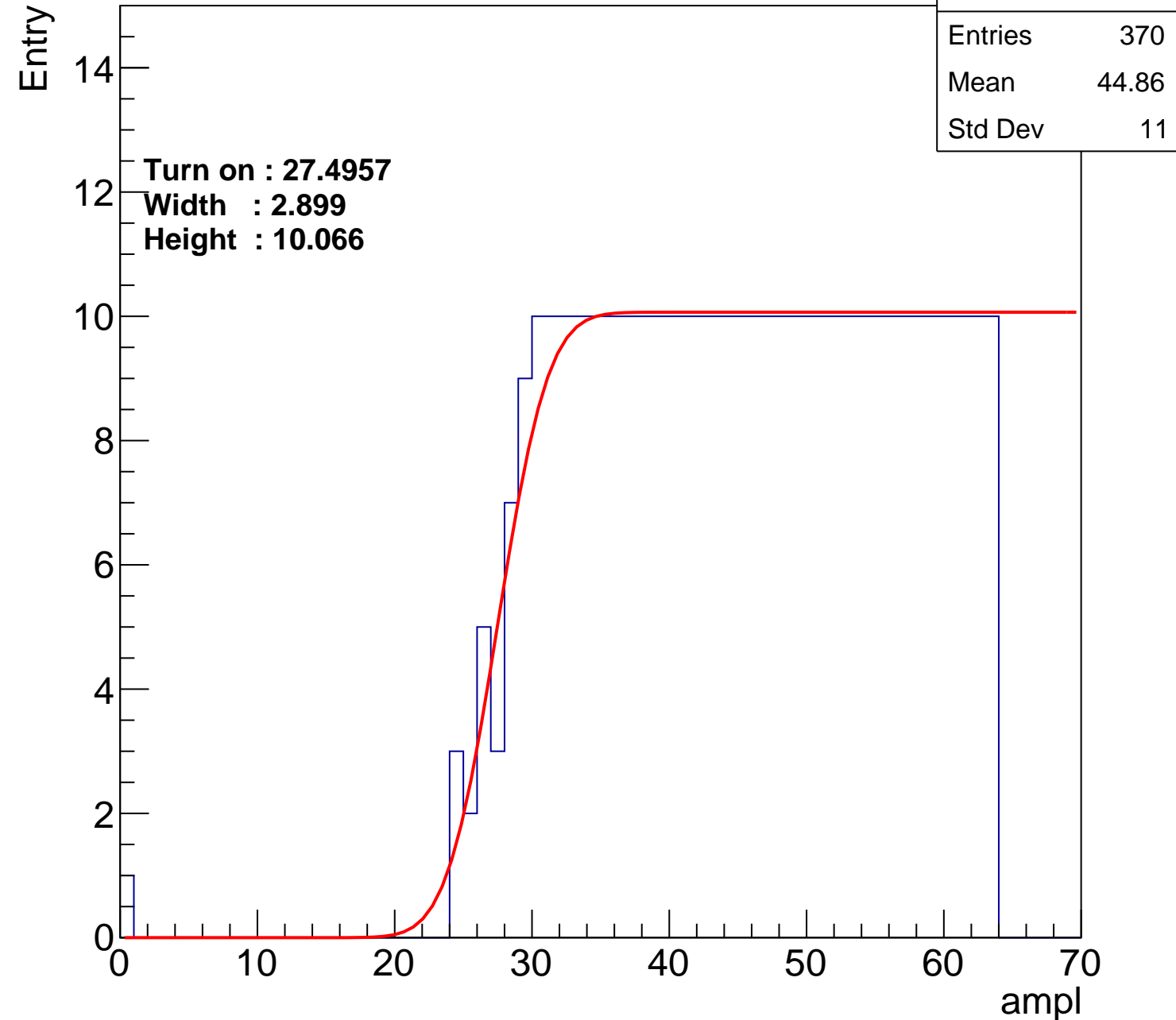
Width : 2.899

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.63
Std Dev	11.22

Turn on : 26.9907

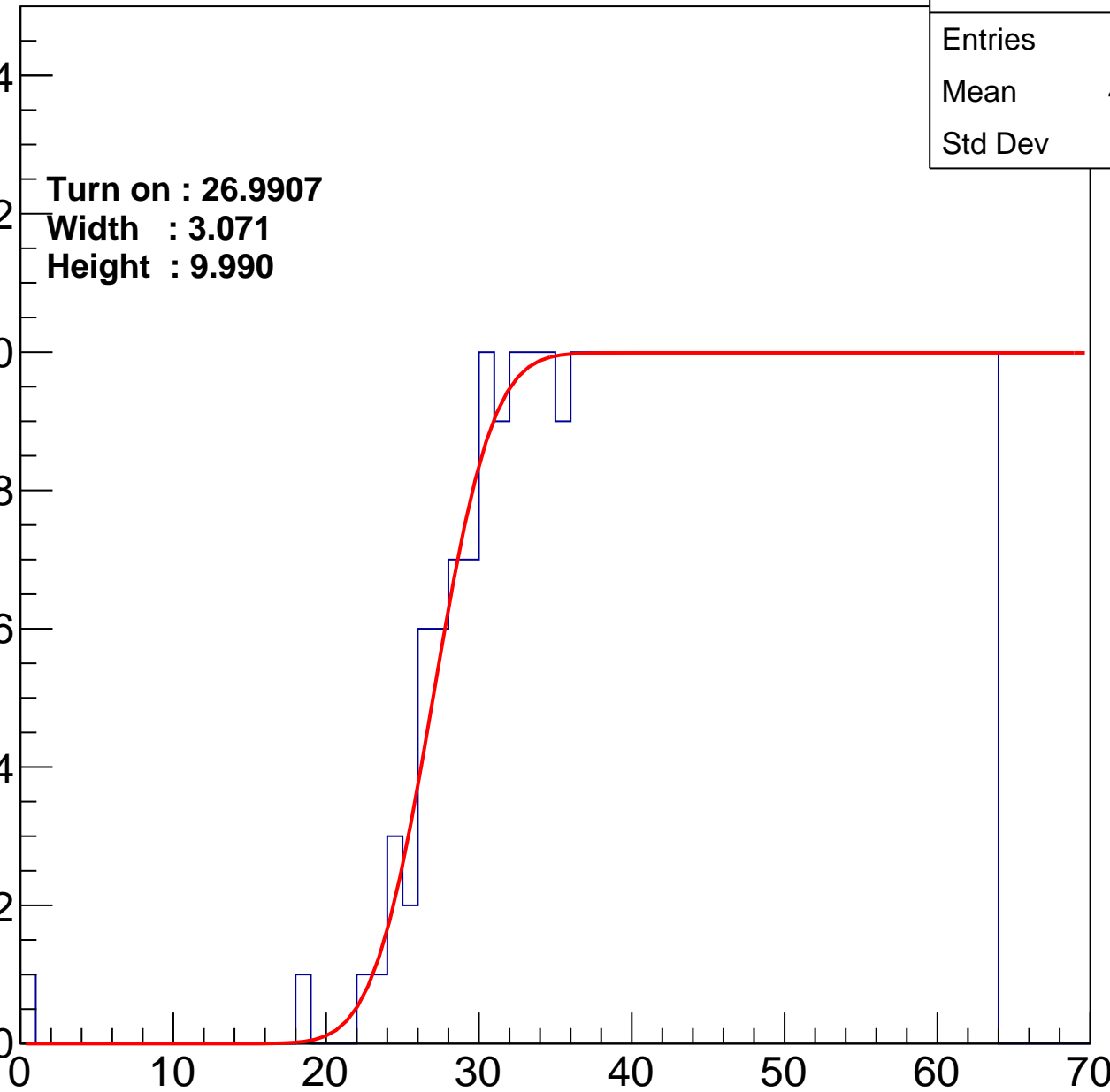
Width : 3.071

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.09
Std Dev	11.09

Turn on : 28.4201

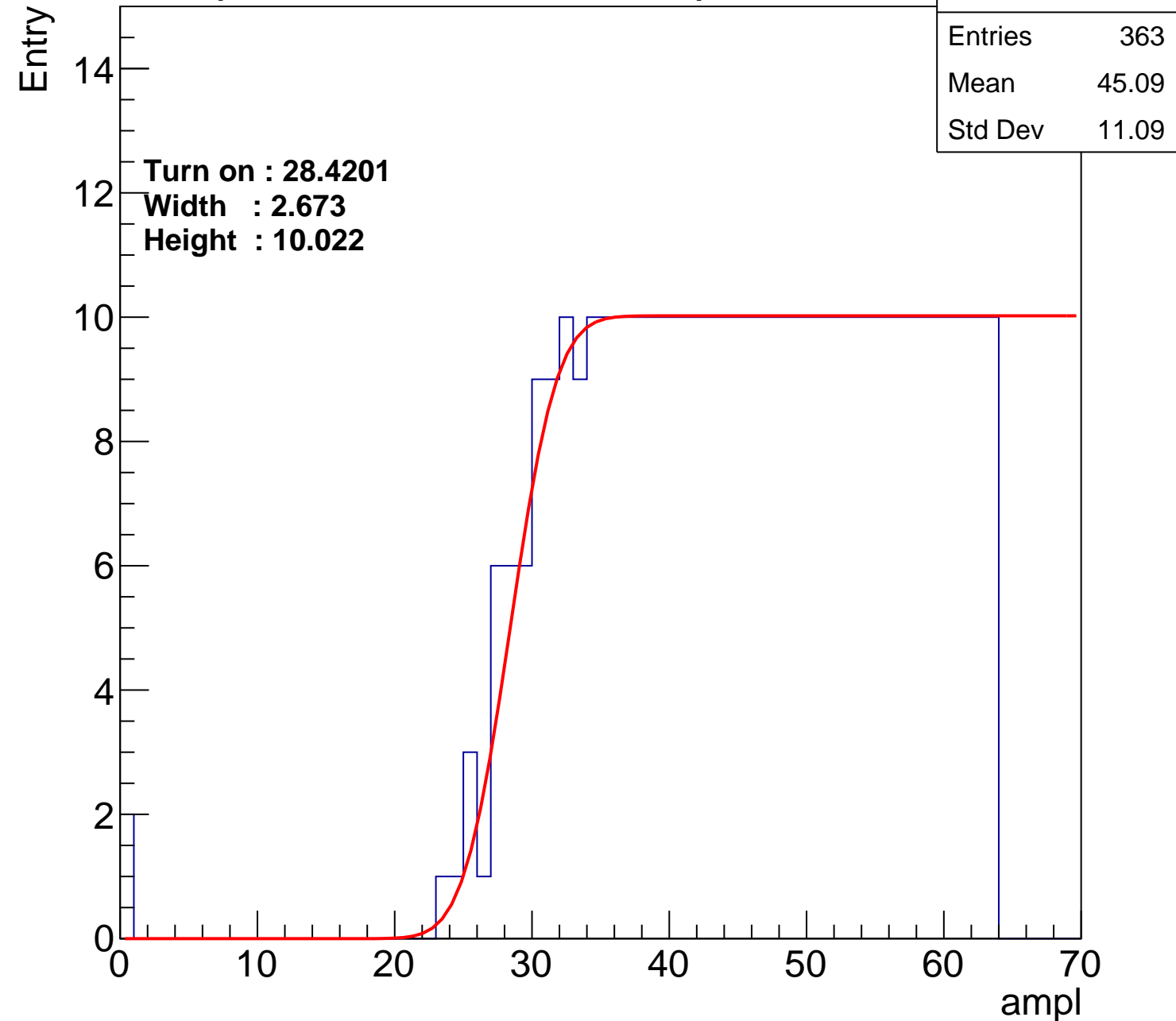
Width : 2.673

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.6201
Width : 2.267
Height : 10.008



B0L002S, U10-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 27.0245

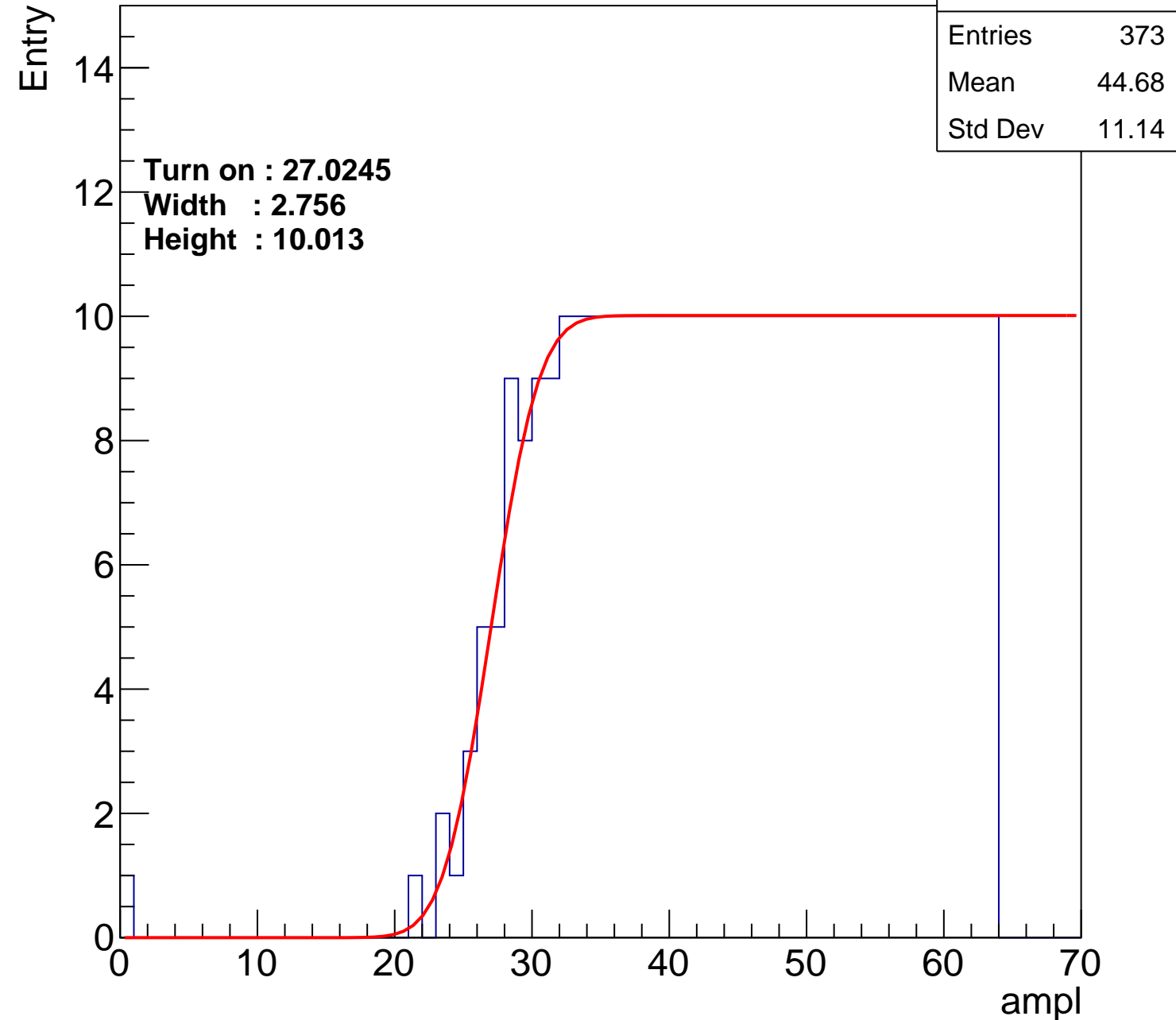
Width : 2.756

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 27.6164

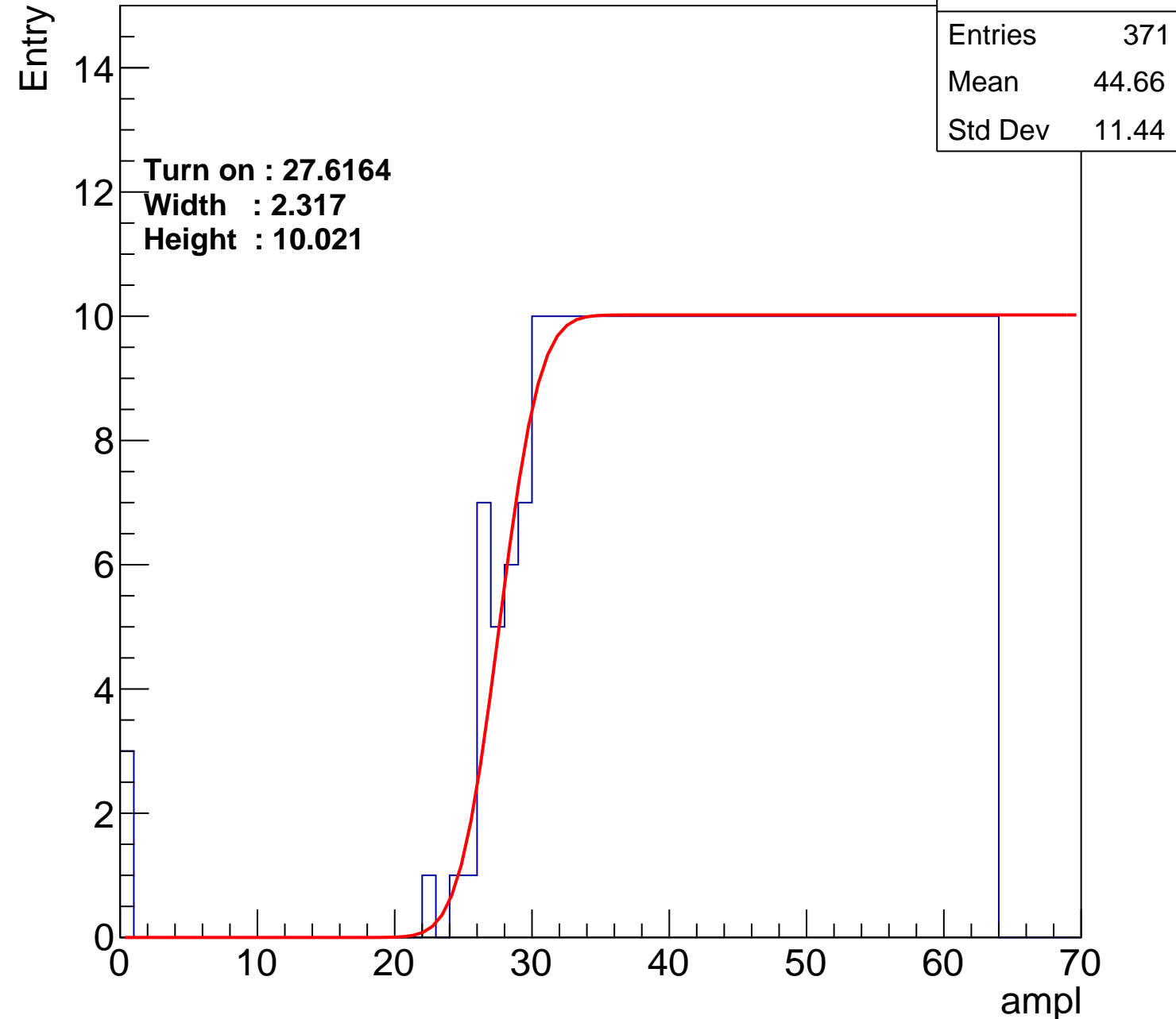
Width : 2.317

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.76
Std Dev	10.55

Turn on : 29.0928

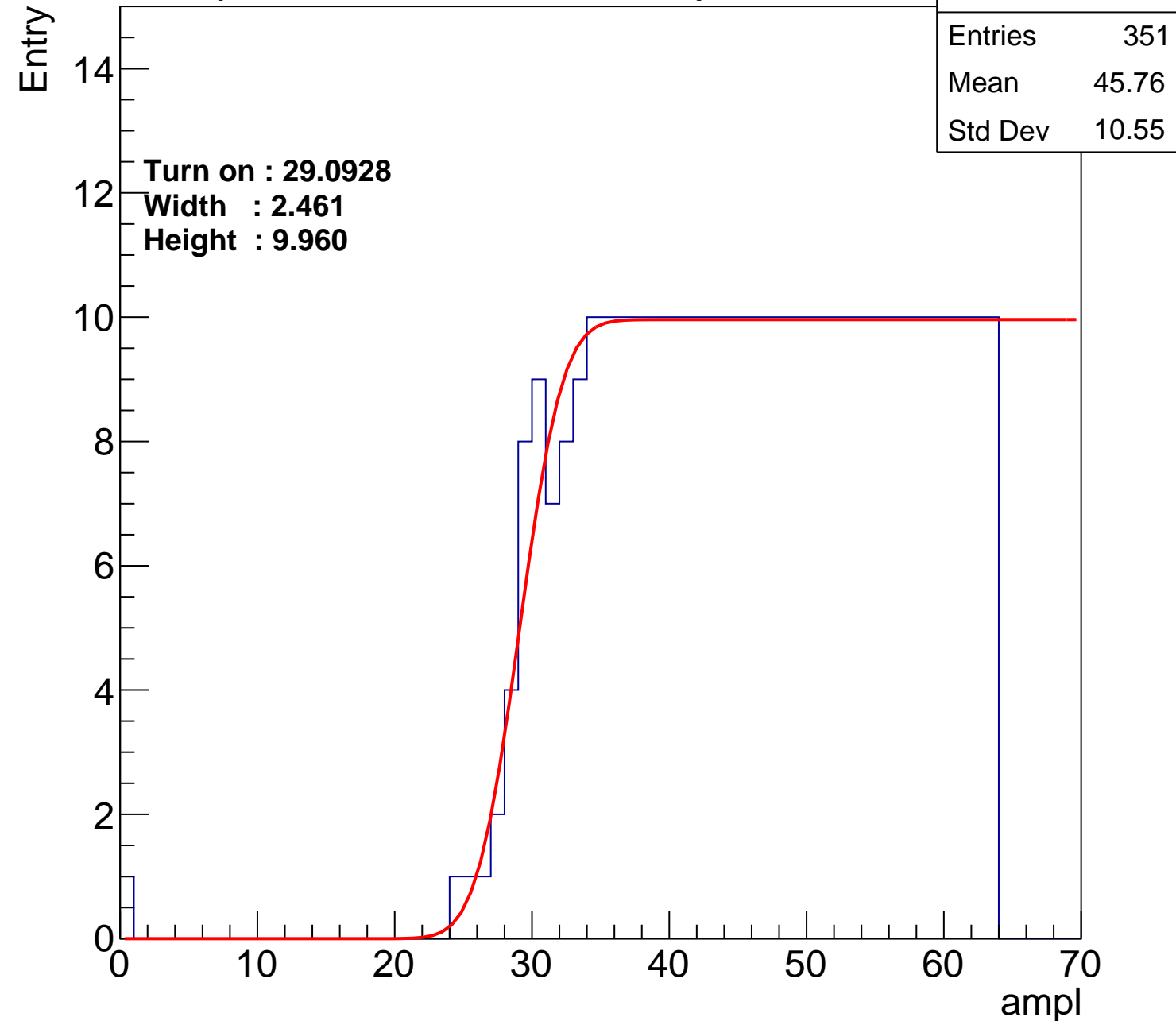
Width : 2.461

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch58

calib_packv5_042523_0143.root, FC#8, port C1

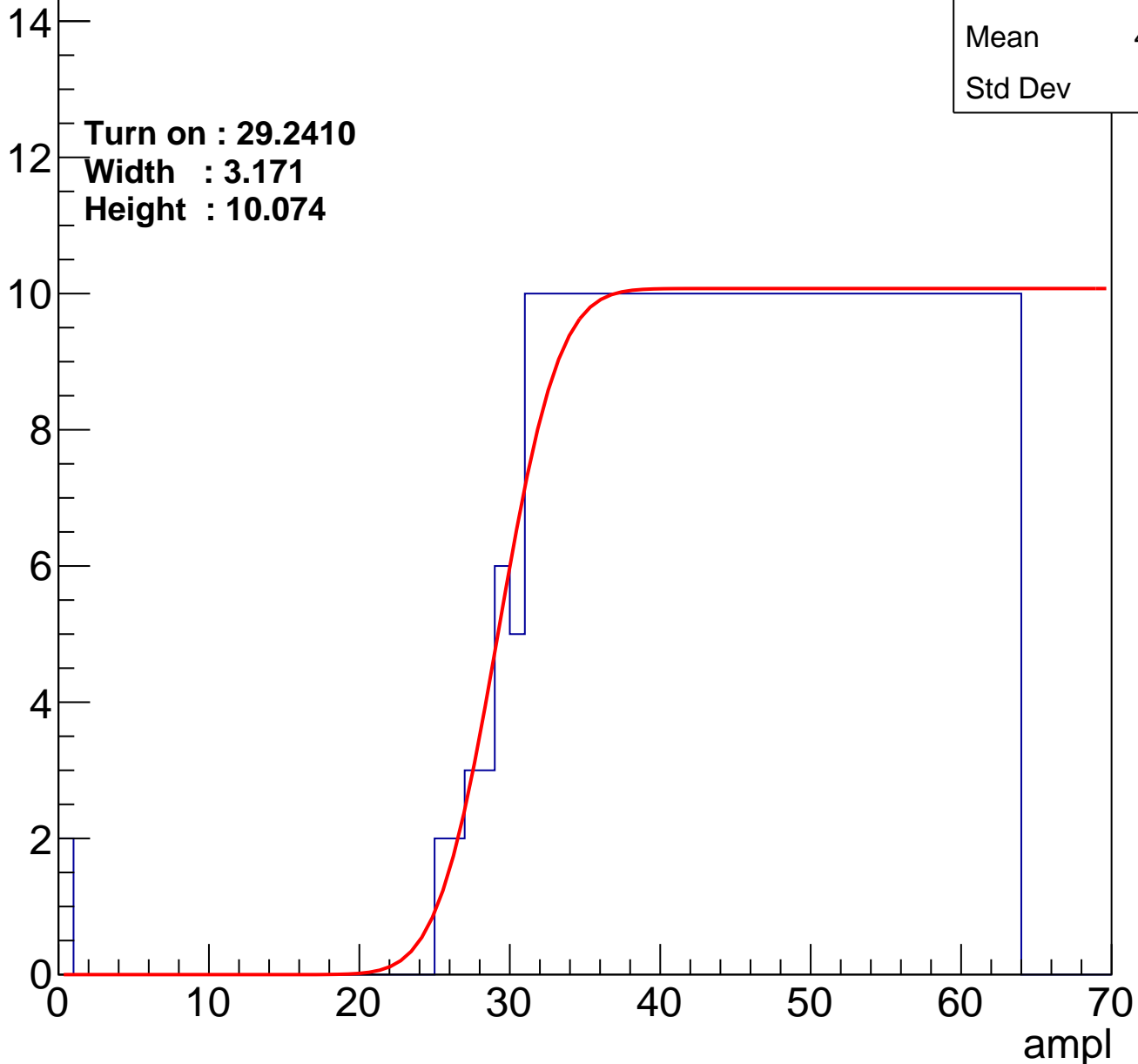
Entries	353
Mean	45.61
Std Dev	10.8

Turn on : 29.2410

Width : 3.171

Height : 10.074

Entry



B0L002S, U10-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.4312

Width : 1.931

Height : 10.000

Entries	368
Mean	44.97
Std Dev	10.93



B0L002S, U10-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.38
Std Dev	10.7

Turn on : 28.1451

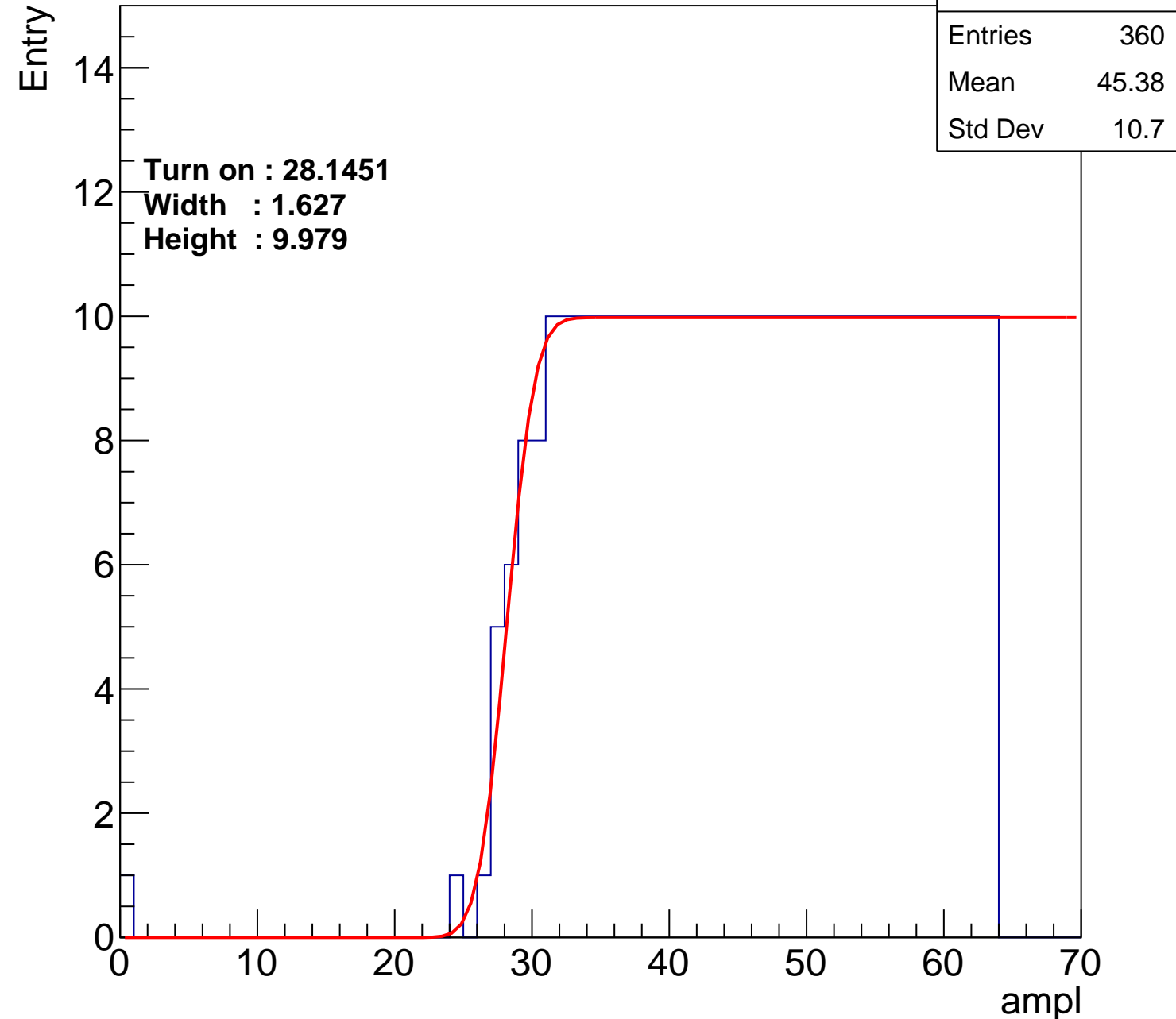
Width : 1.627

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.35
Std Dev	10.95

Turn on : 28.8461

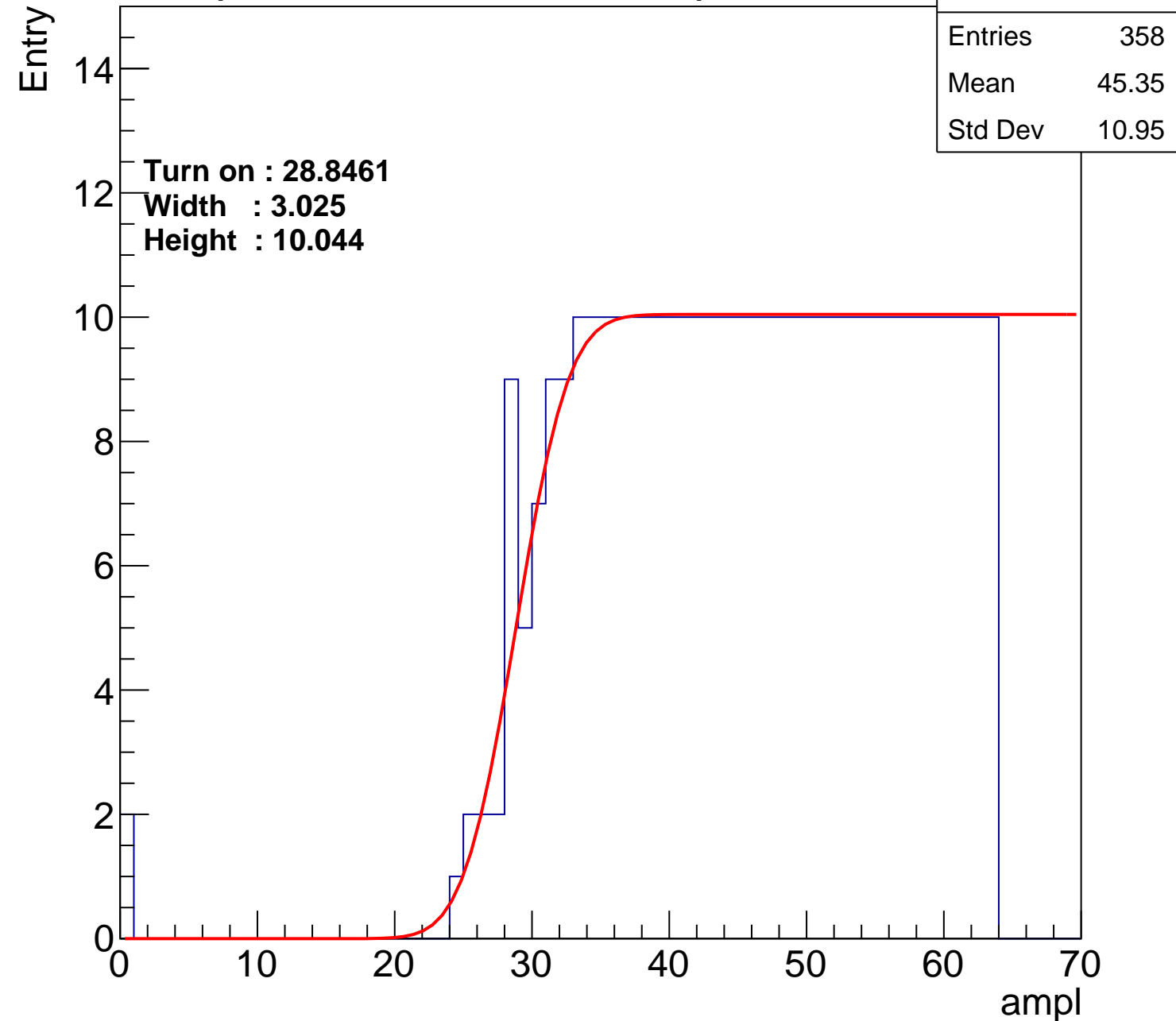
Width : 3.025

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch62

calib_packv5_042523_0143.root, FC#8, port C1

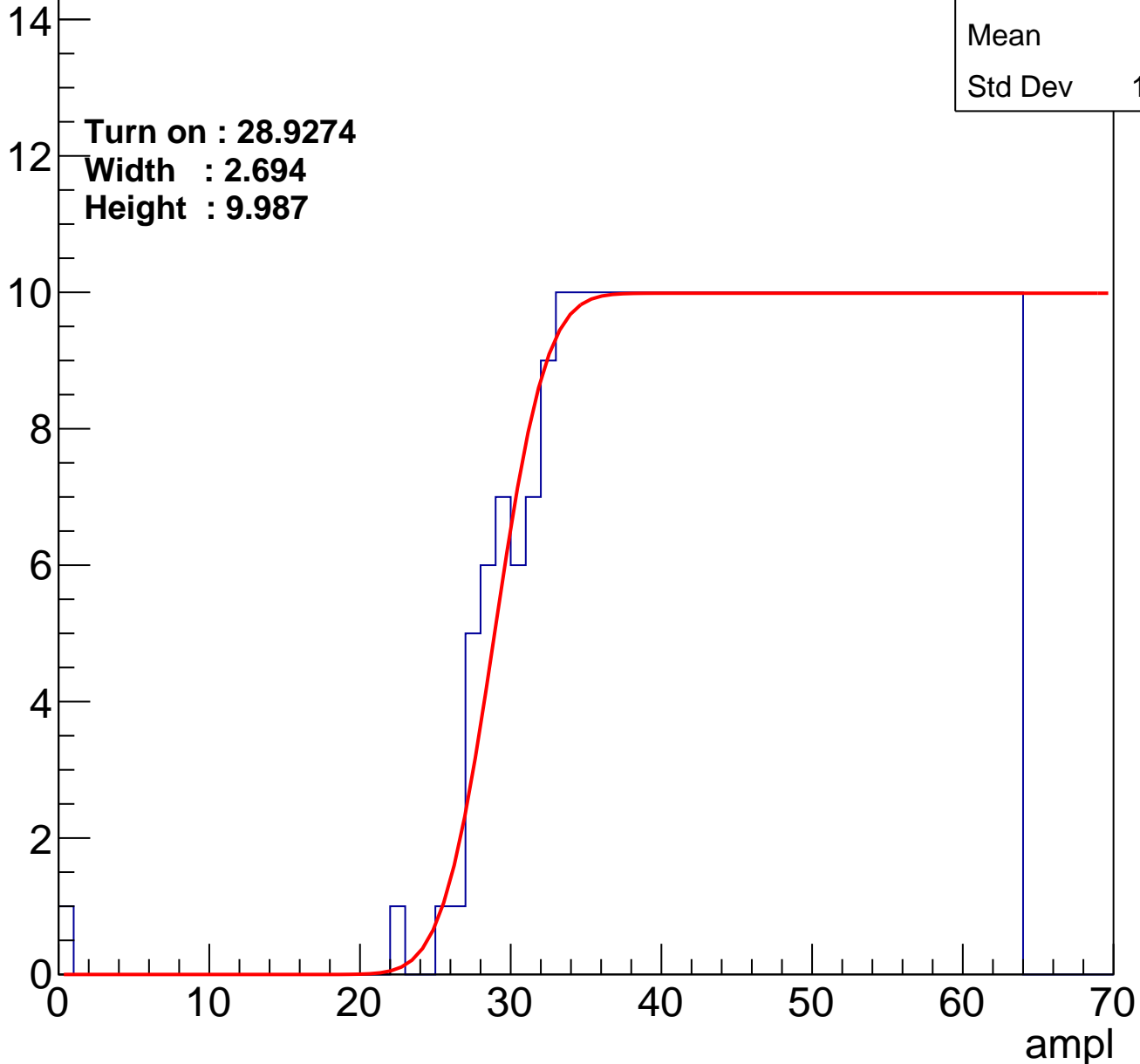
Entries	354
Mean	45.6
Std Dev	10.65

Turn on : 28.9274

Width : 2.694

Height : 9.987

Entry



B0L002S, U10-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.24
Std Dev	11.56

Turn on : 26.2685

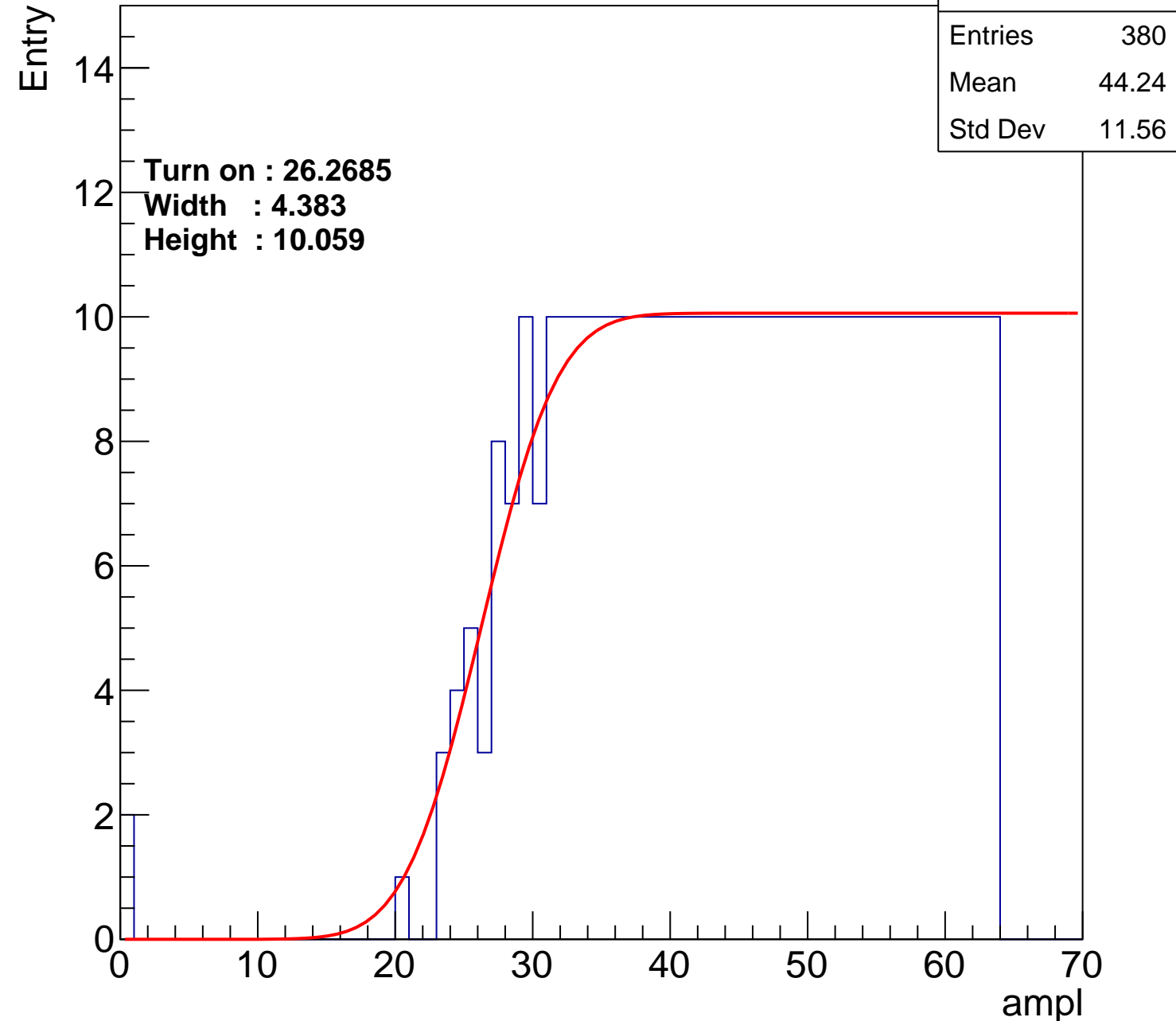
Width : 4.383

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch64

calib_packv5_042523_0143.root, FC#8, port C1

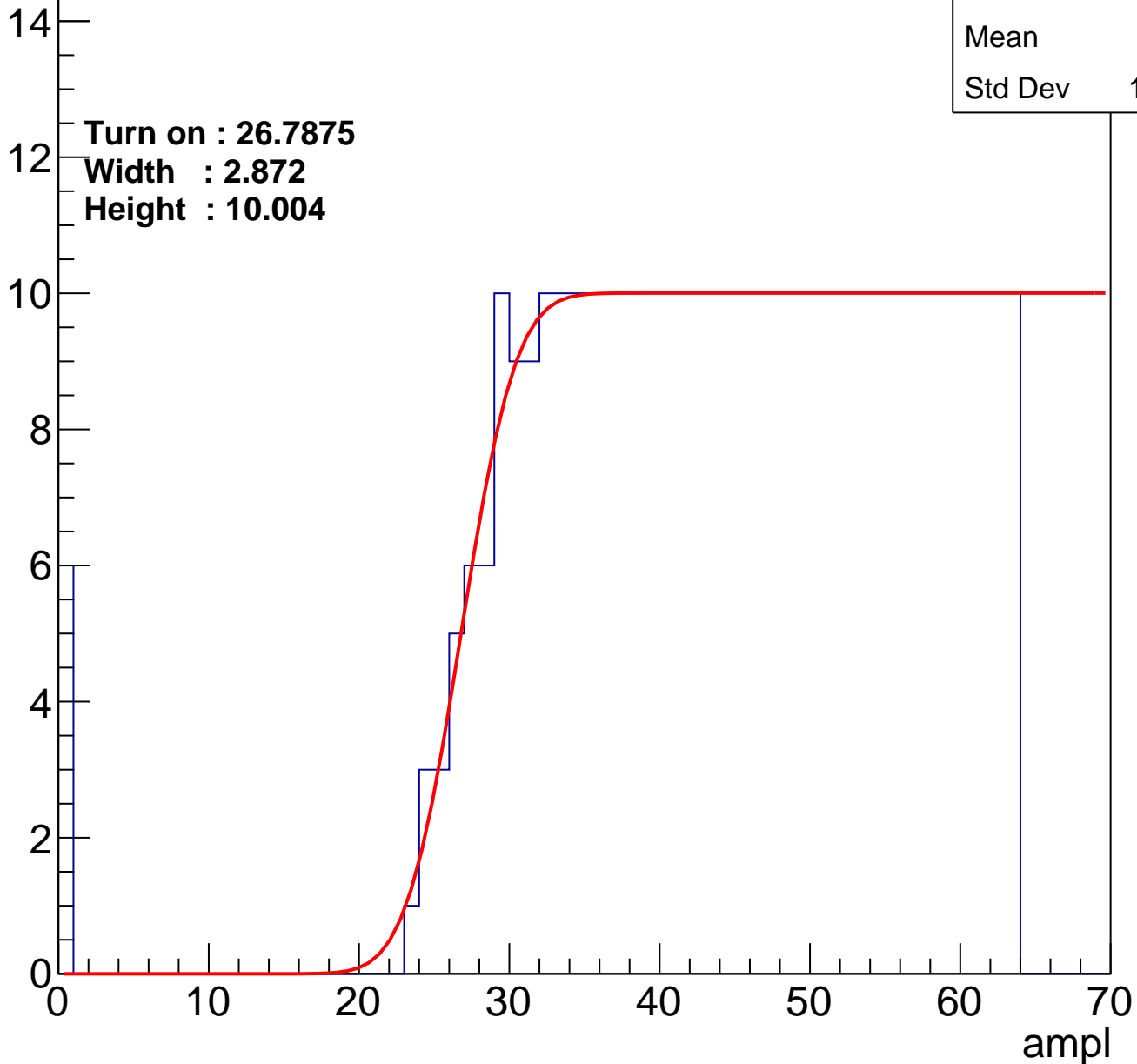
Entries	378
Mean	44.1
Std Dev	12.17

Turn on : 26.7875

Width : 2.872

Height : 10.004

Entry



B0L002S, U10-ch65

calib_packv5_042523_0143.root, FC#8, port C1

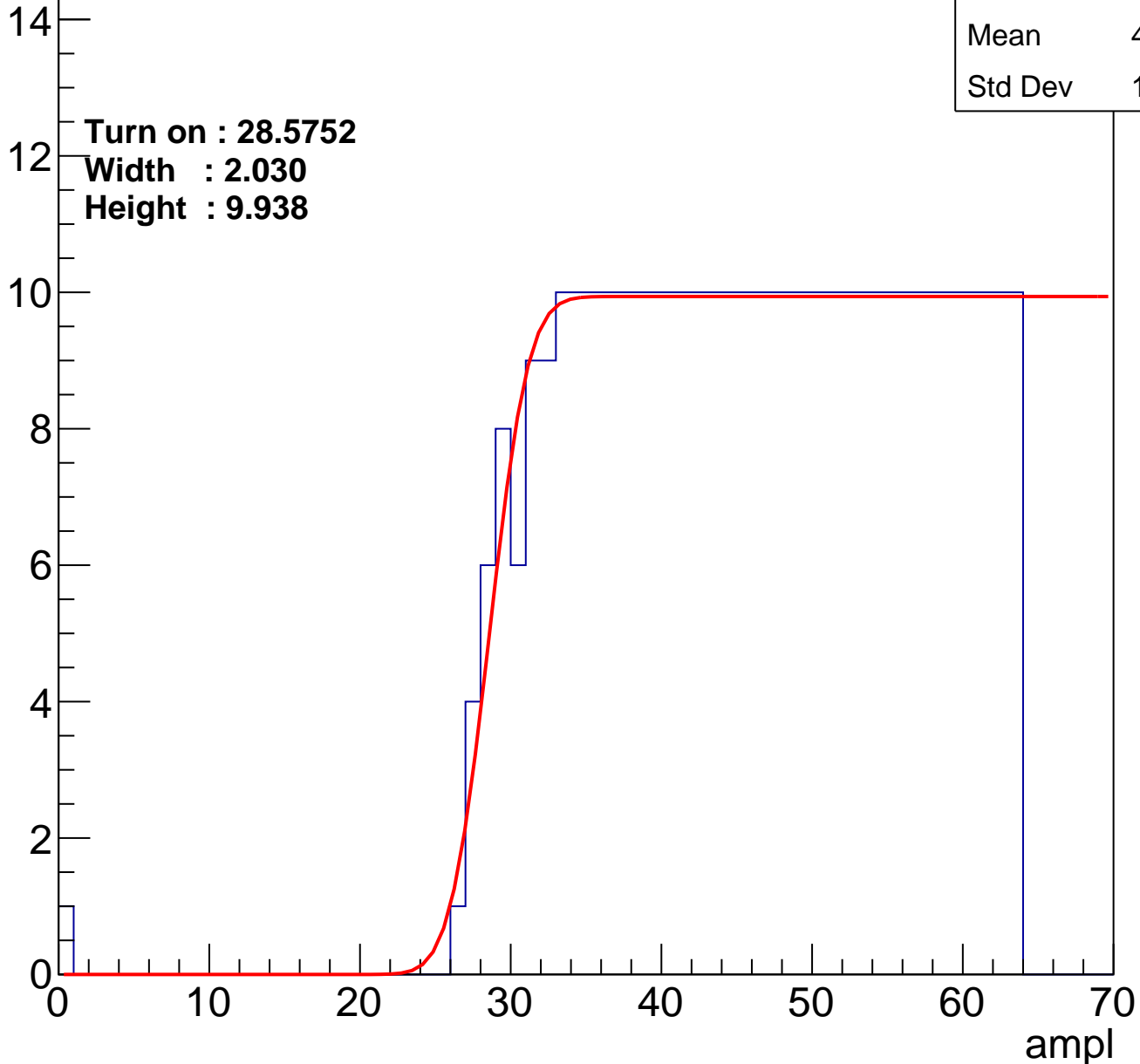
Entries	354
Mean	45.65
Std Dev	10.57

Turn on : 28.5752

Width : 2.030

Height : 9.938

Entry



B0L002S, U10-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.38
Std Dev	10.74

Turn on : 28.1711

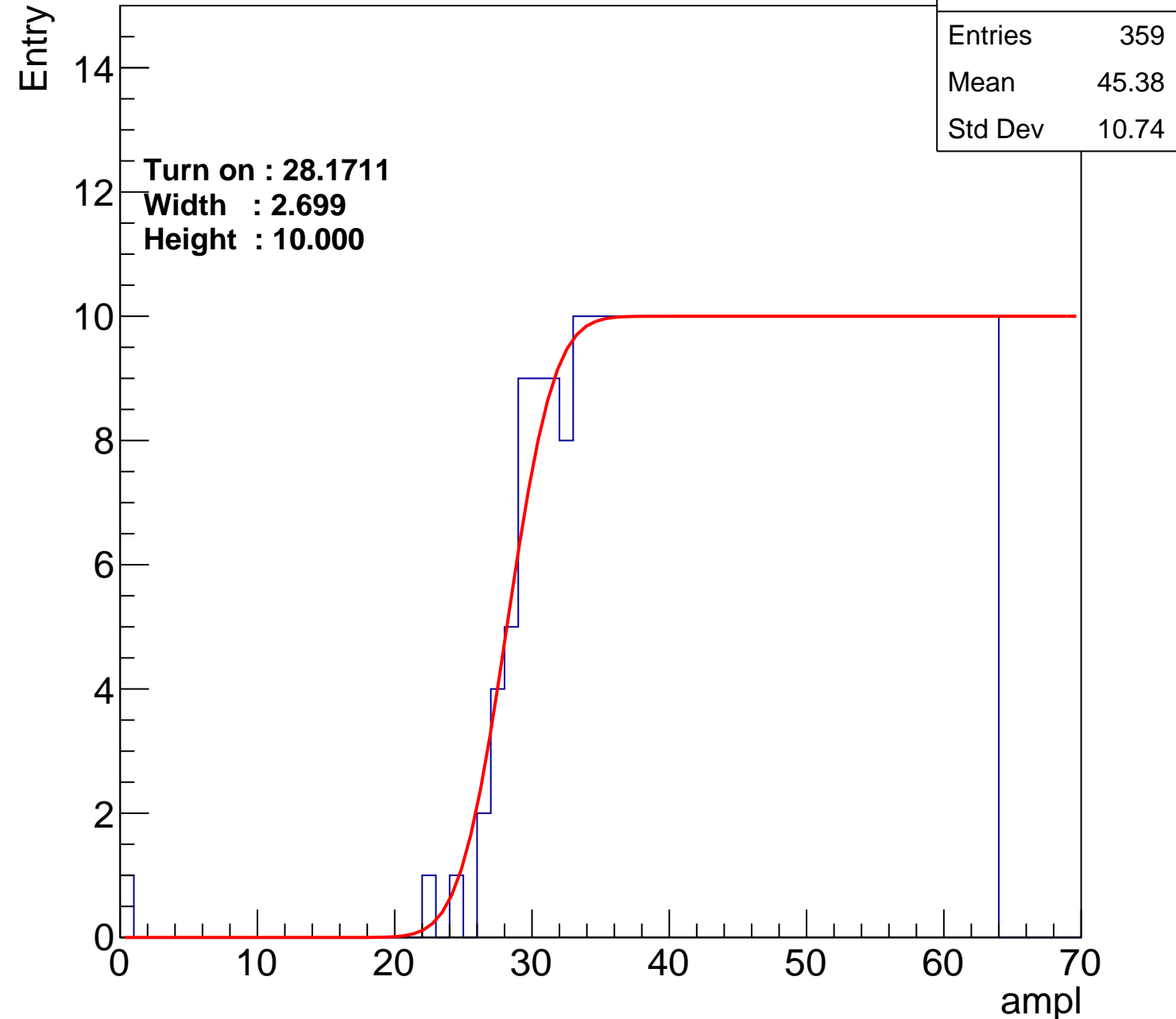
Width : 2.699

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.75
Std Dev	10.53

Turn on : 28.9889

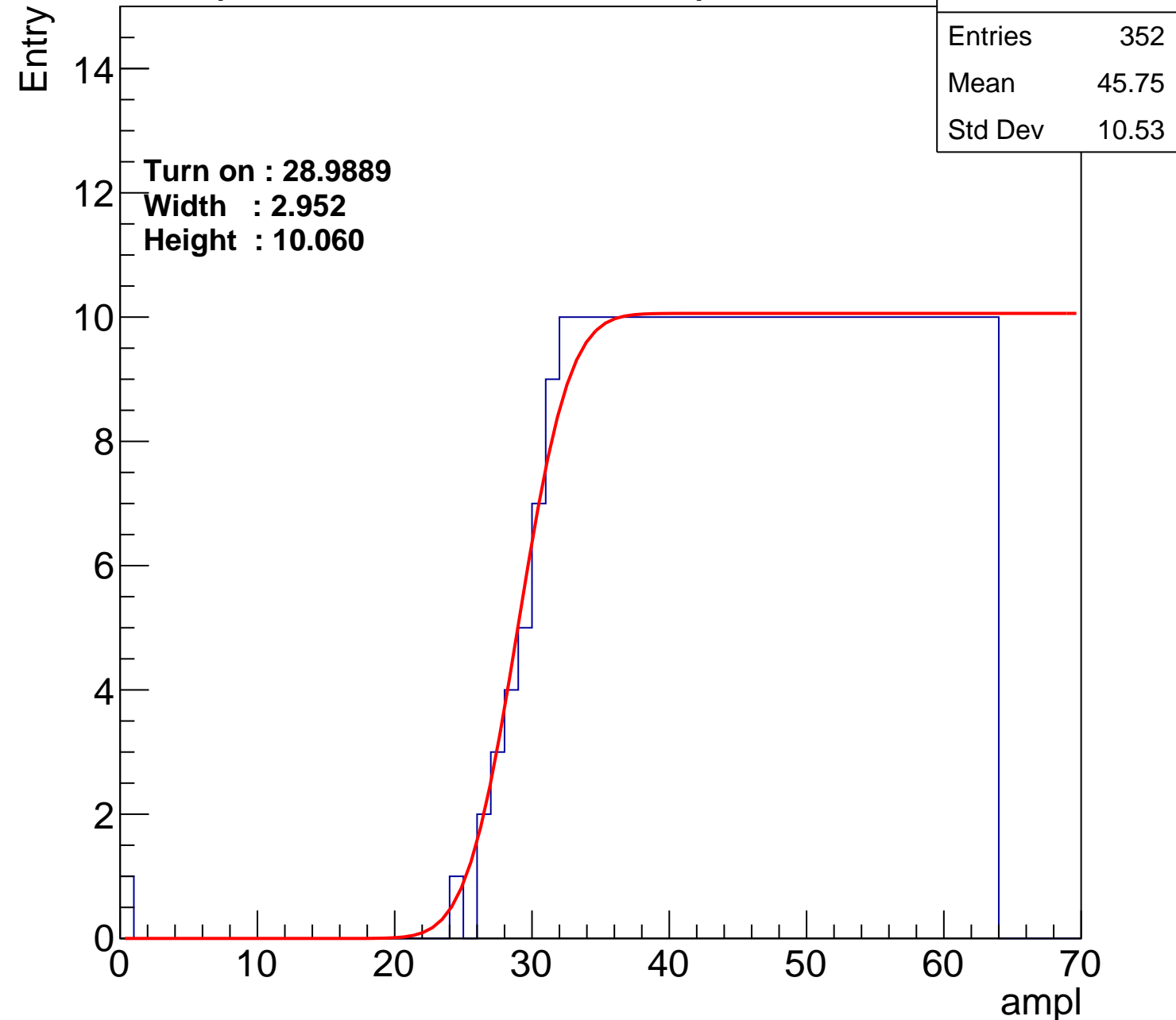
Width : 2.952

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch68

calib_packv5_042523_0143.root, FC#8, port C1

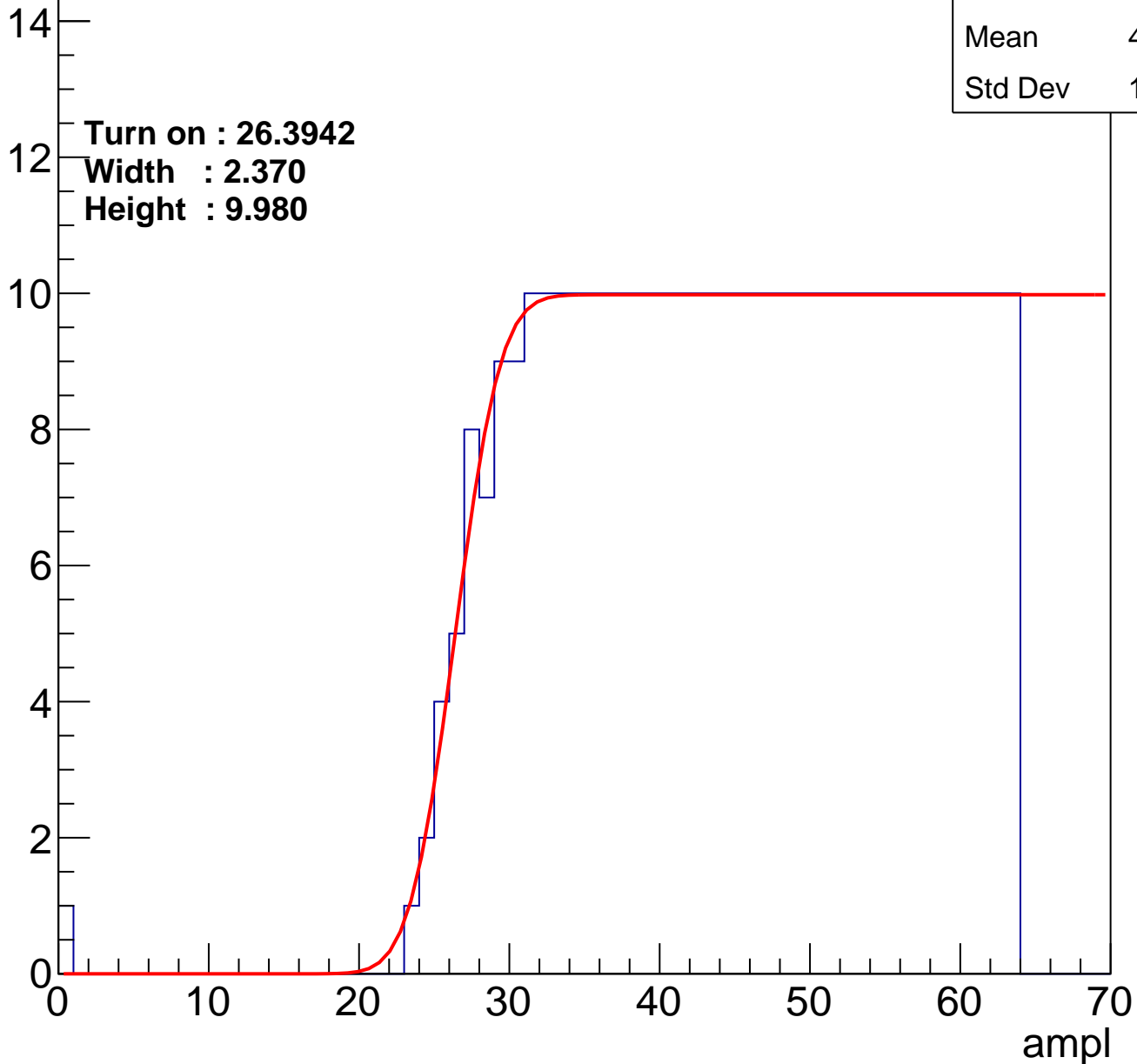
Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.3942

Width : 2.370

Height : 9.980

Entry



B0L002S, U10-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.19
Std Dev	10.29

Turn on : 30.0689

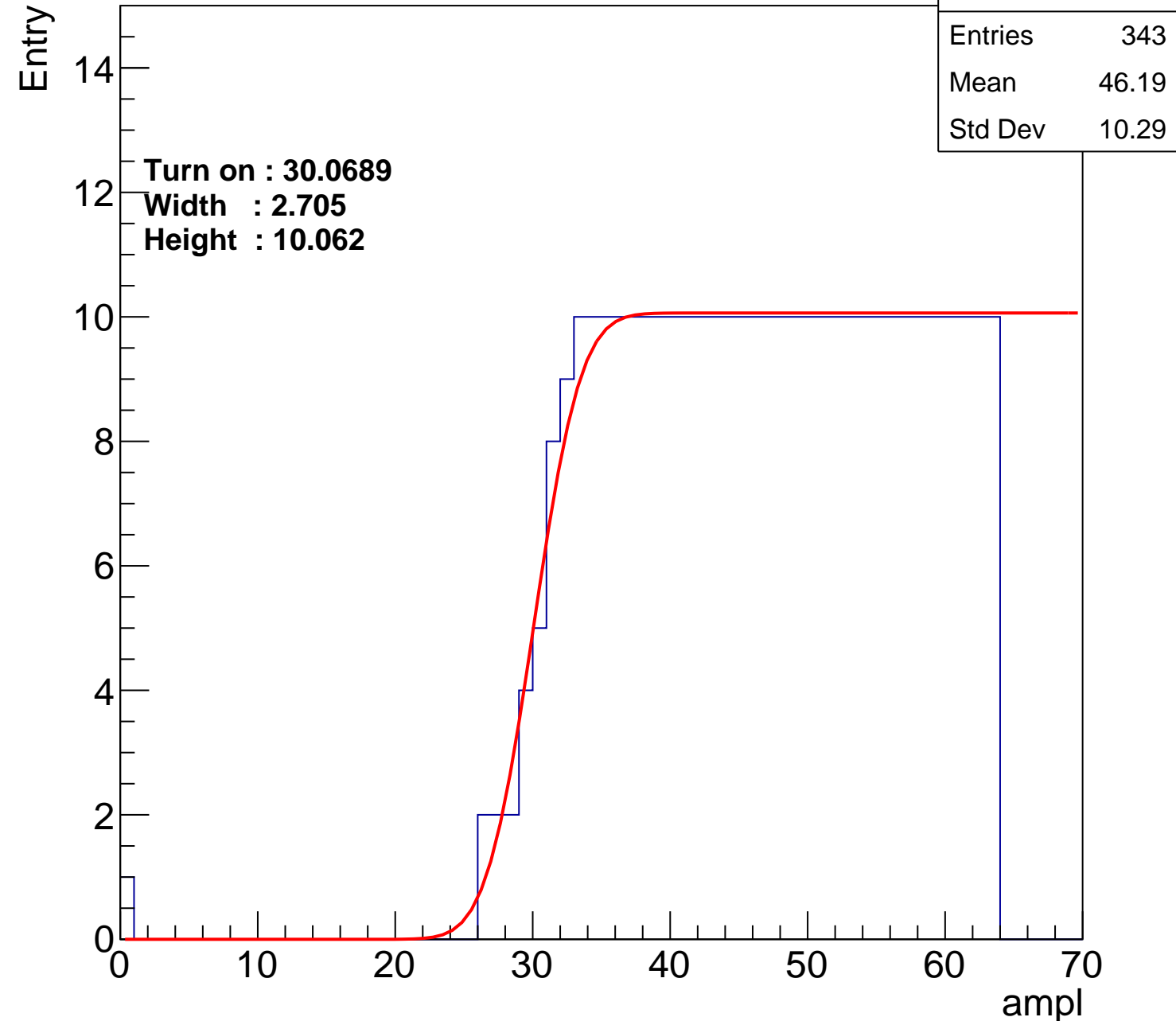
Width : 2.705

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.57
Std Dev	11.02

Turn on : 29.3799

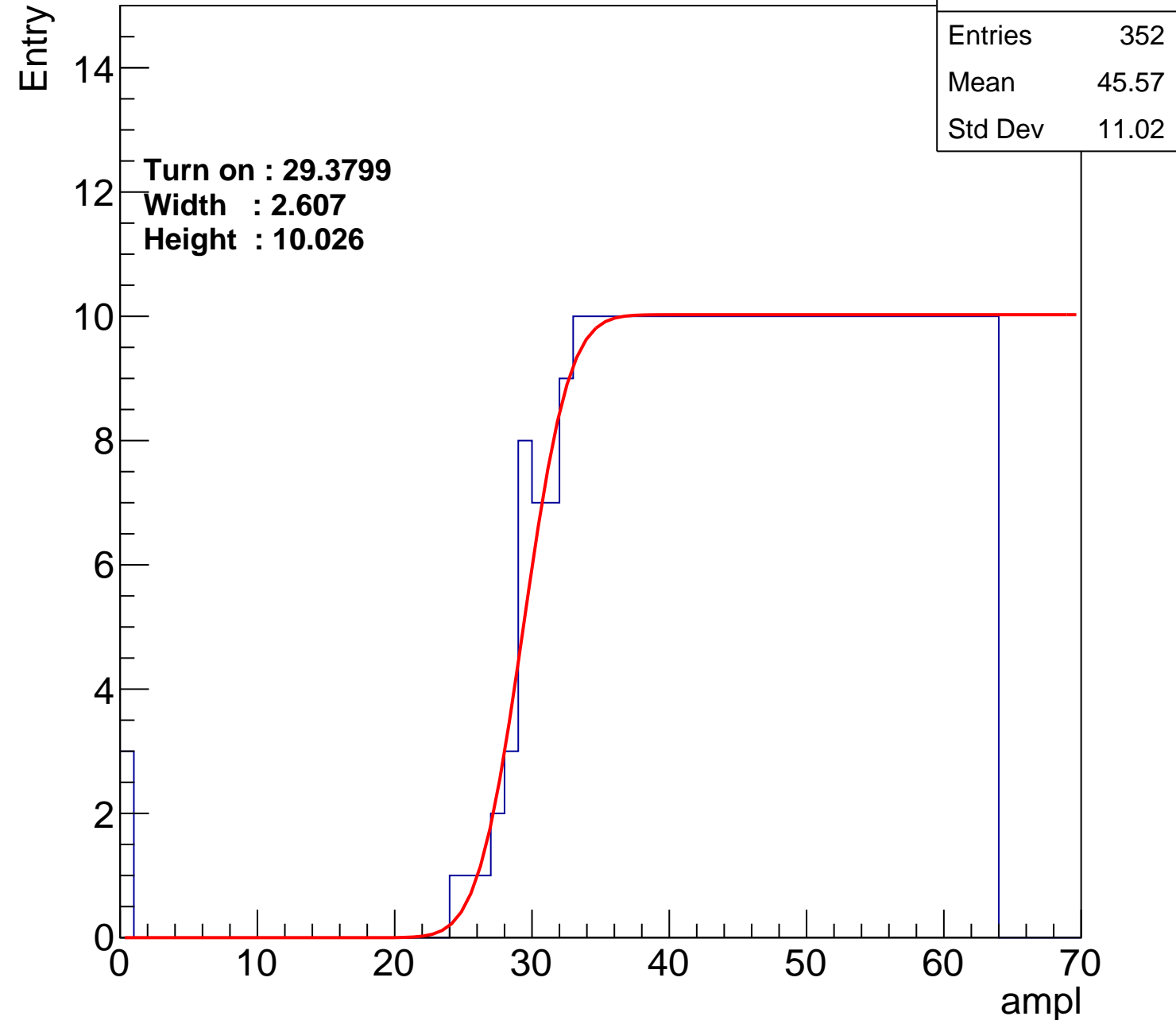
Width : 2.607

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.7
Std Dev	10.76

Turn on : 28.6935

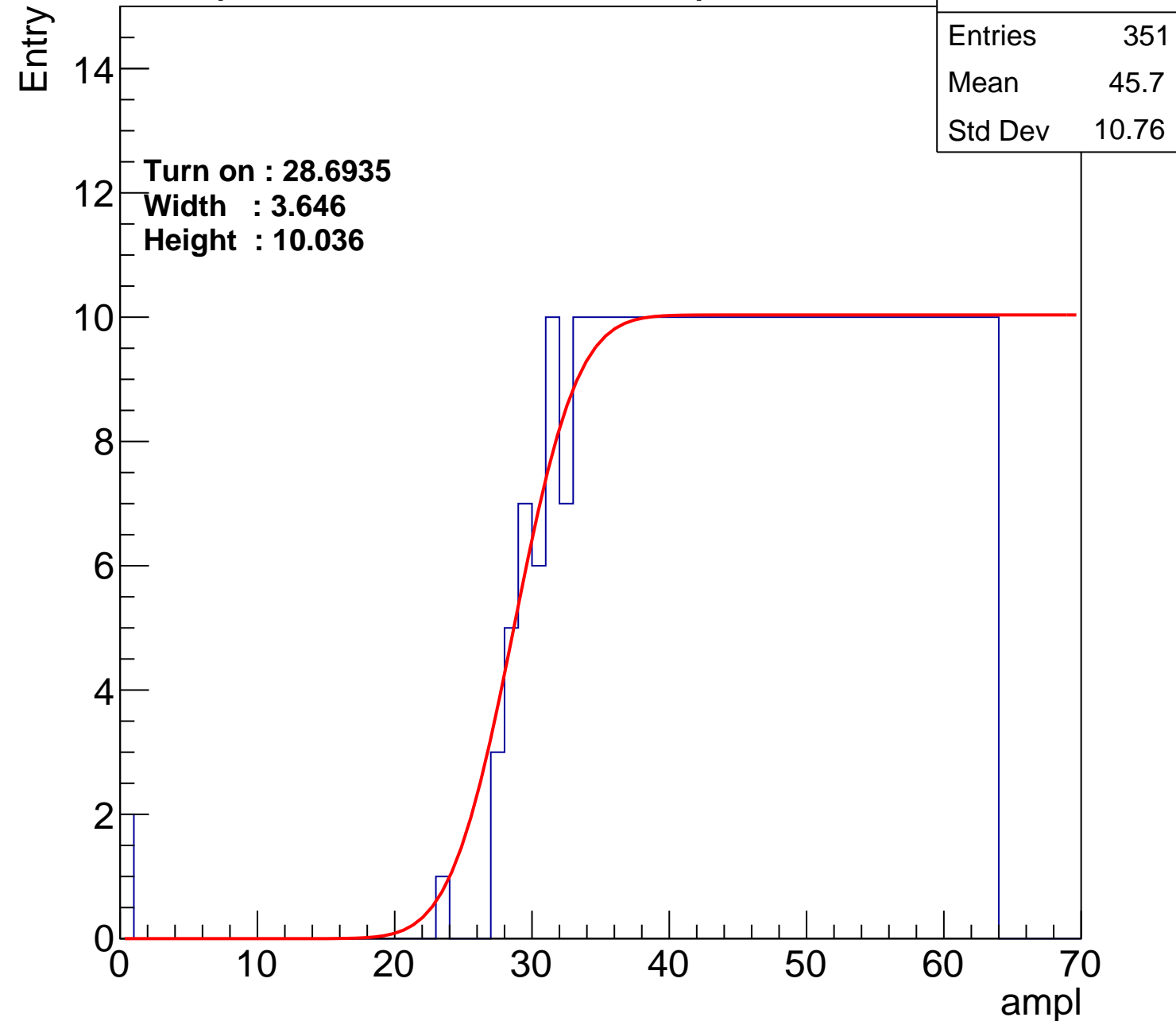
Width : 3.646

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.02
Std Dev	11.25

Turn on : 28.1311

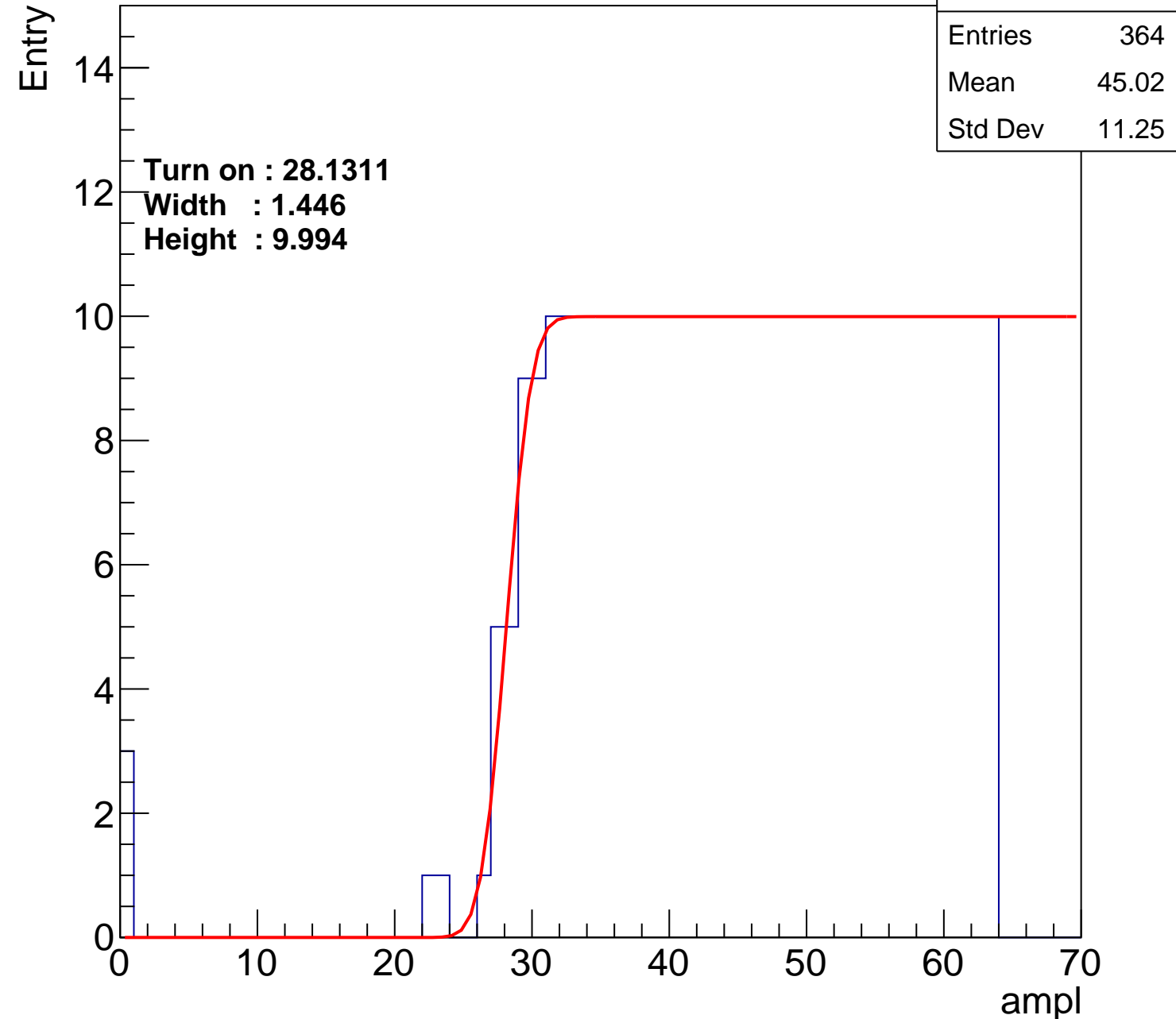
Width : 1.446

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.98
Std Dev	10.47

Turn on : 30.4515

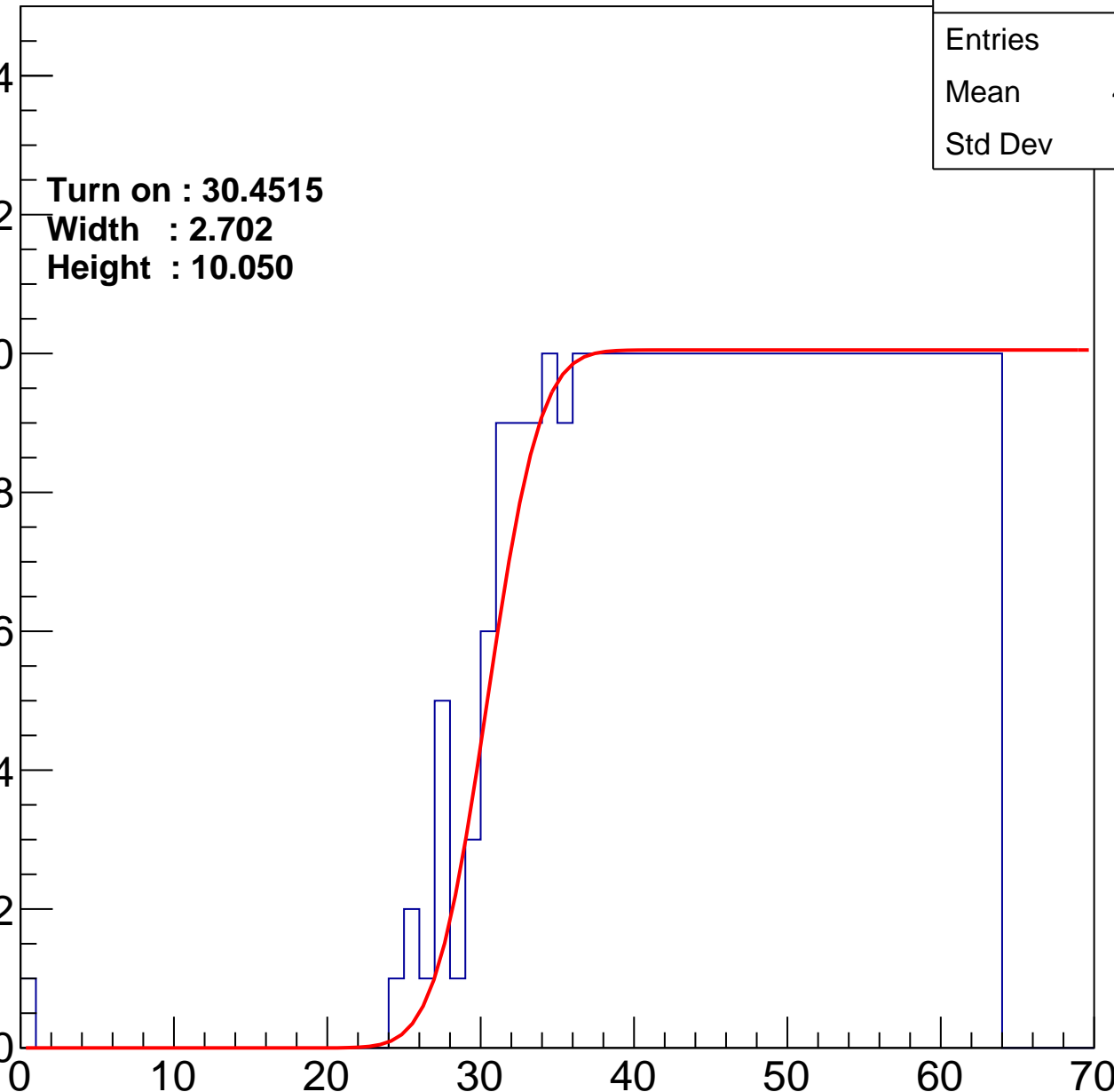
Width : 2.702

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch74

calib_packv5_042523_0143.root, FC#8, port C1

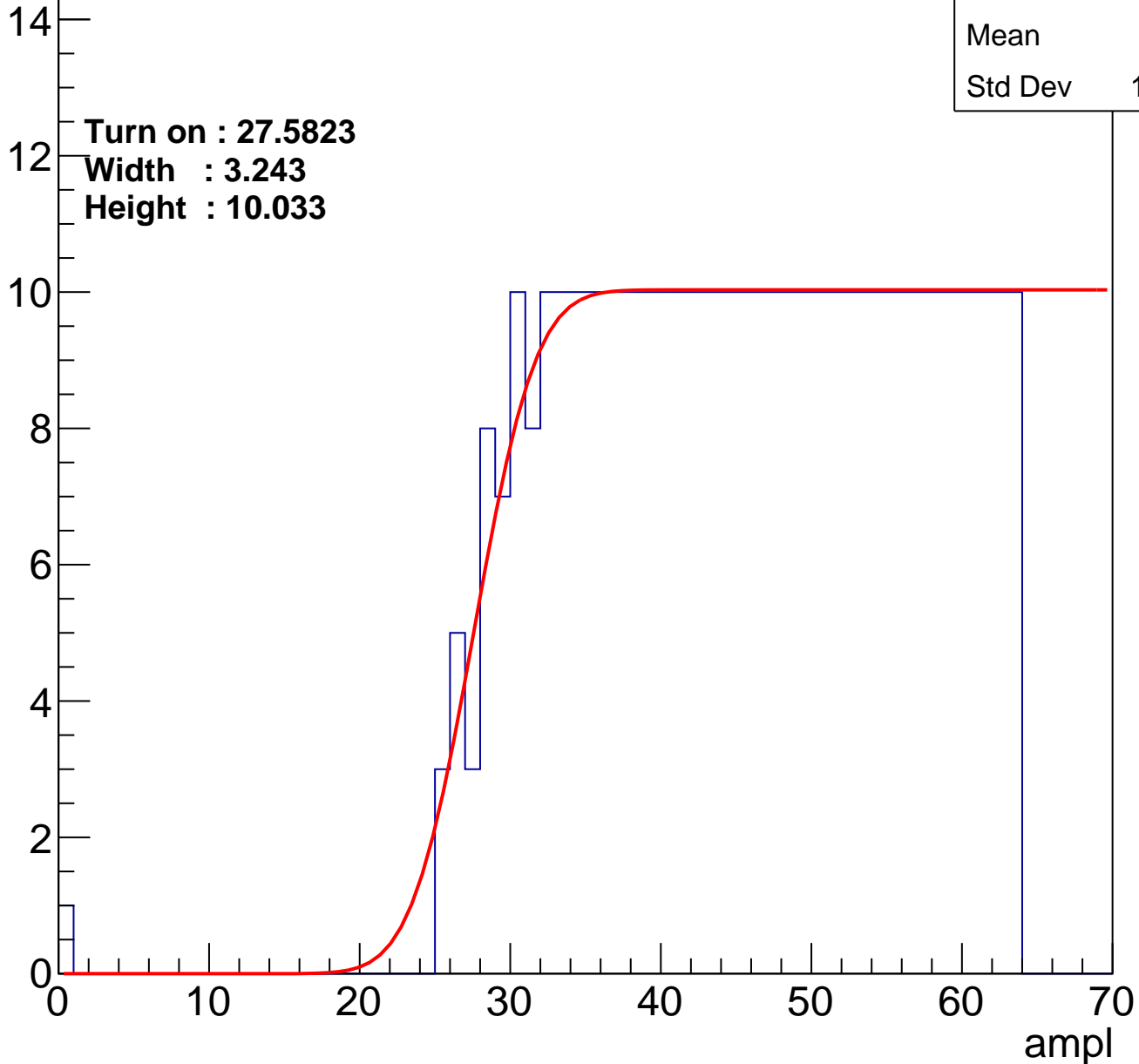
Entries	365
Mean	45.1
Std Dev	10.88

Turn on : 27.5823

Width : 3.243

Height : 10.033

Entry



B0L002S, U10-ch75

calib_packv5_042523_0143.root, FC#8, port C1

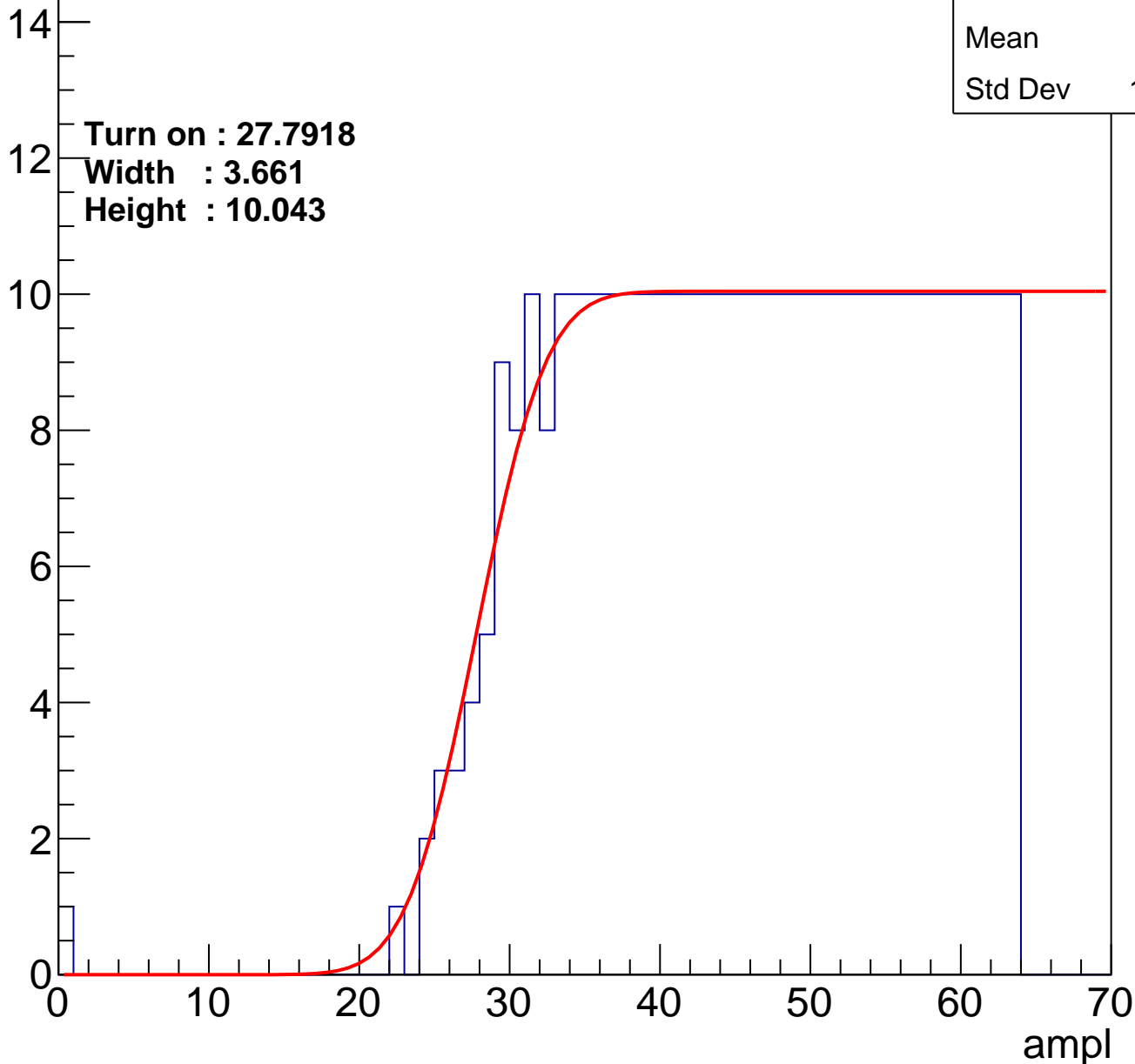
Entry

Entries	364
Mean	45.1
Std Dev	10.93

Turn on : 27.7918

Width : 3.661

Height : 10.043



B0L002S, U10-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.1351

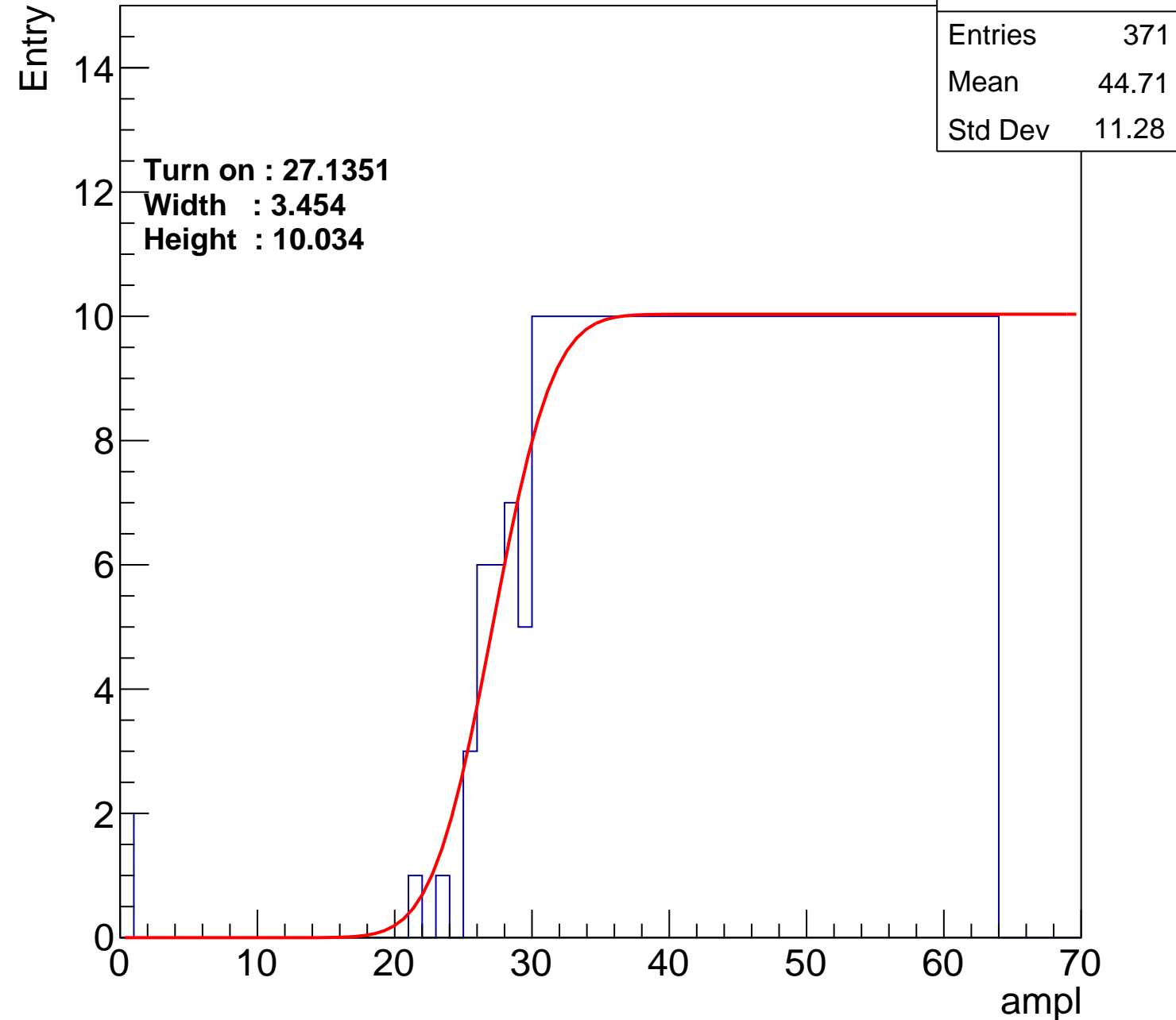
Width : 3.454

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.42
Std Dev	10.94

Turn on : 28.6176

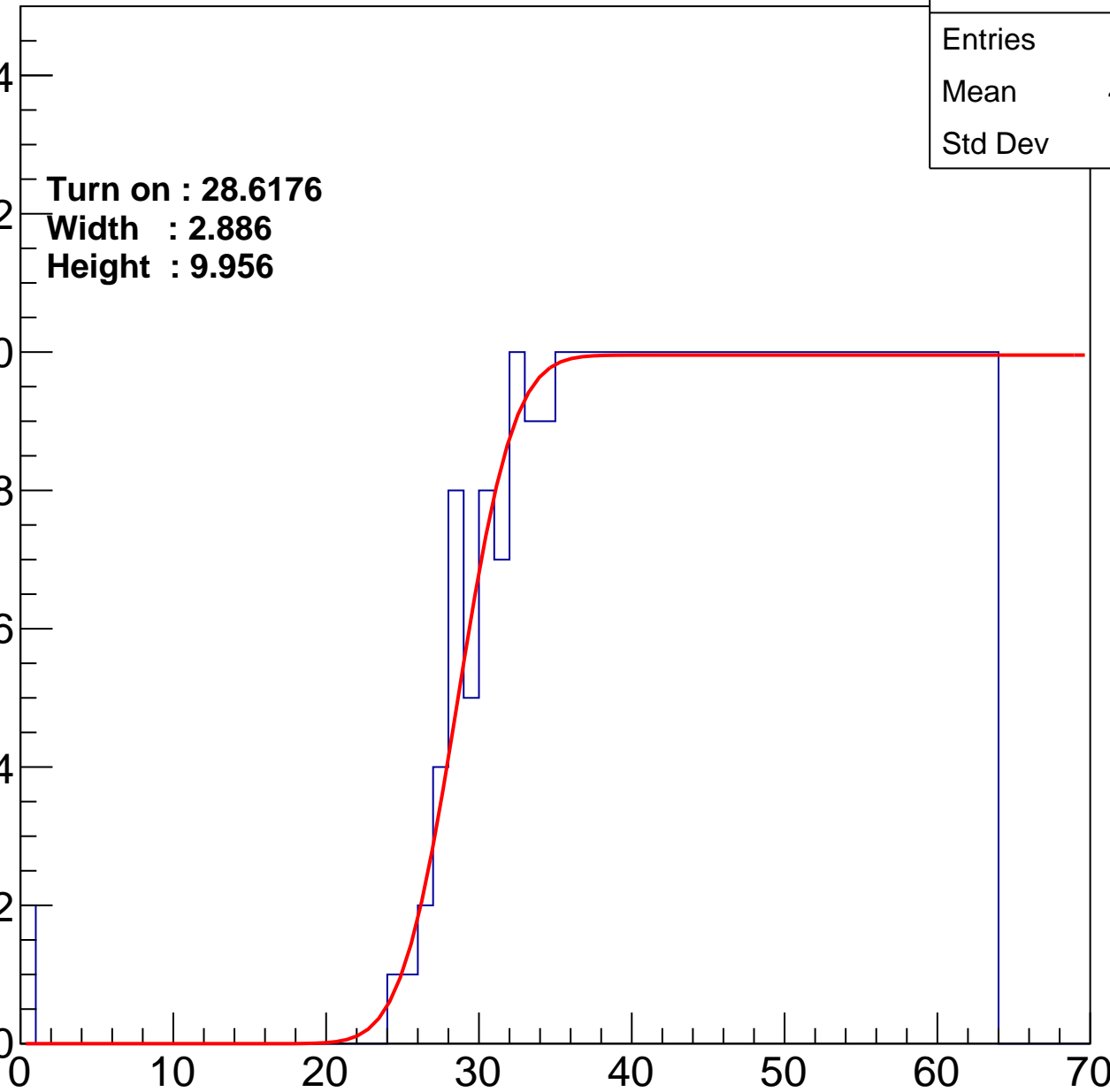
Width : 2.886

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch78

calib_packv5_042523_0143.root, FC#8, port C1

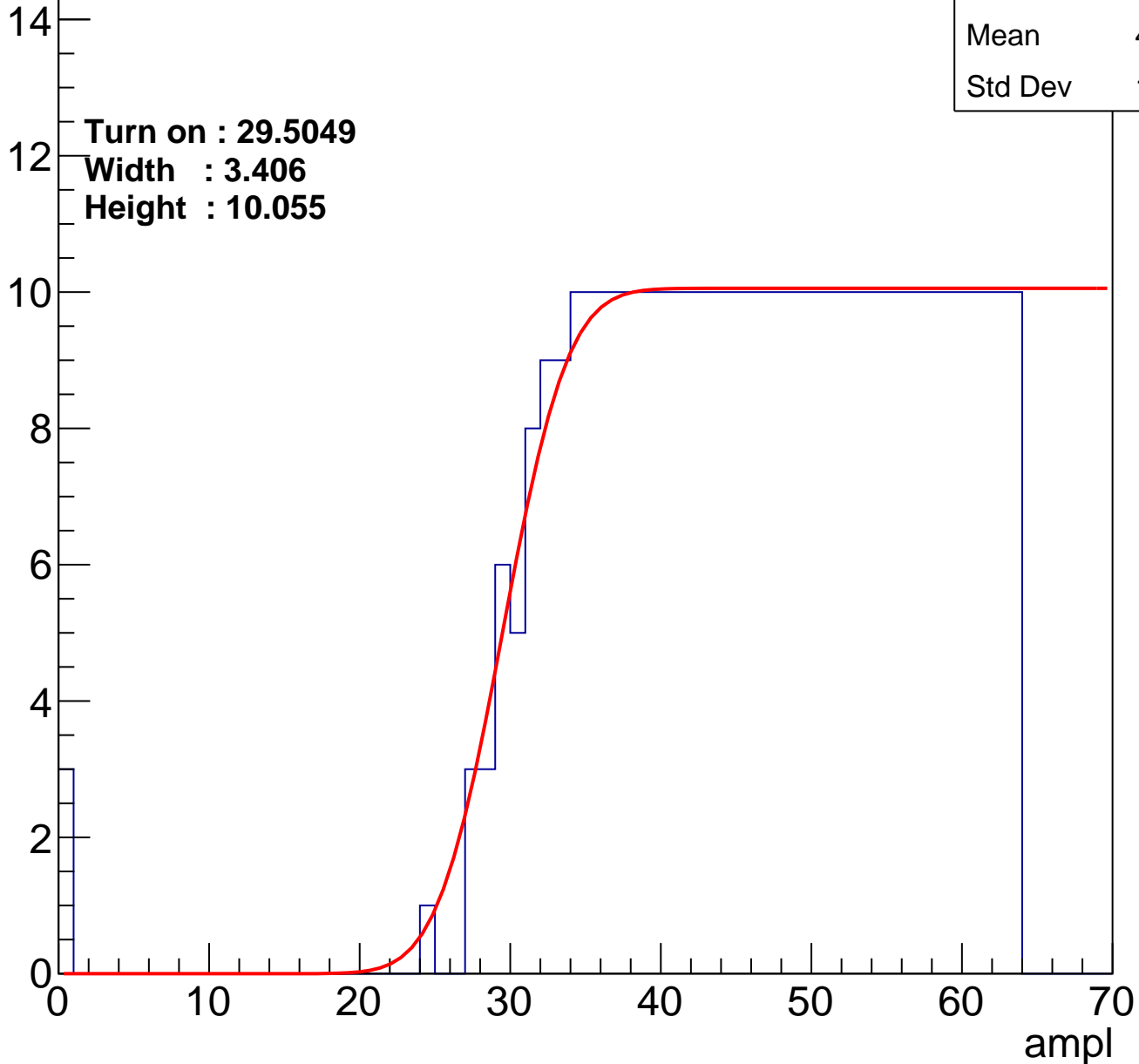
Entries	347
Mean	45.81
Std Dev	10.91

Turn on : 29.5049

Width : 3.406

Height : 10.055

Entry



B0L002S, U10-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.59
Std Dev	10.68

Turn on : 29.1818

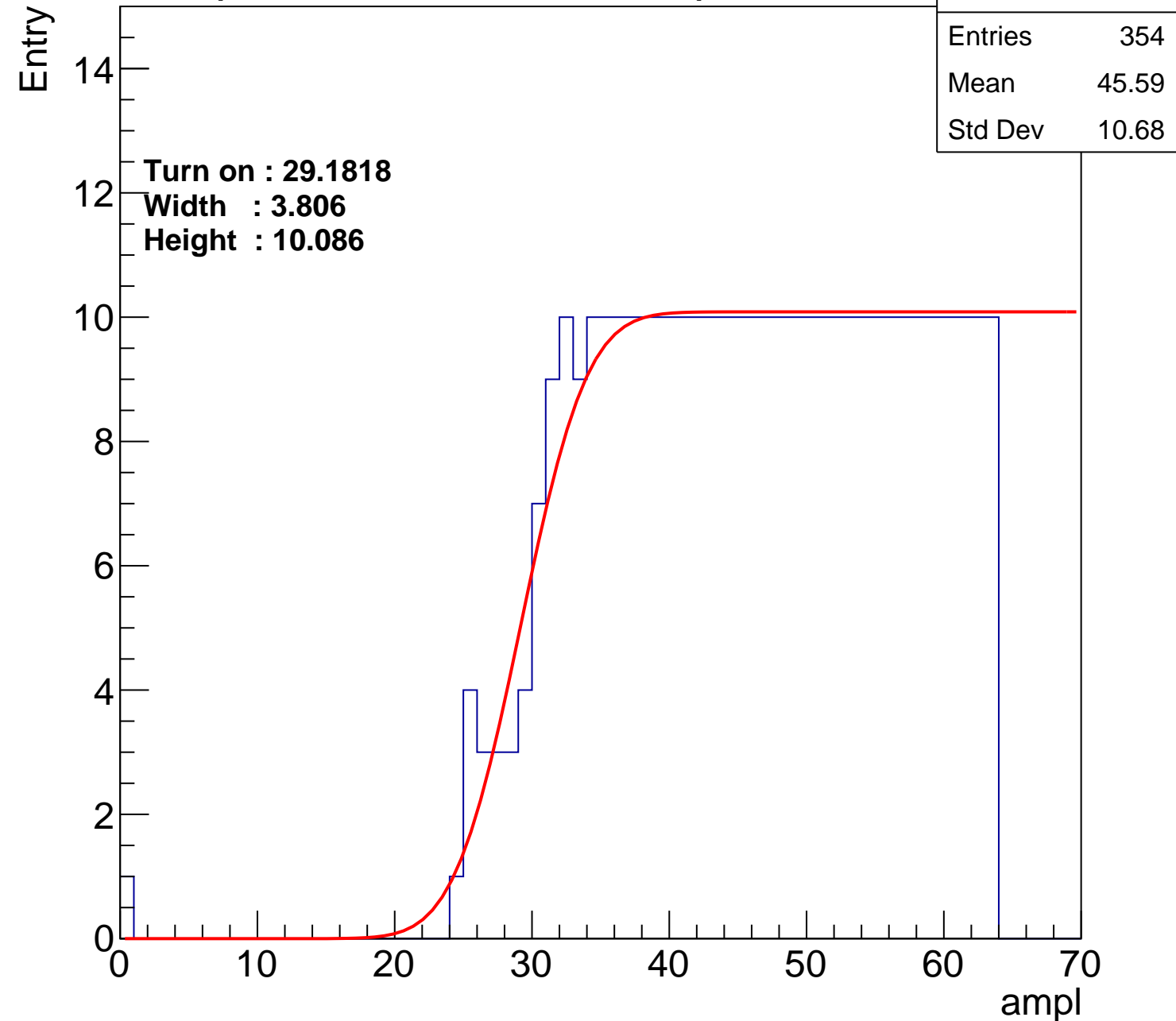
Width : 3.806

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.13
Std Dev	10.88

Turn on : 27.6527

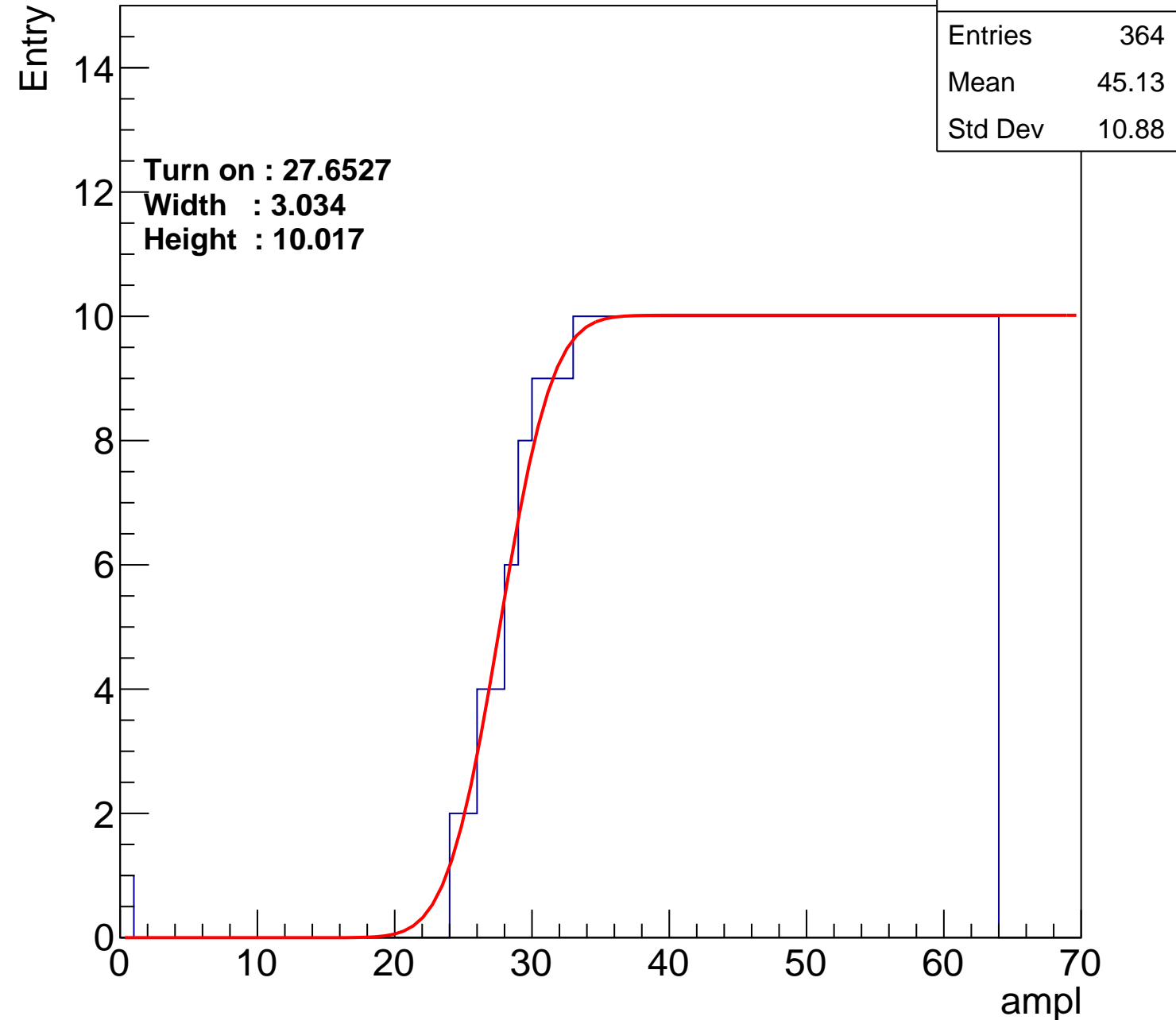
Width : 3.034

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.73
Std Dev	10.56

Turn on : 29.2437

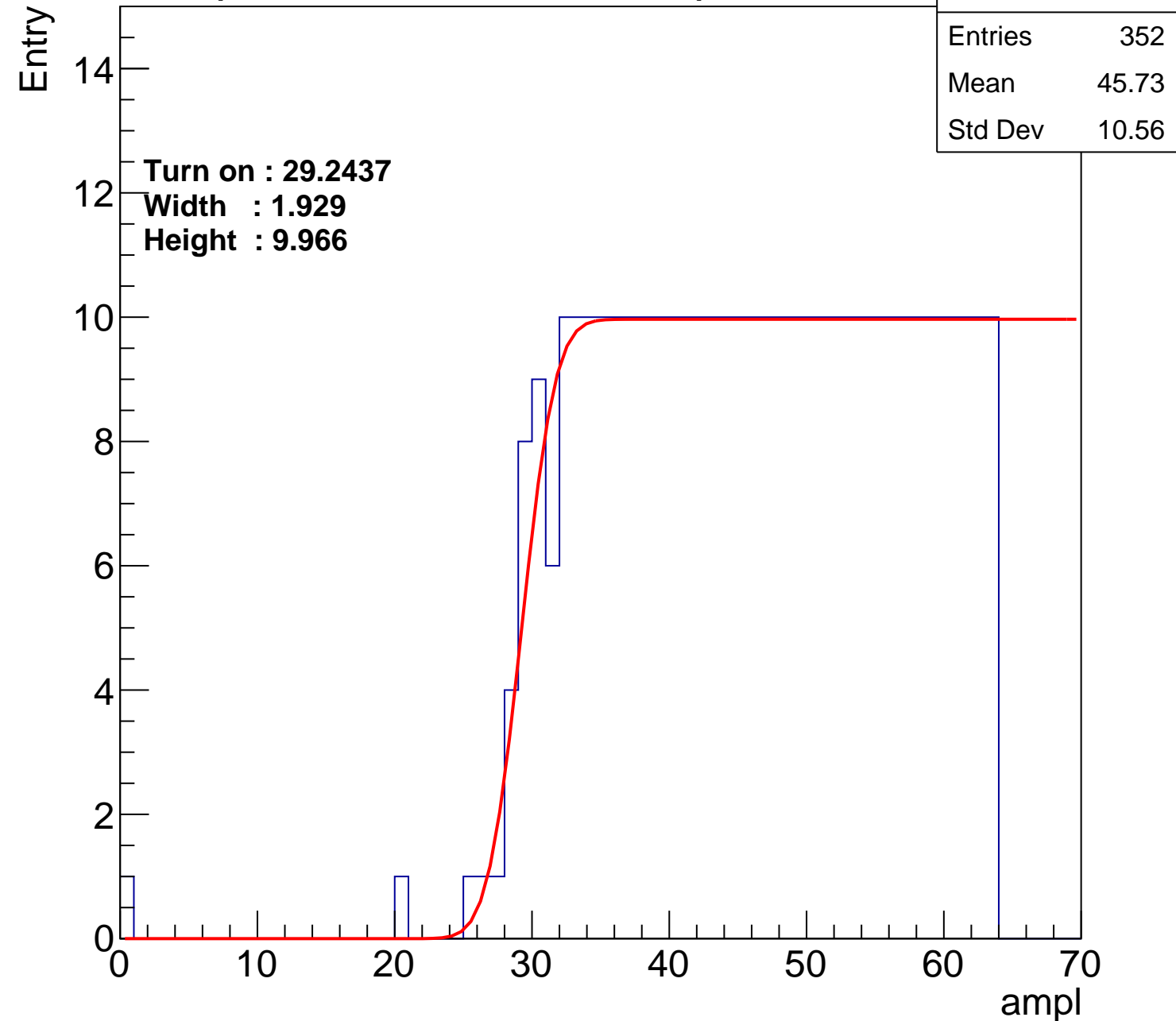
Width : 1.929

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.03
Std Dev	10.62

Turn on : 29.9798

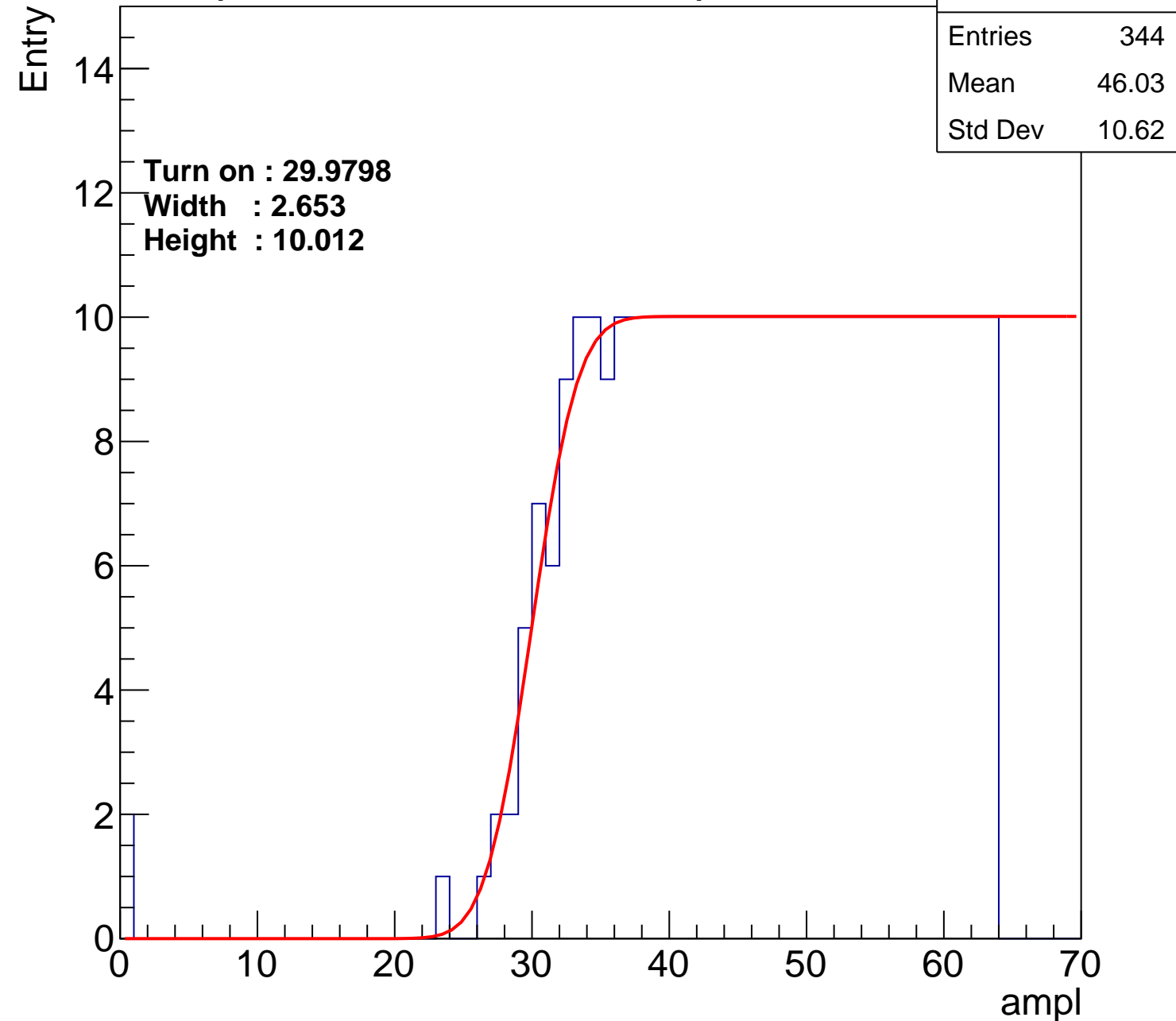
Width : 2.653

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.78
Std Dev	10.52

Turn on : 28.9815

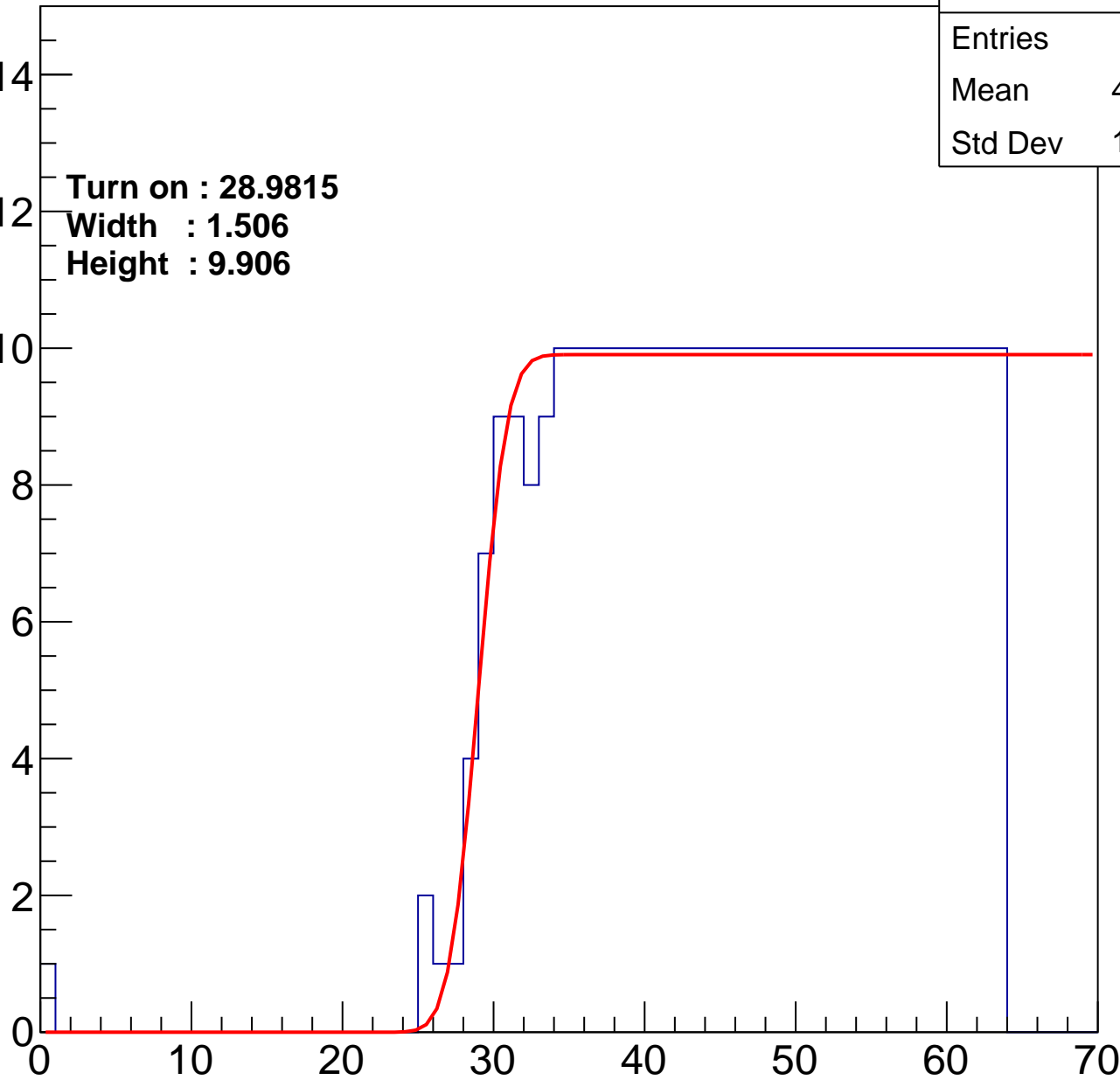
Width : 1.506

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch84

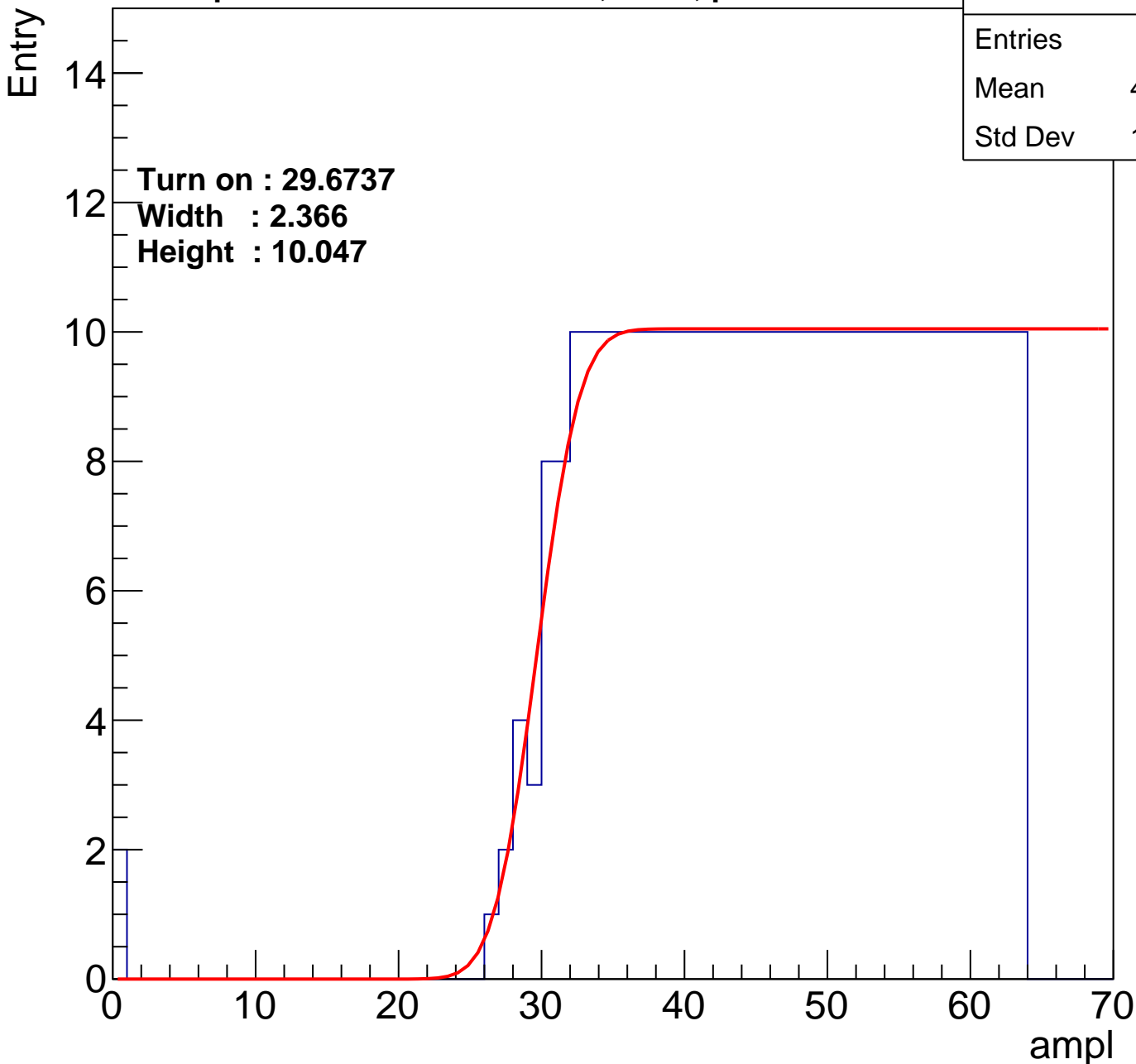
calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.88
Std Dev	10.64

Turn on : 29.6737

Width : 2.366

Height : 10.047



B0L002S, U10-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.44
Std Dev	10.76

Turn on : 28.4752

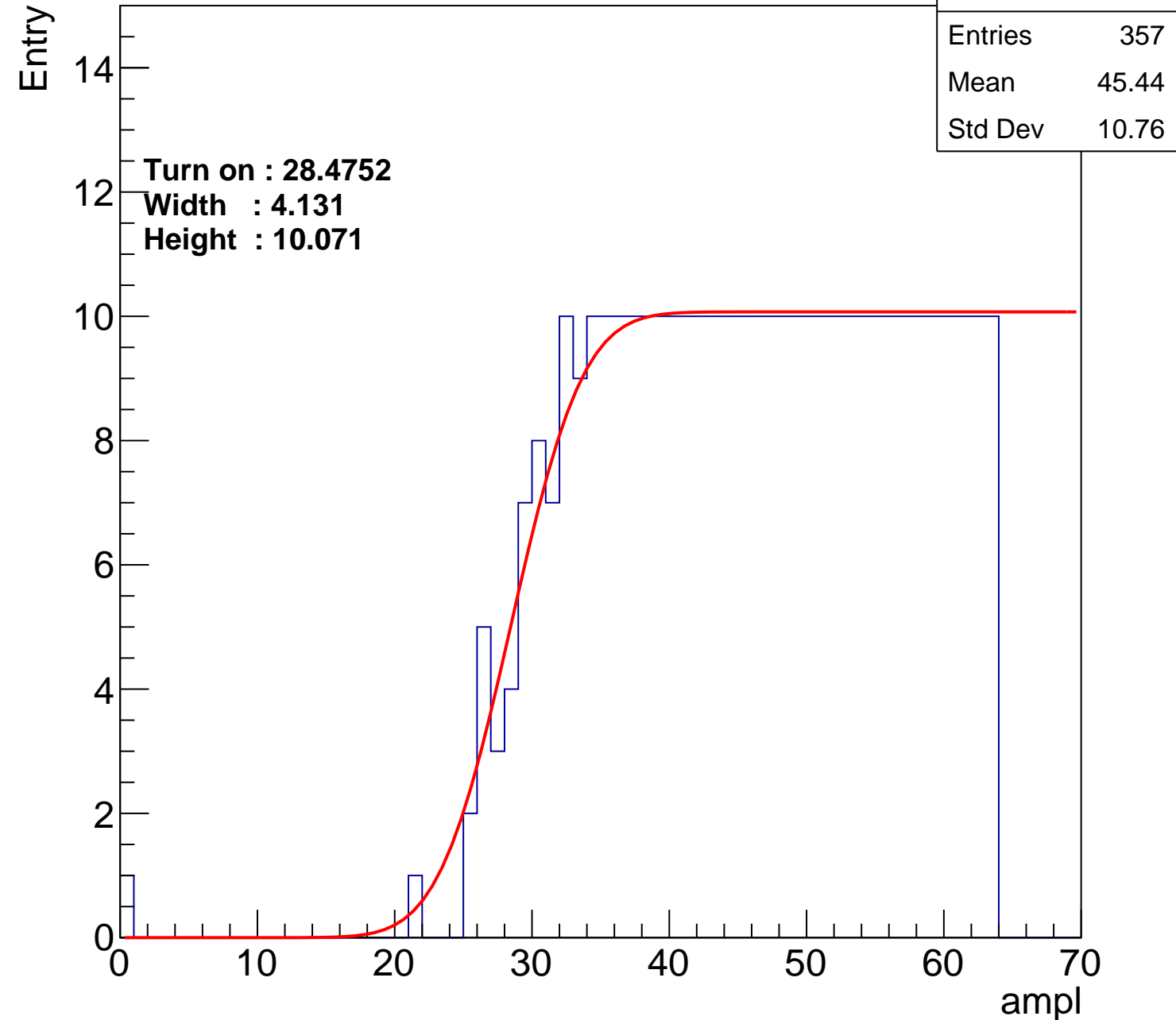
Width : 4.131

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.91
Std Dev	10.48

Turn on : 29.8627

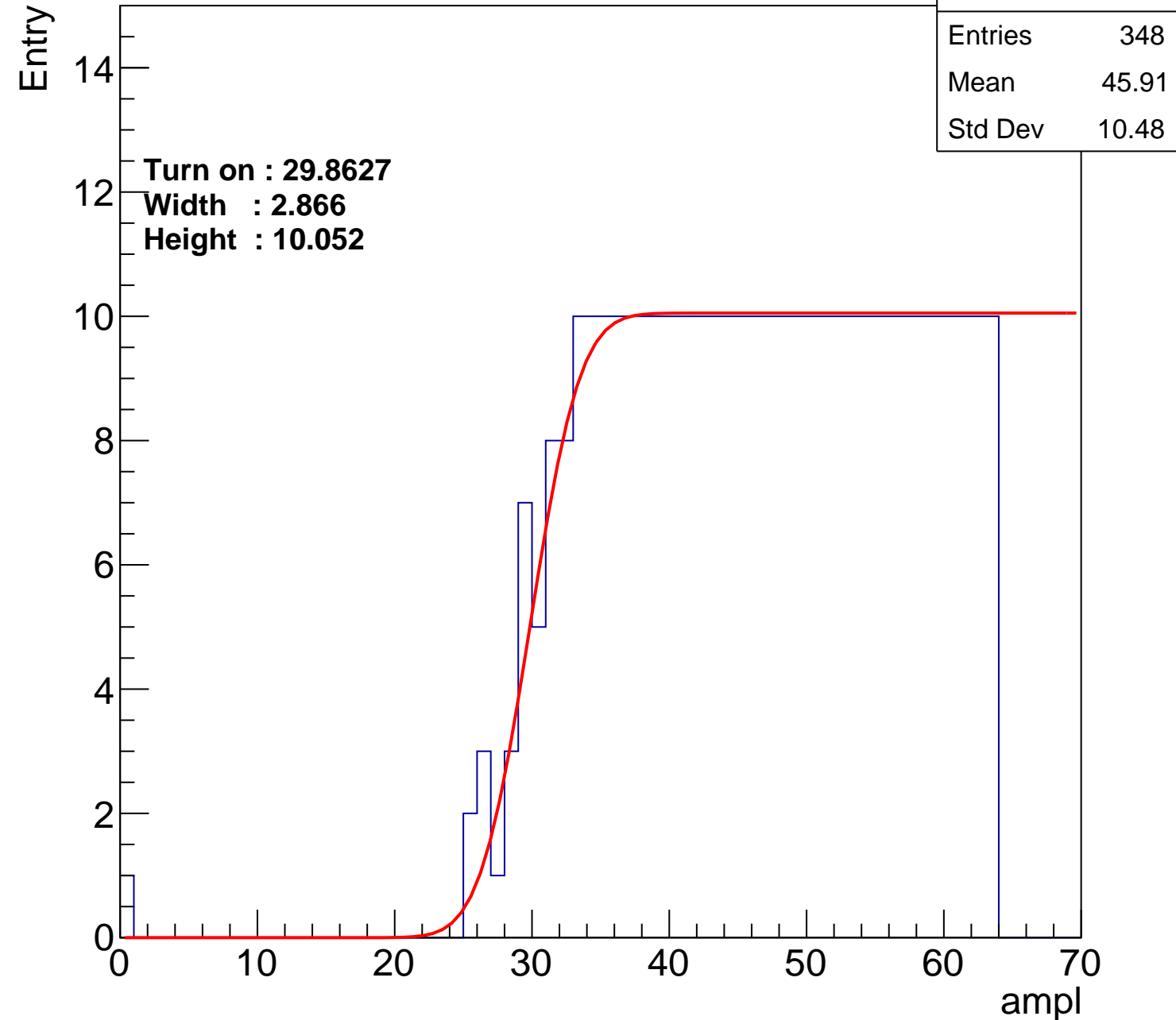
Width : 2.866

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.7031

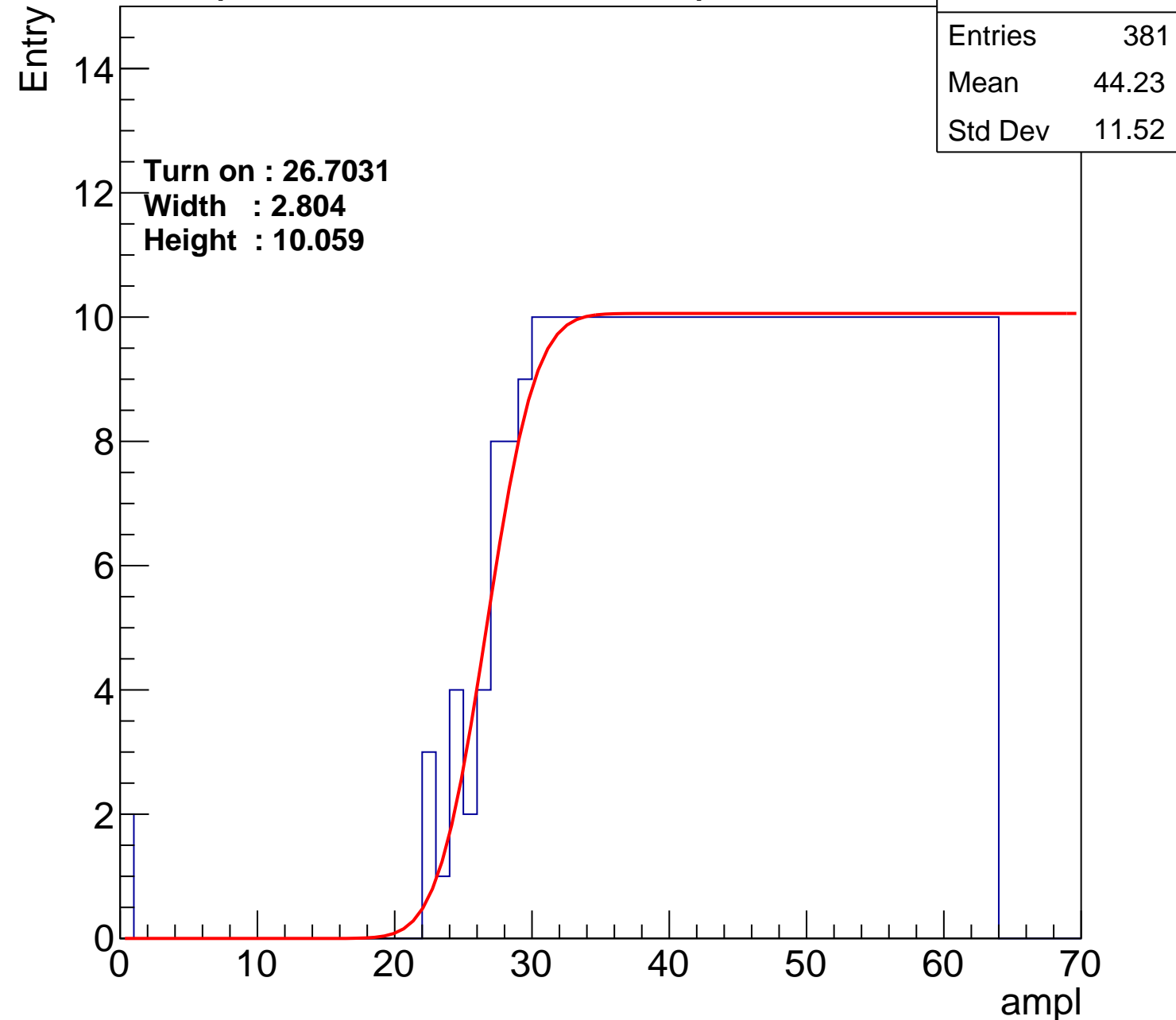
Width : 2.804

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.55
Std Dev	10.84

Turn on : 28.9485

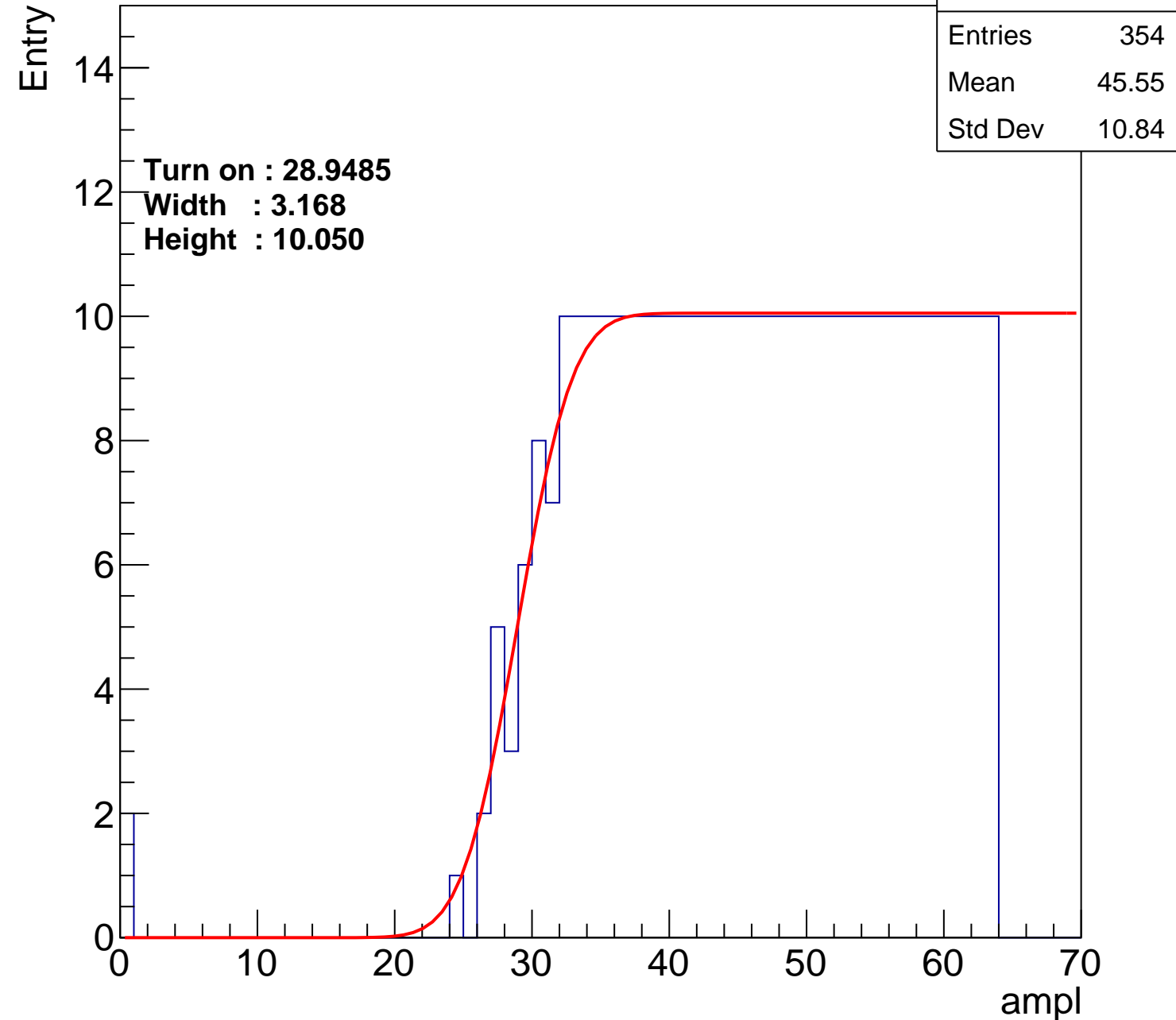
Width : 3.168

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.46
Std Dev	10.24

Turn on : 30.6983

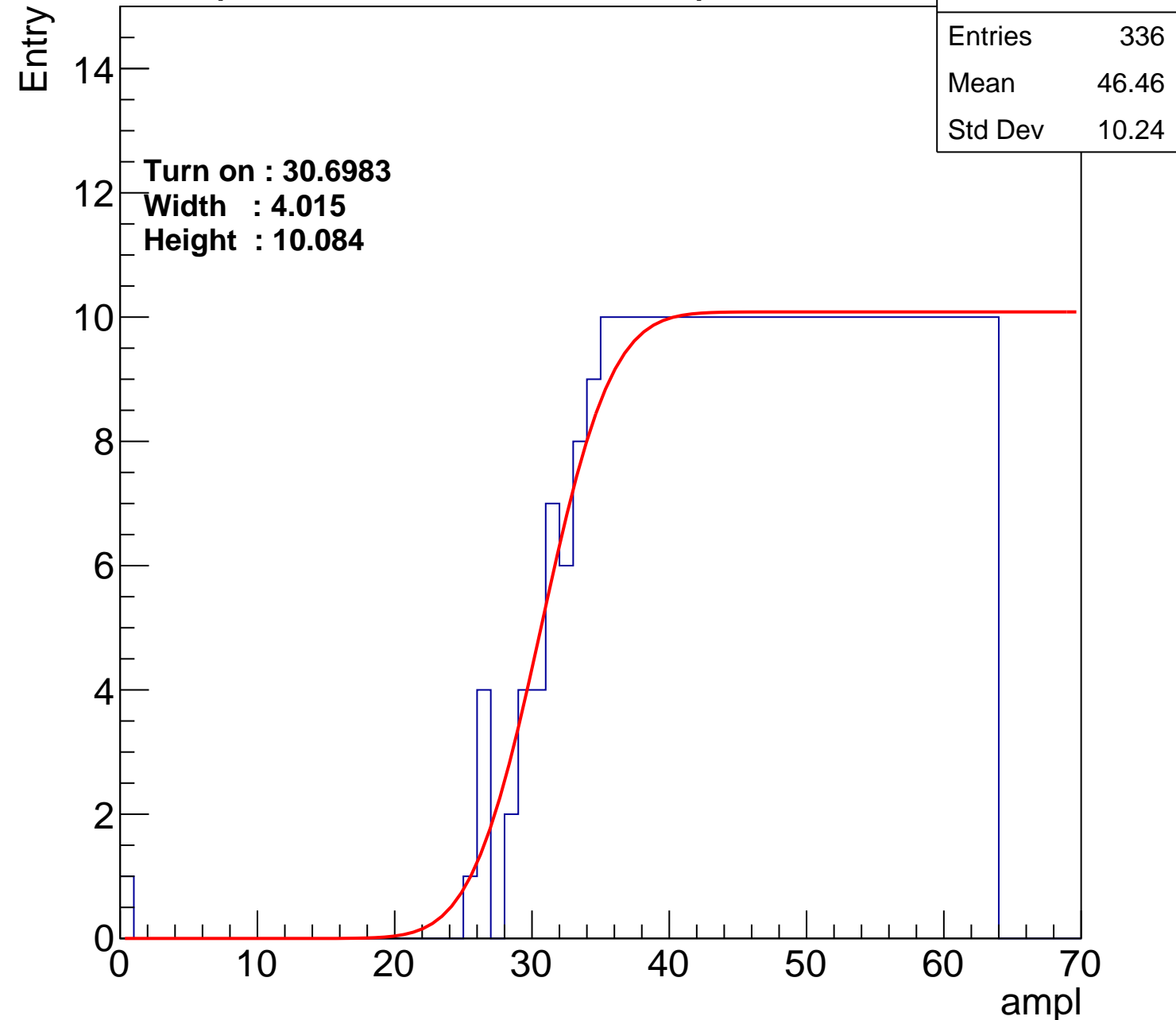
Width : 4.015

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch90

calib_packv5_042523_0143.root, FC#8, port C1

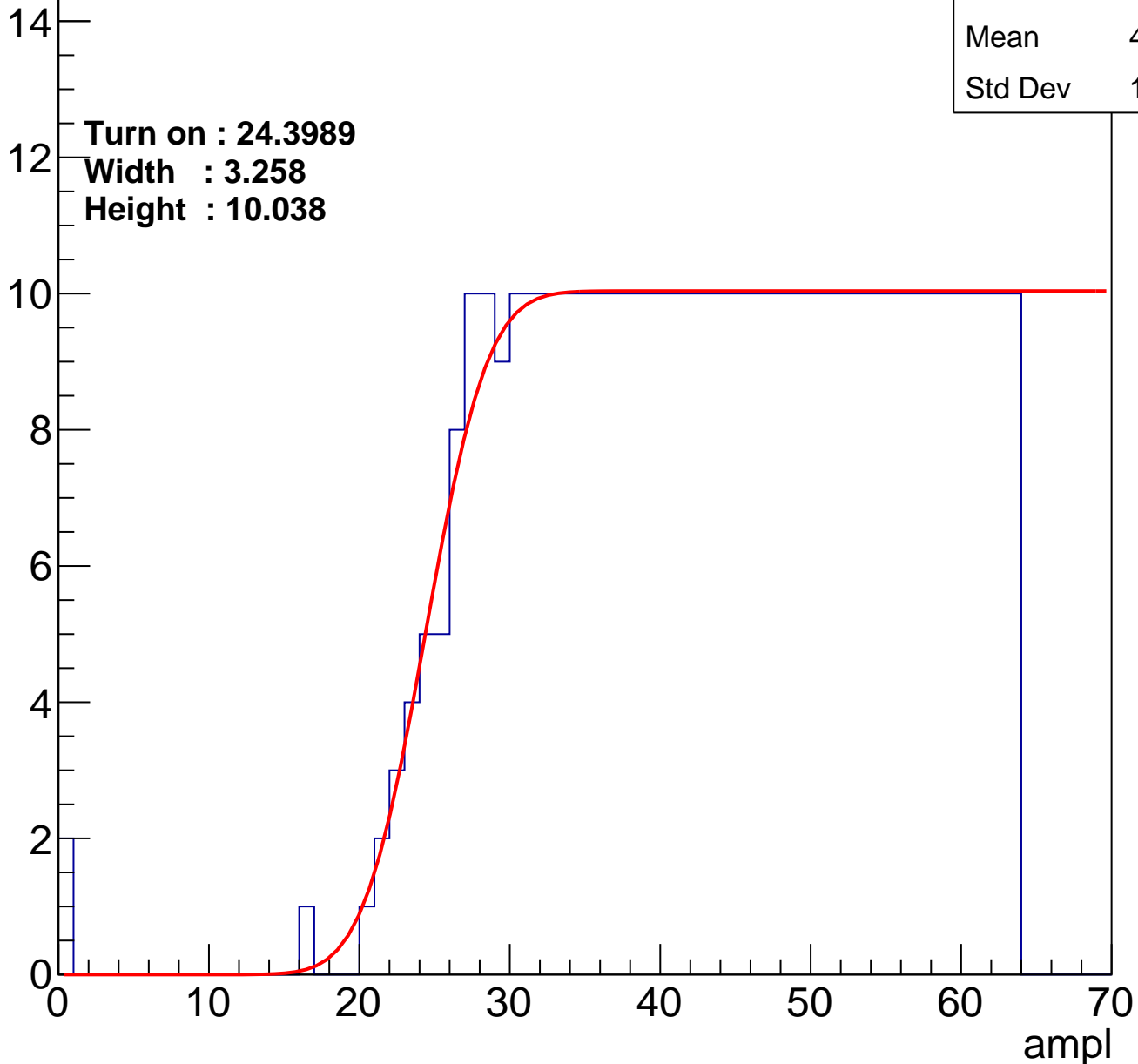
Entries	400
Mean	43.27
Std Dev	12.04

Turn on : 24.3989

Width : 3.258

Height : 10.038

Entry



B0L002S, U10-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.26
Std Dev	10.45

Turn on : 30.0390

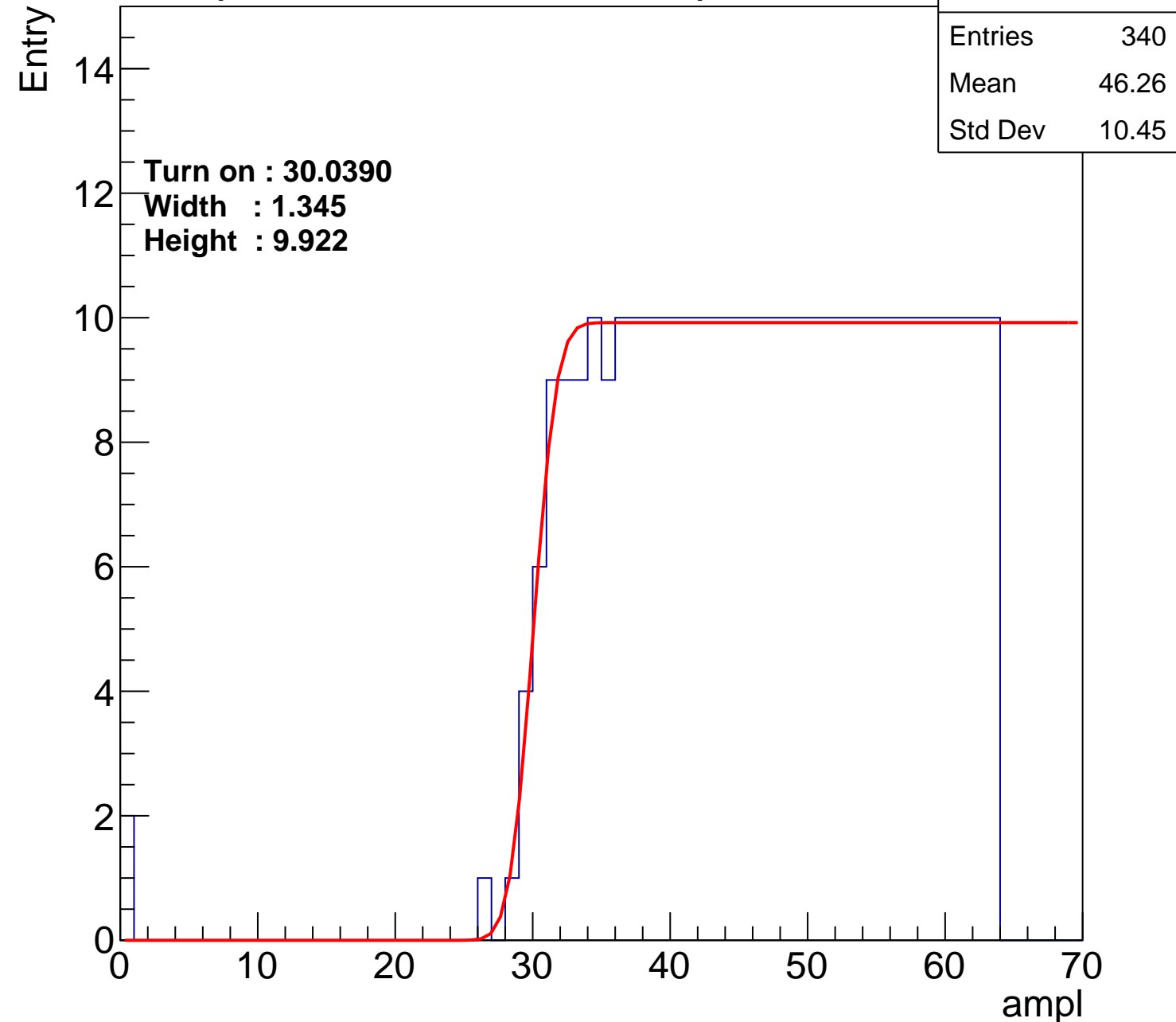
Width : 1.345

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.84
Std Dev	10.86

Turn on : 29.6955

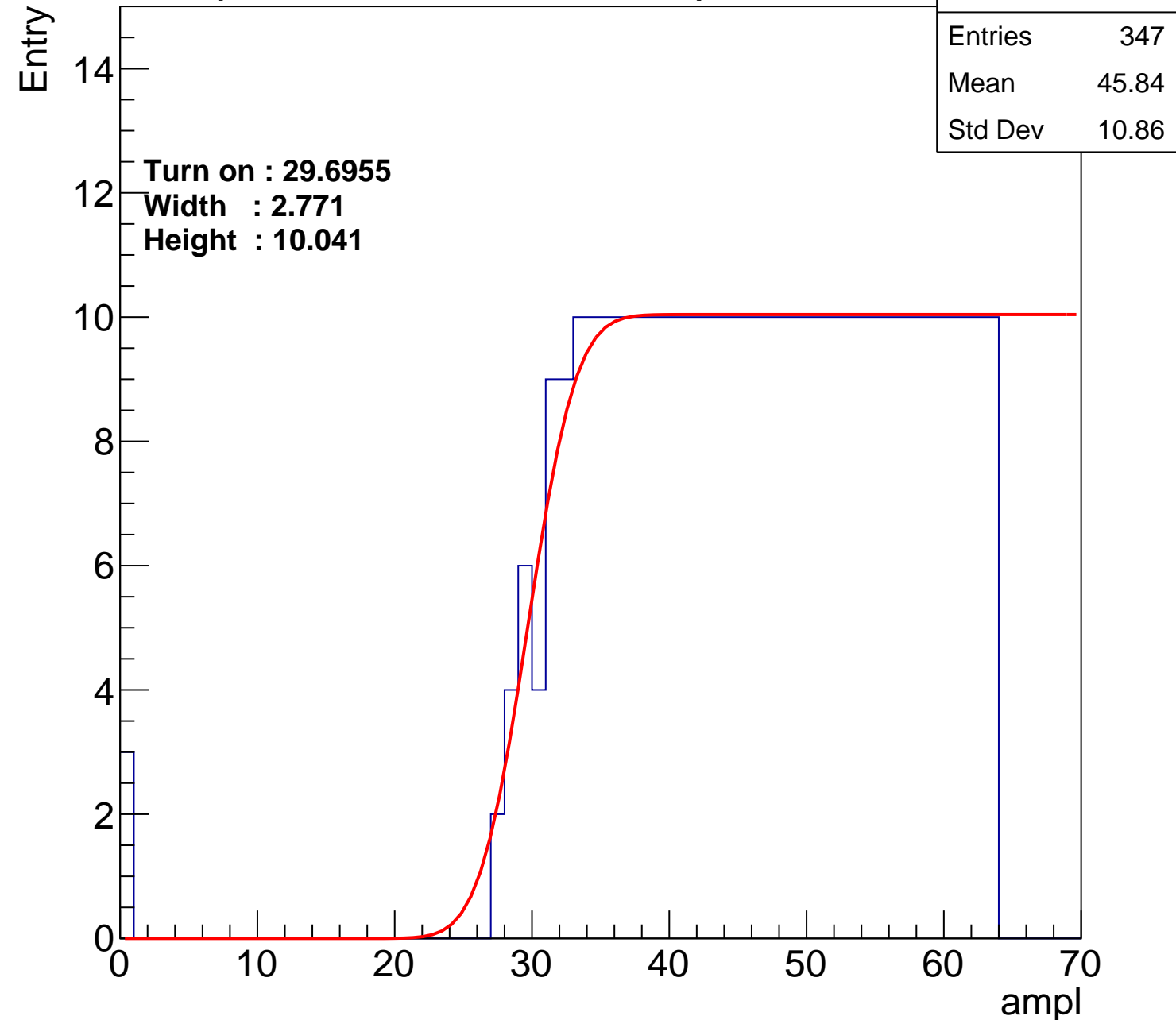
Width : 2.771

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.67
Std Dev	10.66

Turn on : 29.1957

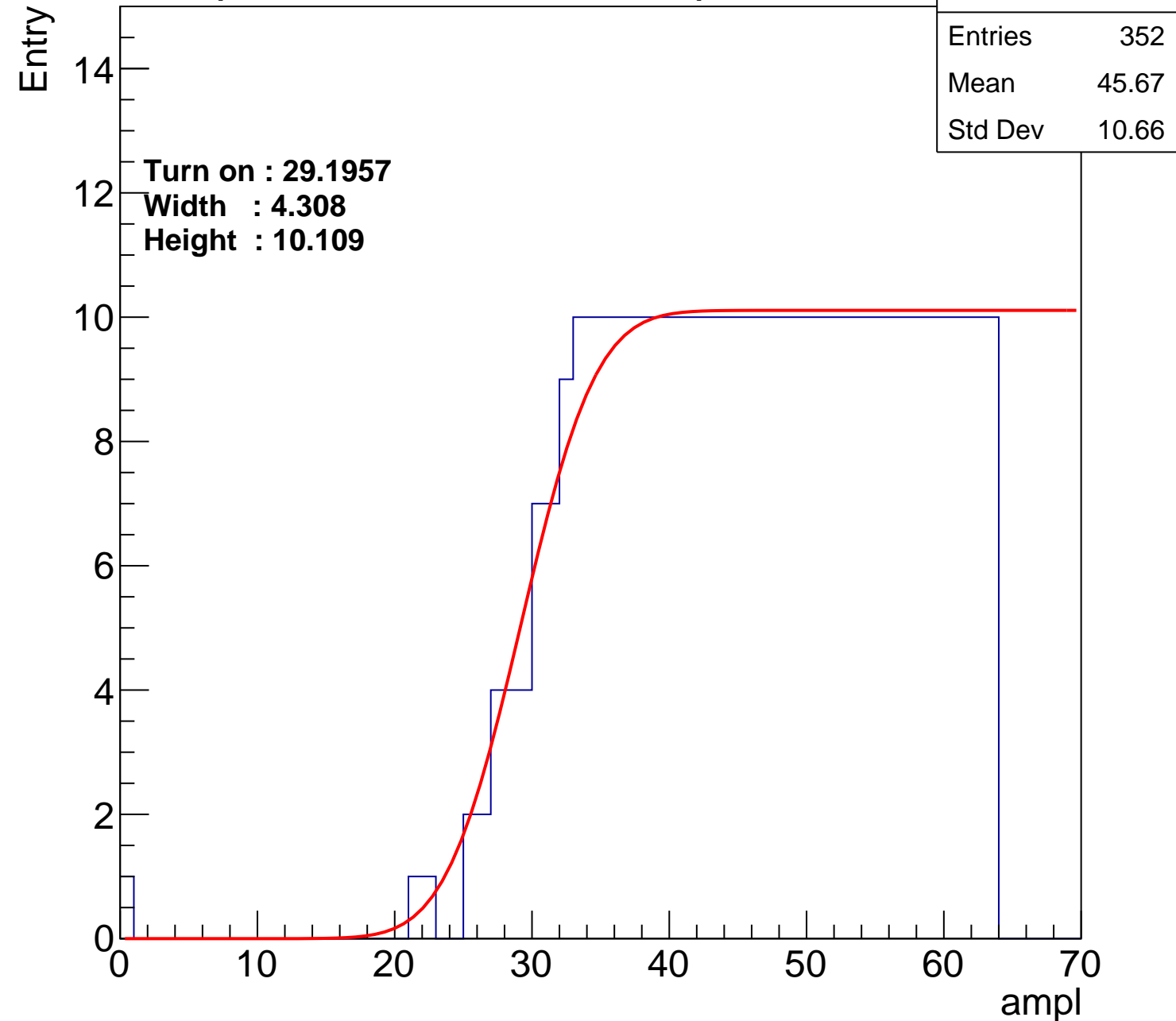
Width : 4.308

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.97

Turn on : 27.1456

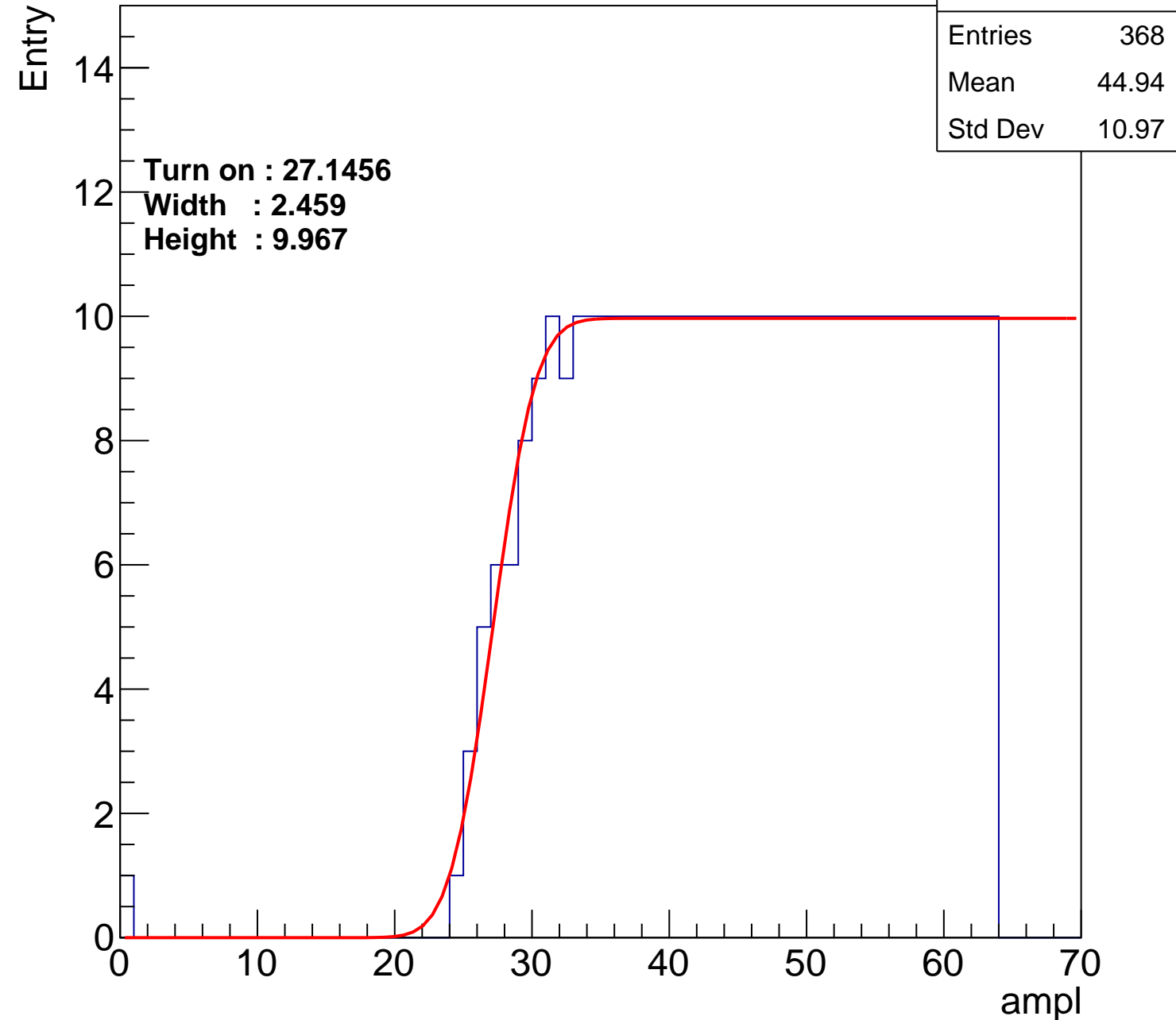
Width : 2.459

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.12
Std Dev	10.34

Turn on : 29.7607

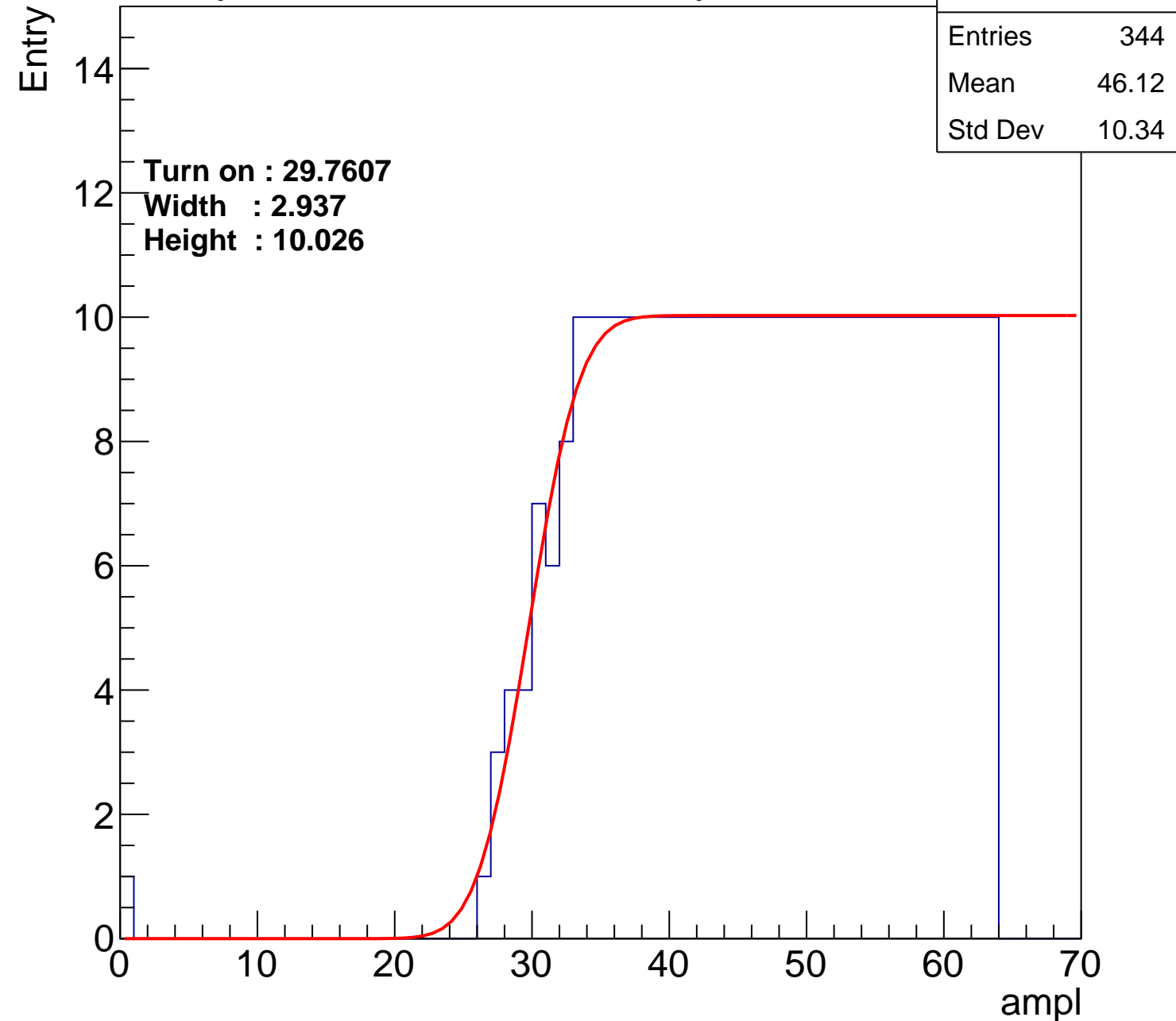
Width : 2.937

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.82
Std Dev	10.95

Turn on : 29.7998

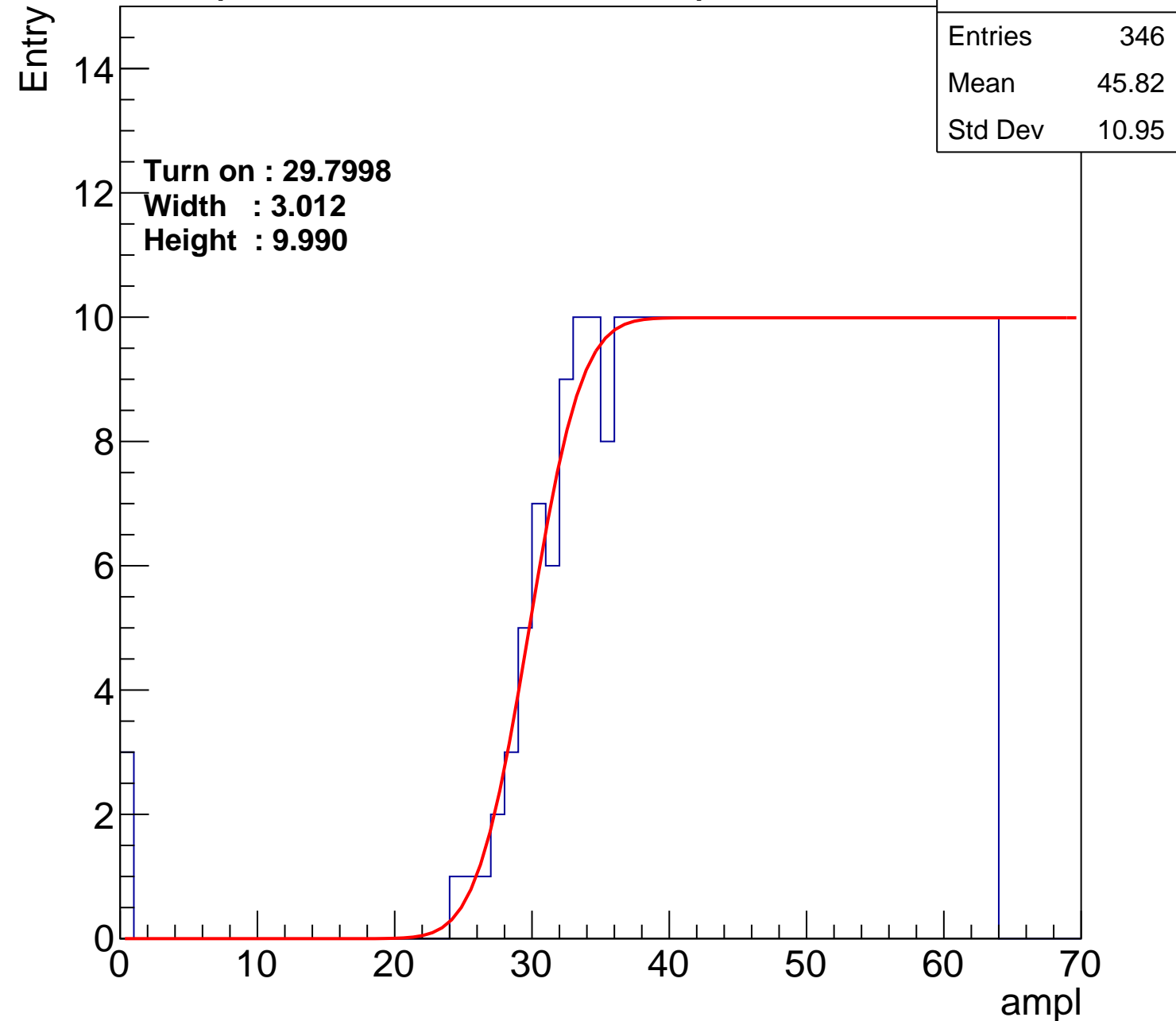
Width : 3.012

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.39
Std Dev	10.92

Turn on : 28.2119

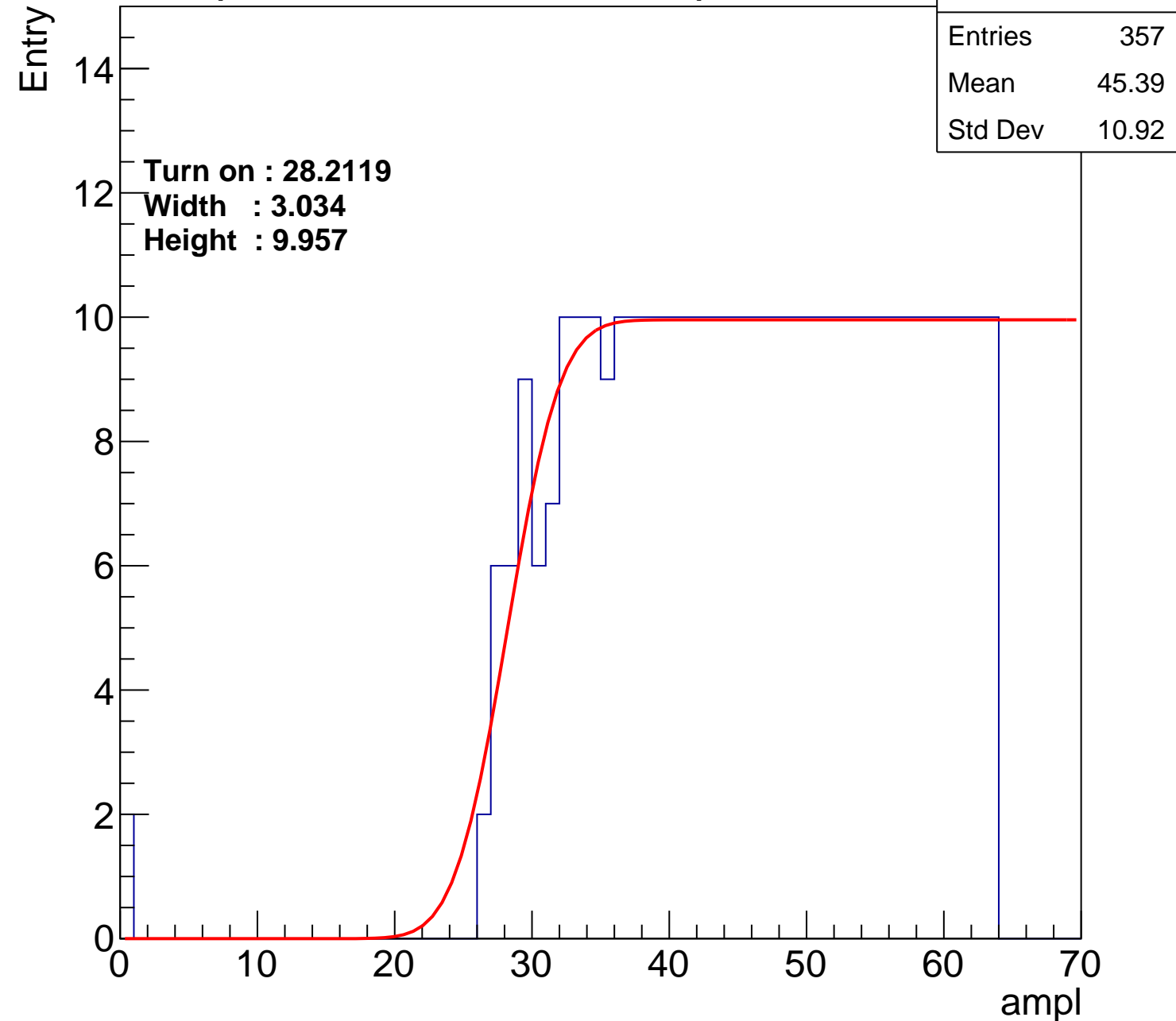
Width : 3.034

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 26.5998

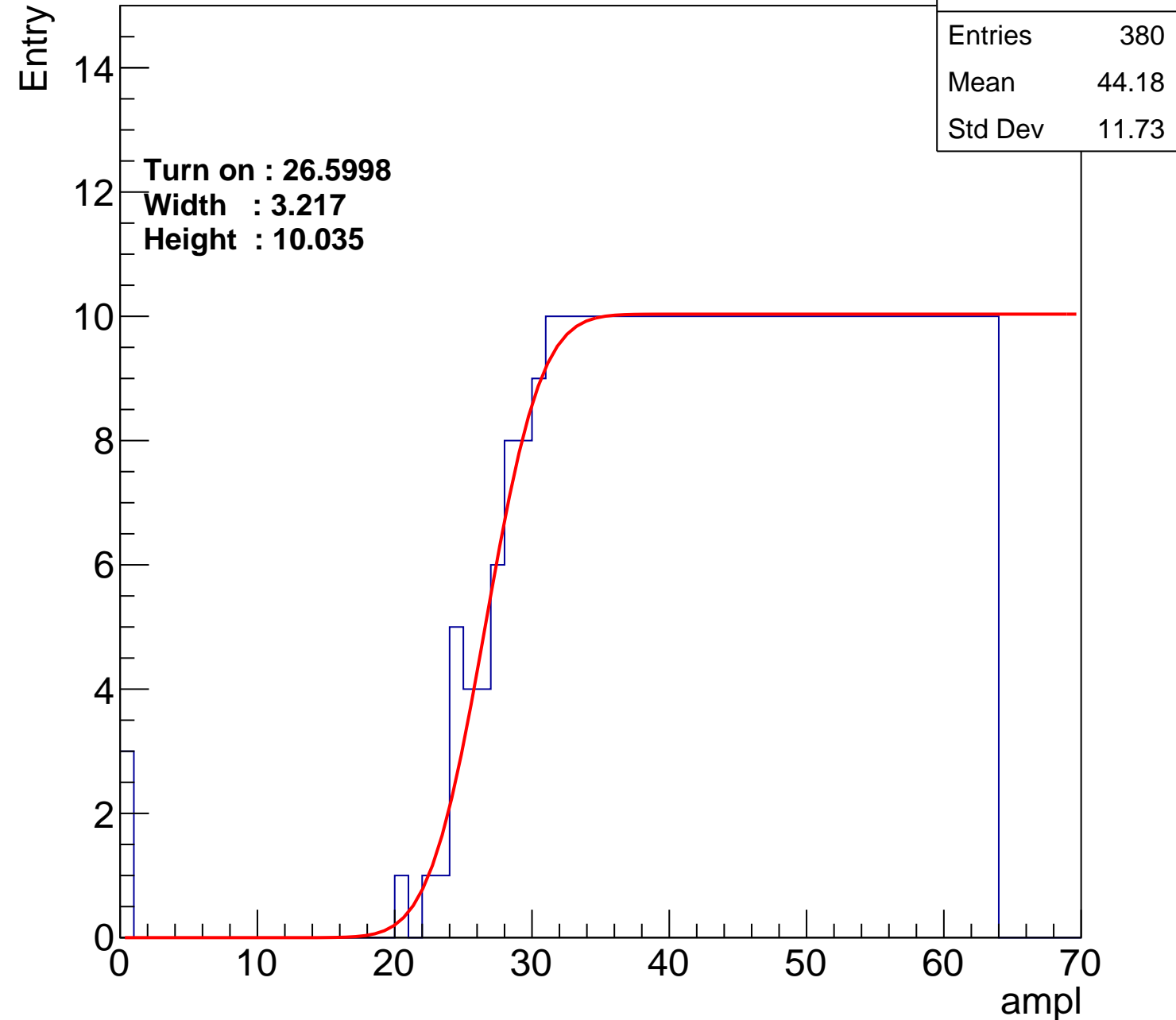
Width : 3.217

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch99

calib_packv5_042523_0143.root, FC#8, port C1

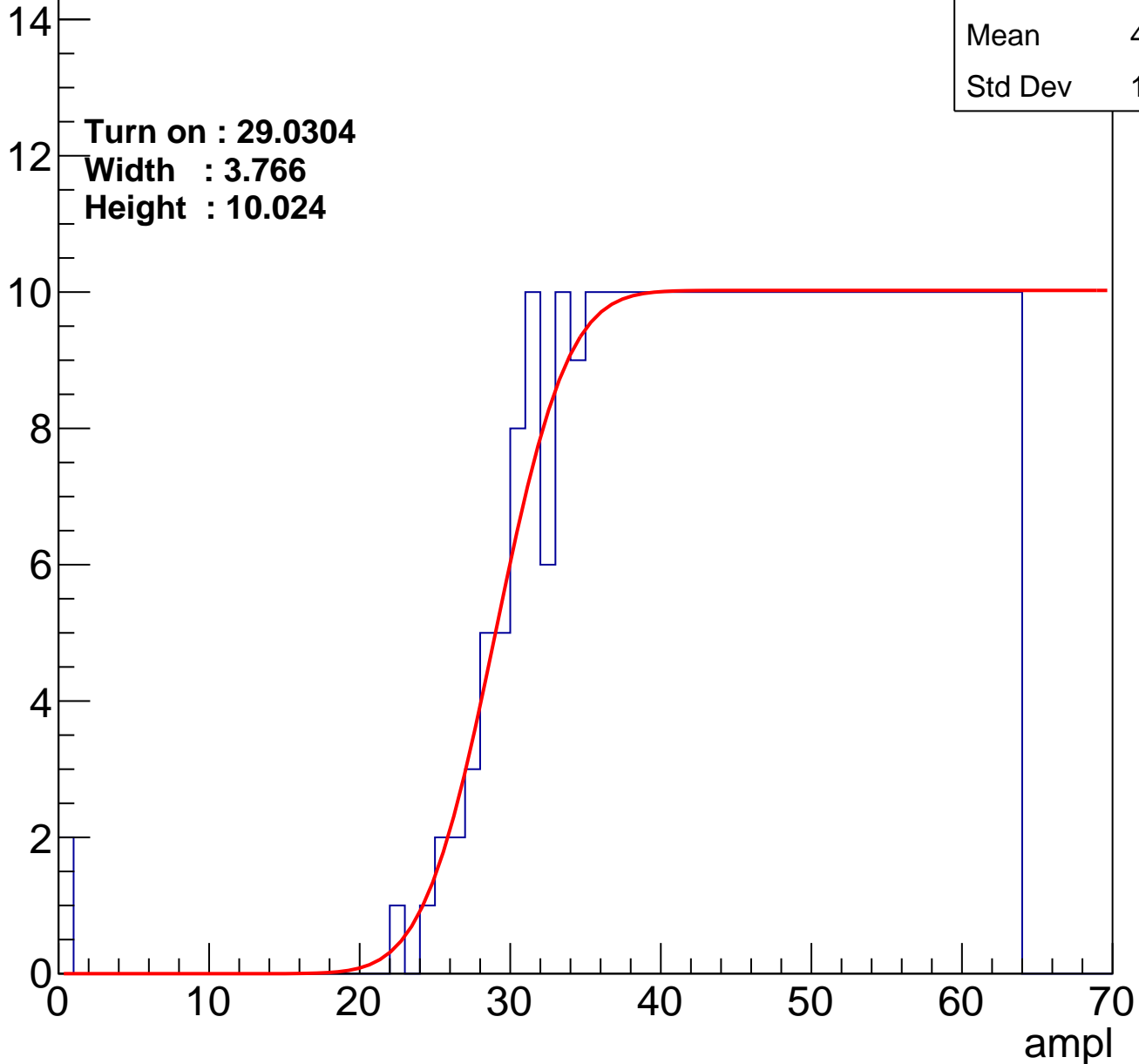
Entries	354
Mean	45.49
Std Dev	10.94

Turn on : 29.0304

Width : 3.766

Height : 10.024

Entry



B0L002S, U10-ch100

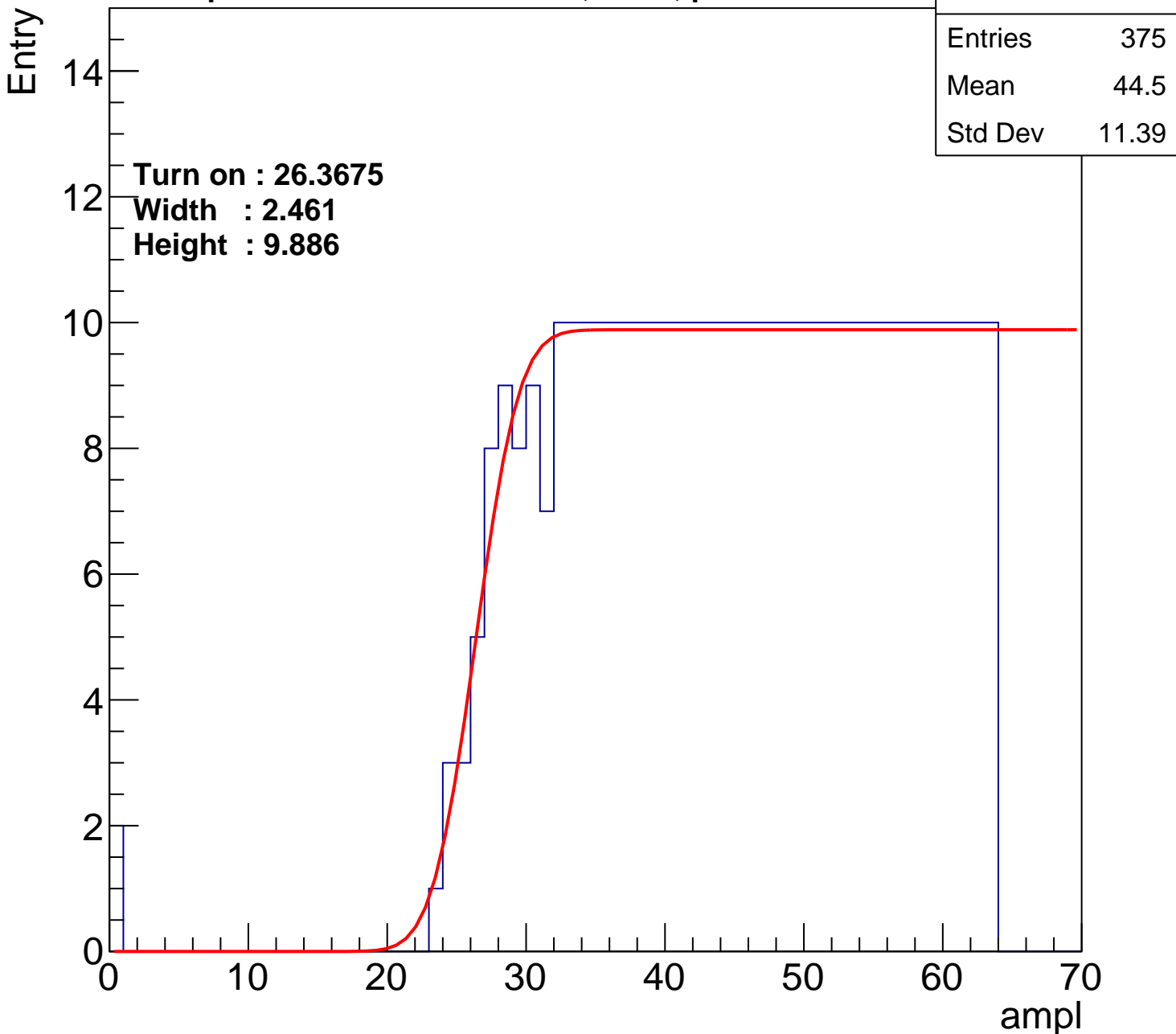
calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.5
Std Dev	11.39

Turn on : 26.3675

Width : 2.461

Height : 9.886



B0L002S, U10-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	326
Mean	46.91
Std Dev	10.17

Turn on : 31.2293

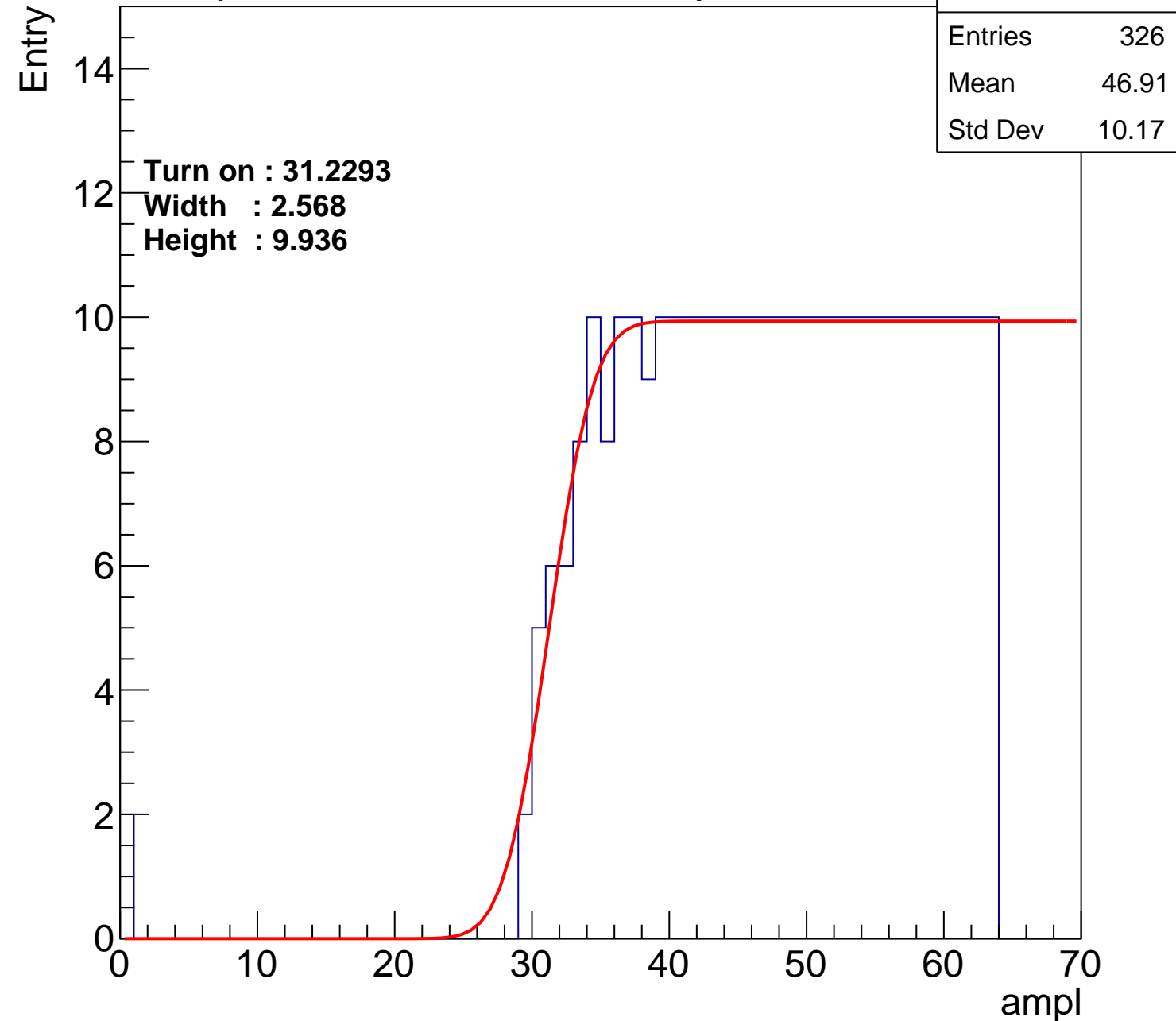
Width : 2.568

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.15
Std Dev	10.85

Turn on : 27.7589

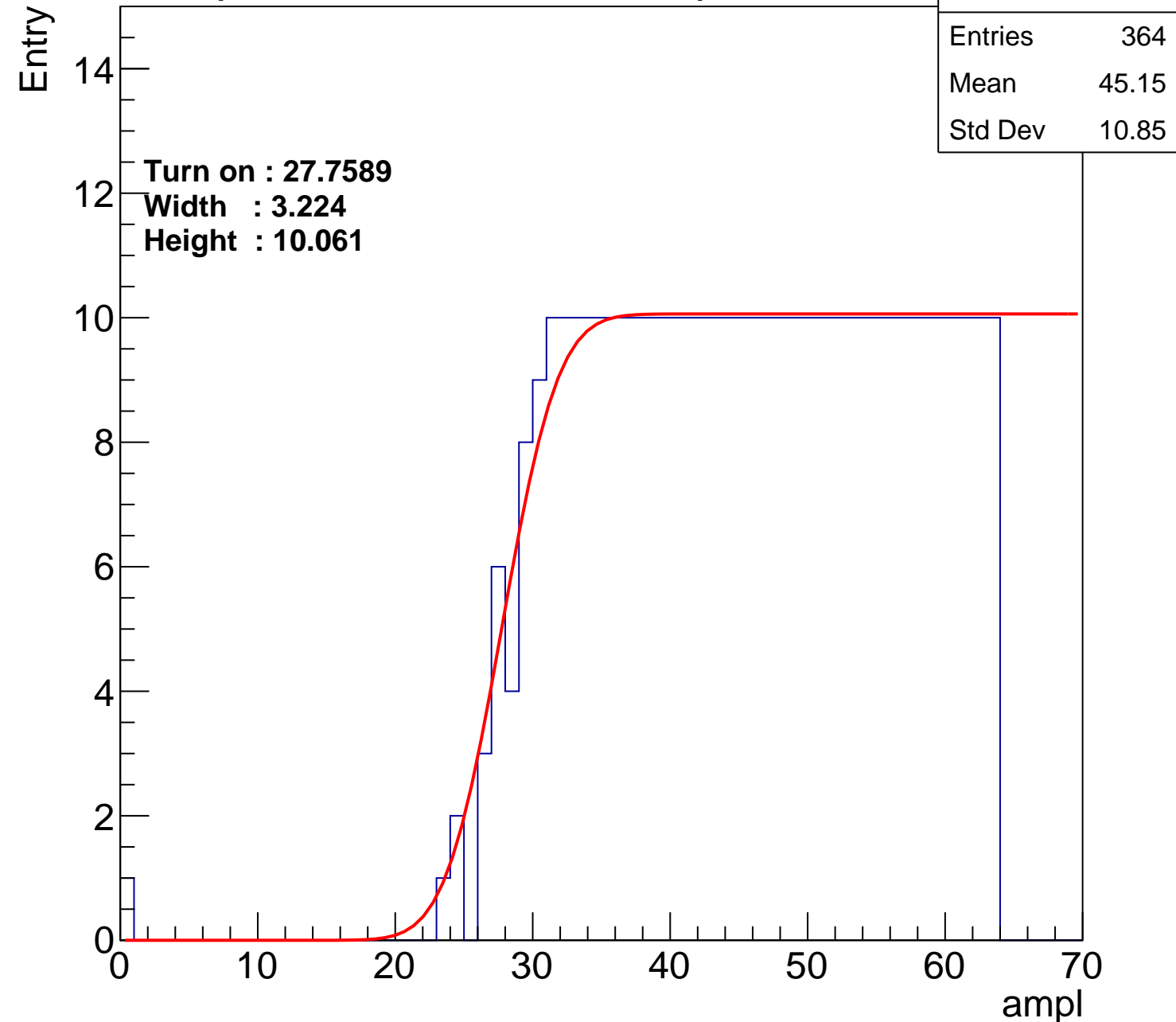
Width : 3.224

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.72
Std Dev	10.74

Turn on : 29.2997

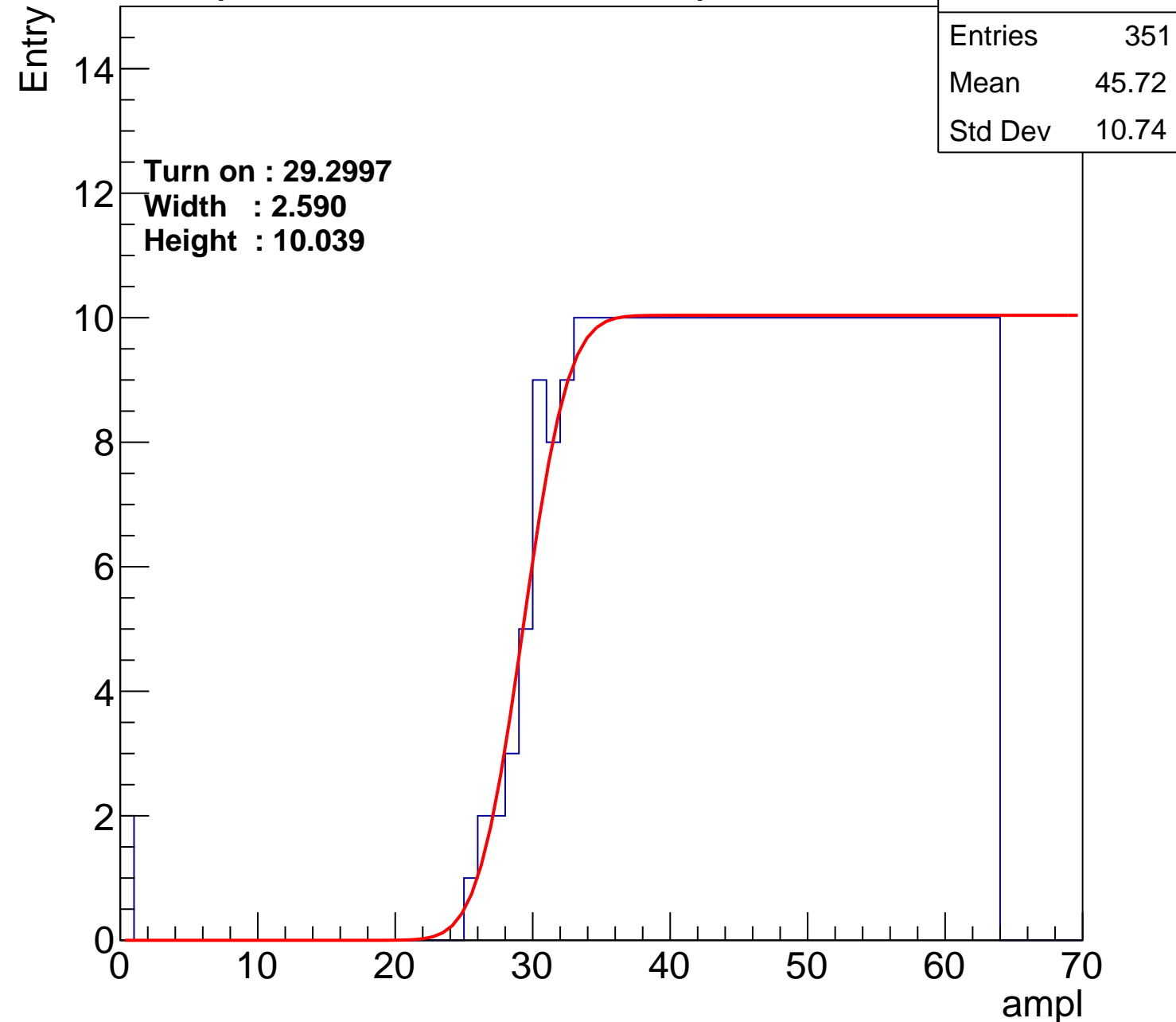
Width : 2.590

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.2715

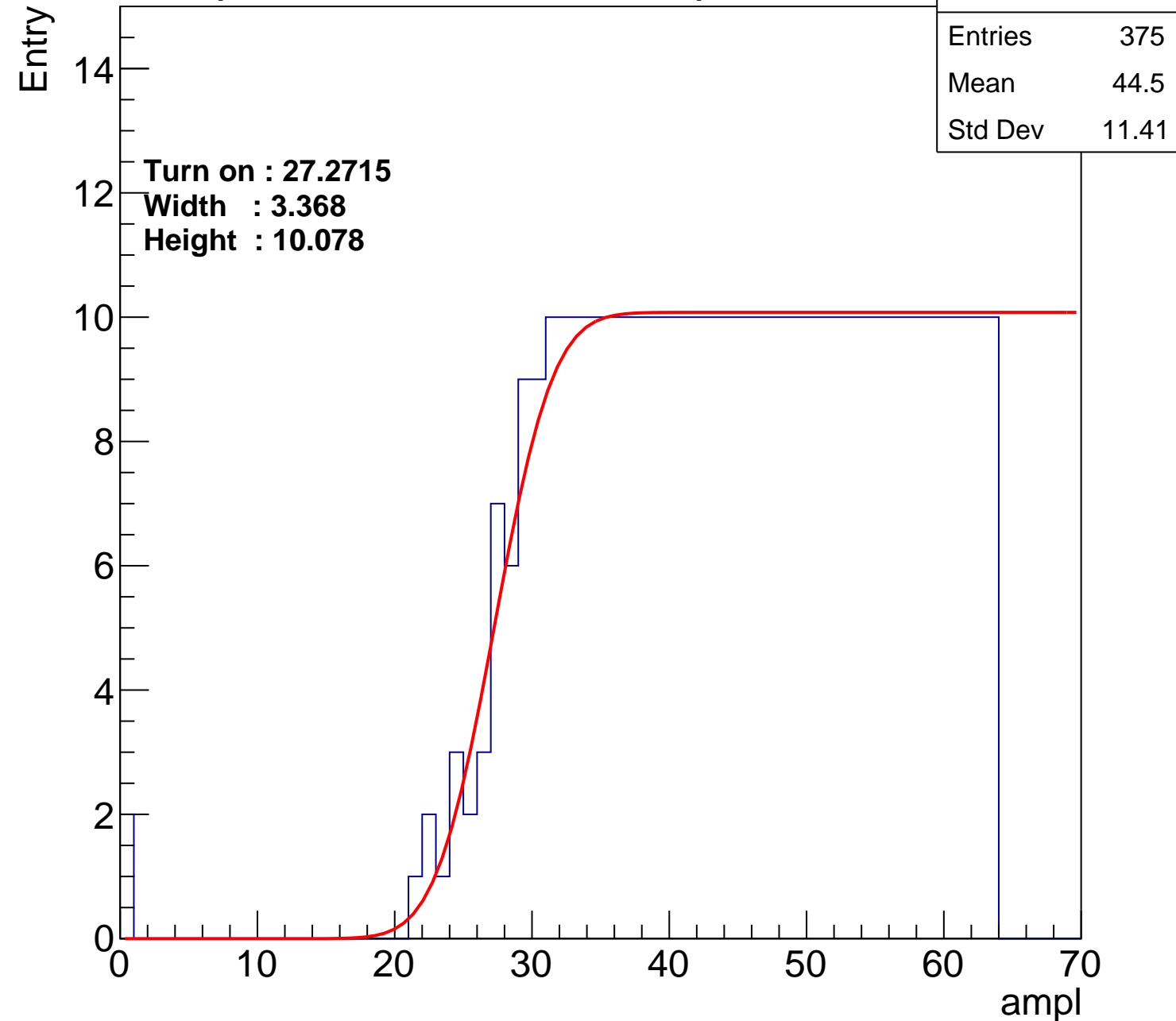
Width : 3.368

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.01
Std Dev	11.38

Turn on : 28.4067

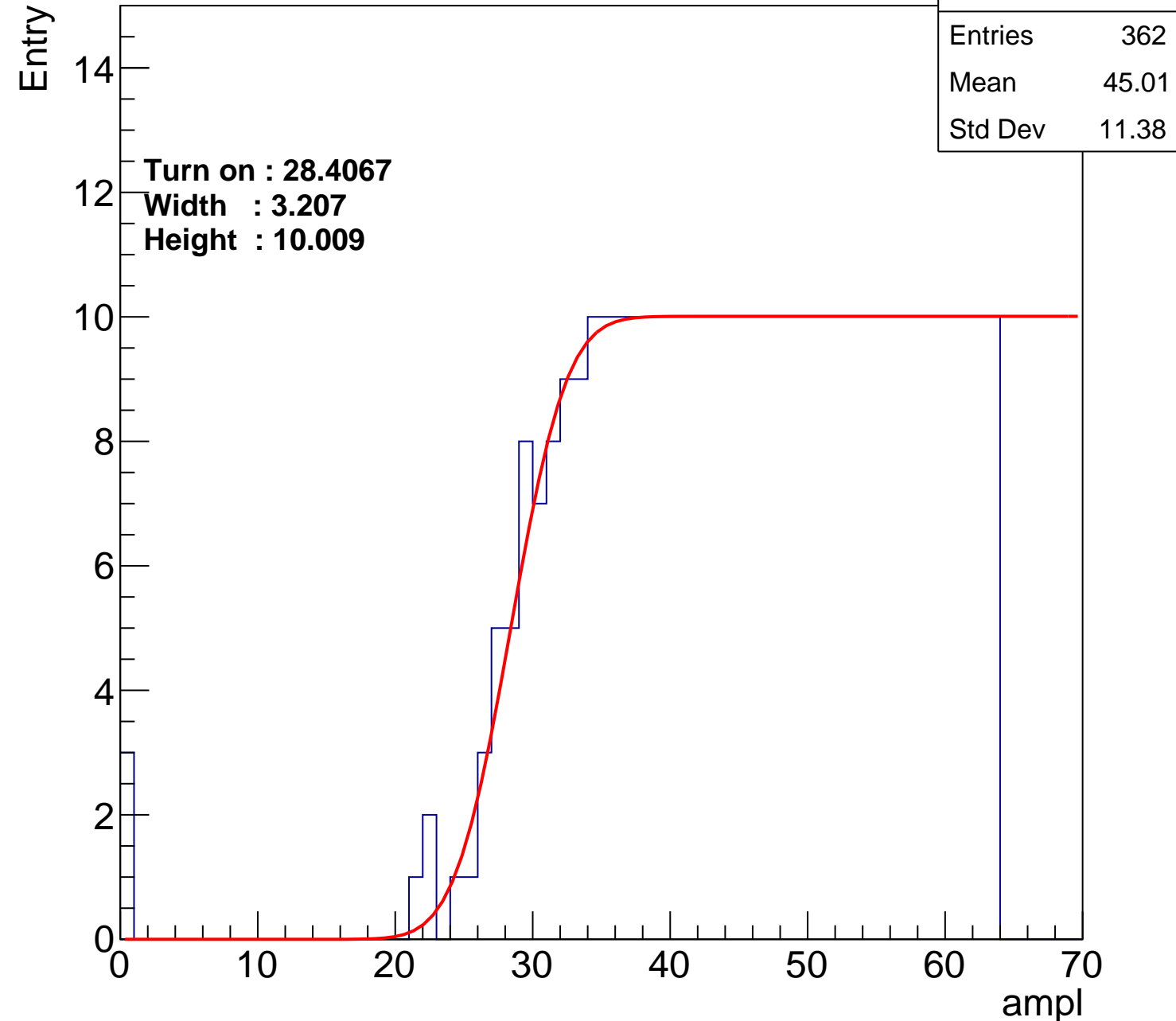
Width : 3.207

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.99
Std Dev	11.15

Turn on : 28.1071

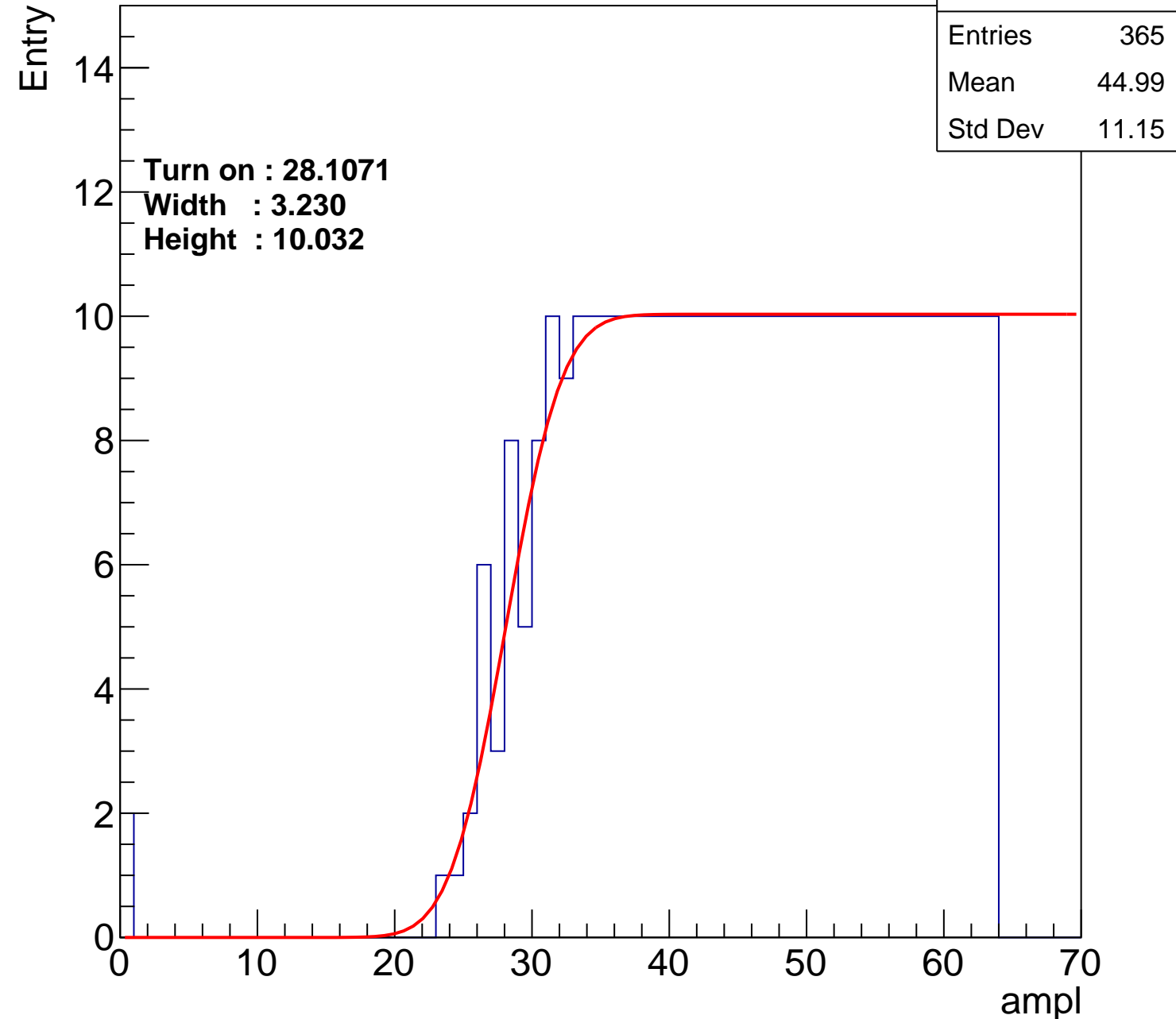
Width : 3.230

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.2102

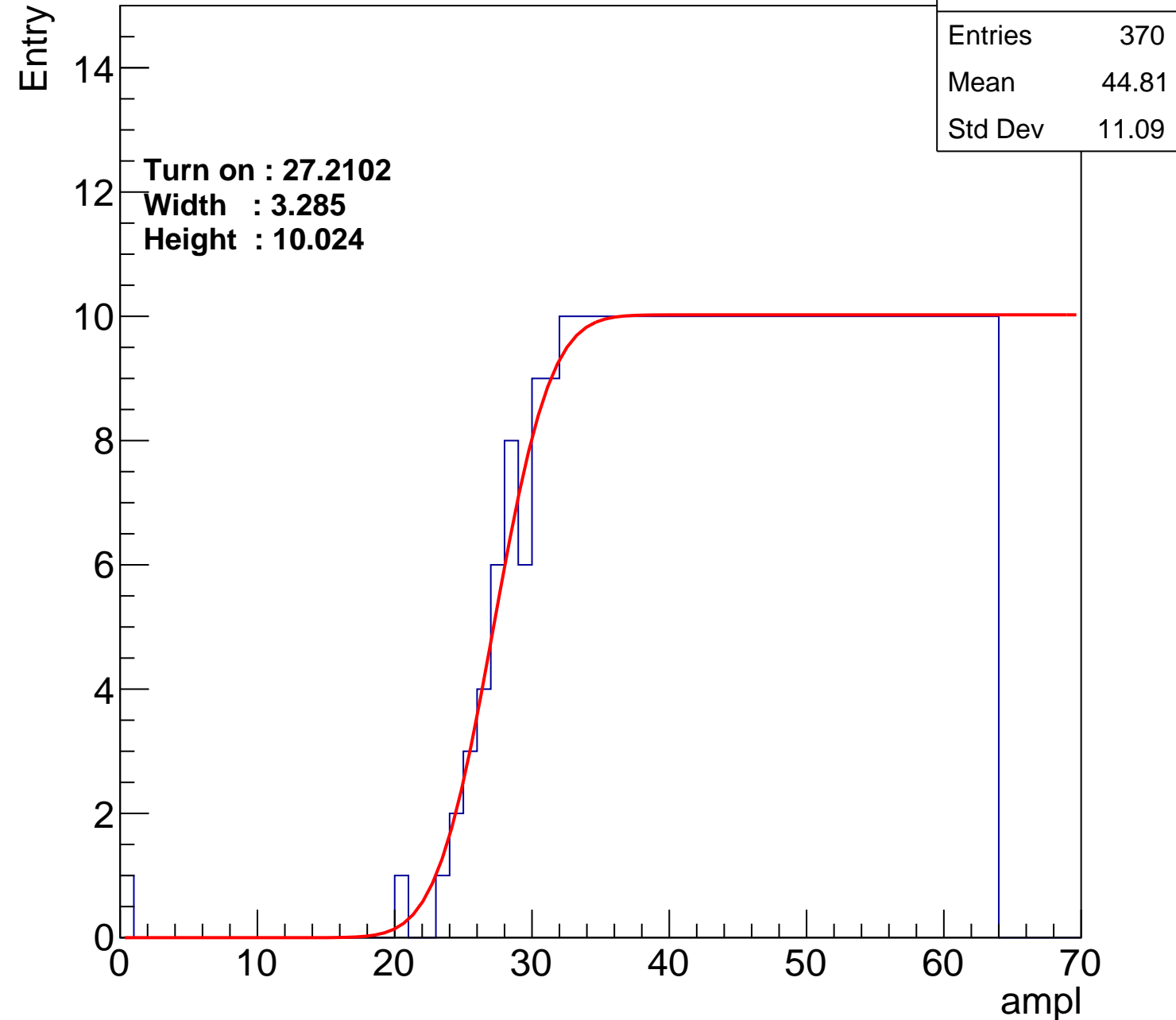
Width : 3.285

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.6
Std Dev	11.15

Turn on : 26.4007

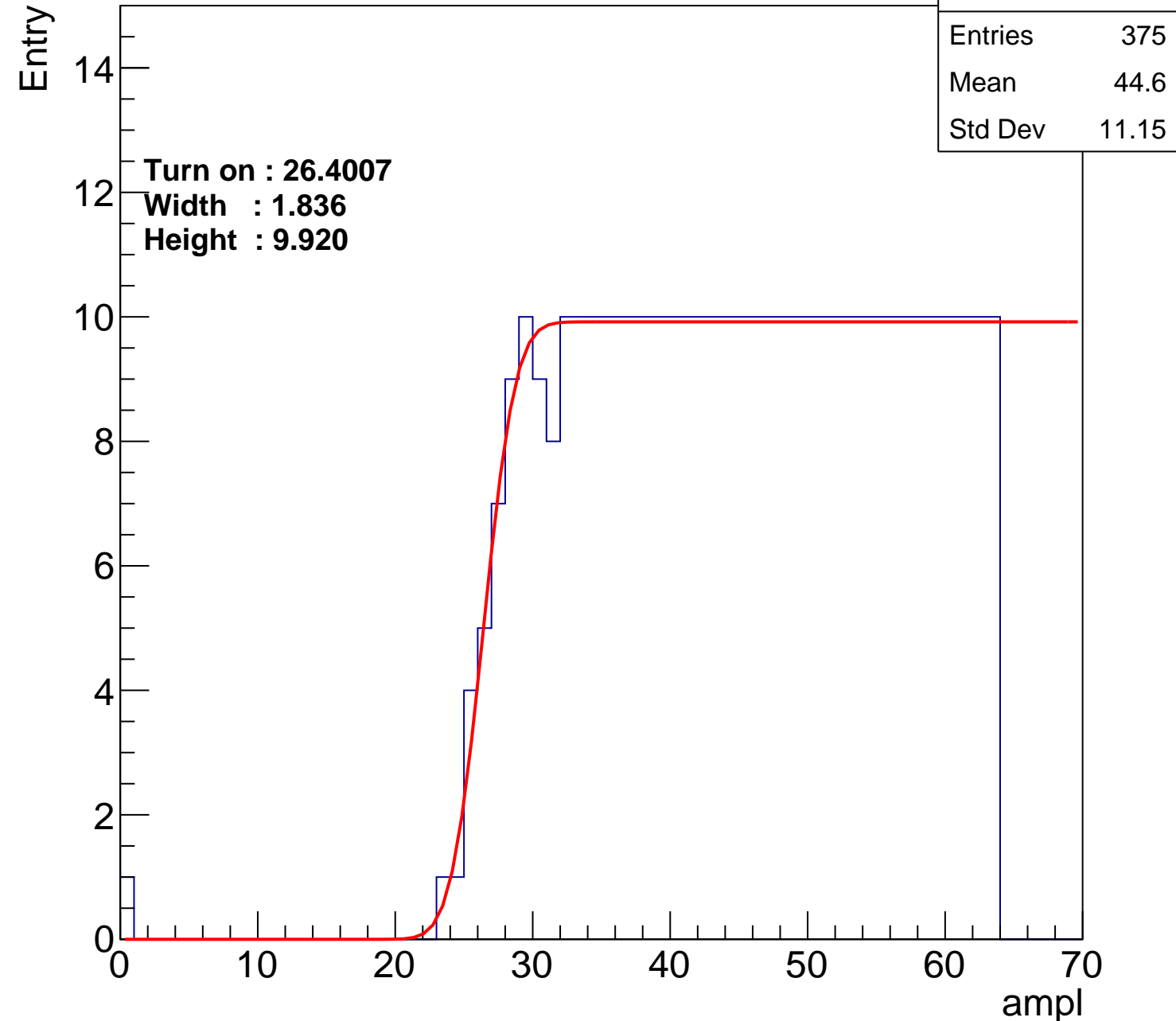
Width : 1.836

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.6417

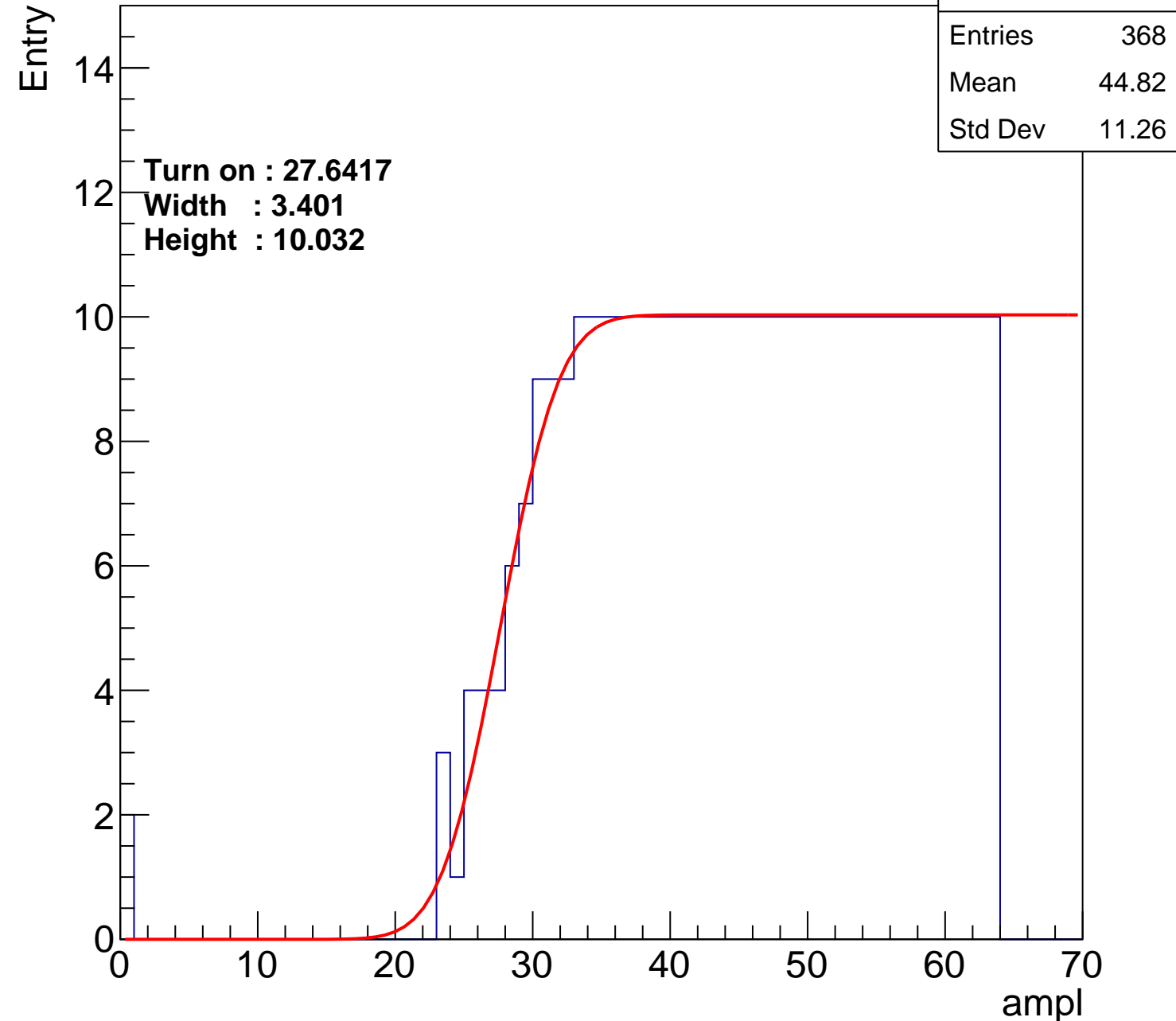
Width : 3.401

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch110

calib_packv5_042523_0143.root, FC#8, port C1

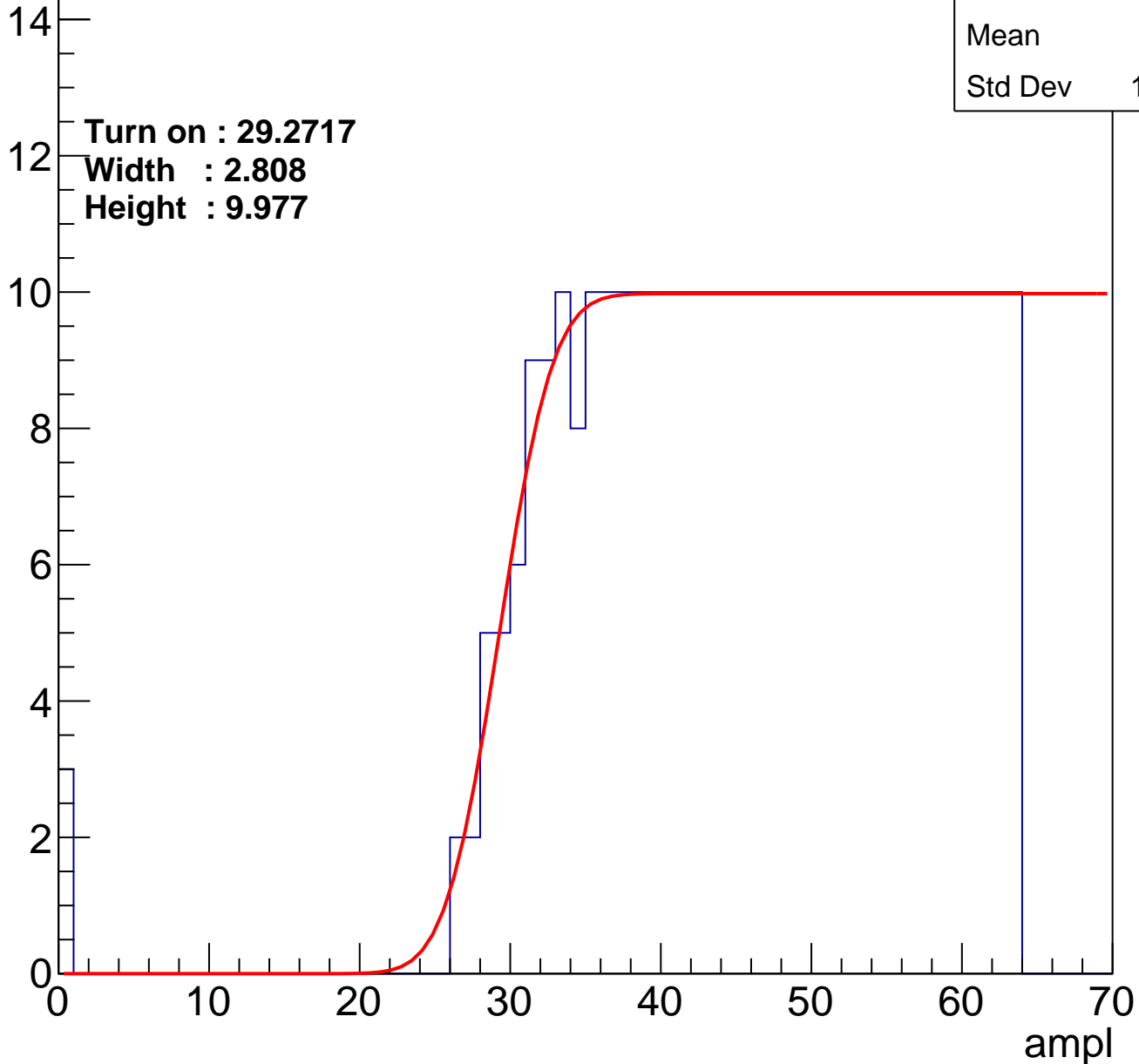
Entries	349
Mean	45.7
Std Dev	10.97

Turn on : 29.2717

Width : 2.808

Height : 9.977

Entry



B0L002S, U10-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.74
Std Dev	11.57

Turn on : 27.7894

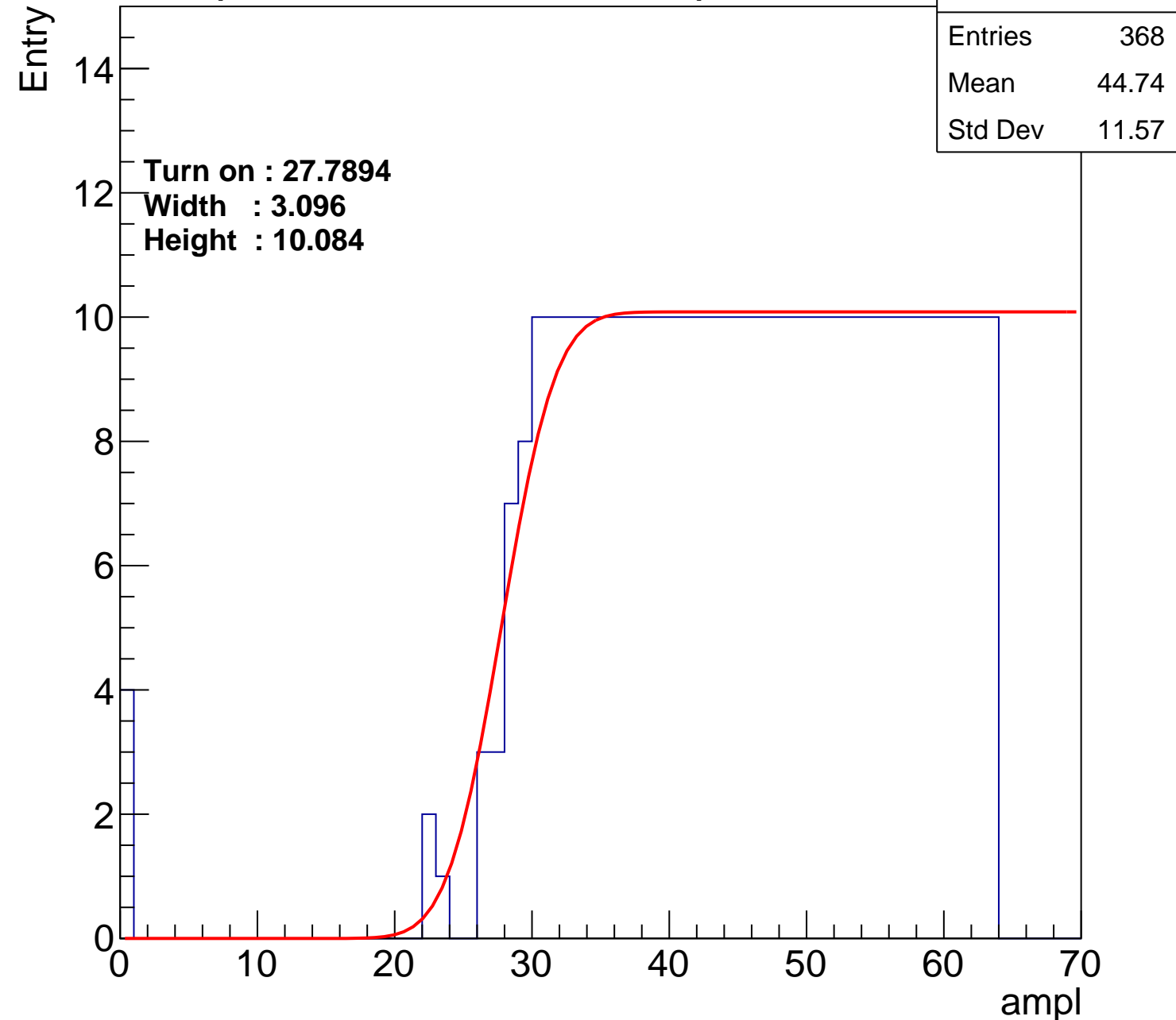
Width : 3.096

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.77
Std Dev	10.7

Turn on : 29.1999

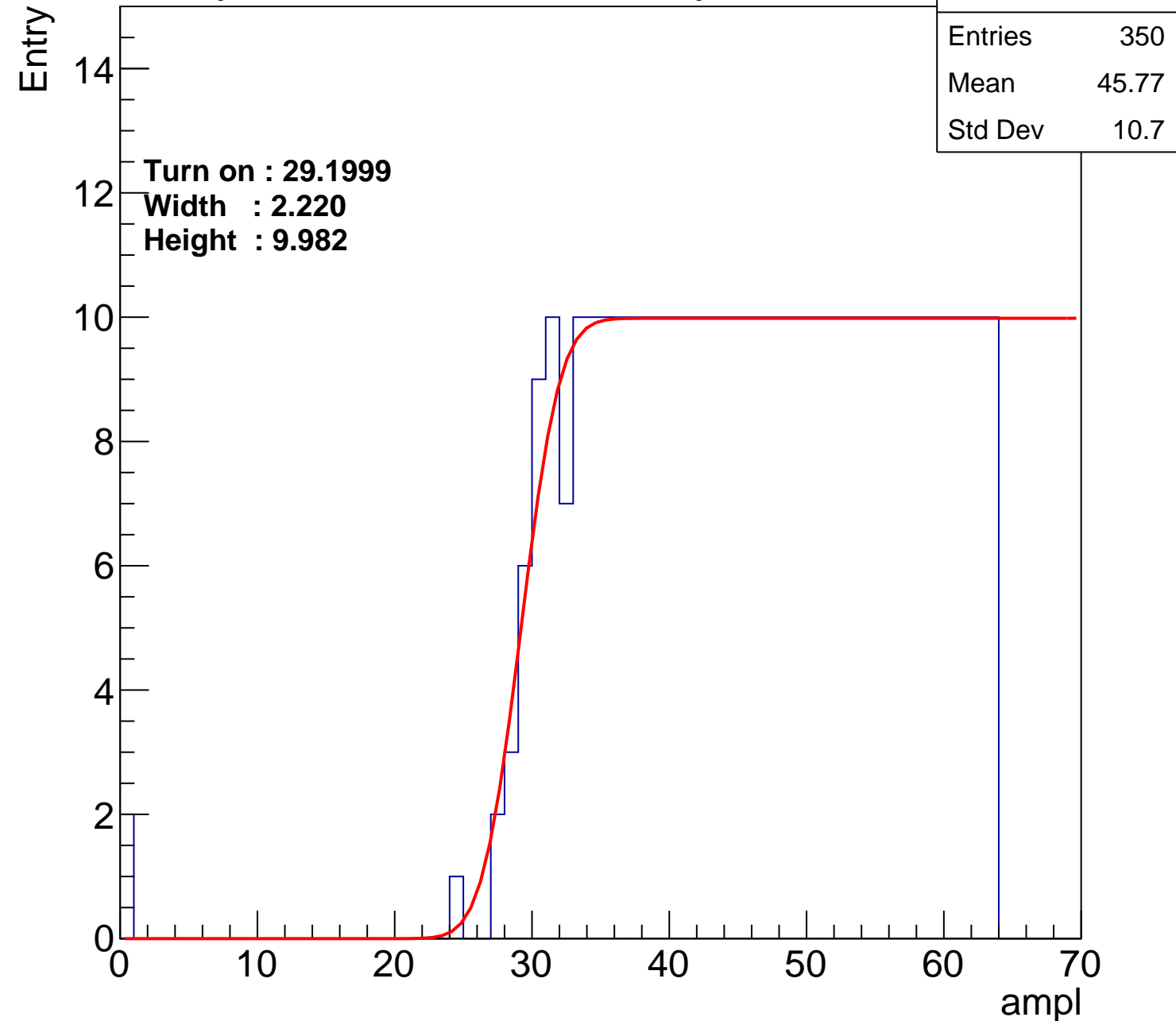
Width : 2.220

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch113

calib_packv5_042523_0143.root, FC#8, port C1

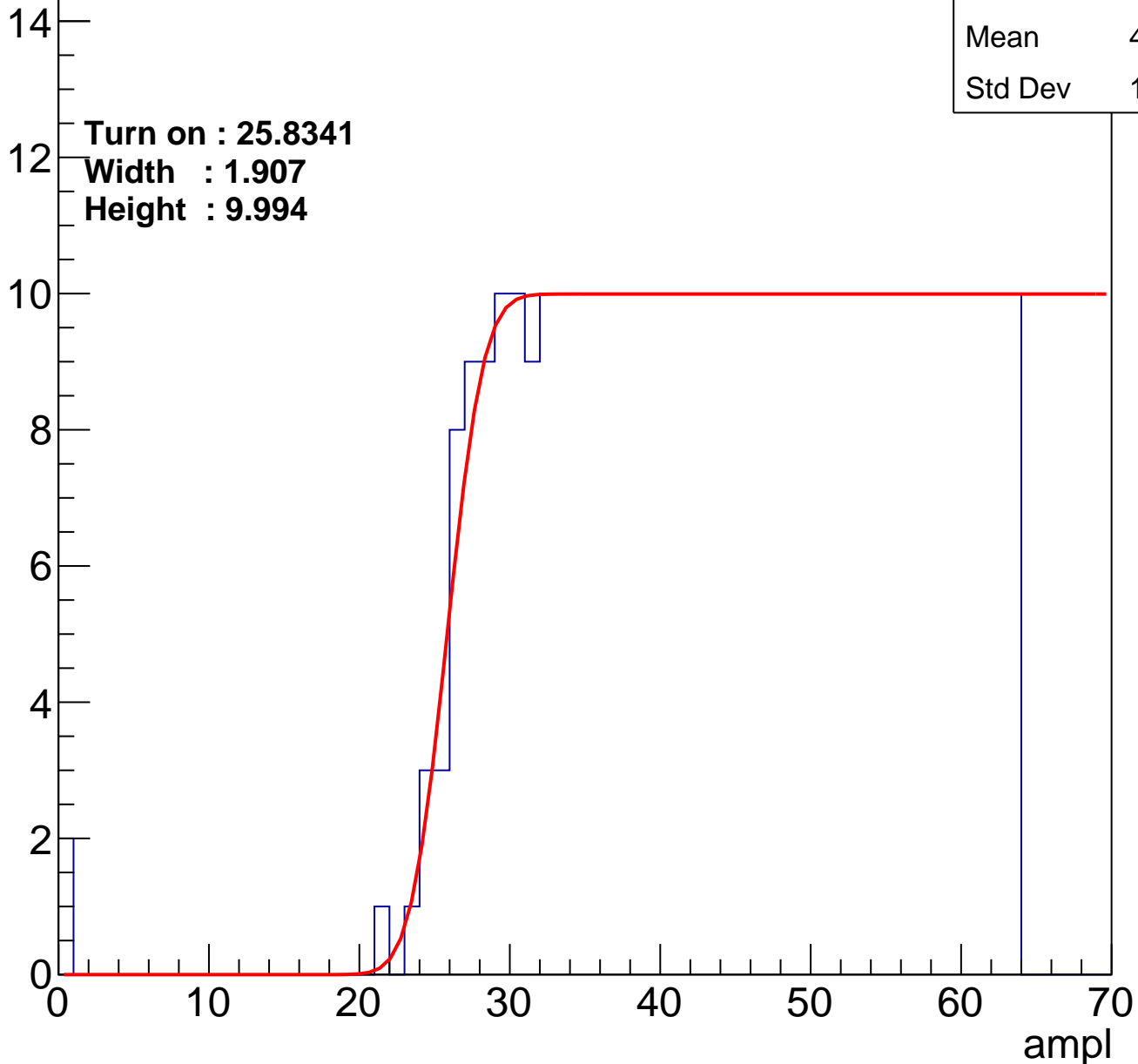
Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.8341

Width : 1.907

Height : 9.994

Entry



B0L002S, U10-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.12
Std Dev	10.34

Turn on : 29.4891

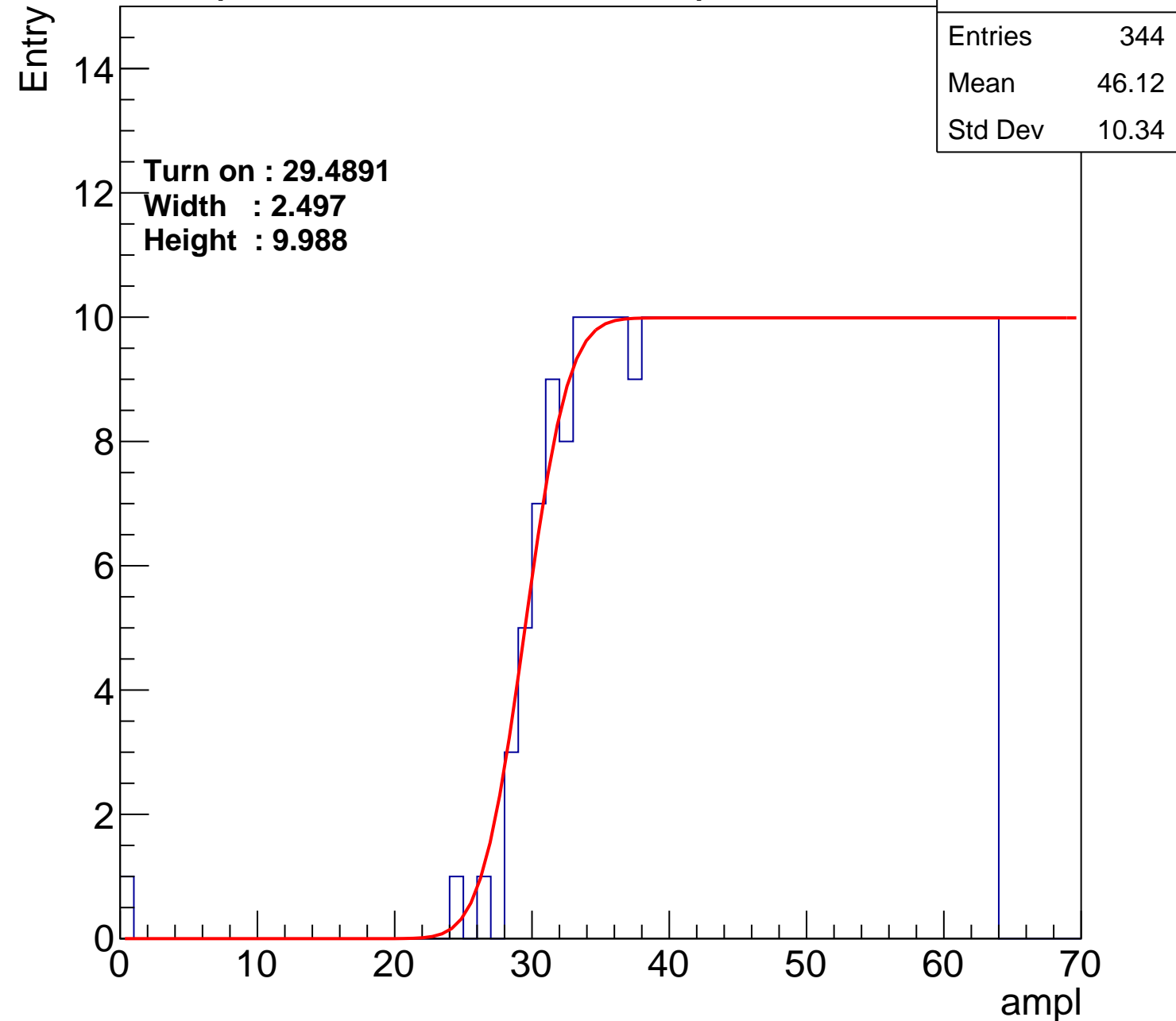
Width : 2.497

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.89
Std Dev	10.67

Turn on : 29.4132

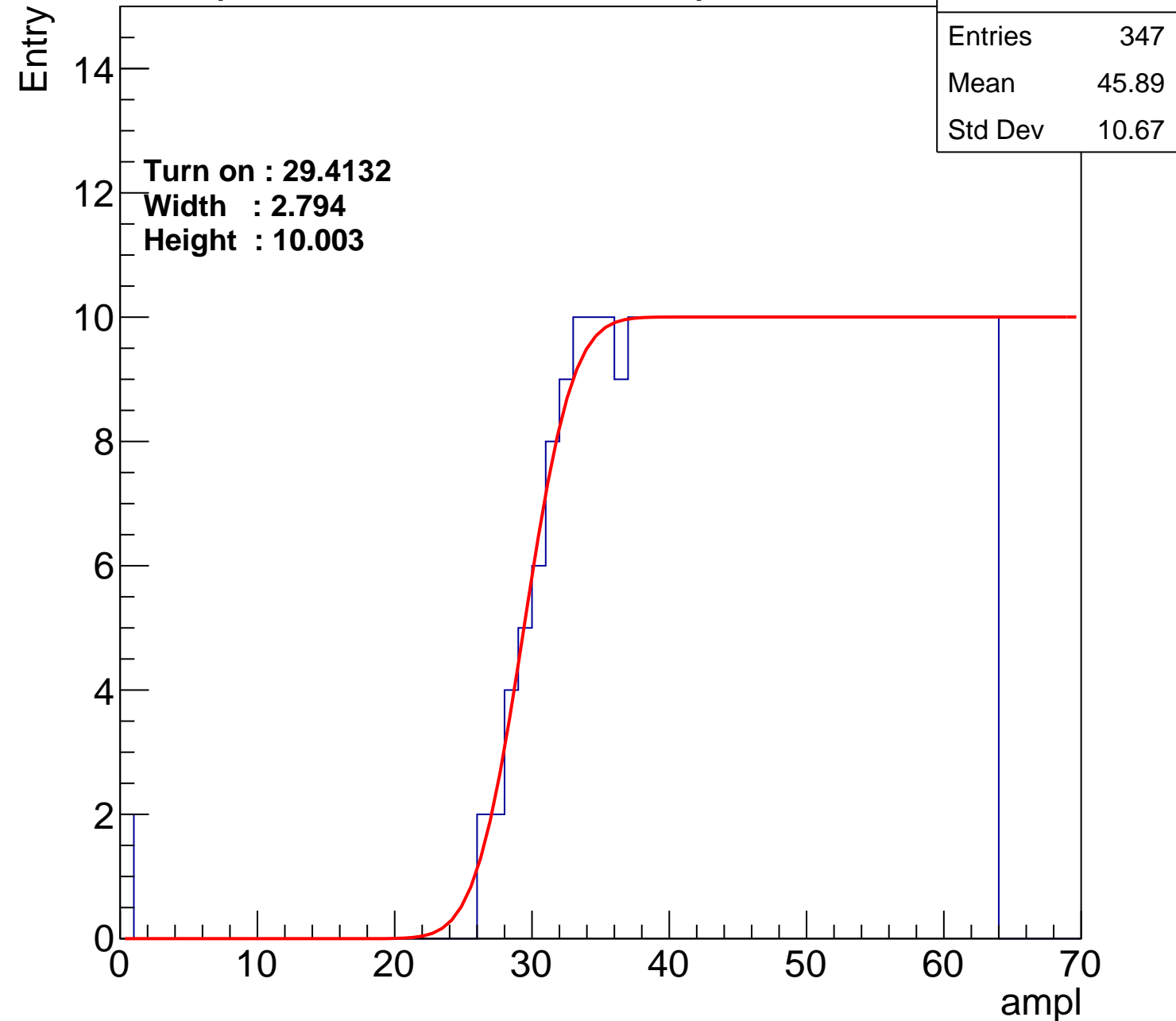
Width : 2.794

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.2265

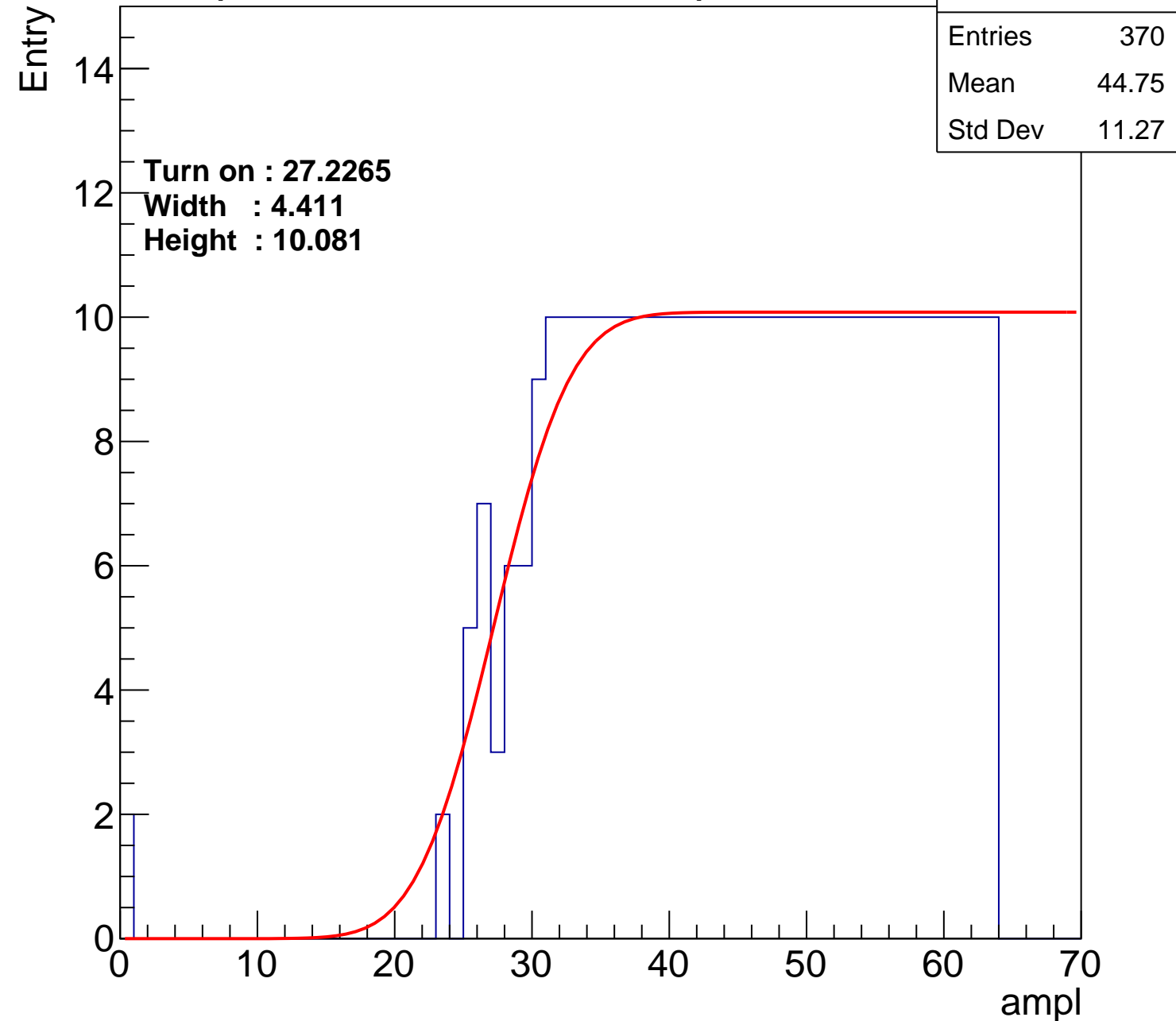
Width : 4.411

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.74
Std Dev	10.8

Turn on : 29.3857

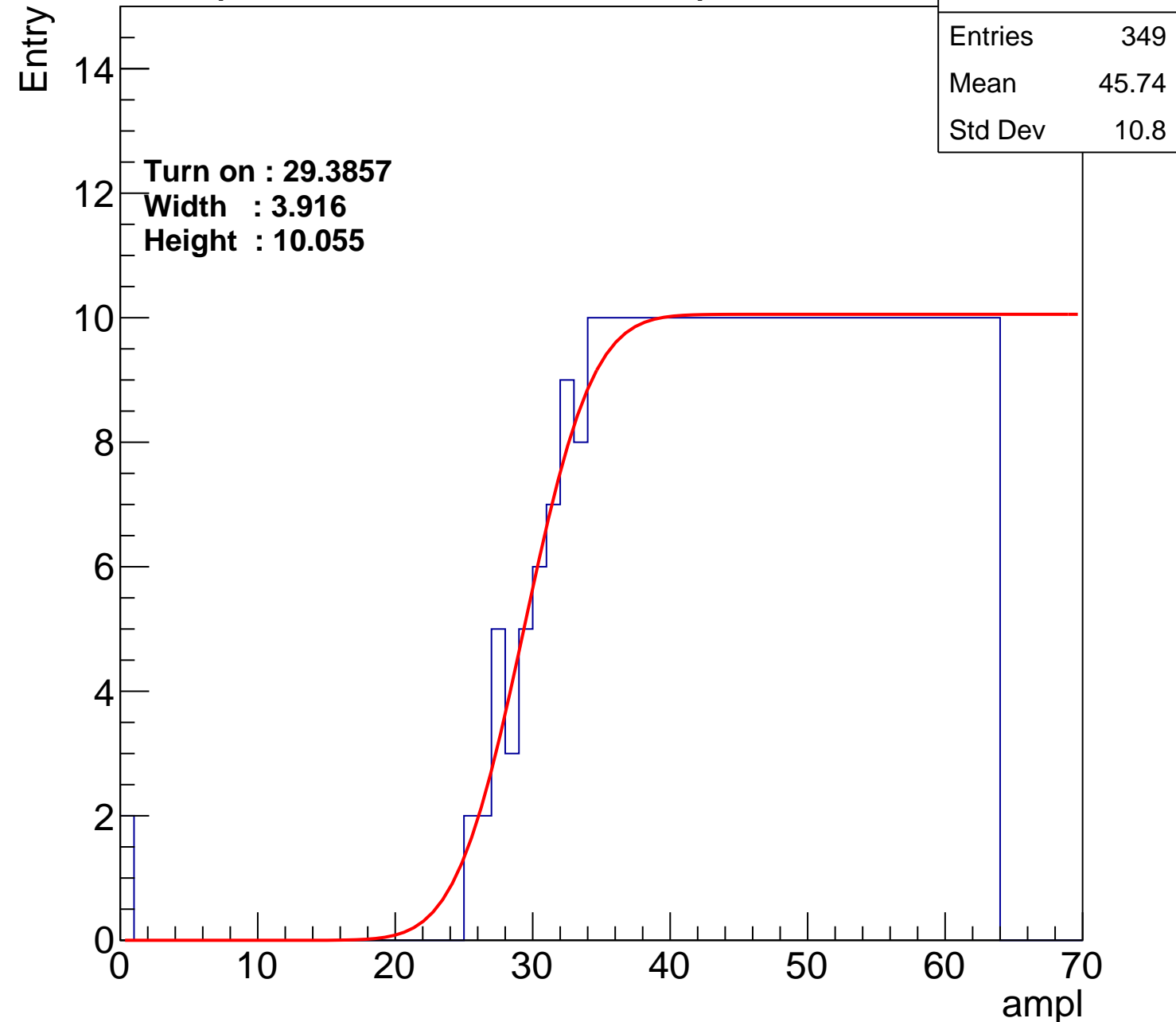
Width : 3.916

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.53
Std Dev	10.72

Turn on : 29.0271

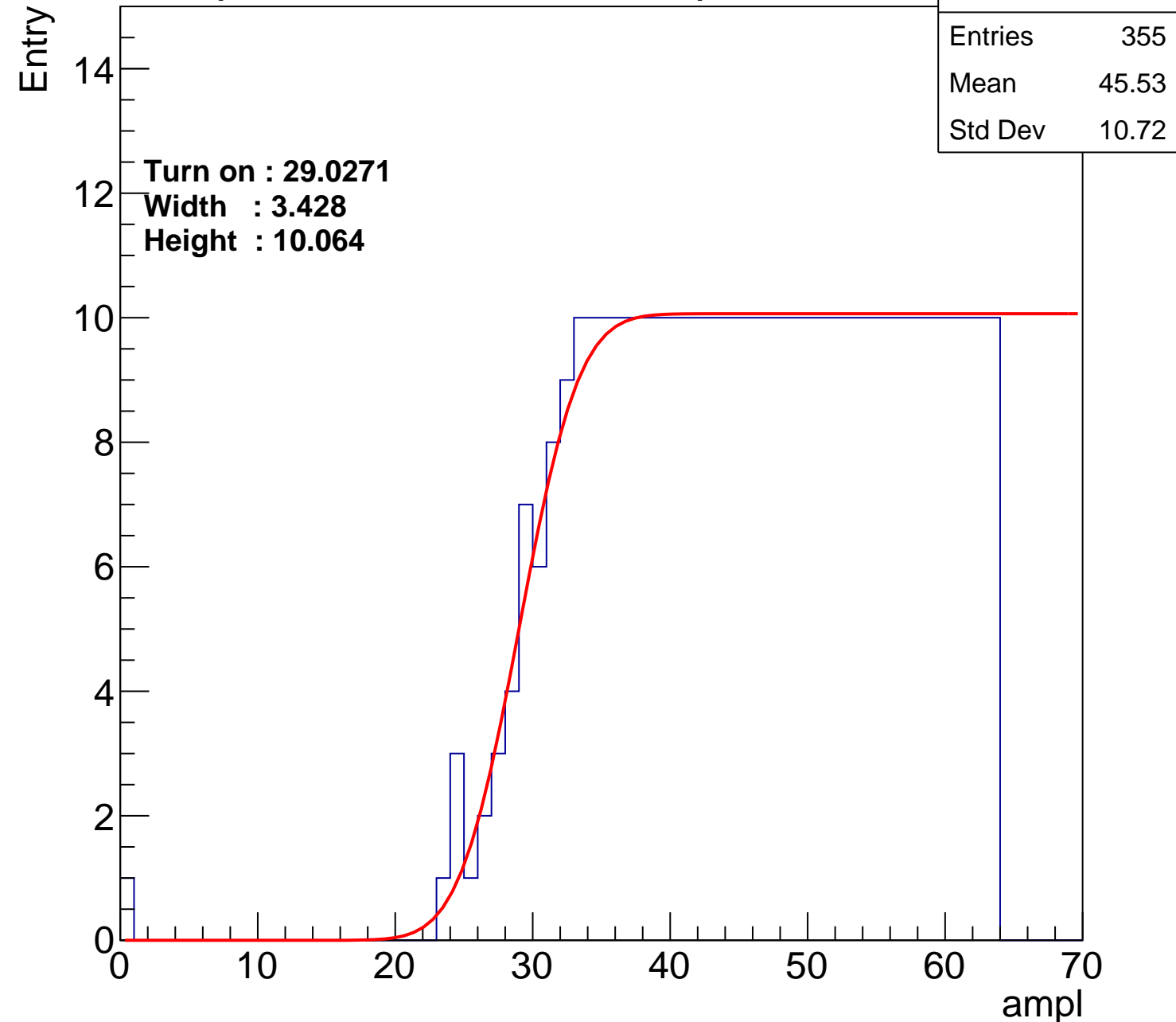
Width : 3.428

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch119

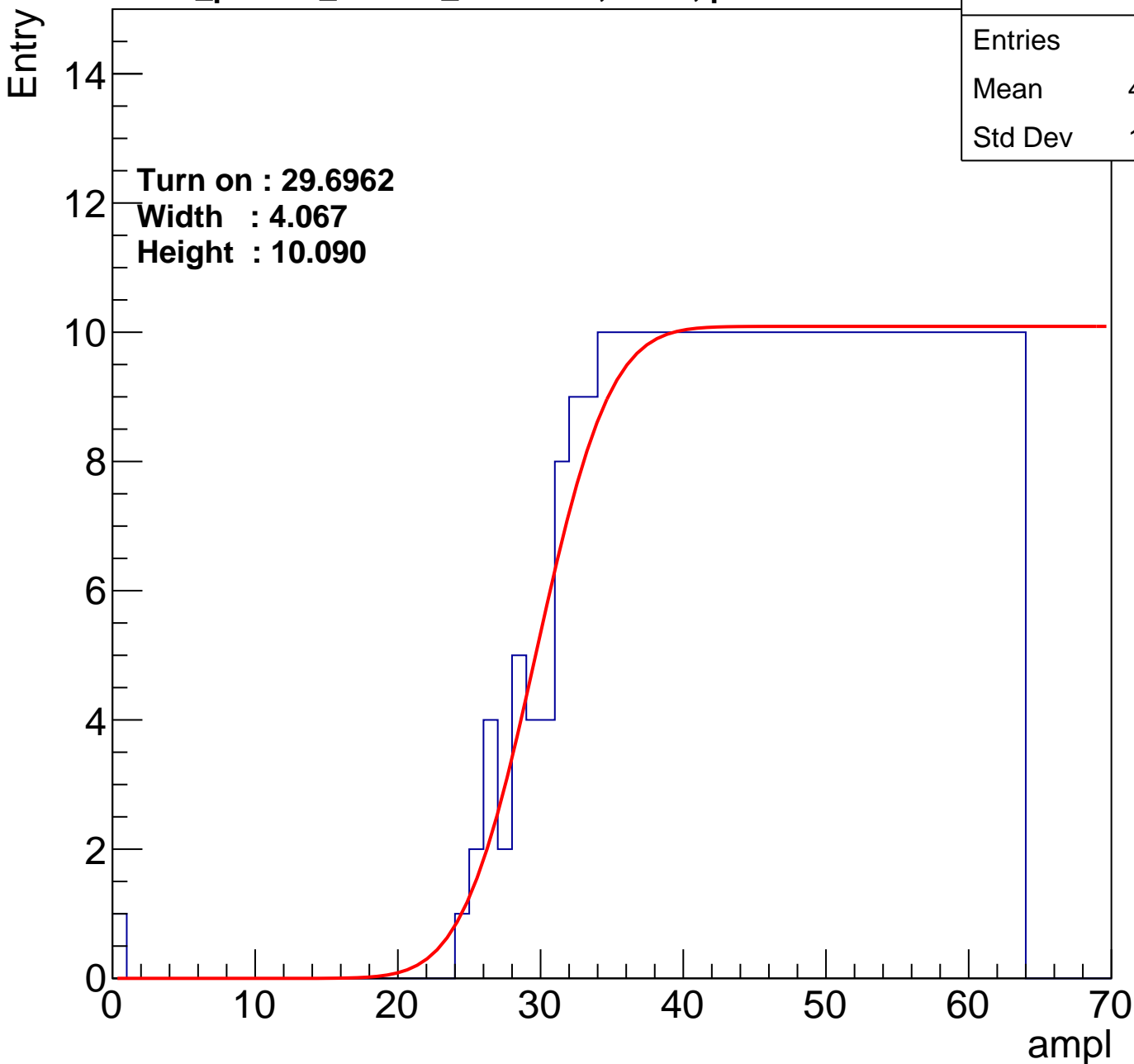
calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.82
Std Dev	10.57

Turn on : 29.6962

Width : 4.067

Height : 10.090



B0L002S, U10-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.27
Std Dev	10.86

Turn on : 28.5111

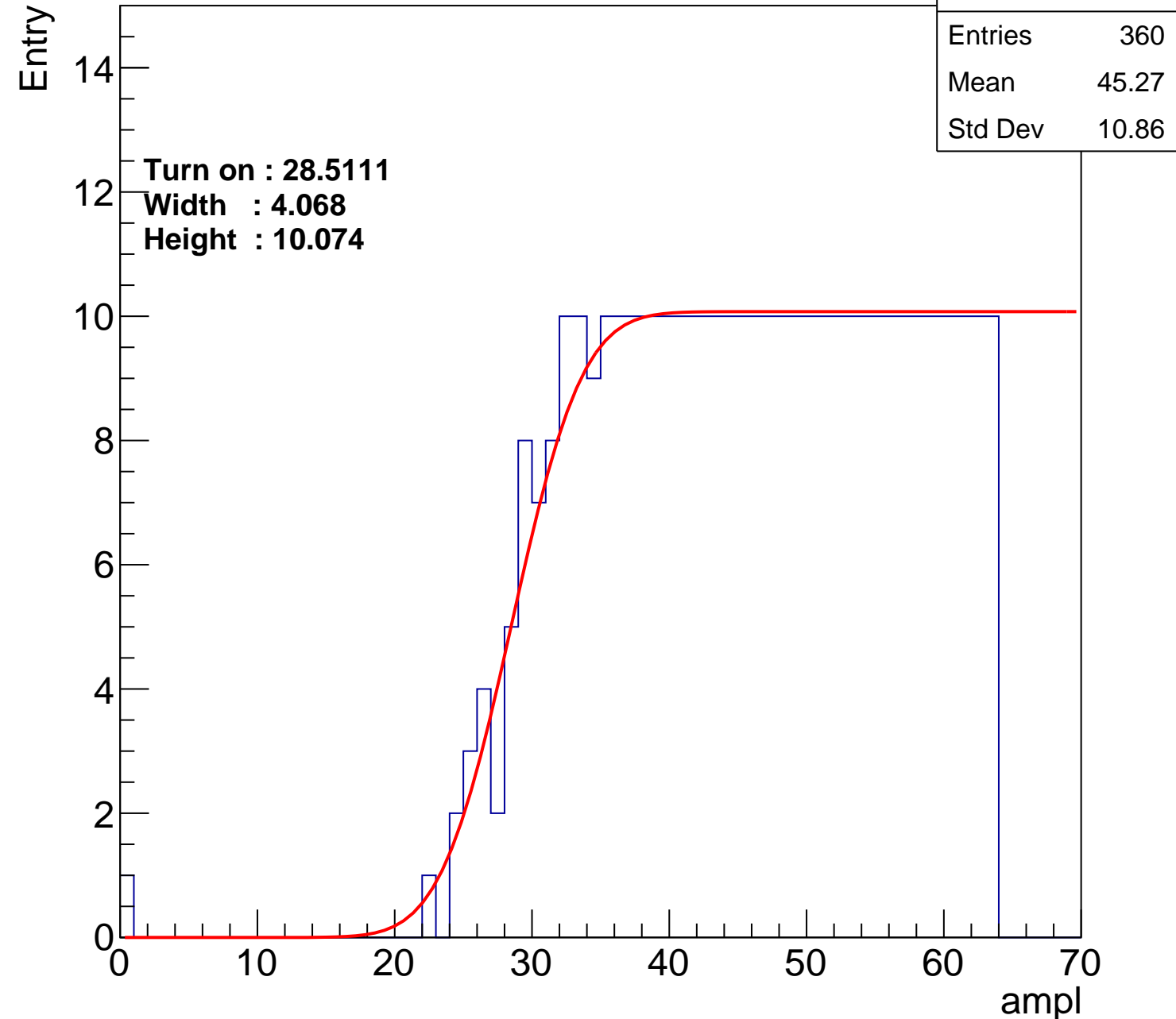
Width : 4.068

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.75
Std Dev	11.68

Turn on : 24.8923

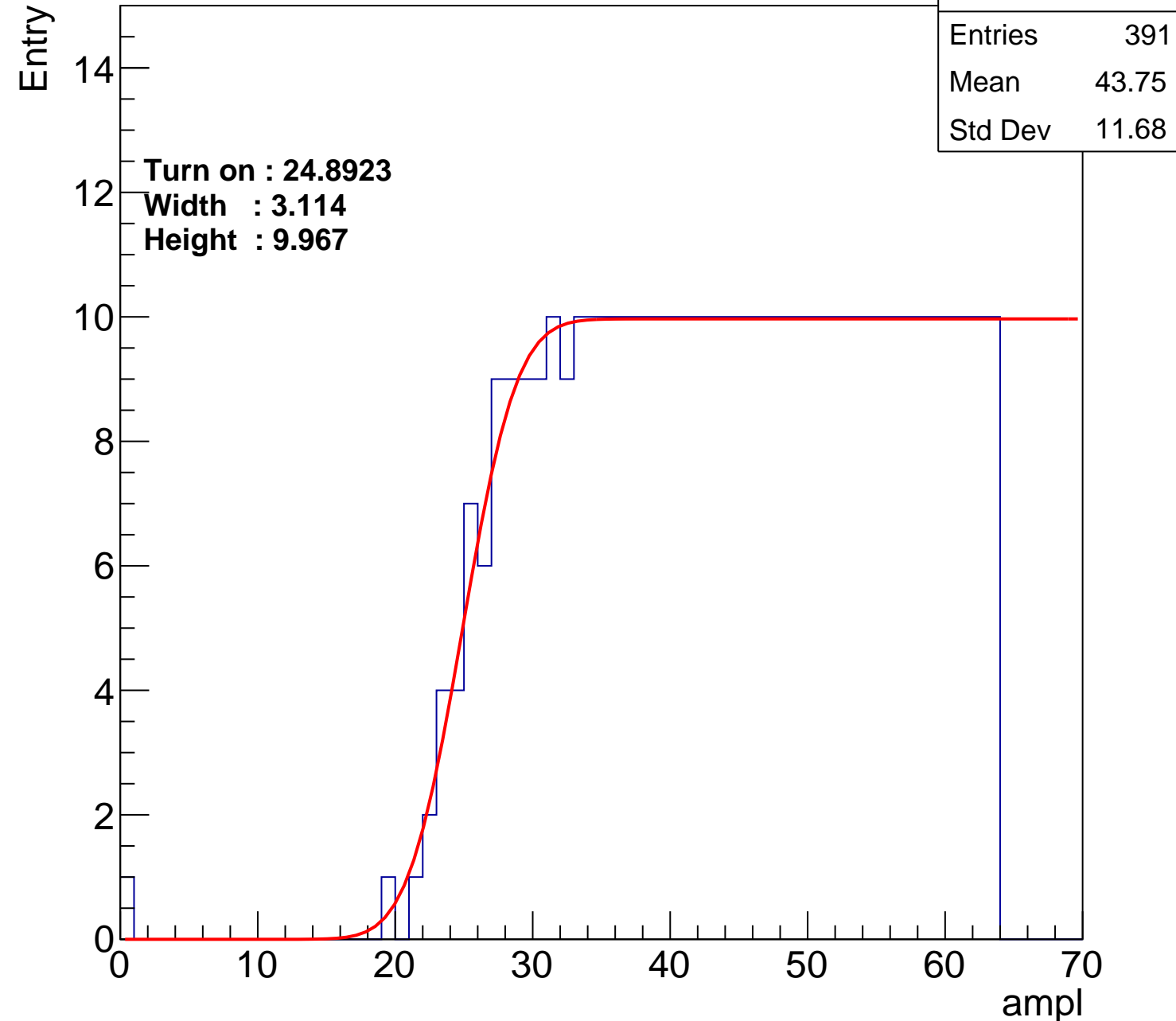
Width : 3.114

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.15
Std Dev	11.44

Turn on : 29.0822

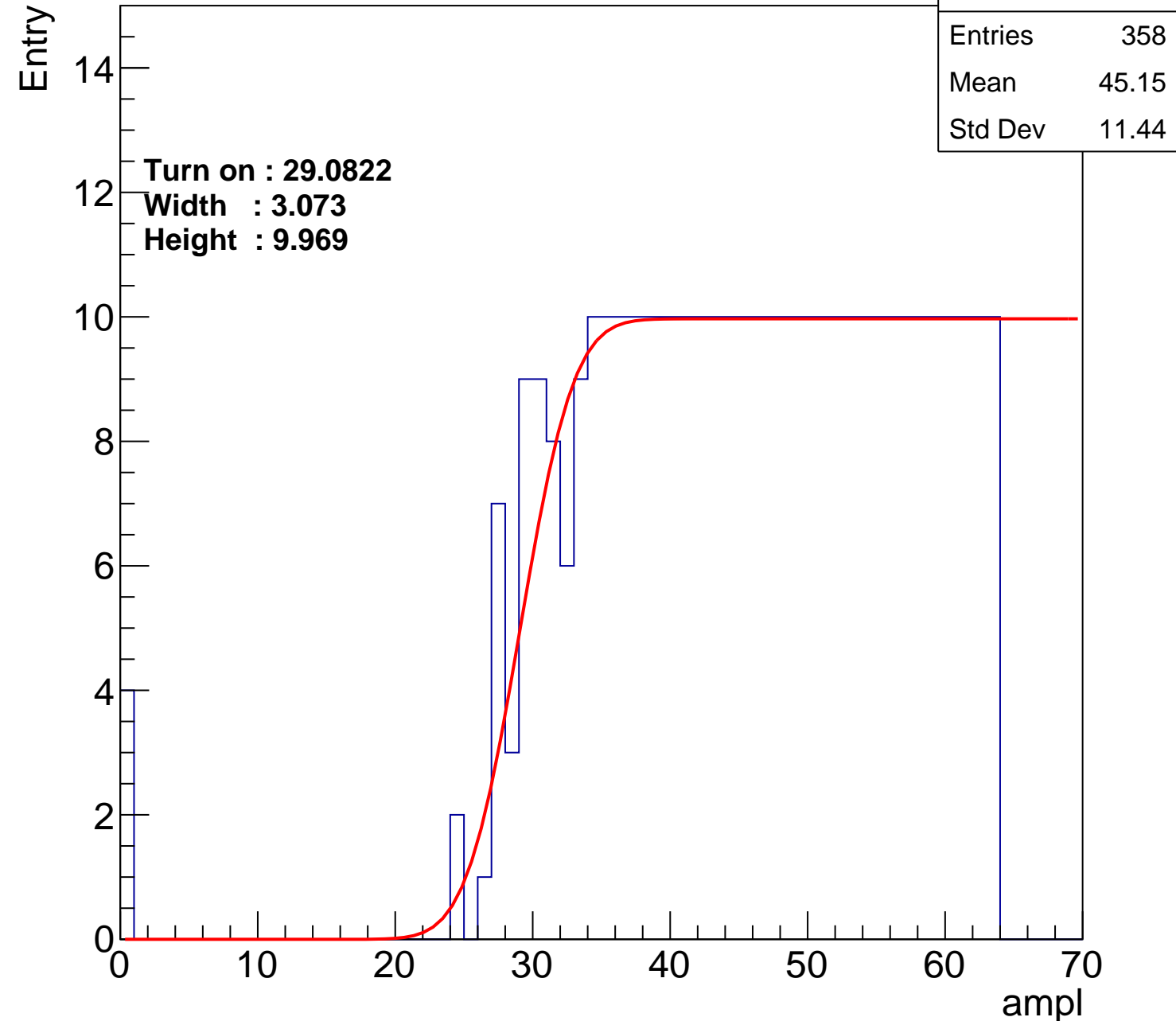
Width : 3.073

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.56
Std Dev	11.04

Turn on : 29.2449

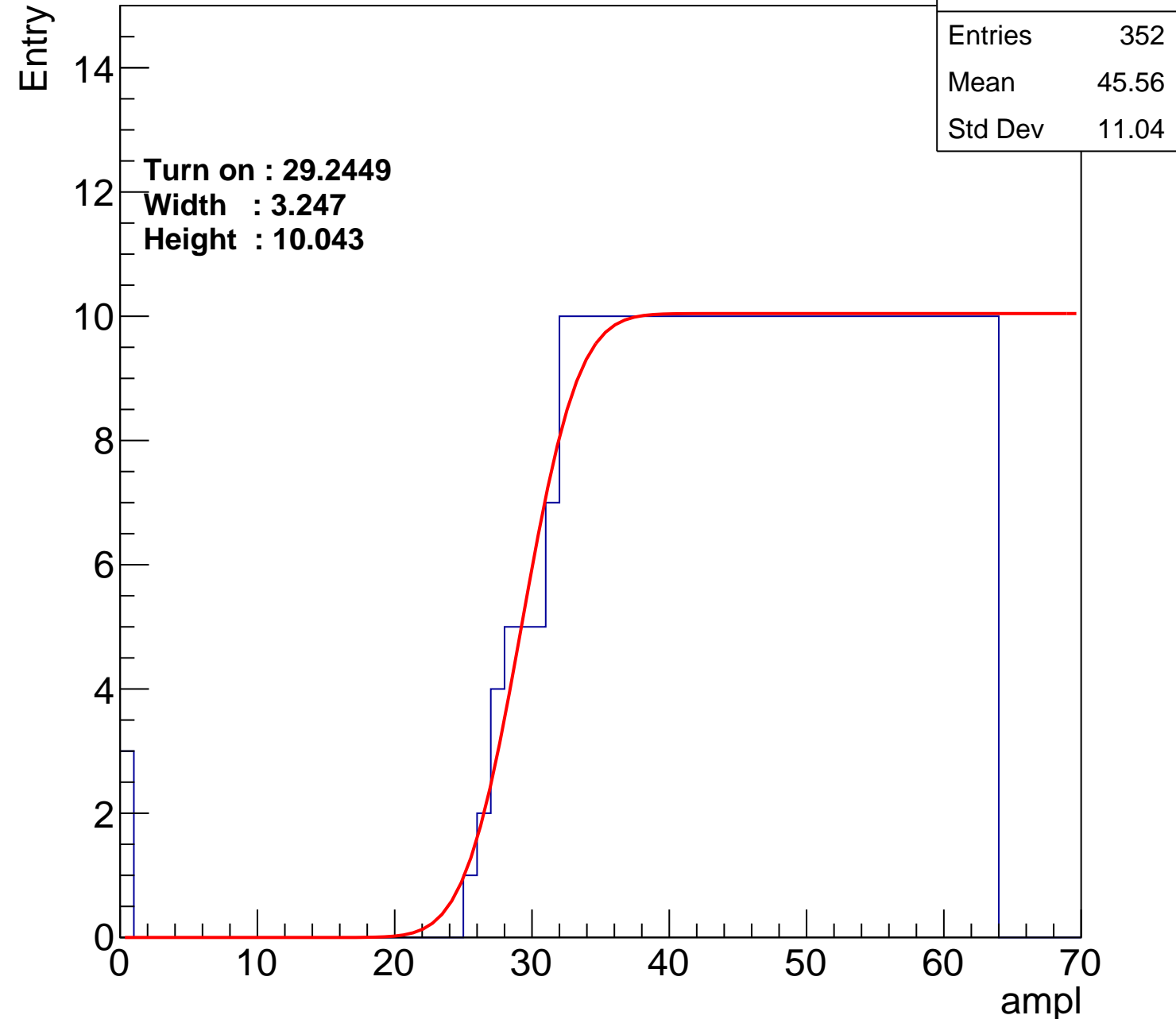
Width : 3.247

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.24
Std Dev	10.82

Turn on : 28.2886

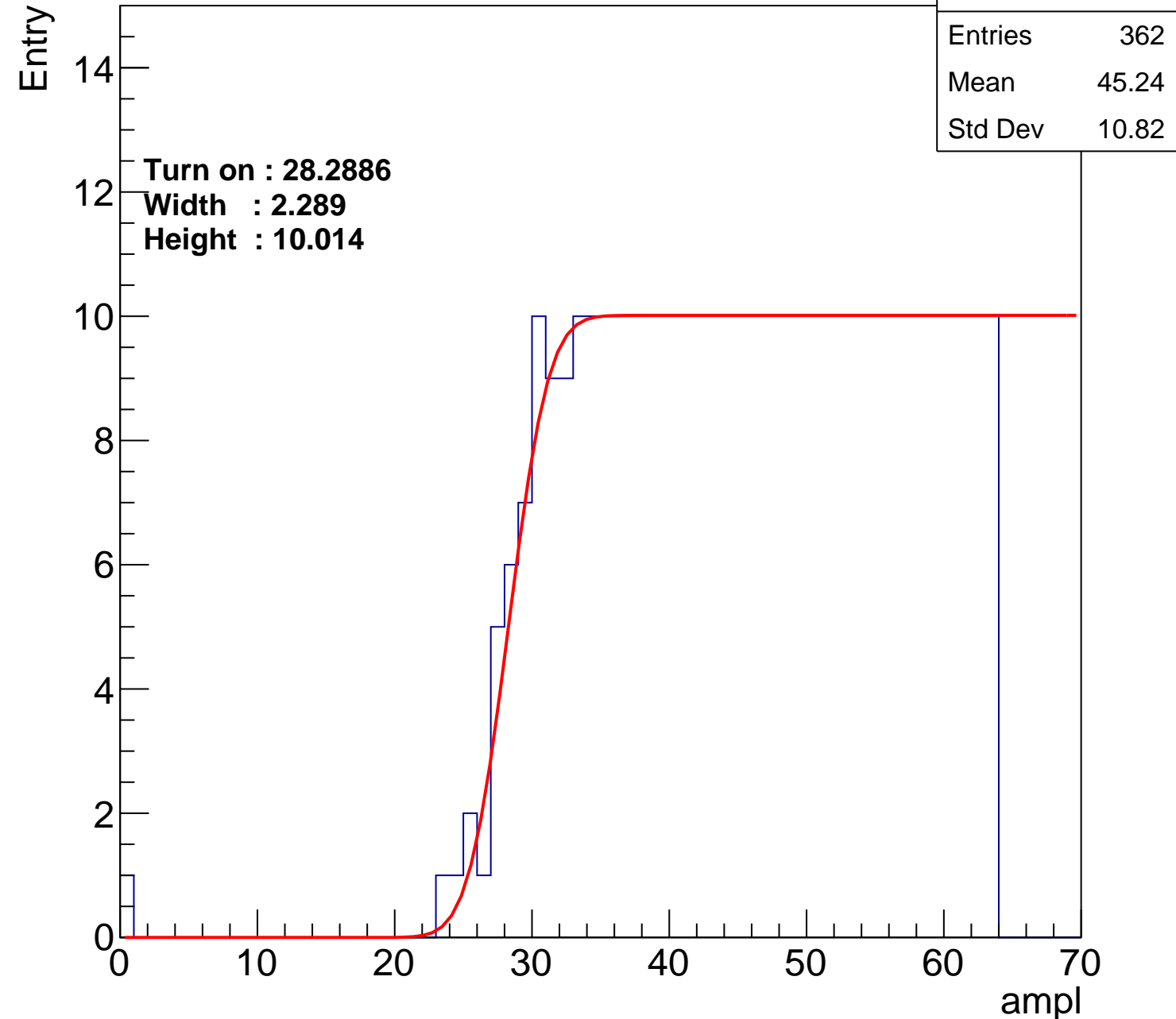
Width : 2.289

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.94
Std Dev	11.32

Turn on : 27.8523

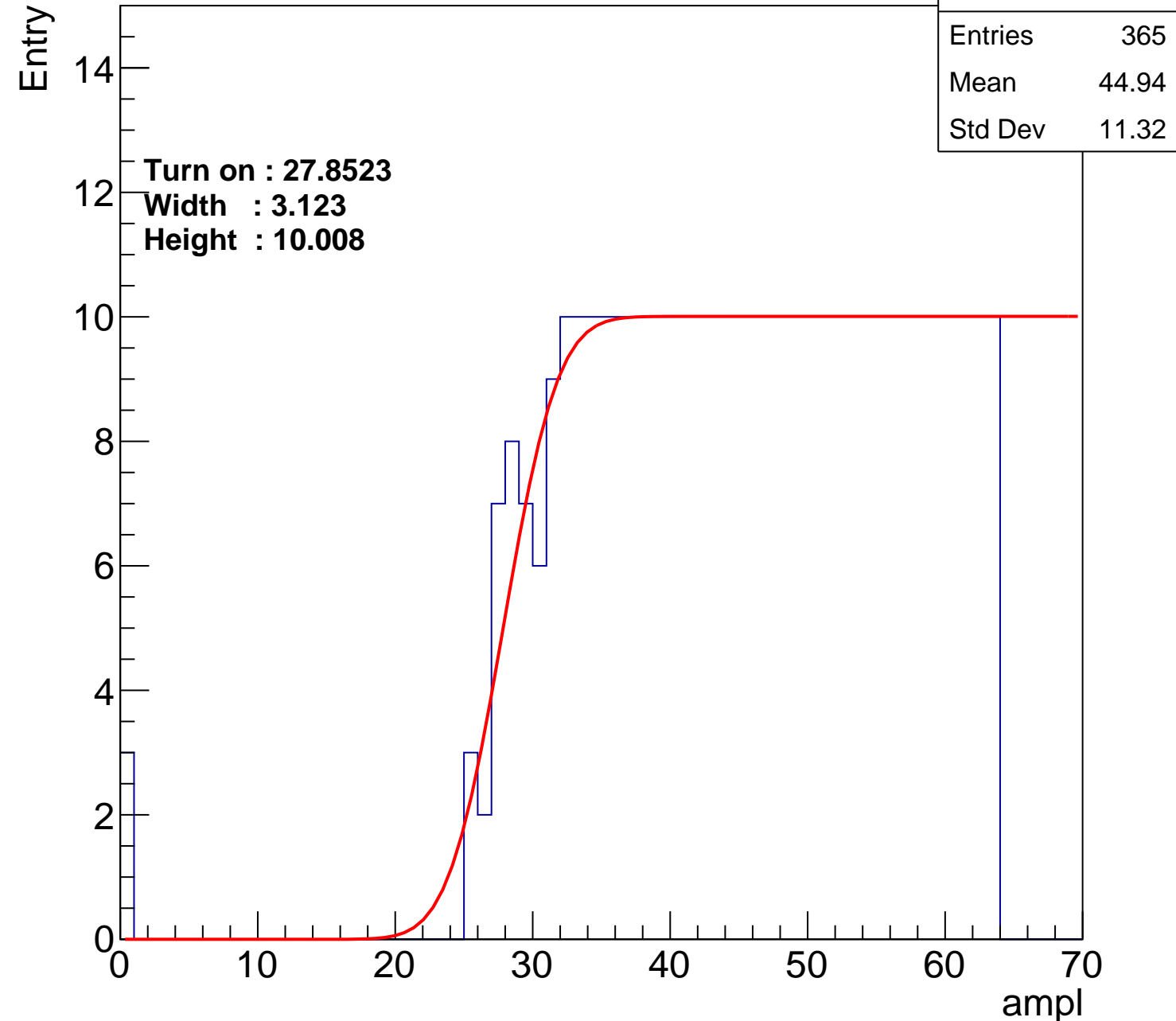
Width : 3.123

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U10-ch126

calib_packv5_042523_0143.root, FC#8, port C1

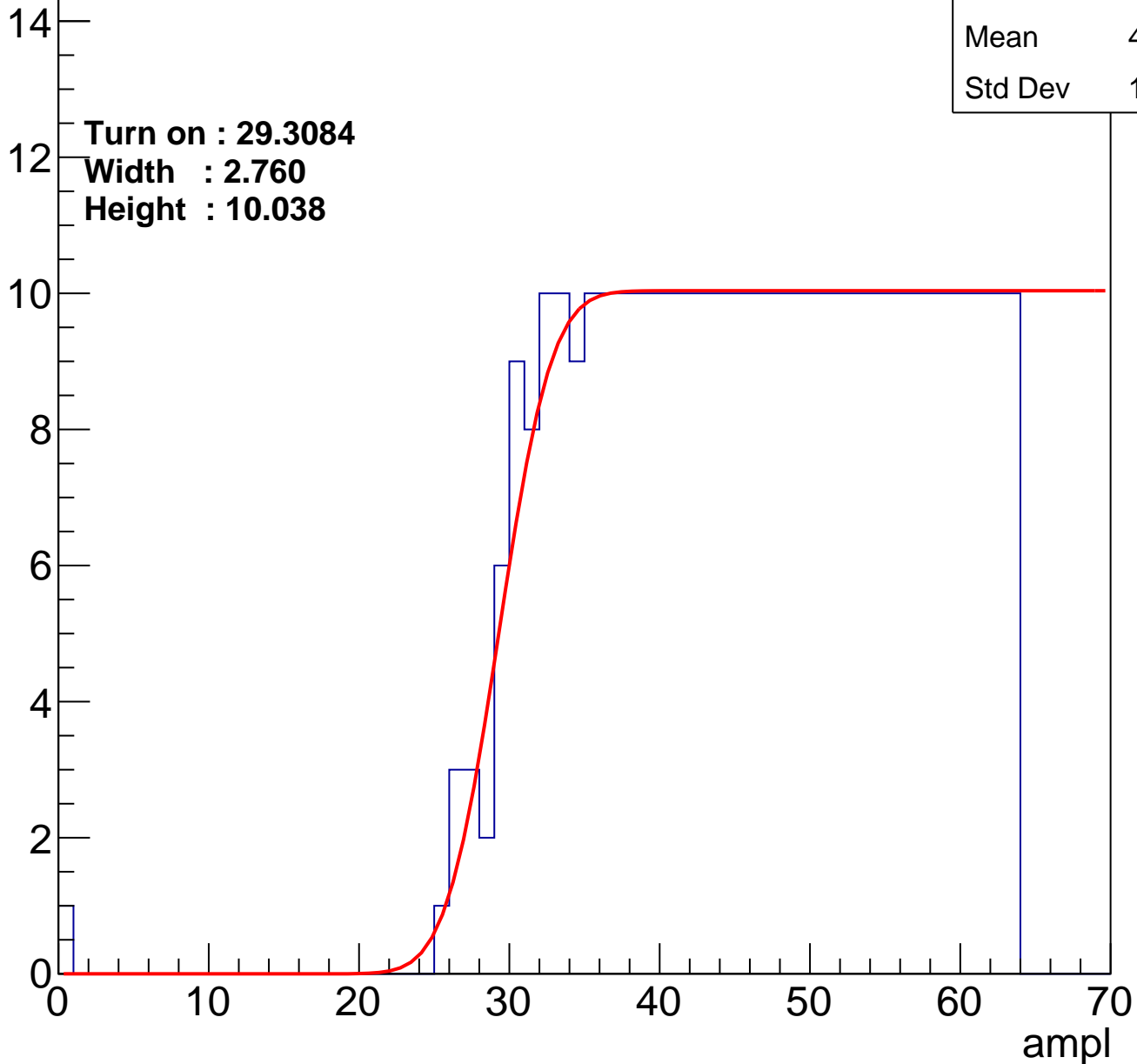
Entries	352
Mean	45.73
Std Dev	10.55

Turn on : 29.3084

Width : 2.760

Height : 10.038

Entry



B0L002S, U10-ch127

calib_packv5_042523_0143.root, FC#8, port C1

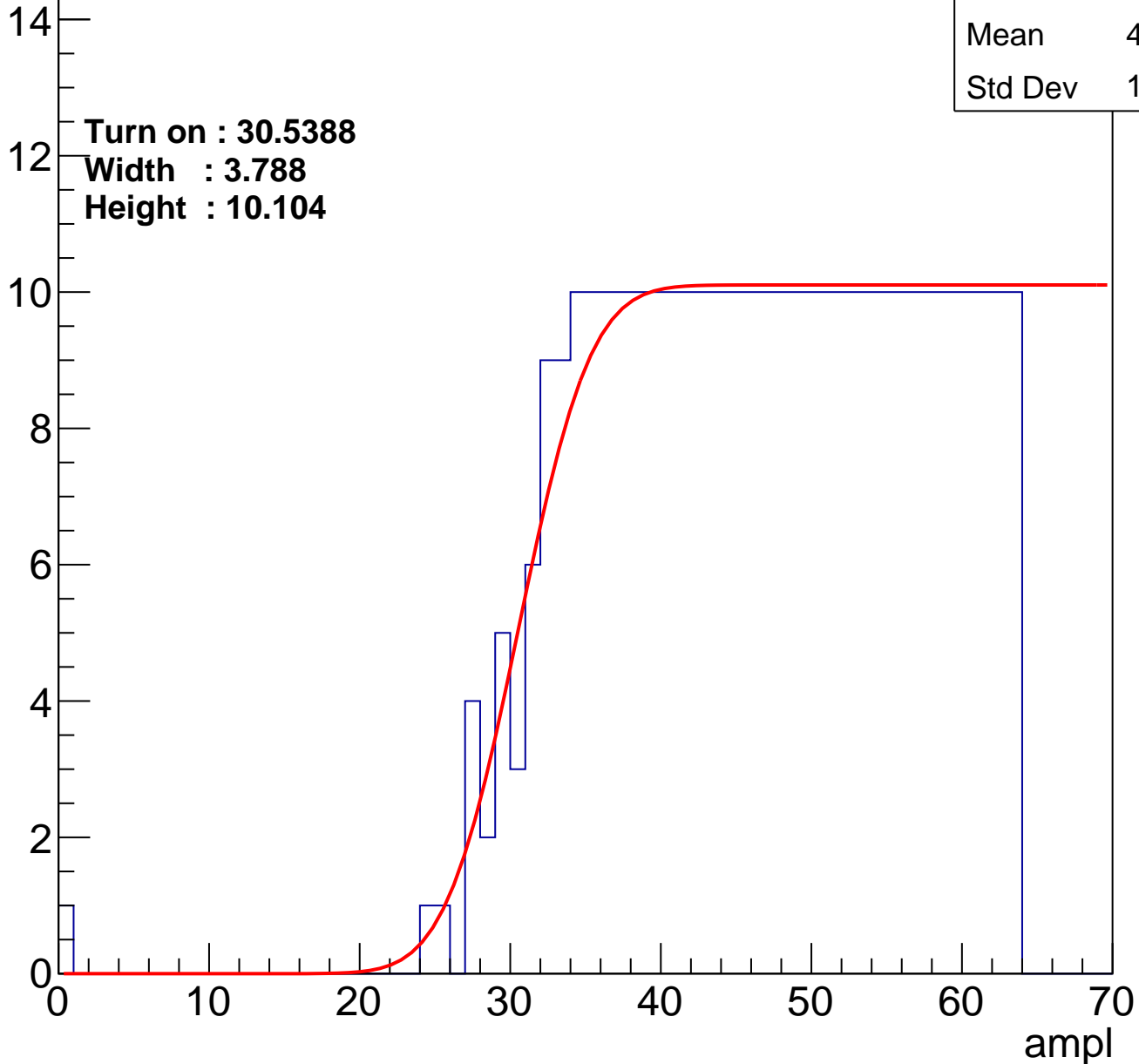
Entries	341
Mean	46.24
Std Dev	10.32

Turn on : 30.5388

Width : 3.788

Height : 10.104

Entry



B0L002S, U10-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.24
Std Dev	10.32

Turn on : 30.5388

Width : 3.788

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl

