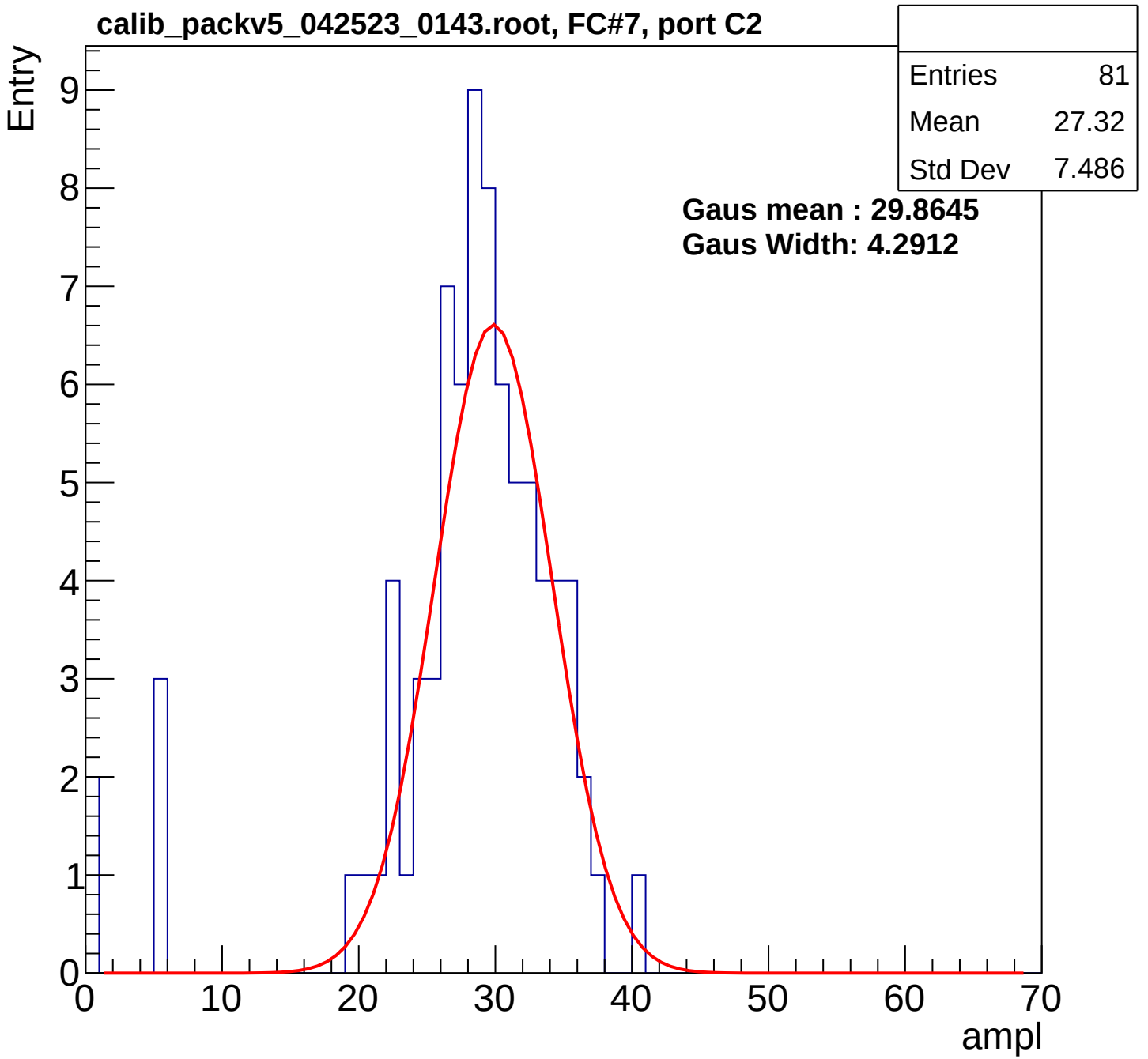
B1L103S, U19-ch0, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 27.32
Std Dev 7.486
Gaus mean : 29.8645
Gaus Width: 4.2912
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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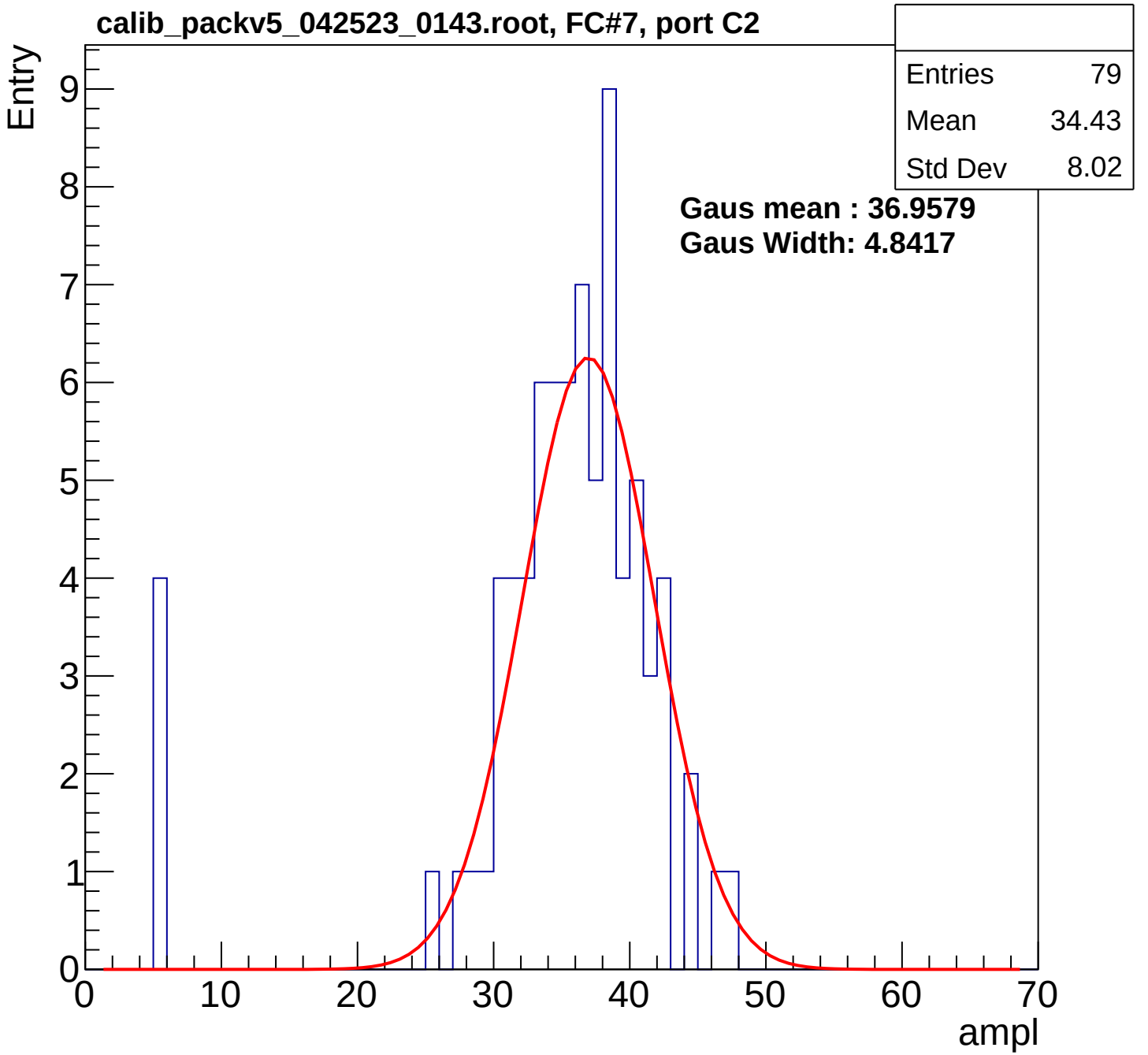
B1L103S, U19-ch0, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 34.43
Std Dev 8.02
Gaus mean : 36.9579
Gaus Width: 4.8417
Entry
ampl
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10
20
30
40
50
60
70
1
2
3
4
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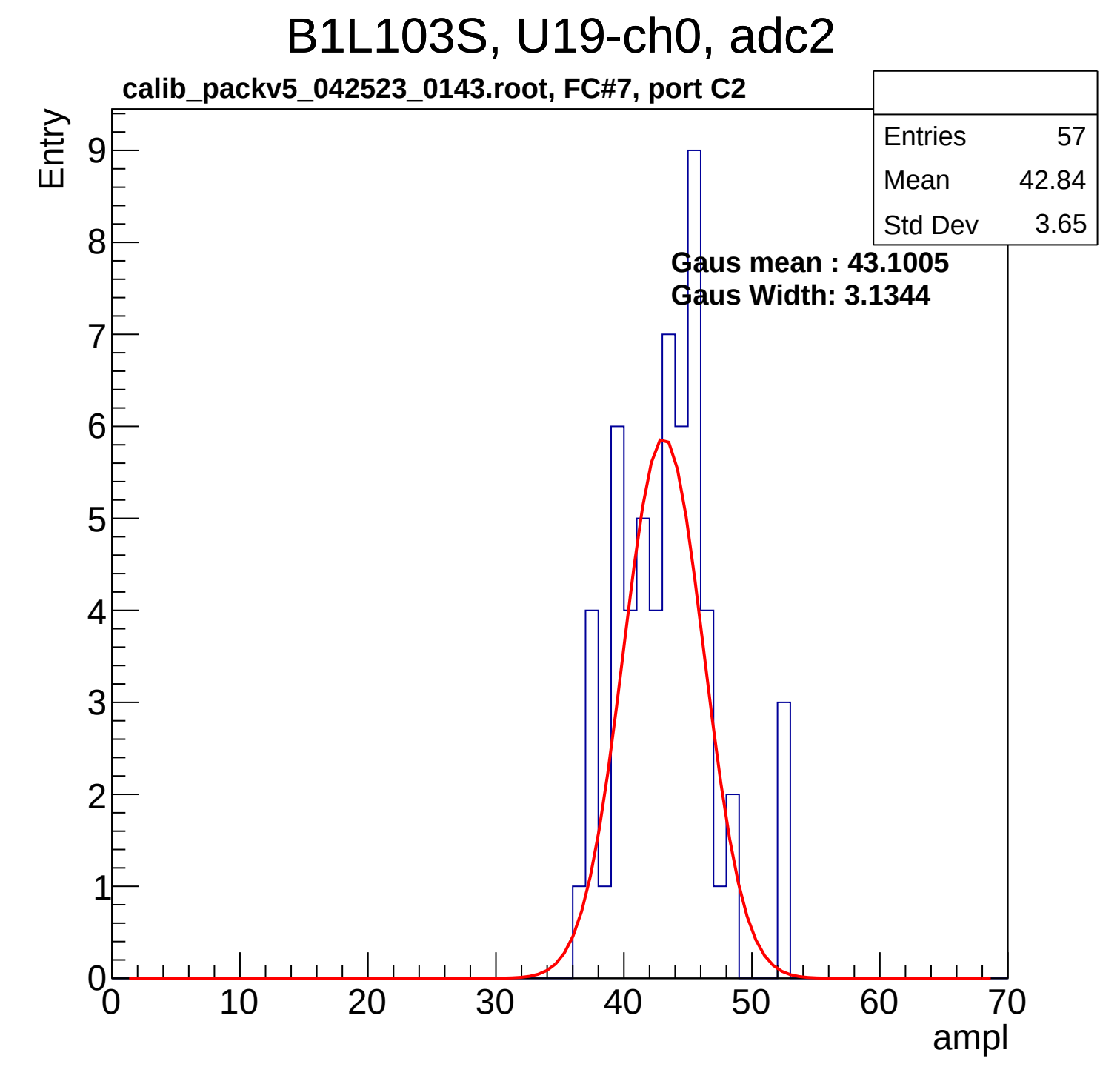
6
7
8
9


B1L103S, U19-ch0, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.84
Std Dev 3.65
Gaus mean : 43.1005
Gaus Width: 3.1344
Entry
ampl
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2
3
4
5
6
7
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9
0
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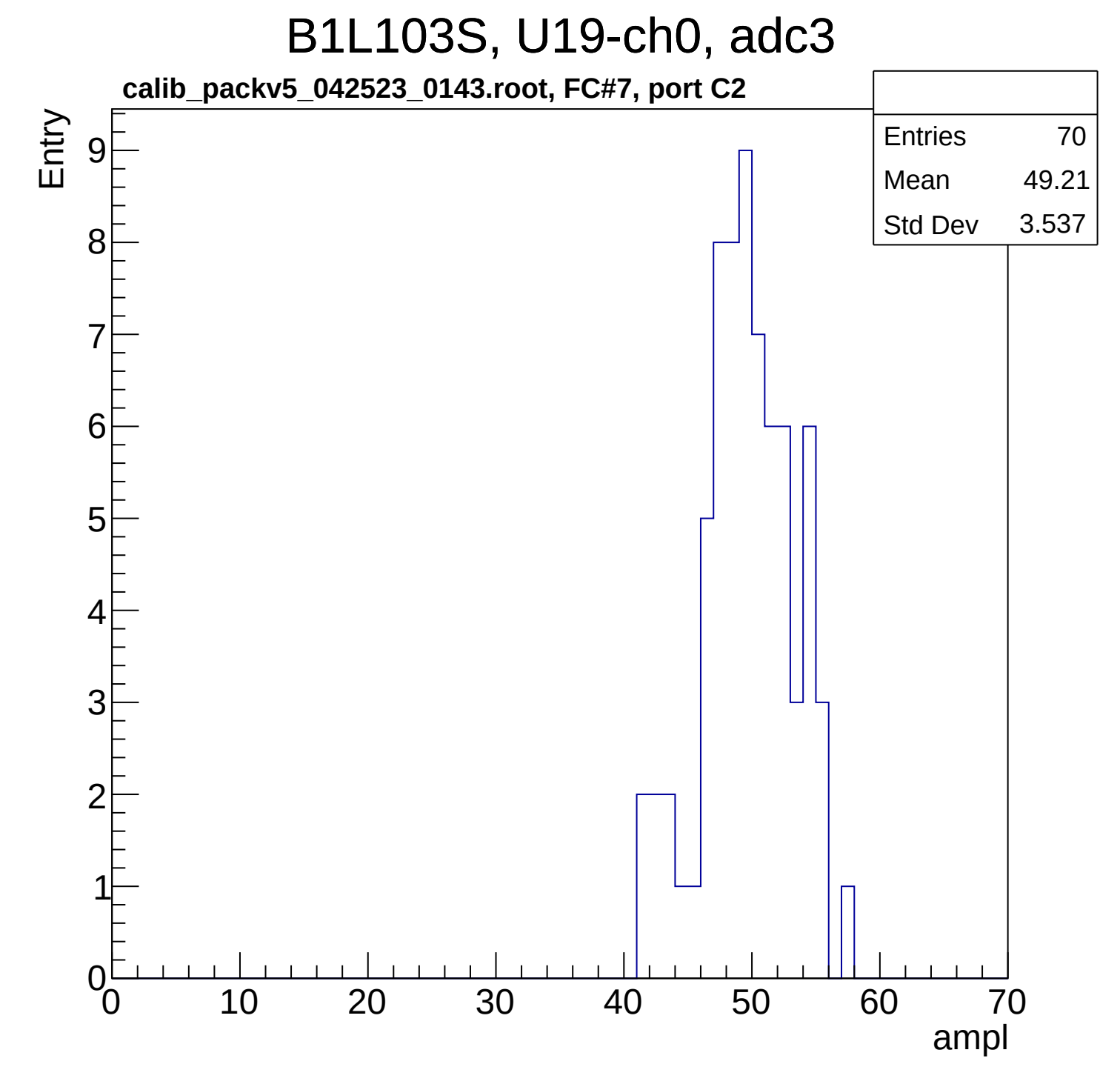
B1L103S, U19-ch0, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.21
Std Dev 3.537
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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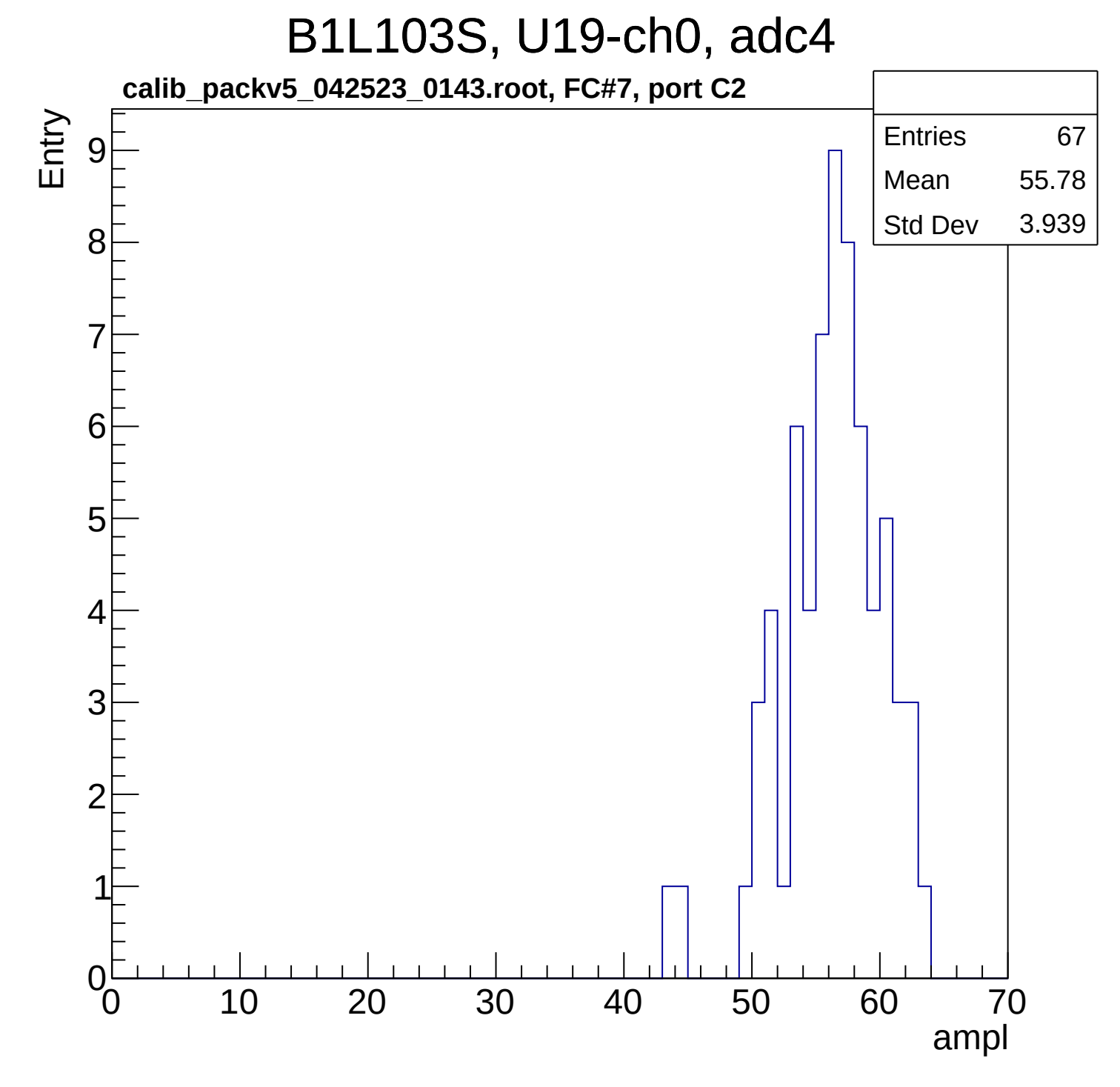
B1L103S, U19-ch0, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 55.78
Std Dev 3.939
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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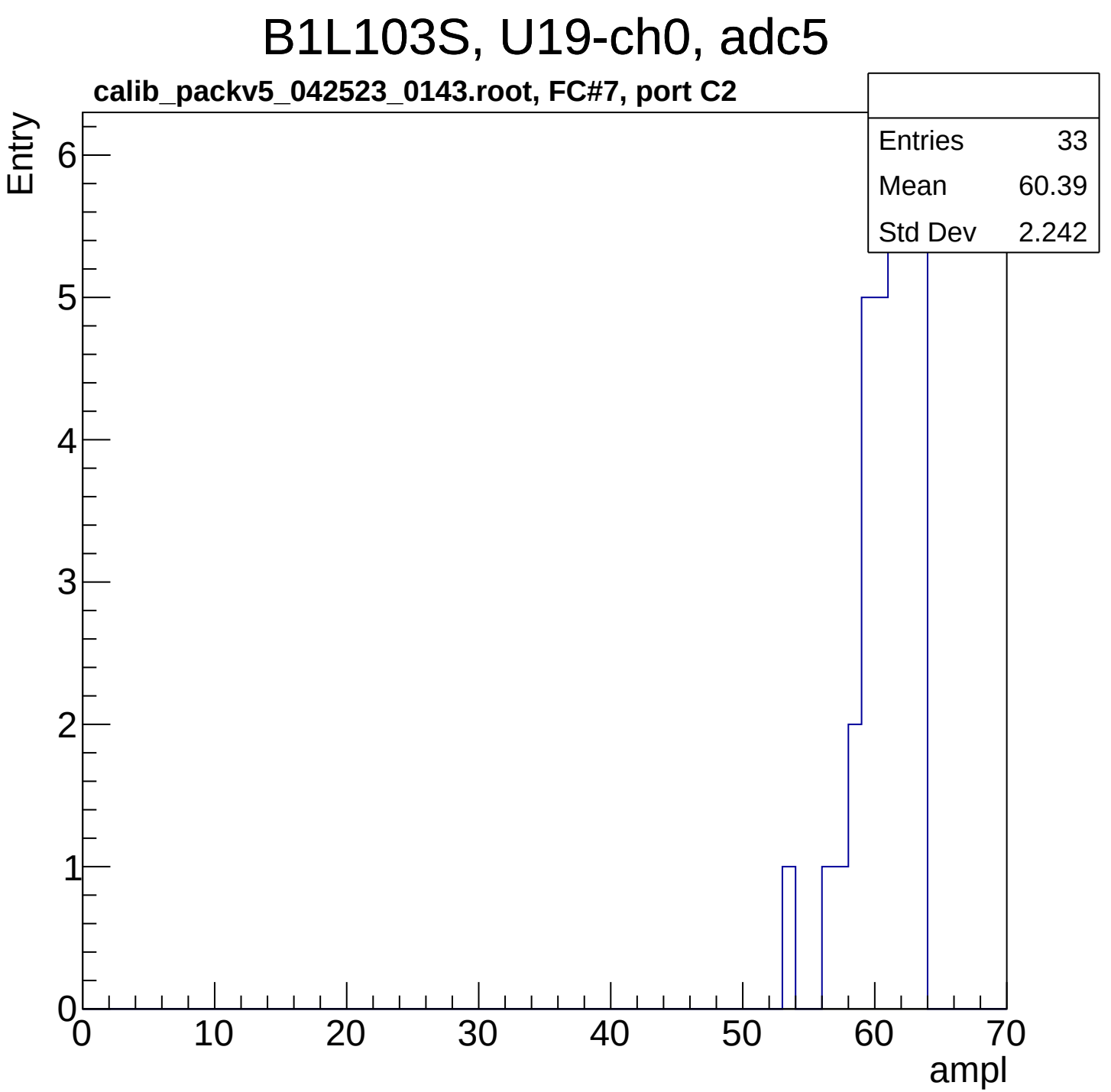
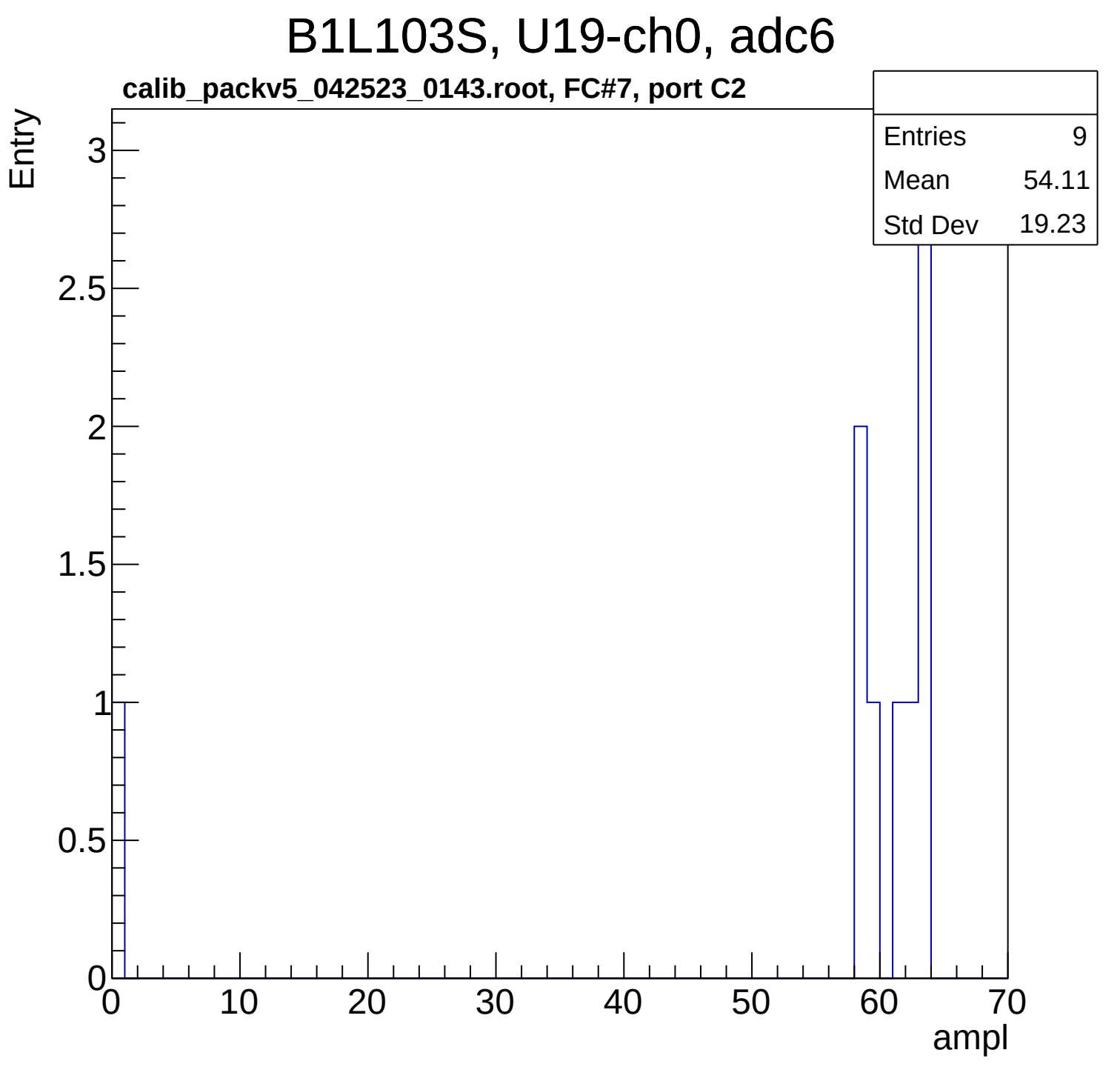

B1L103S, U19-ch0, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 60.39
Std Dev 2.242
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch0, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 54.11
Std Dev 19.23
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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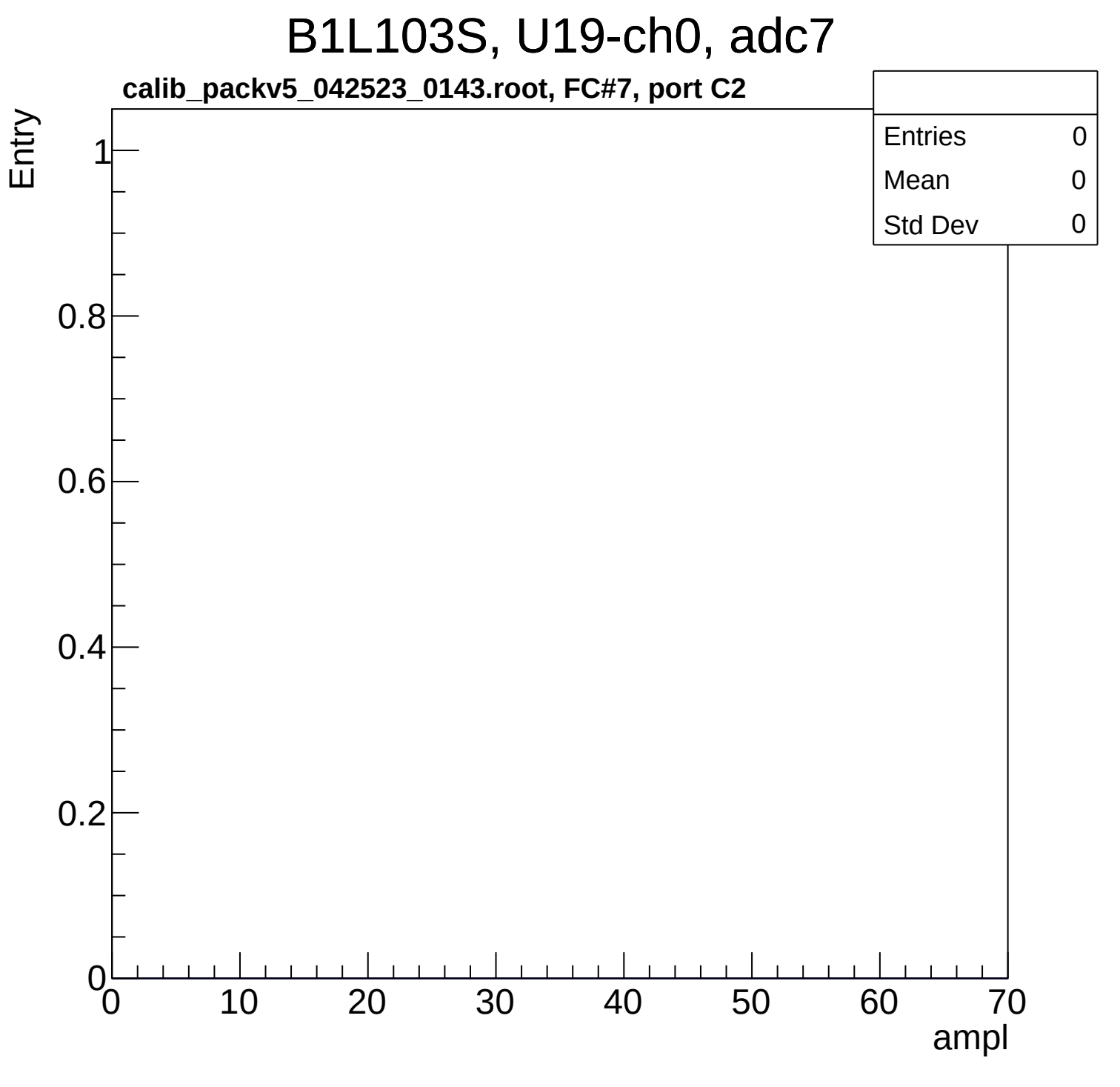
B1L103S, U19-ch0, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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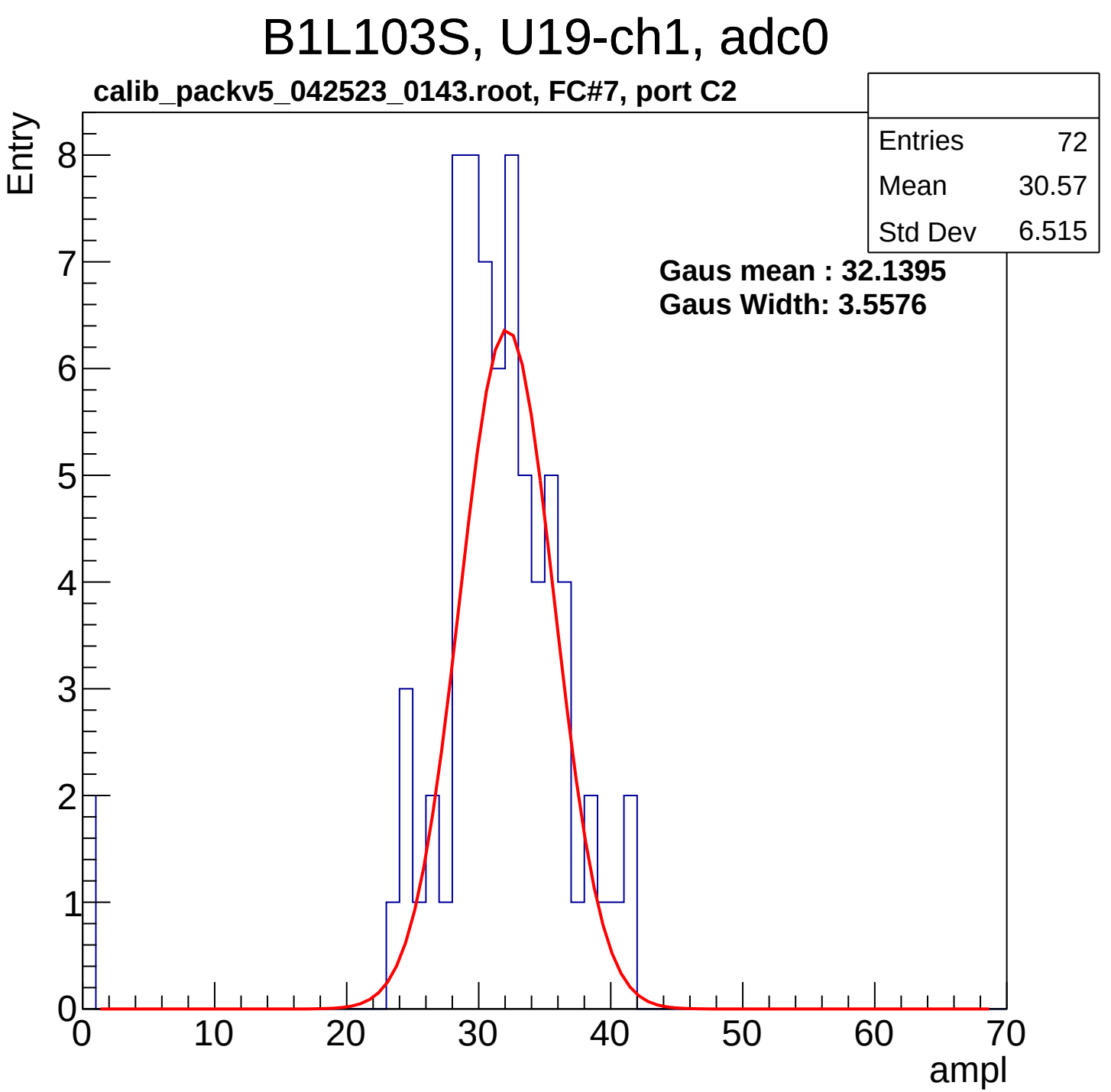
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch1, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 30.57
Std Dev 6.515
Gaus mean : 32.1395
Gaus Width: 3.5576
Entry
ampl
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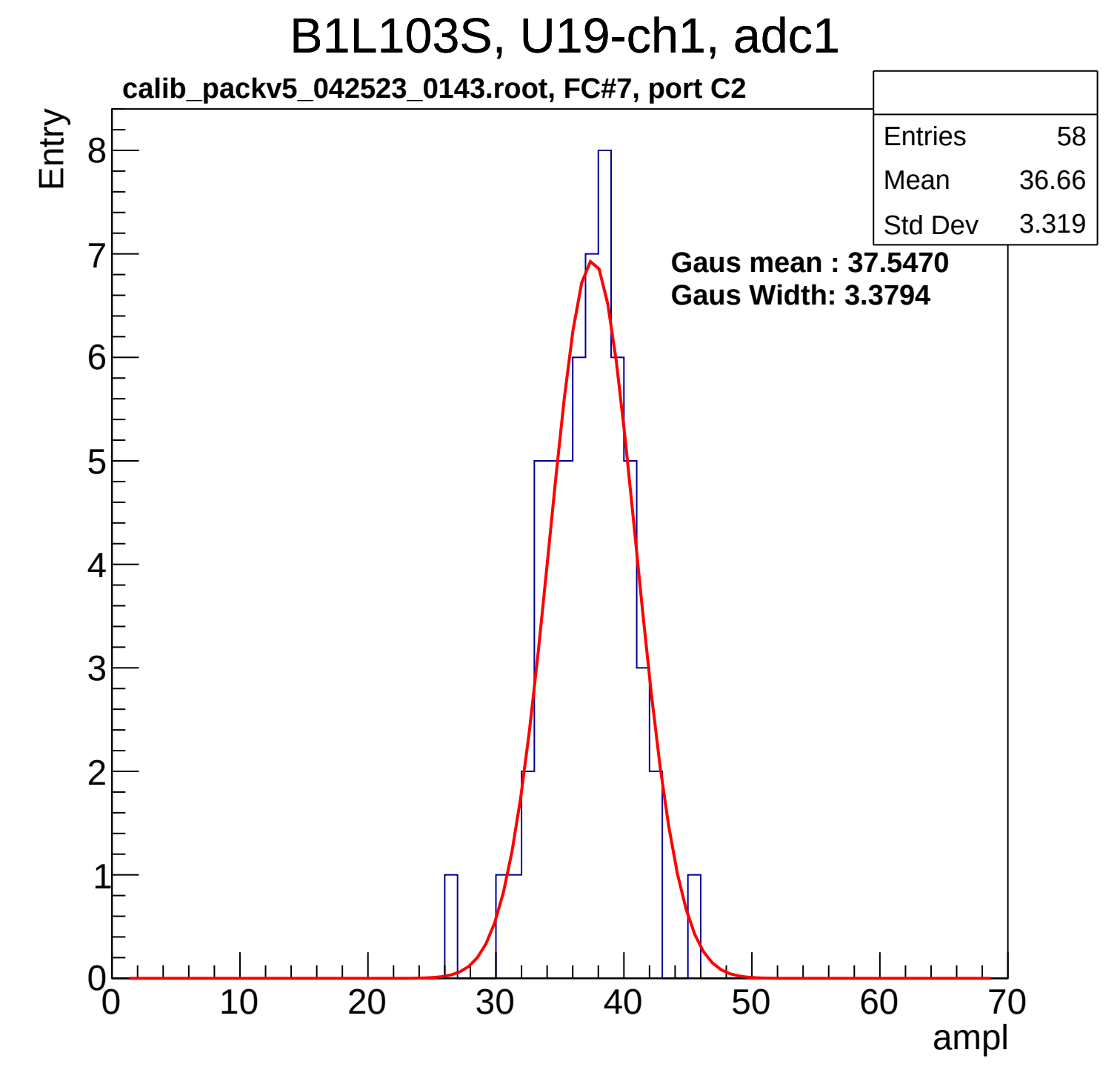
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch1, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 36.66
Std Dev 3.319
Gaus mean : 37.5470
Gaus Width: 3.3794
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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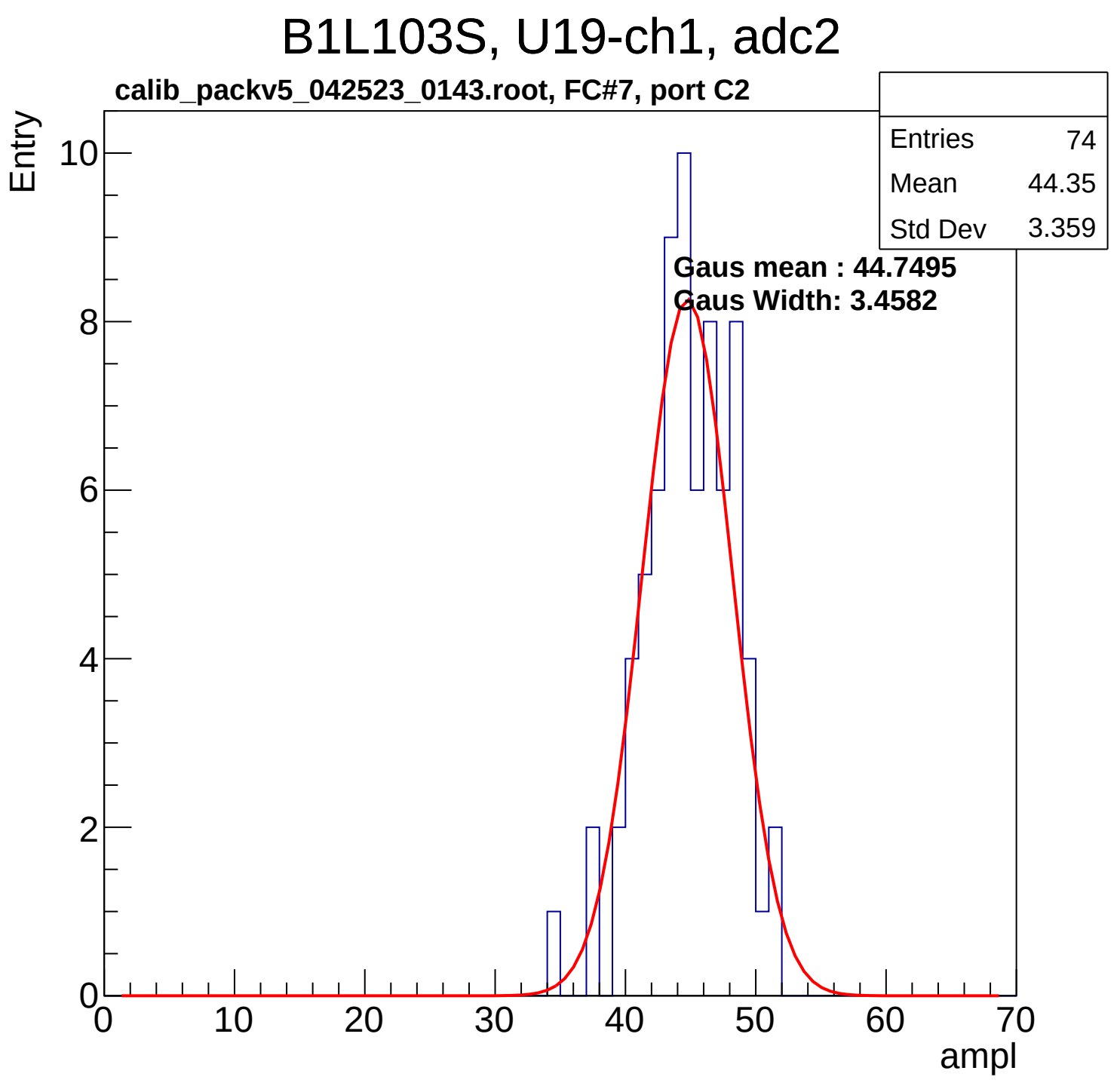
B1L103S, U19-ch1, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 44.35
Std Dev 3.359
Gaus mean : 44.7495
Gaus Width: 3.4582
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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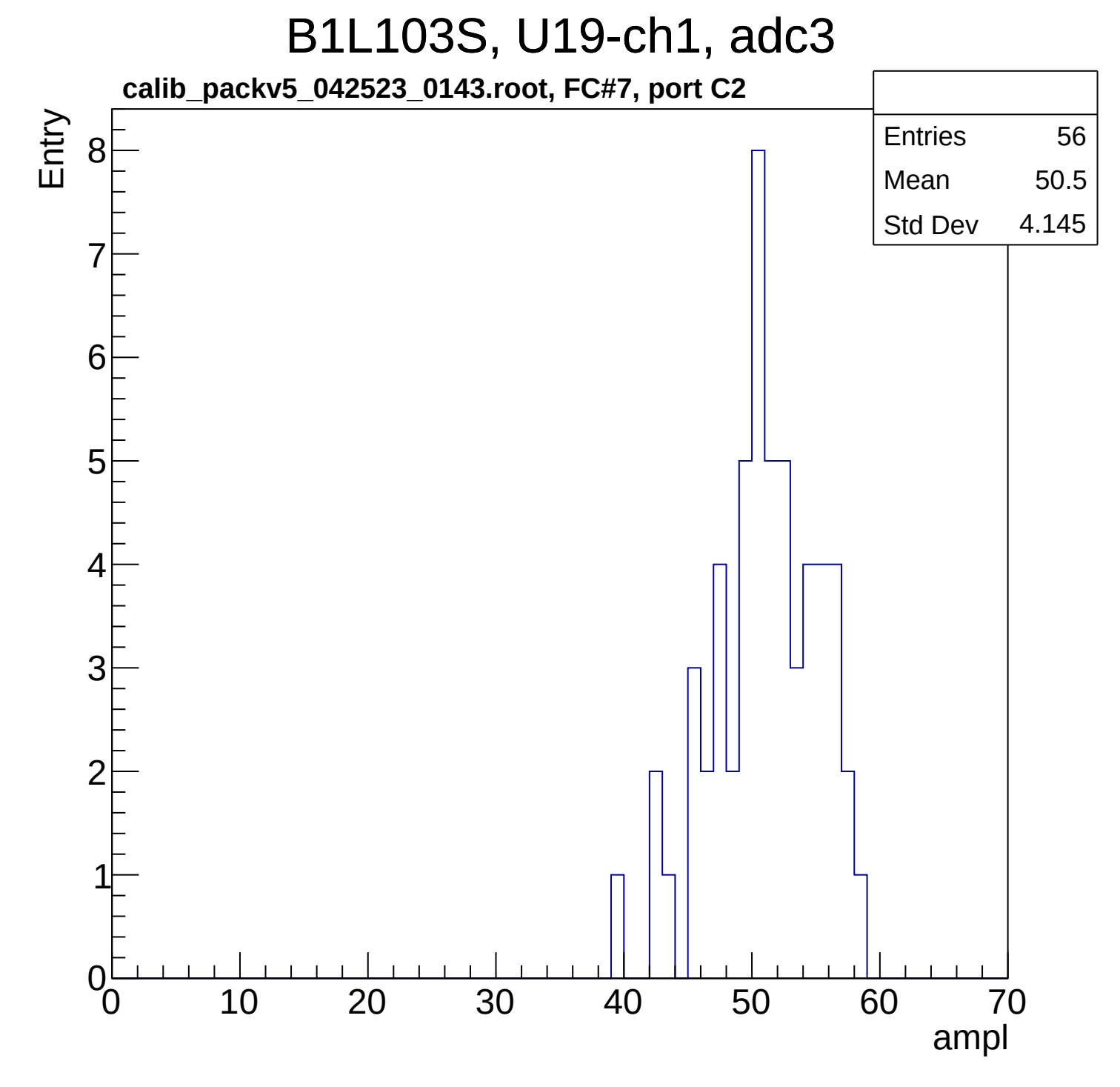
B1L103S, U19-ch1, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 50.5
Std Dev 4.145
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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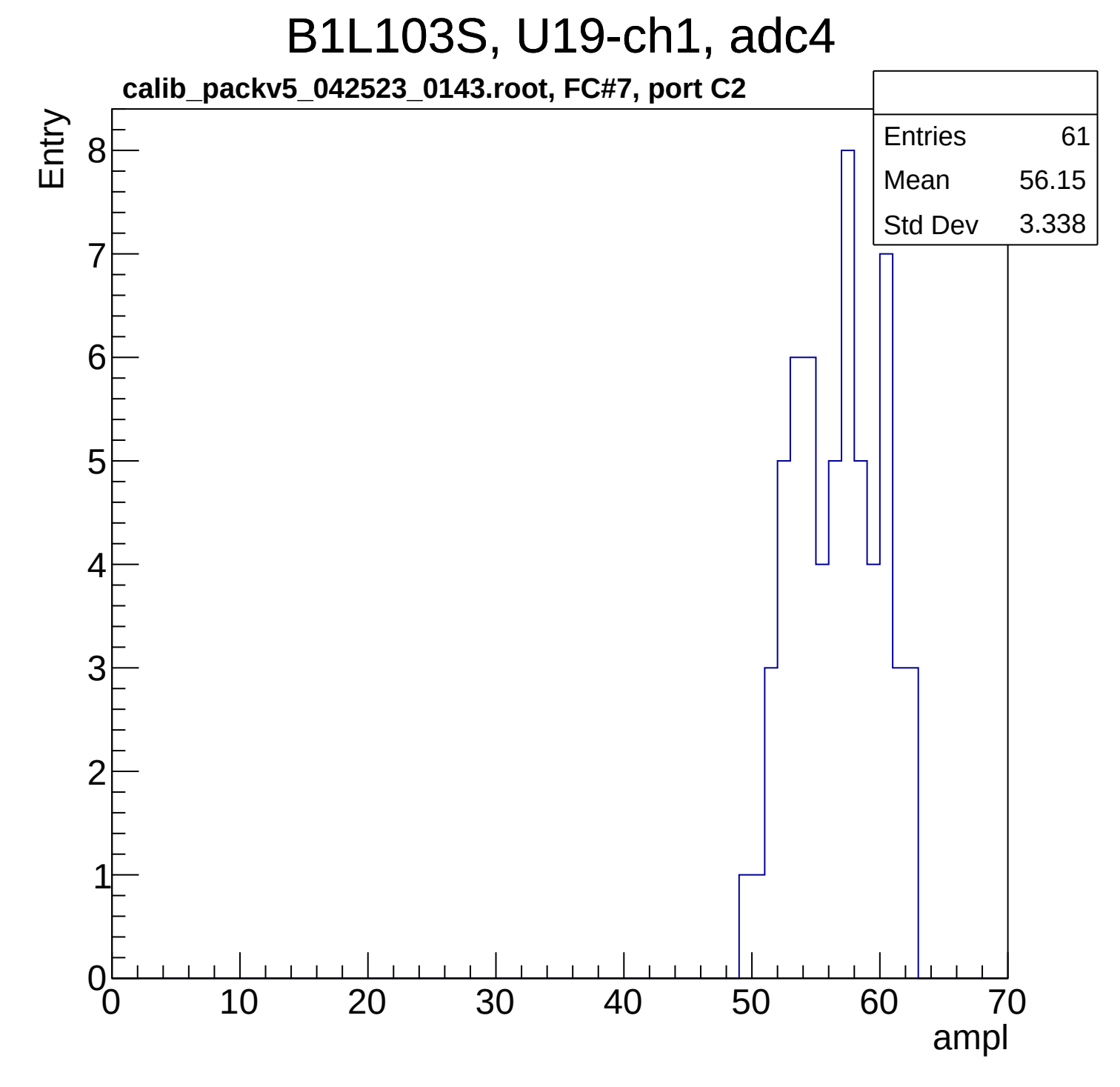
B1L103S, U19-ch1, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.15
Std Dev 3.338
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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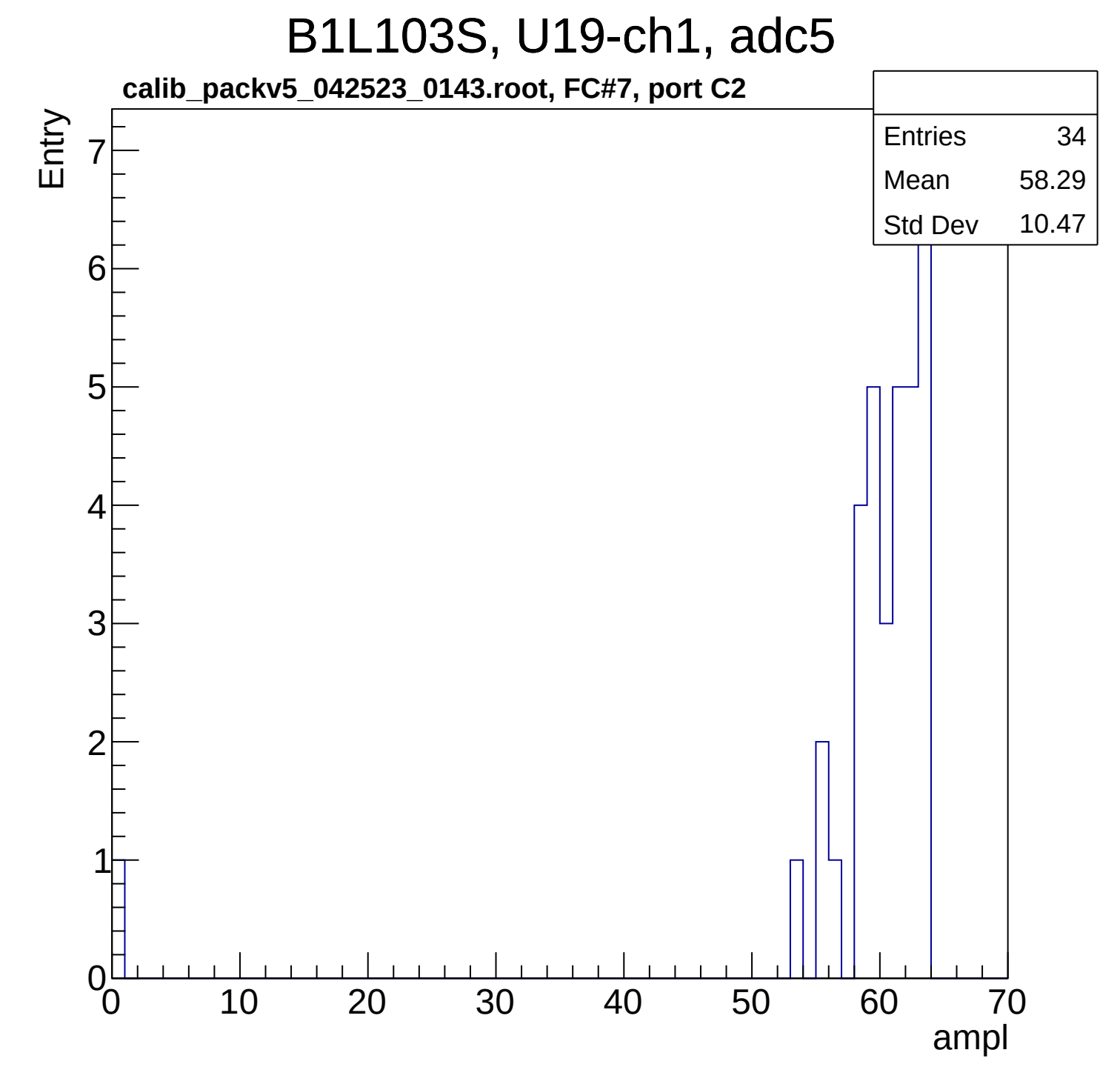
B1L103S, U19-ch1, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.29
Std Dev 10.47
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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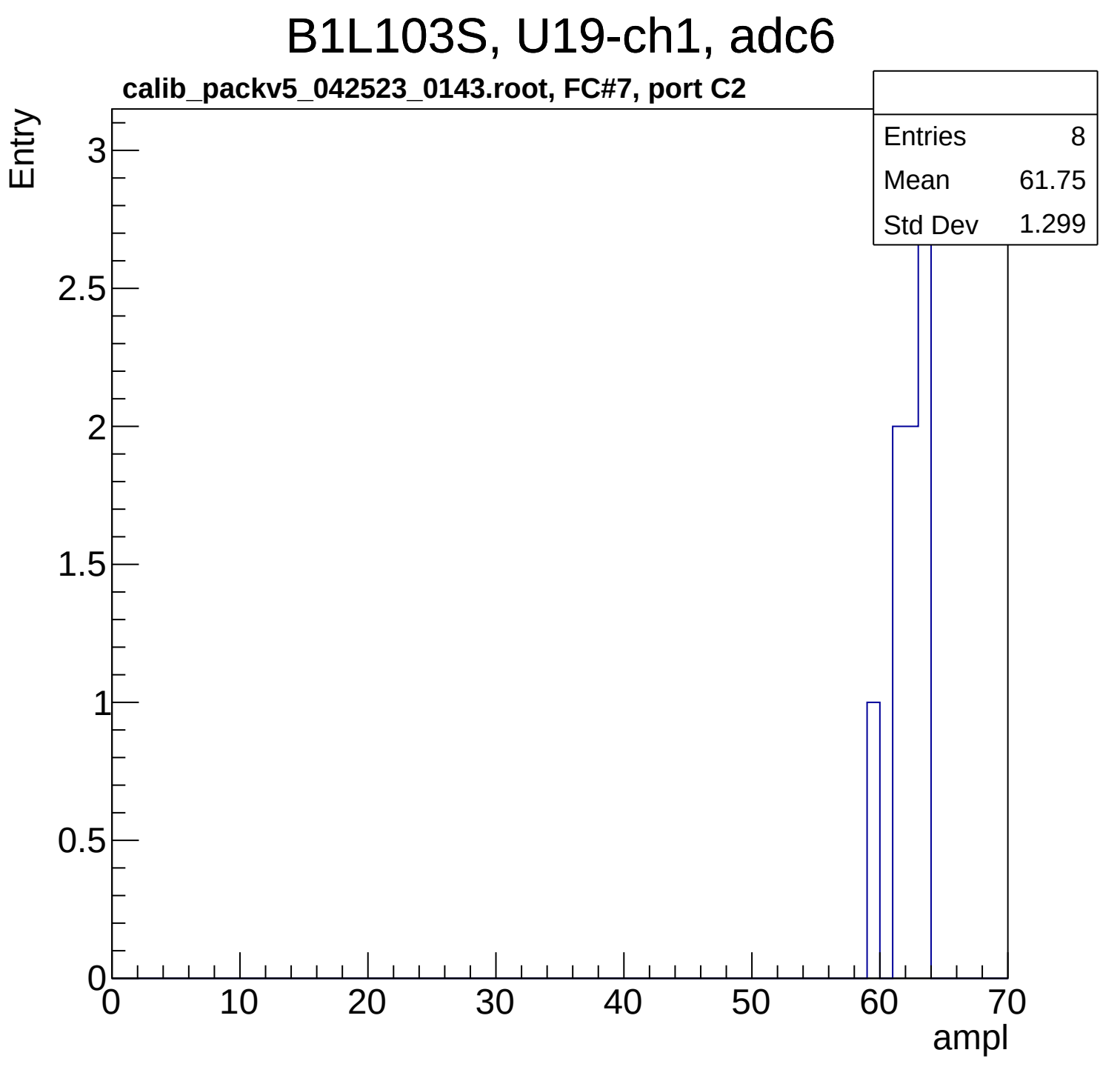
B1L103S, U19-ch1, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.75
Std Dev 1.299
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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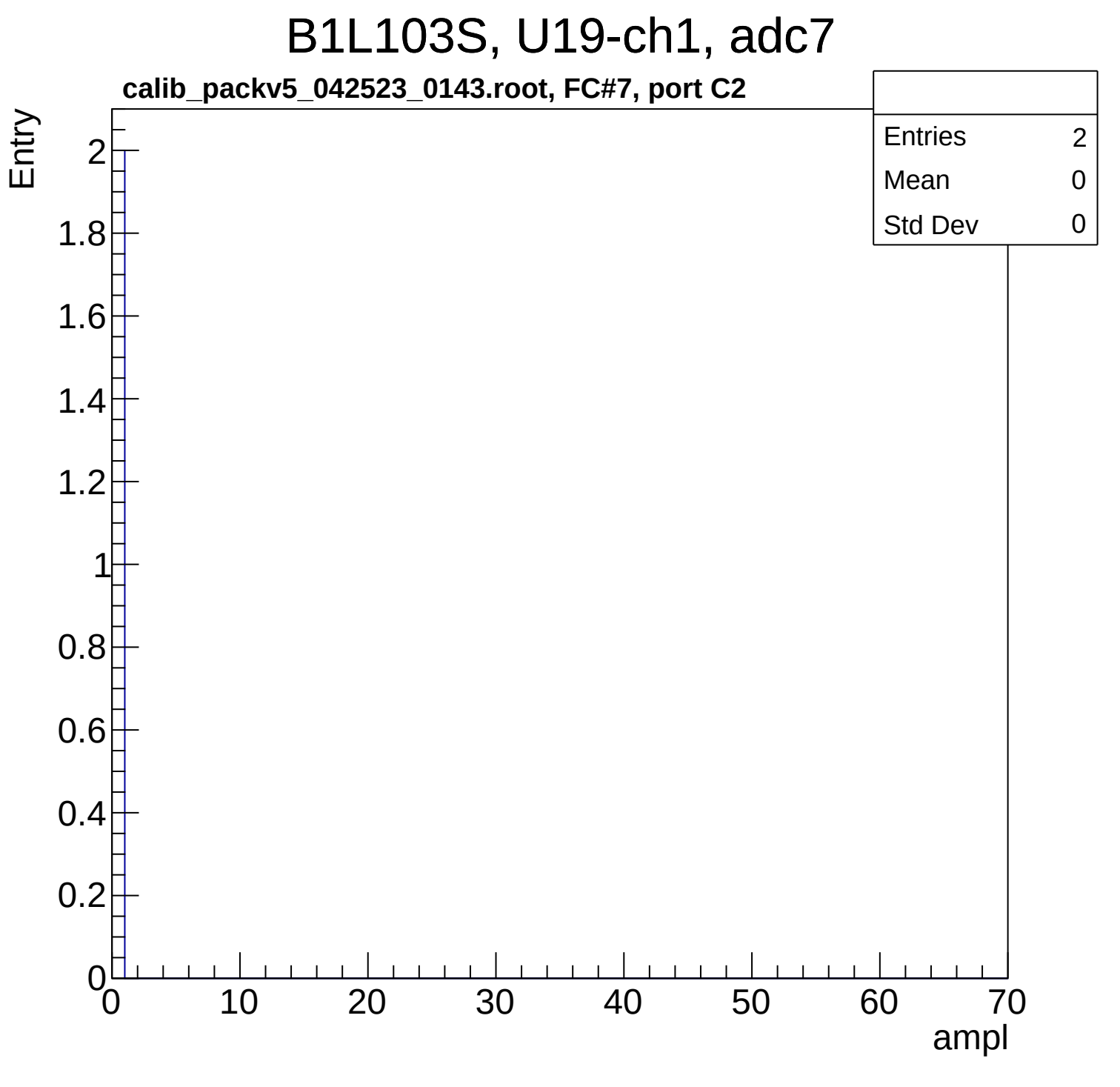
B1L103S, U19-ch1, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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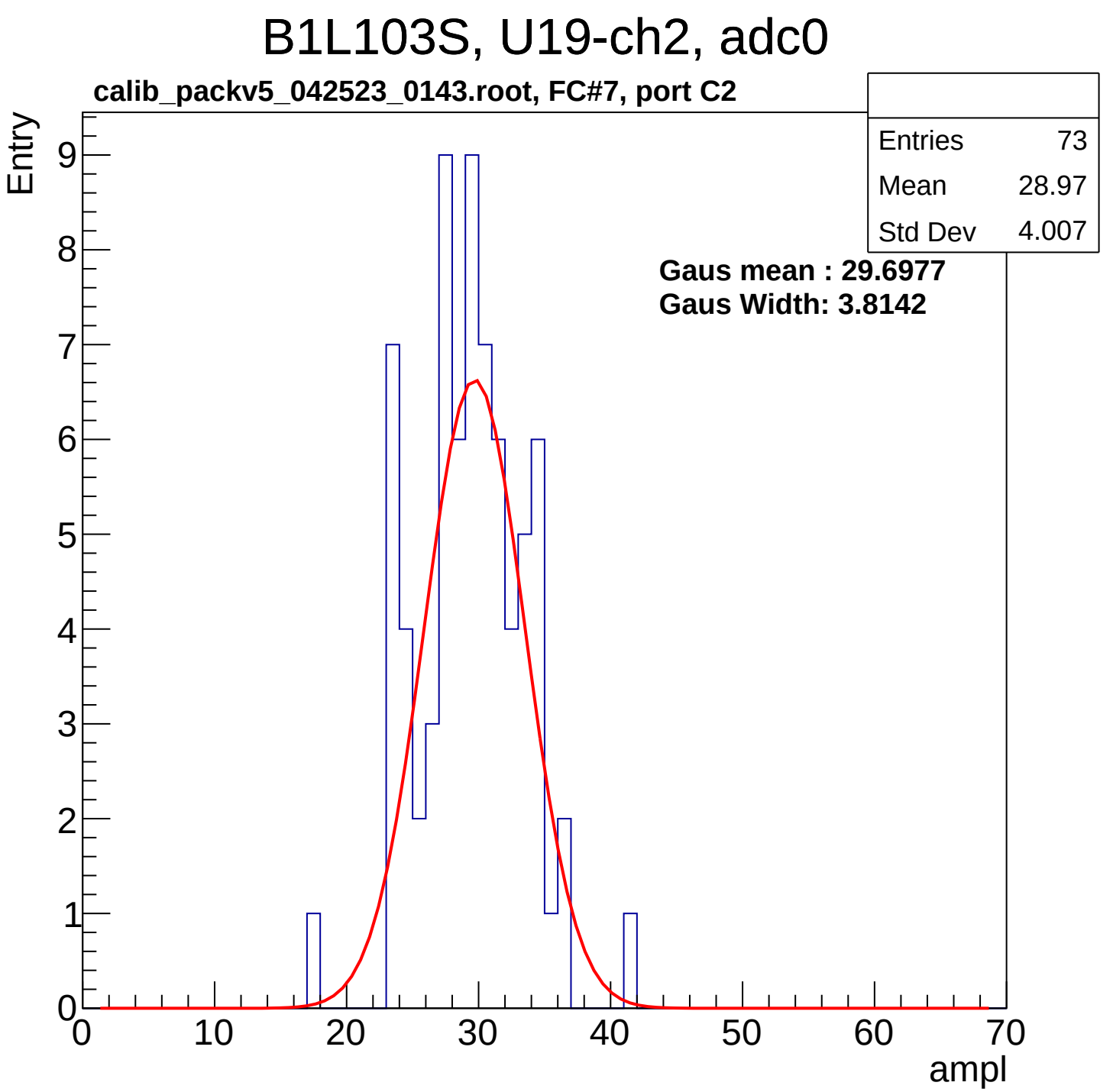
B1L103S, U19-ch2, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 28.97
Std Dev 4.007
Gaus mean : 29.6977
Gaus Width: 3.8142
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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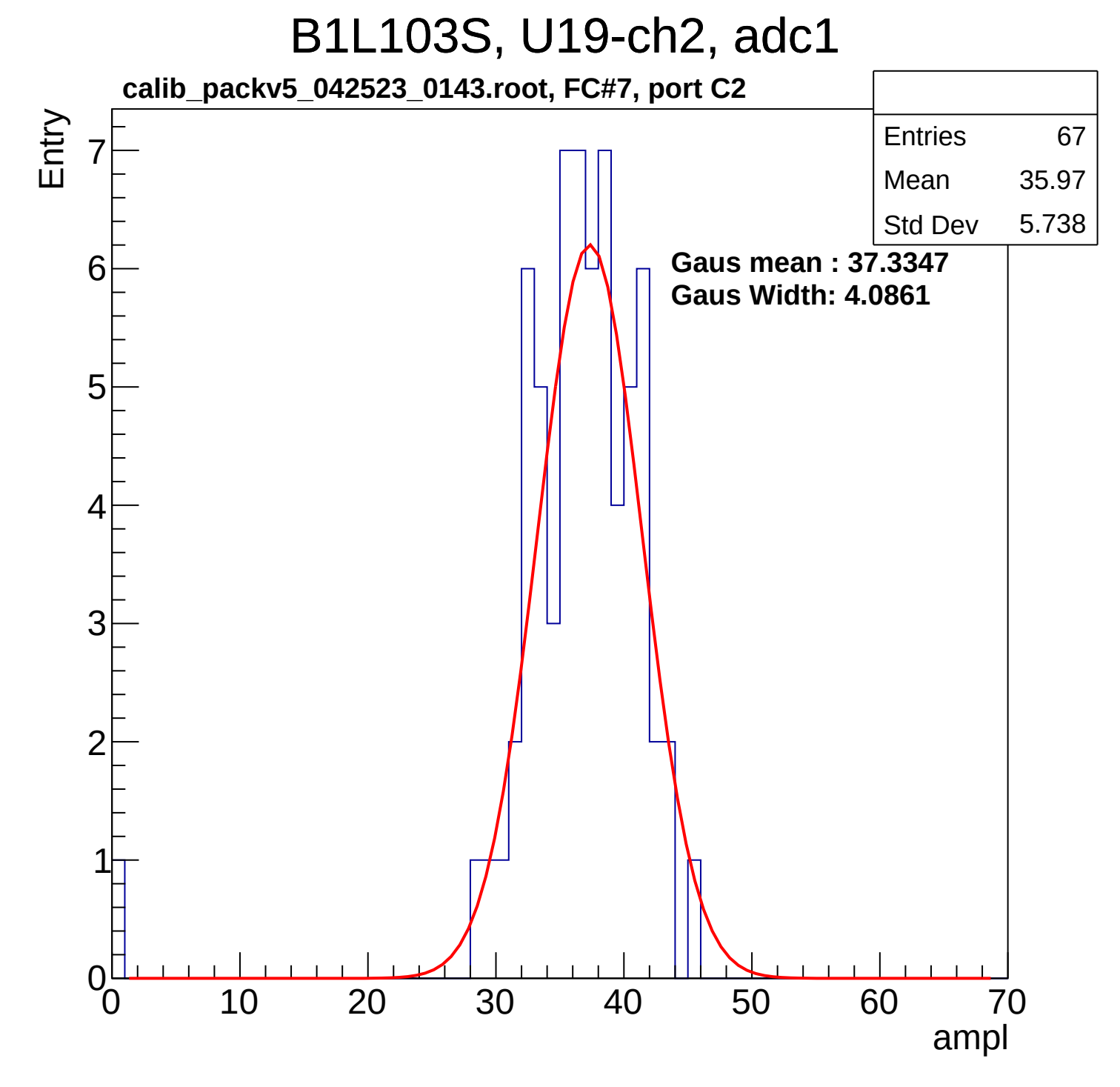
B1L103S, U19-ch2, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.97
Std Dev 5.738
Gaus mean : 37.3347
Gaus Width: 4.0861
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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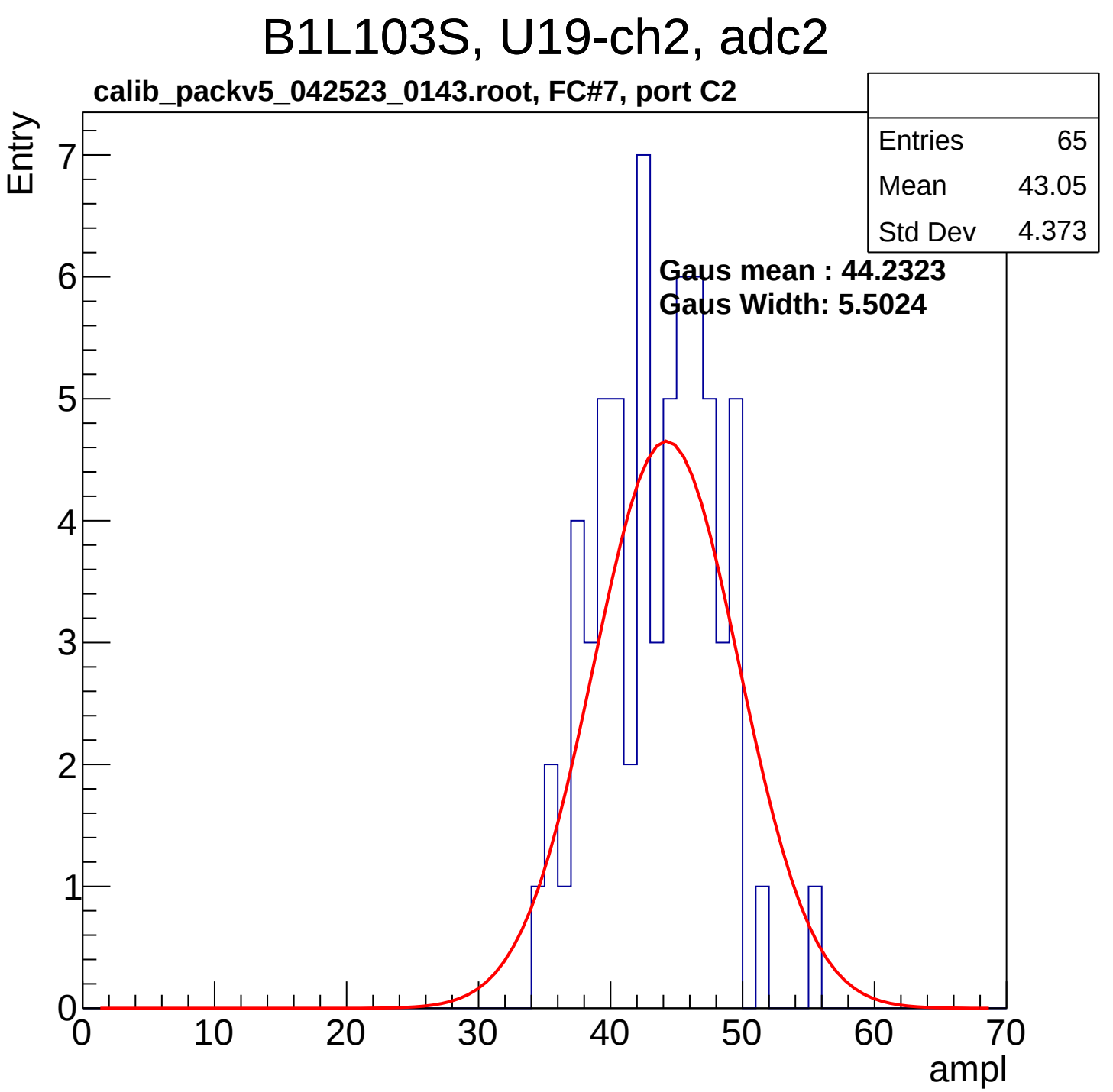
B1L103S, U19-ch2, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.05
Std Dev 4.373
Gaus mean : 44.2323
Gaus Width: 5.5024
Entry
ampl
0
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2
3
4
5
6
7
0
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20
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40
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60
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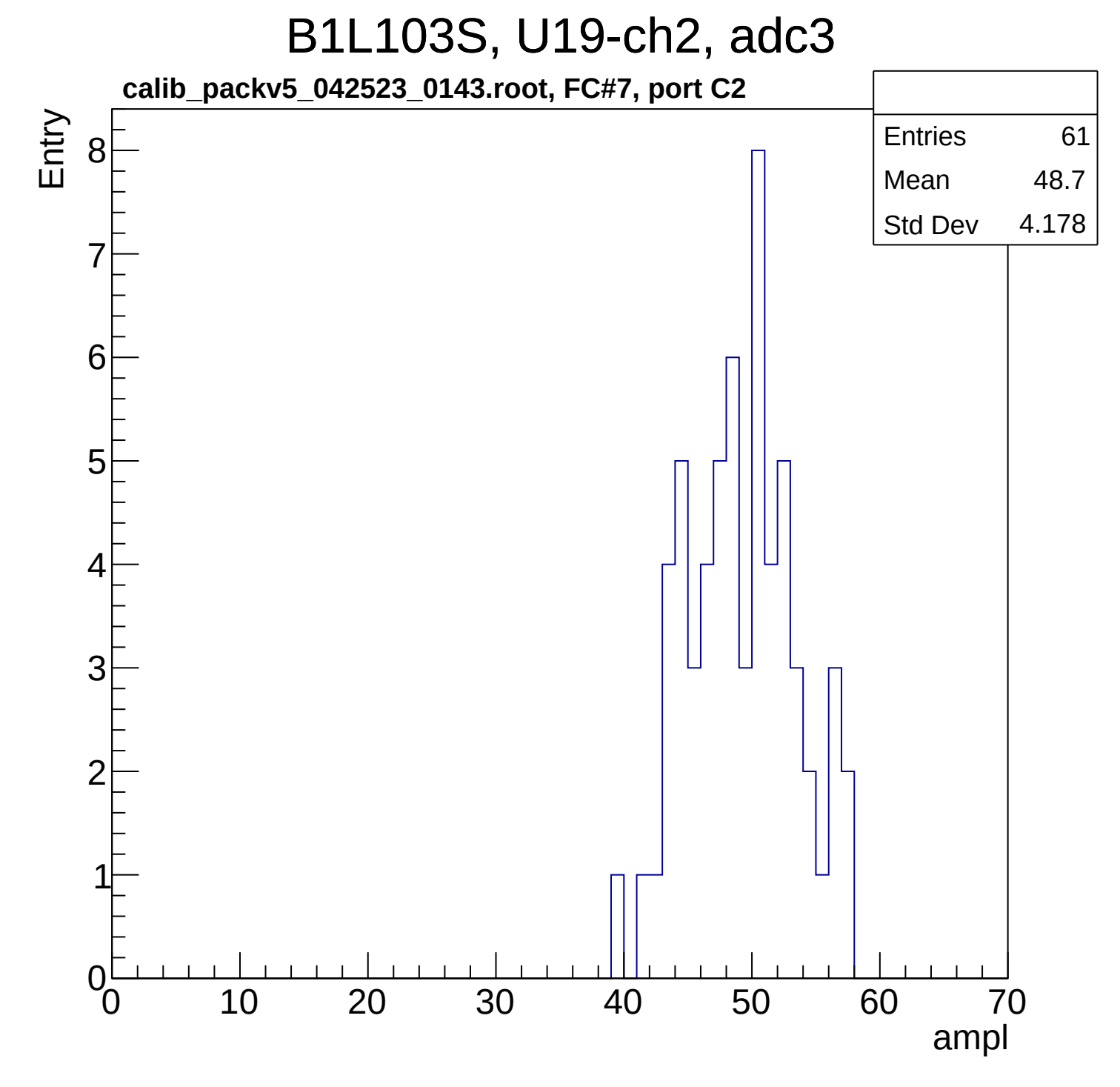
B1L103S, U19-ch2, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 48.7
Std Dev 4.178
Entry
ampl
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2
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4
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0
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20
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60
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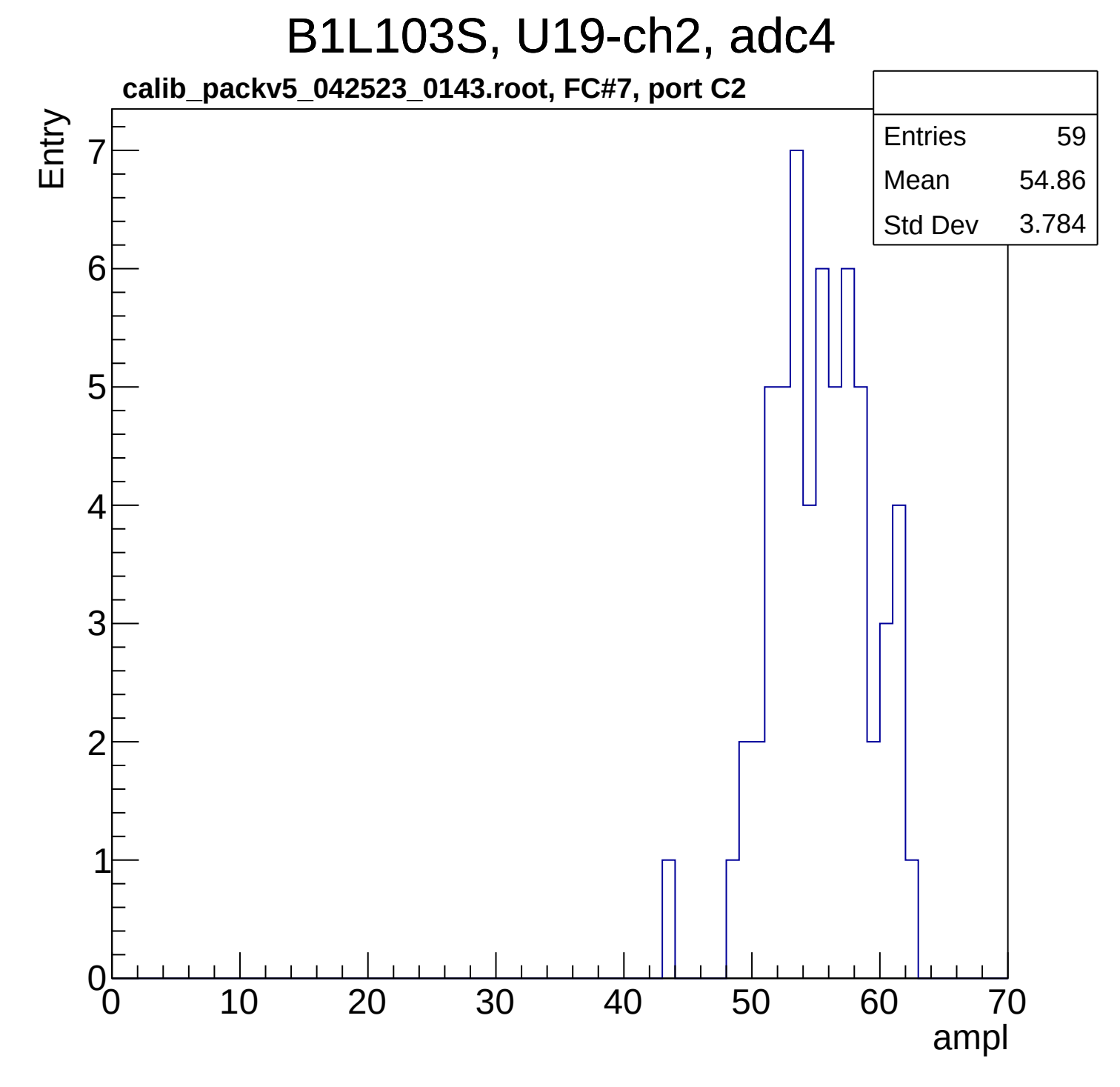
B1L103S, U19-ch2, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.86
Std Dev 3.784
Entry
ampl
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1
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4
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6
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0
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20
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60
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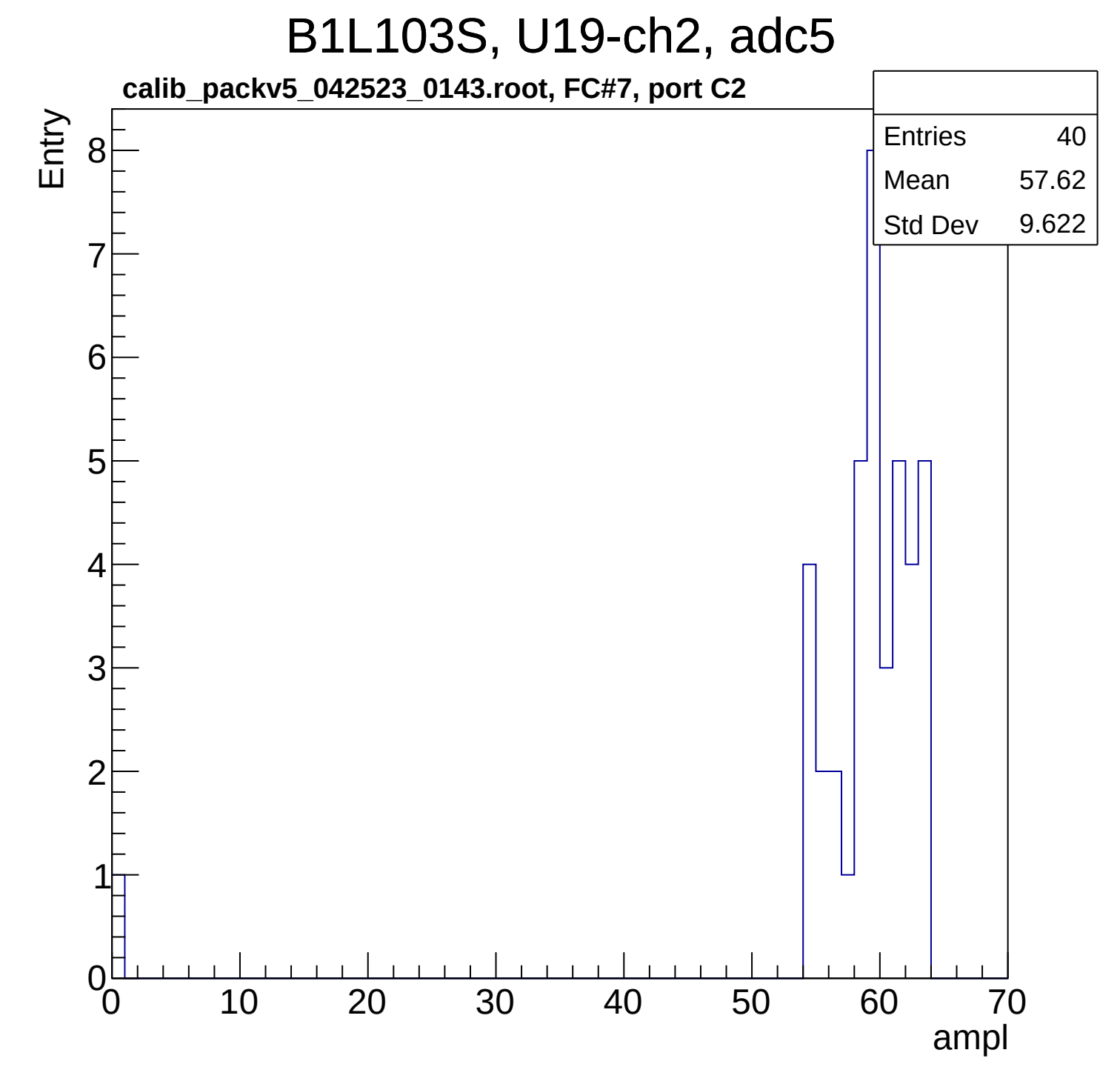
B1L103S, U19-ch2, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 57.62
Std Dev 9.622
Entry
ampl
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2
3
4
5
6
7
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0
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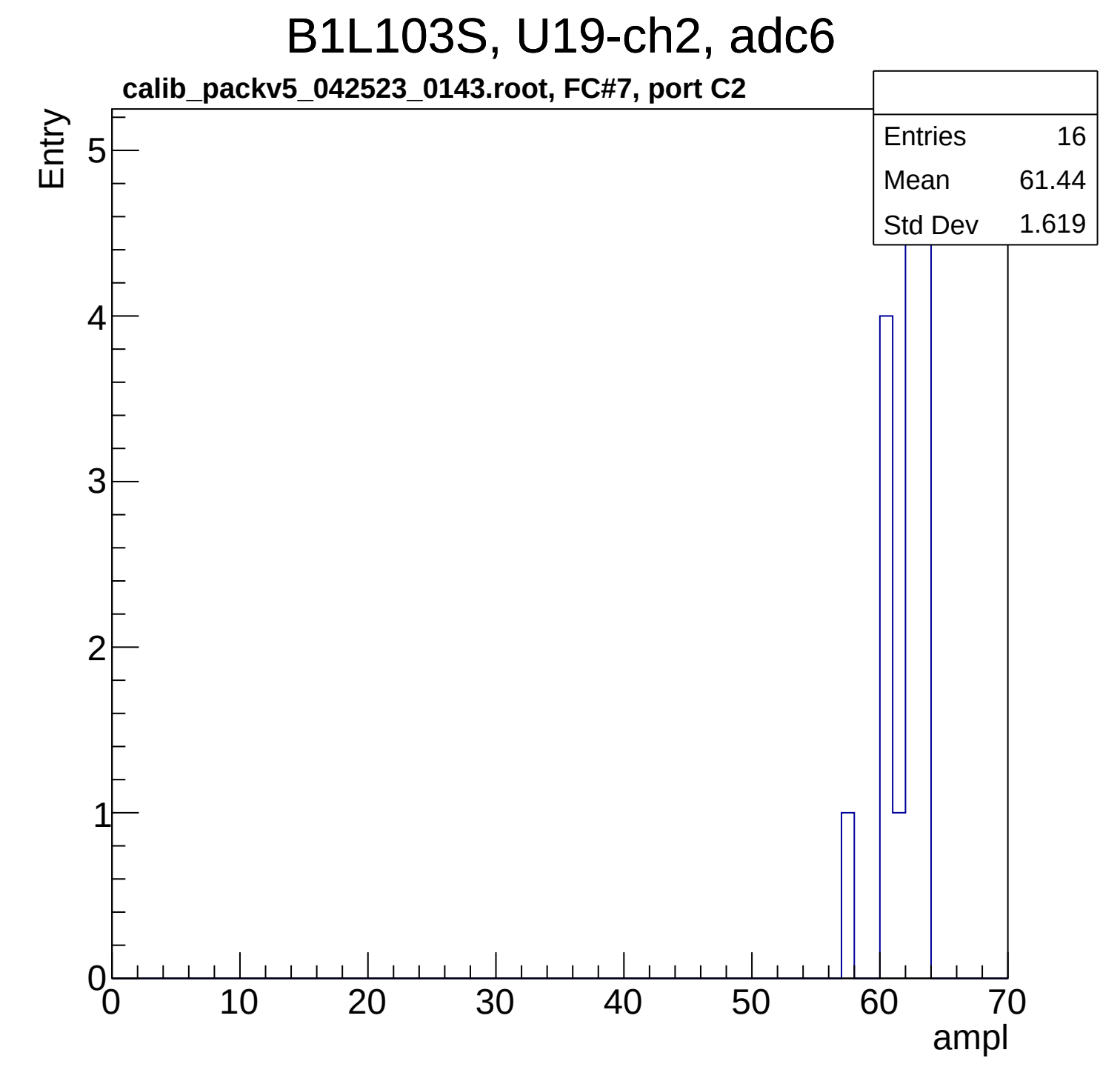
B1L103S, U19-ch2, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.44
Std Dev 1.619
Entry
ampl
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2
3
4
5
0
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20
30
40
50
60
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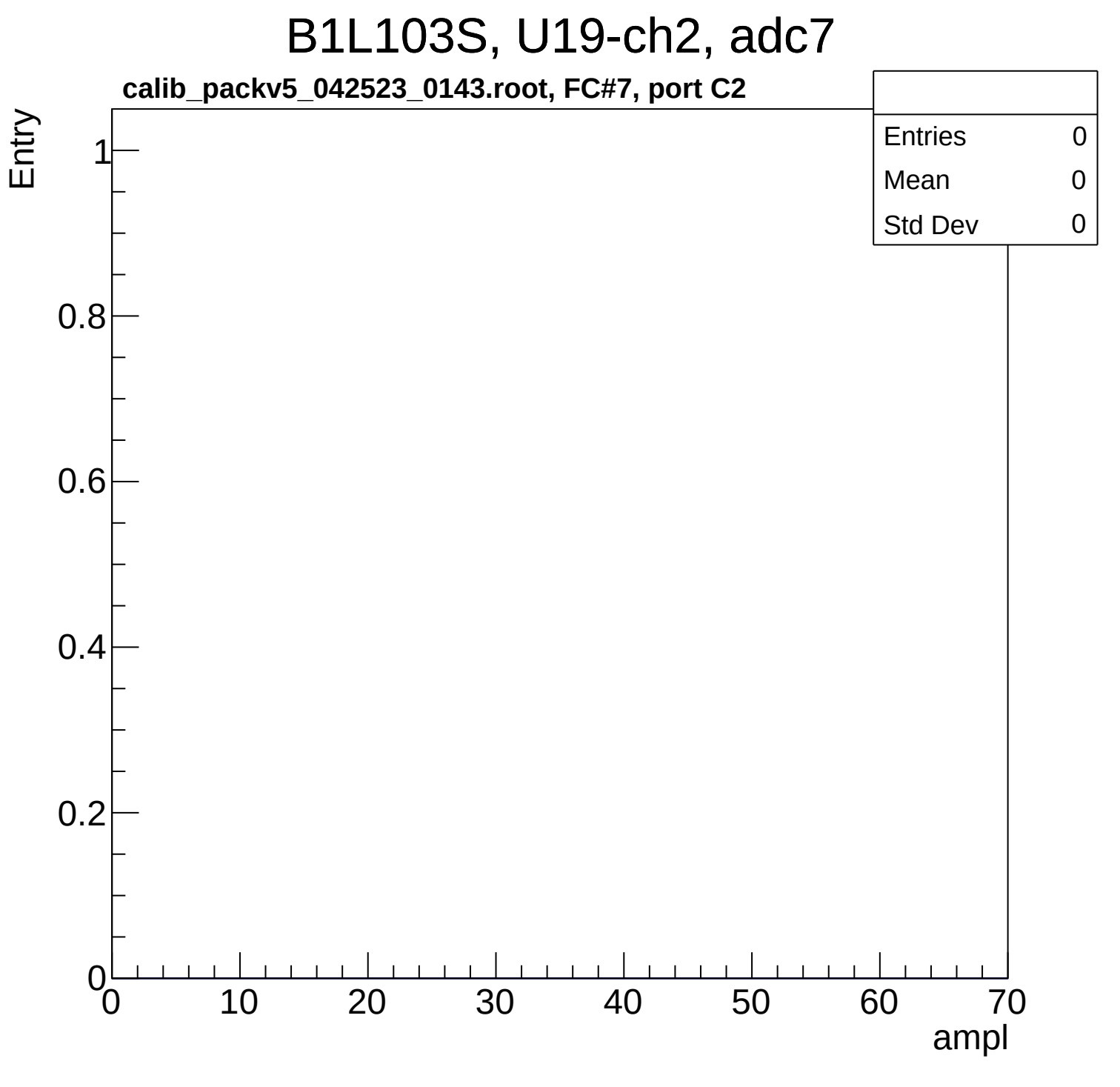
B1L103S, U19-ch2, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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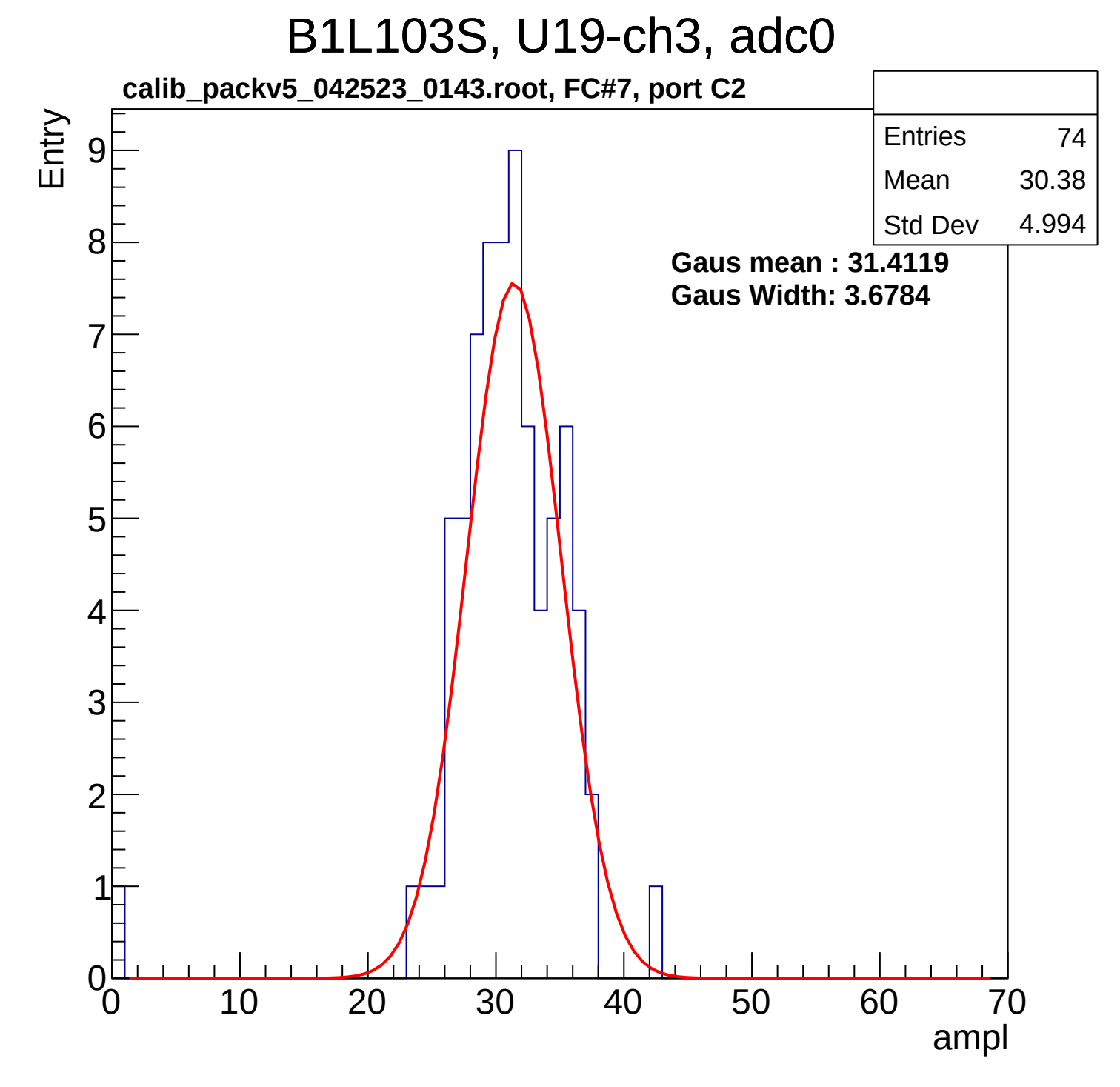
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch3, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.38
Std Dev 4.994
Gaus mean : 31.4119
Gaus Width: 3.6784
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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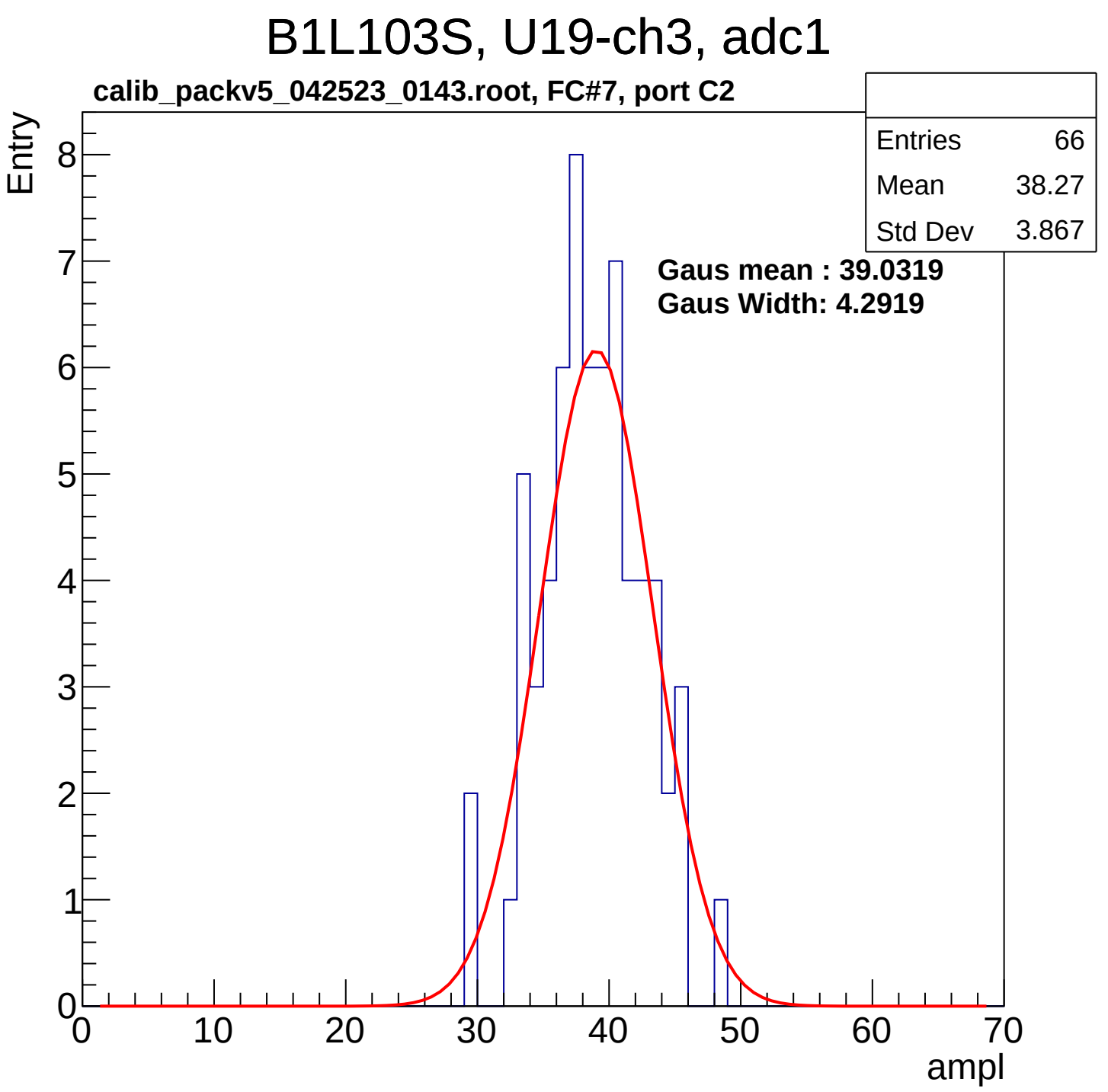
B1L103S, U19-ch3, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 38.27
Std Dev 3.867
Gaus mean : 39.0319
Gaus Width: 4.2919
Entry
ampl
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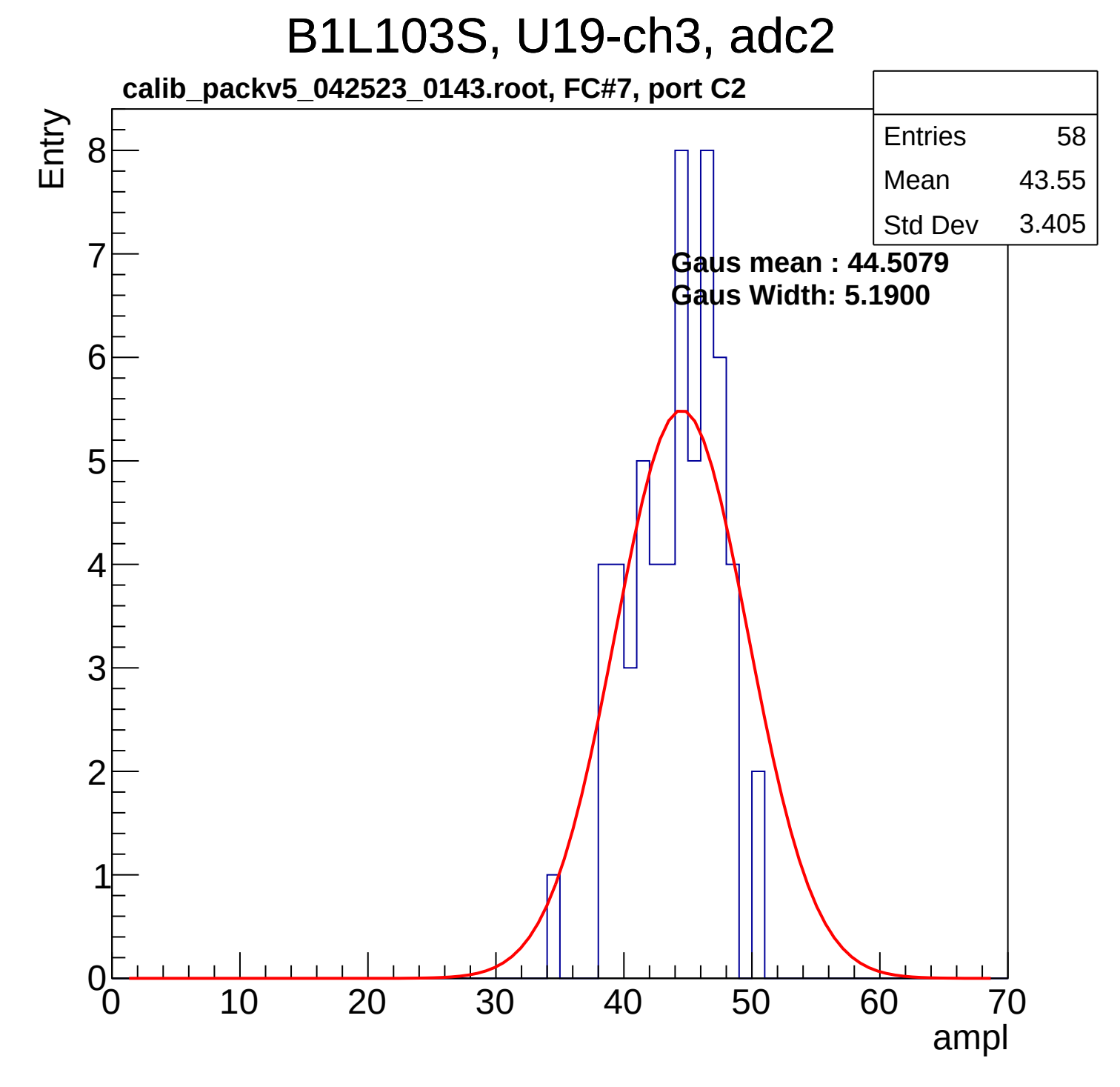
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch3, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 43.55
Std Dev 3.405
Gaus mean : 44.5079
Gaus Width: 5.1900
Entry
ampl
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1
2
3
4
5
6
7
8
0
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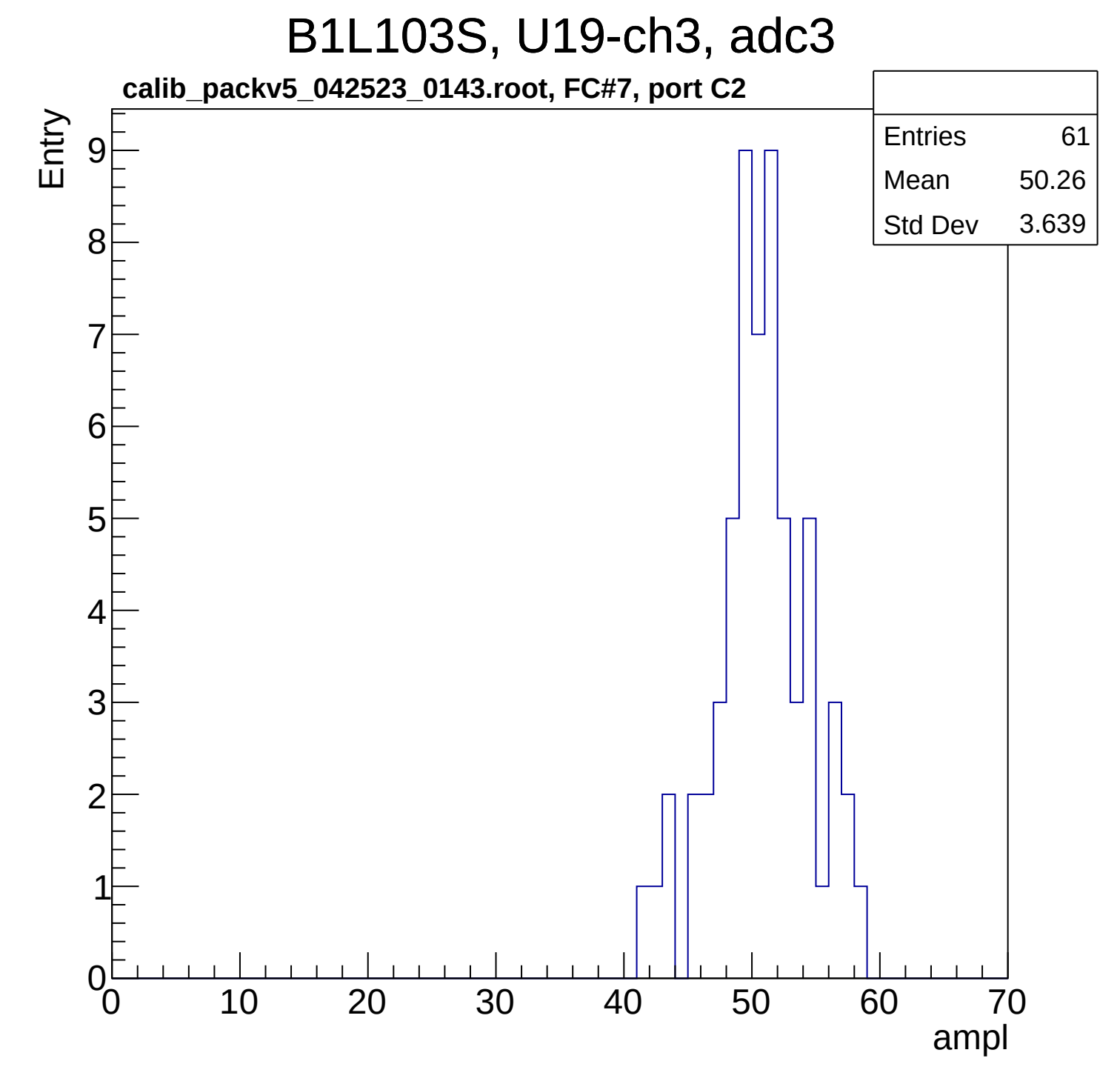
B1L103S, U19-ch3, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 50.26
Std Dev 3.639
Entry
ampl
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3
4
5
6
7
8
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0
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60
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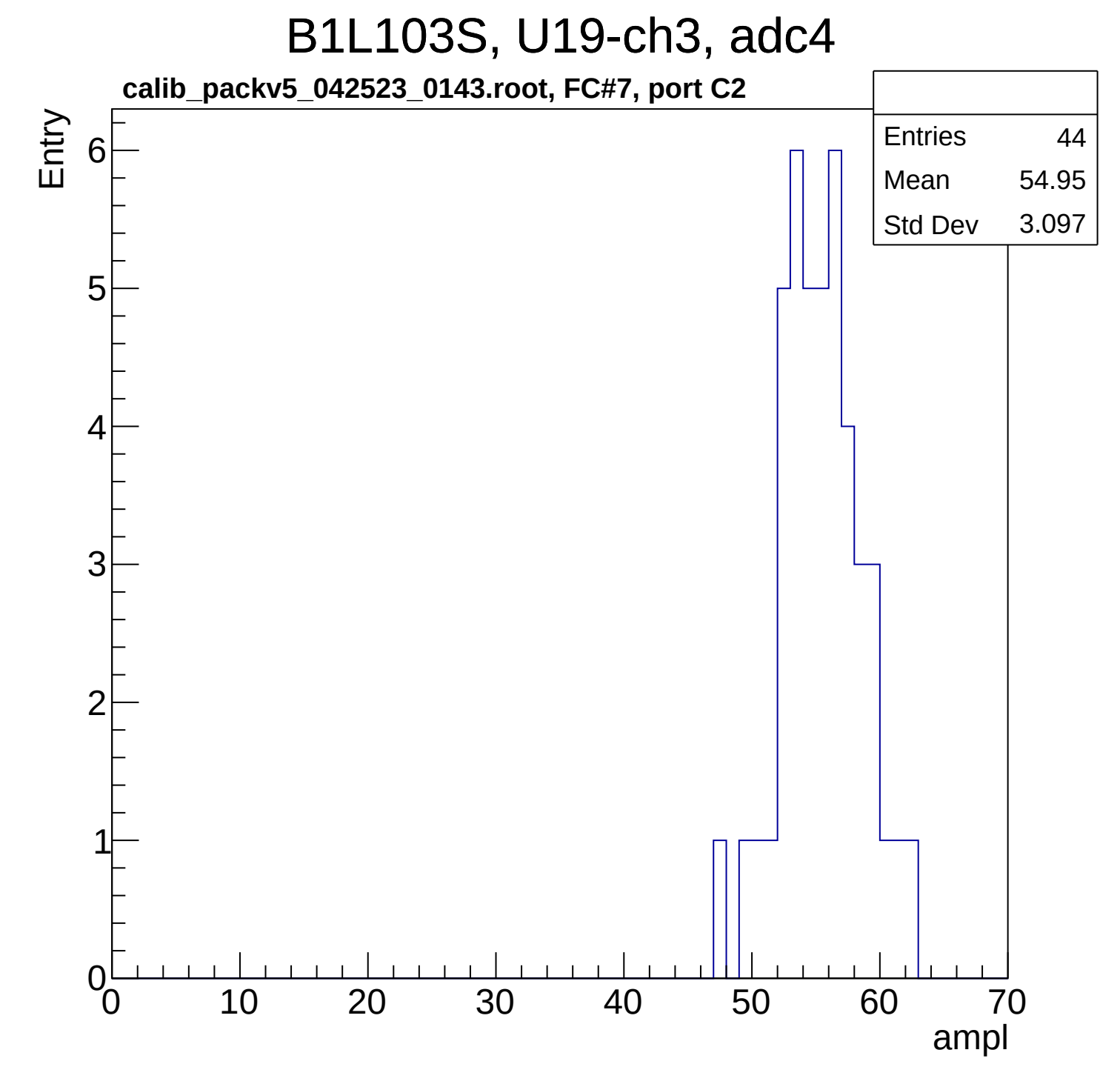
B1L103S, U19-ch3, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 54.95
Std Dev 3.097
Entry
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2
3
4
5
6
0
10
20
30
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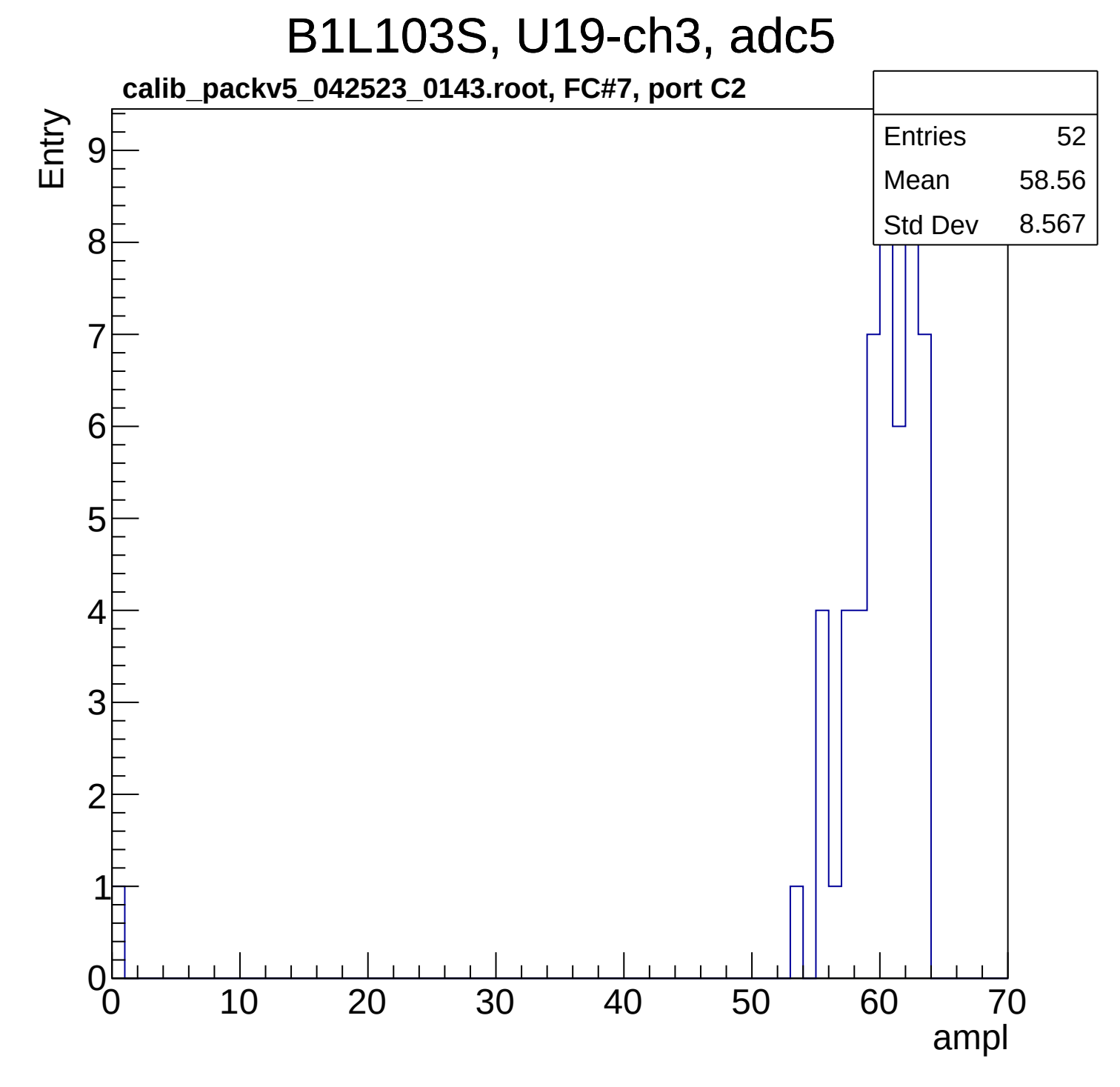
50
60
70
ampl


B1L103S, U19-ch3, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.56
Std Dev 8.567
Entry
ampl
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3
4
5
6
7
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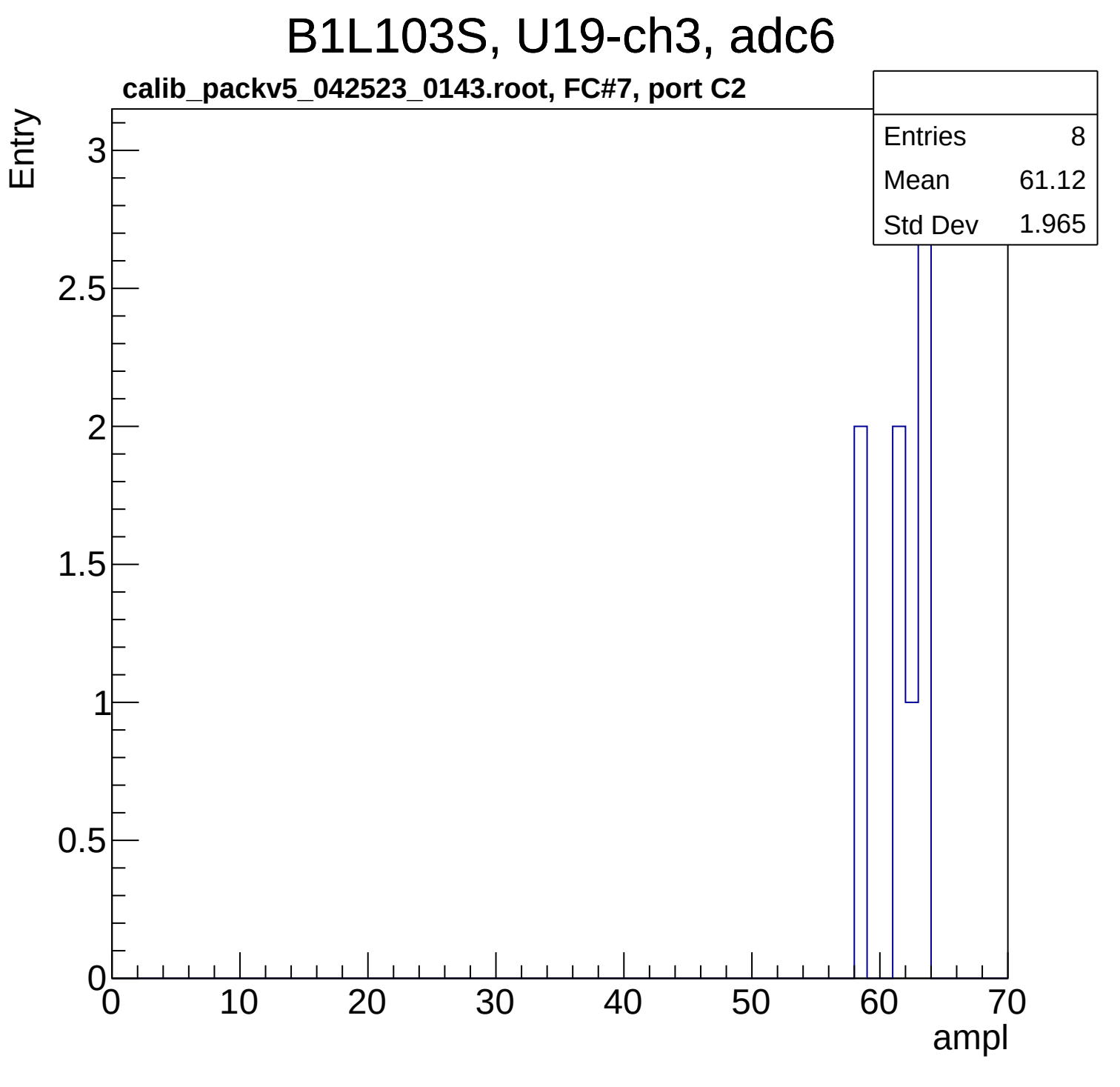
B1L103S, U19-ch3, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.12
Std Dev 1.965
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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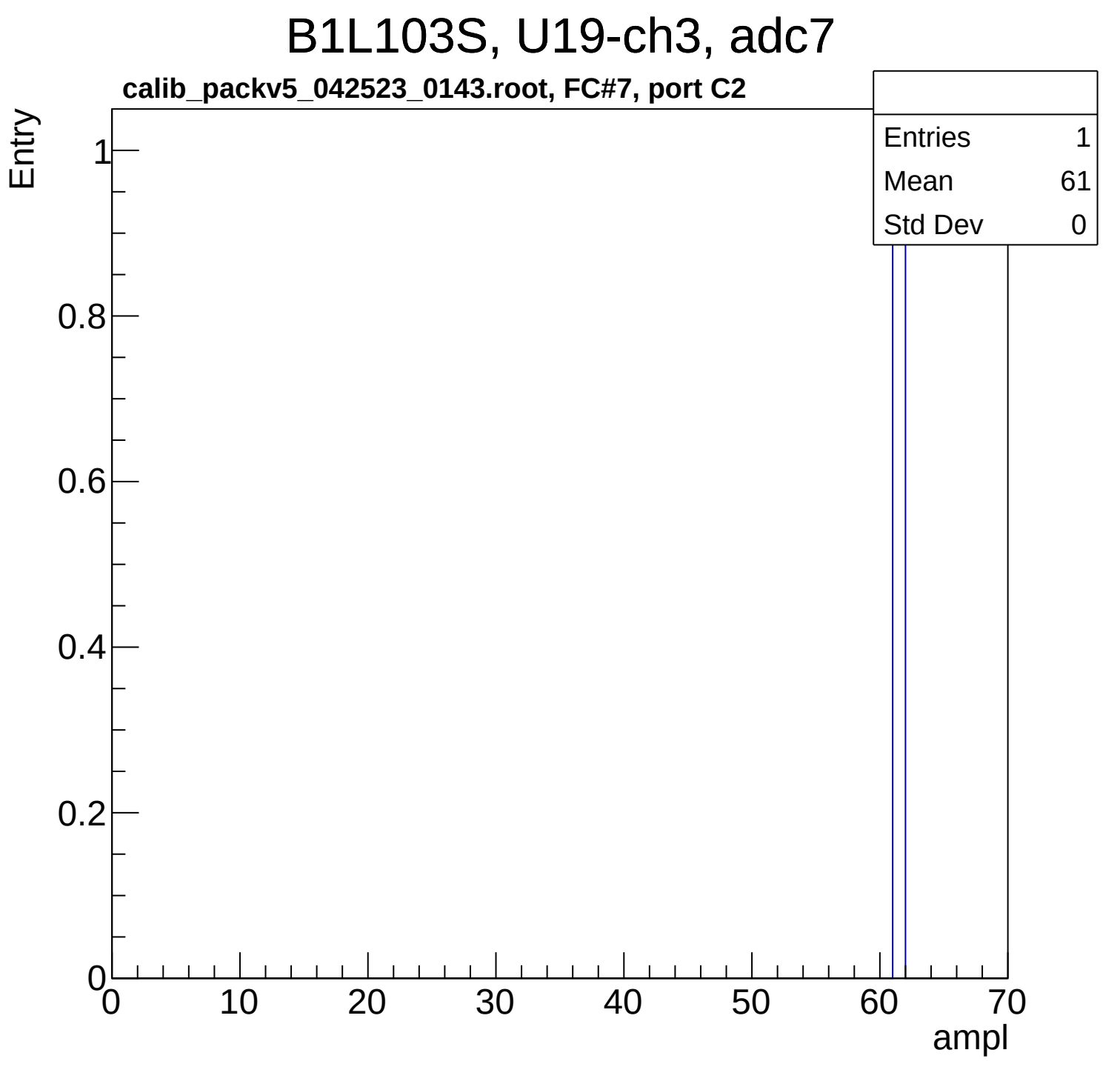
B1L103S, U19-ch3, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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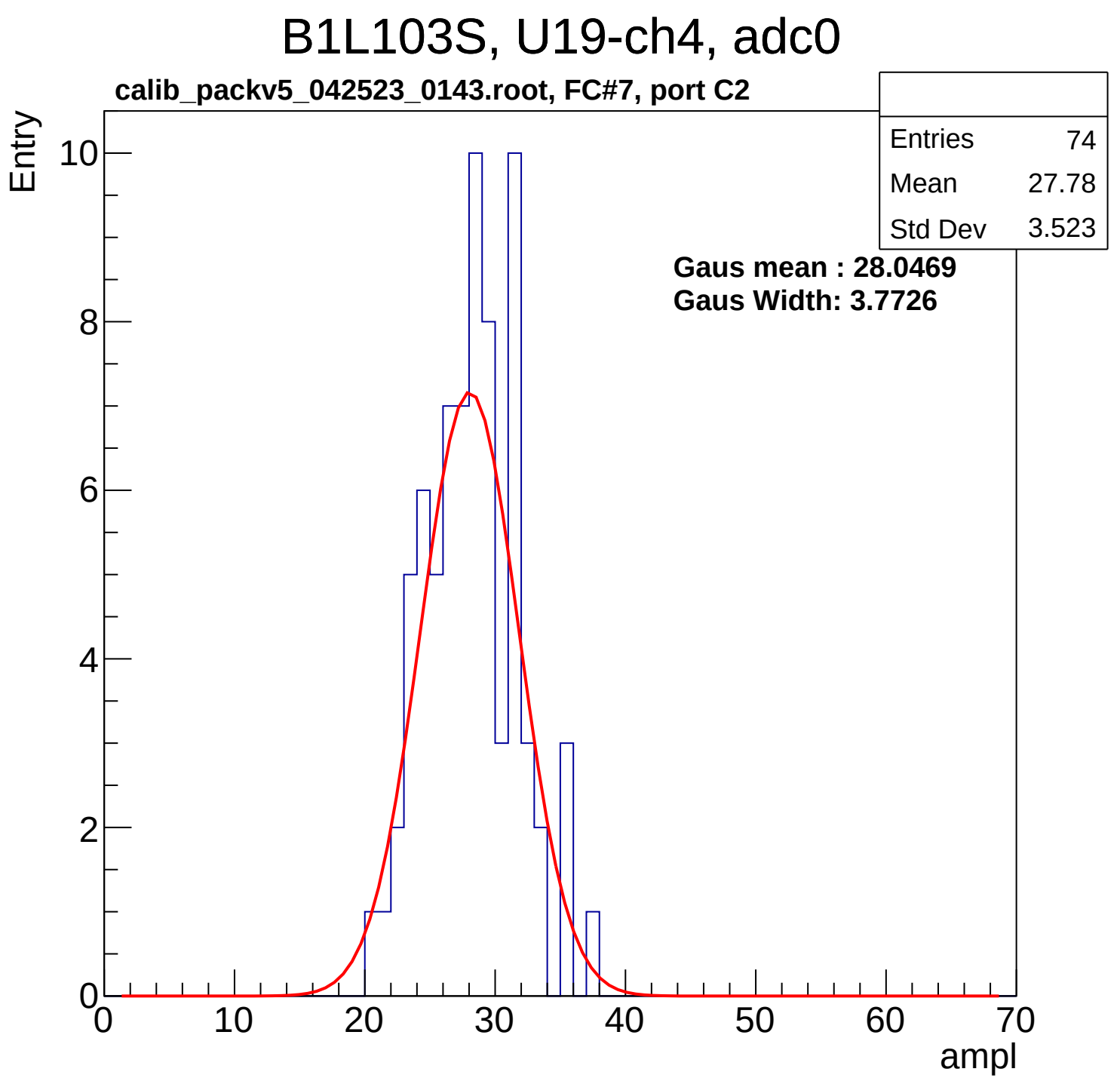
40
50
60
70
ampl


B1L103S, U19-ch4, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 27.78
Std Dev 3.523
Gaus mean : 28.0469
Gaus Width: 3.7726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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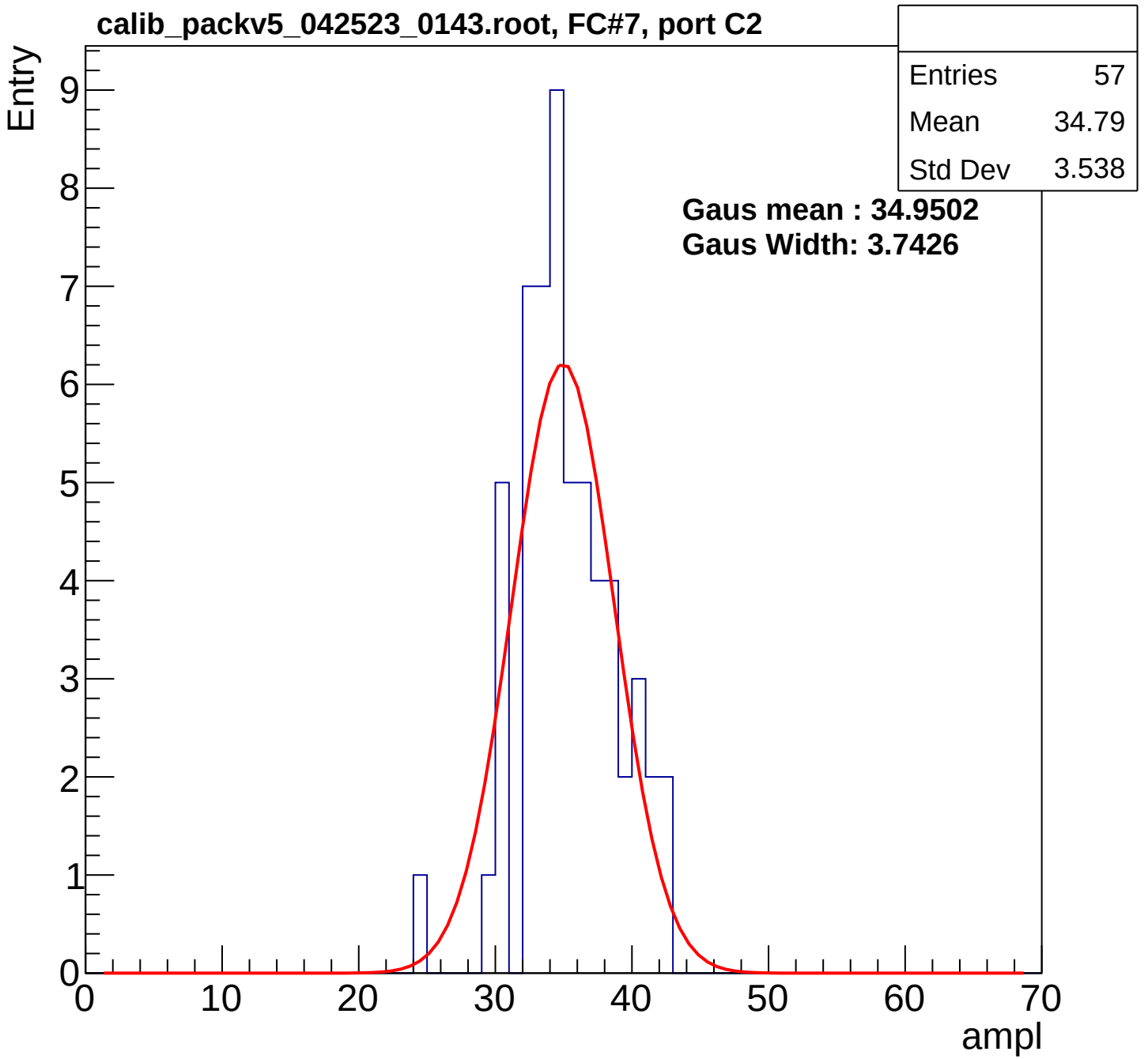
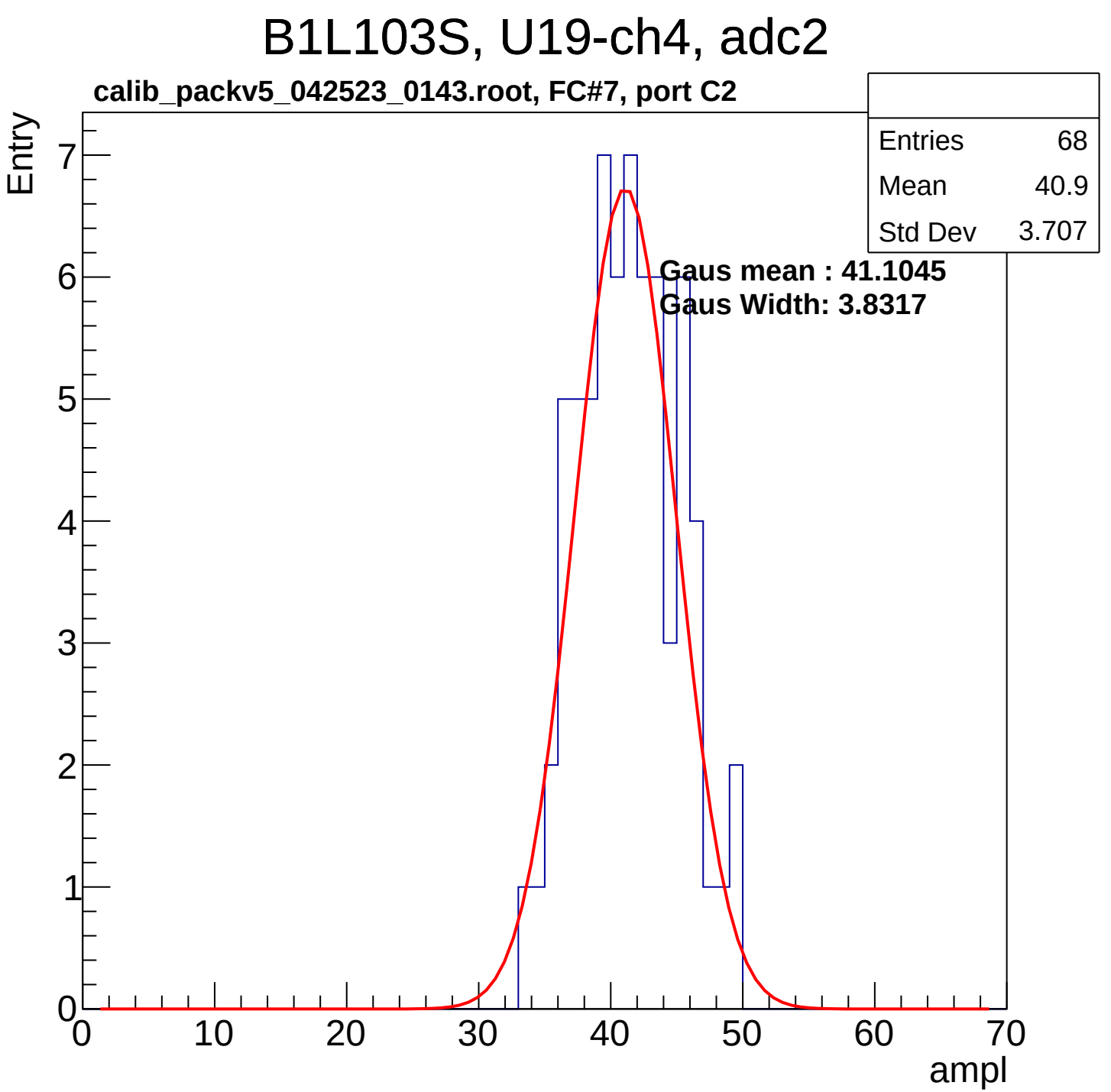

B1L103S, U19-ch4, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 34.79
Std Dev 3.538
Gaus mean : 34.9502
Gaus Width: 3.7426
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L103S, U19-ch4, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 40.9
Std Dev 3.707
Gaus mean : 41.1045
Gaus Width: 3.8317
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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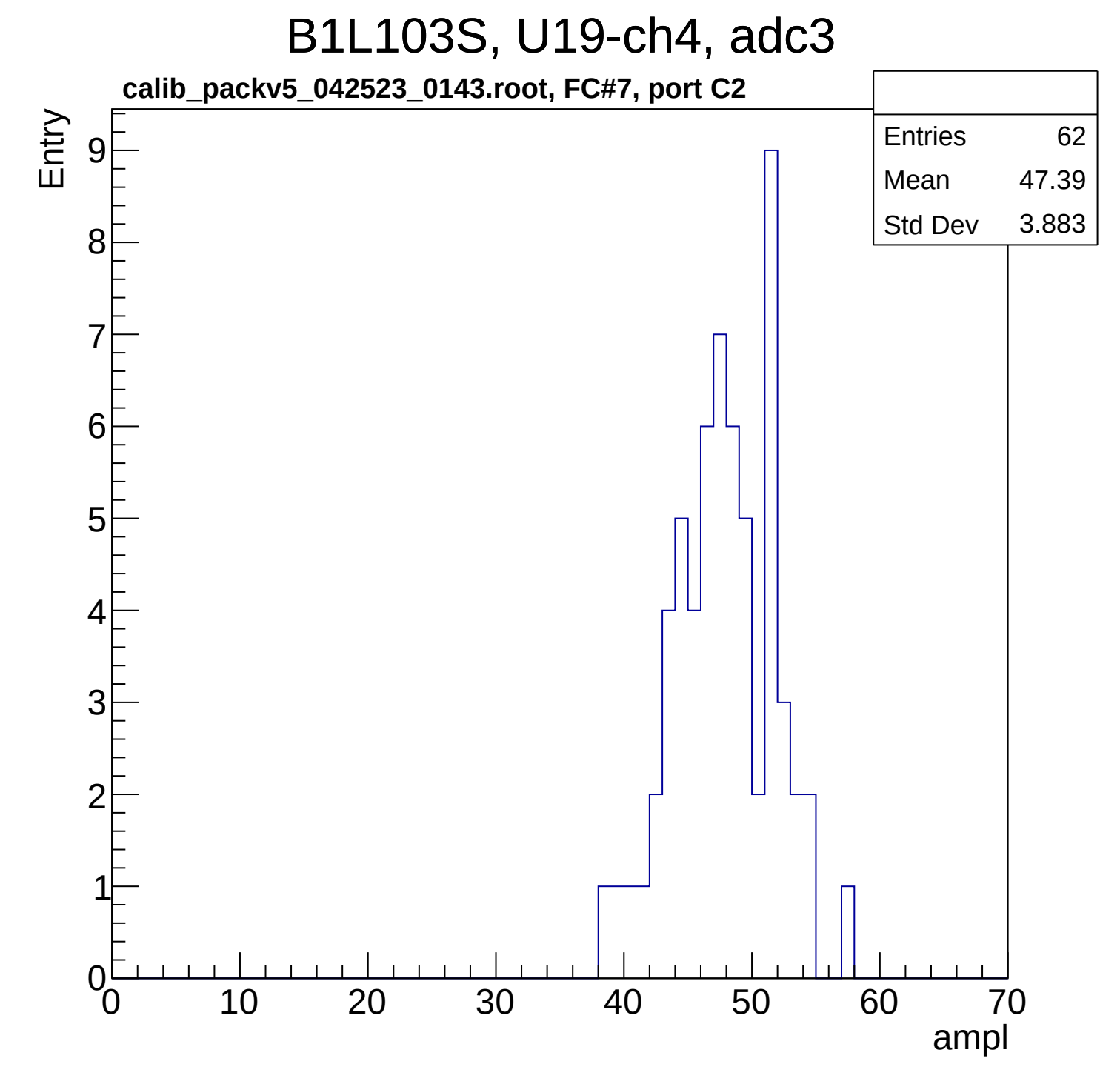
B1L103S, U19-ch4, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 47.39
Std Dev 3.883
Entry
ampl
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2
3
4
5
6
7
8
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0
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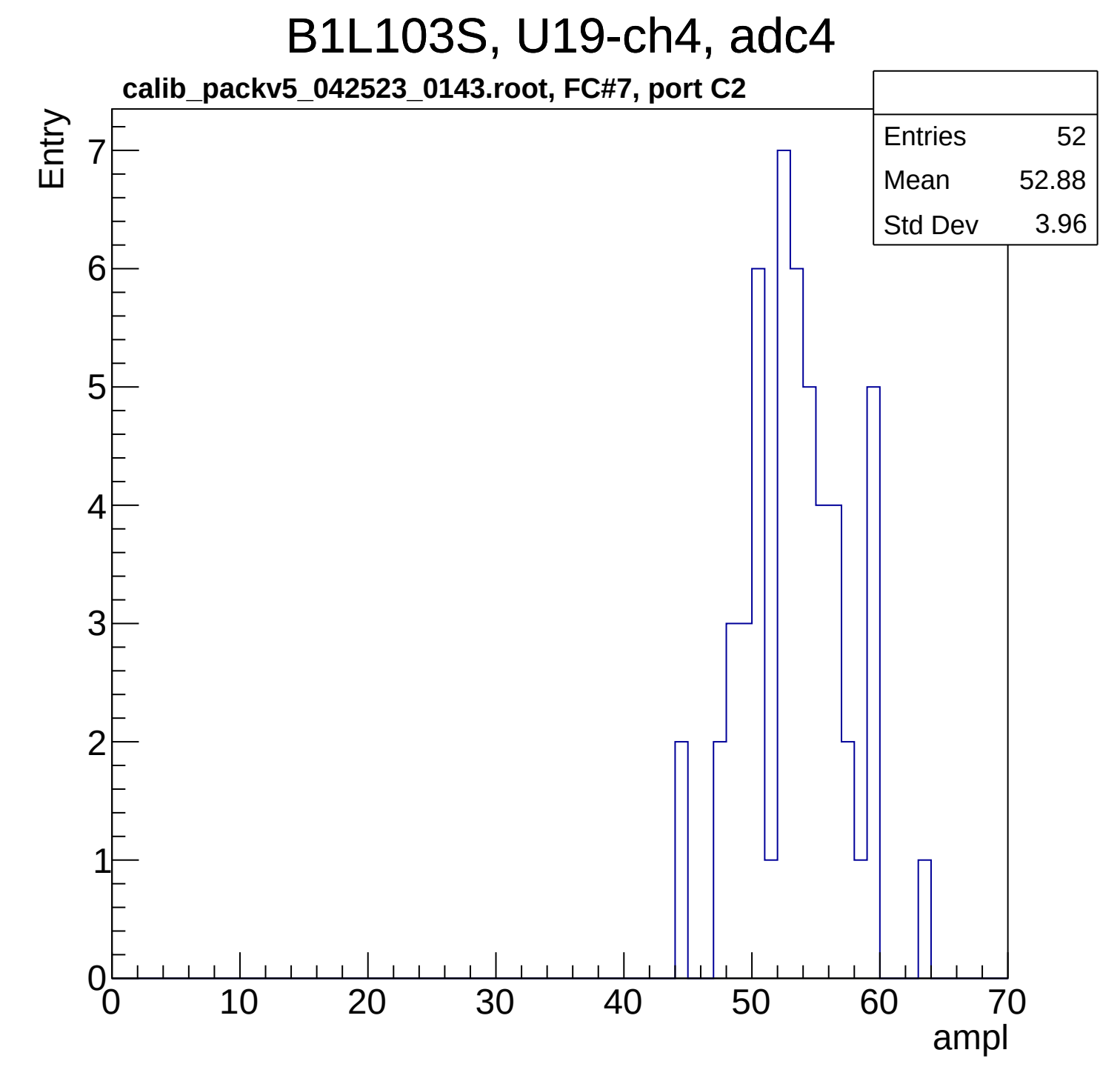
B1L103S, U19-ch4, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 52.88
Std Dev 3.96
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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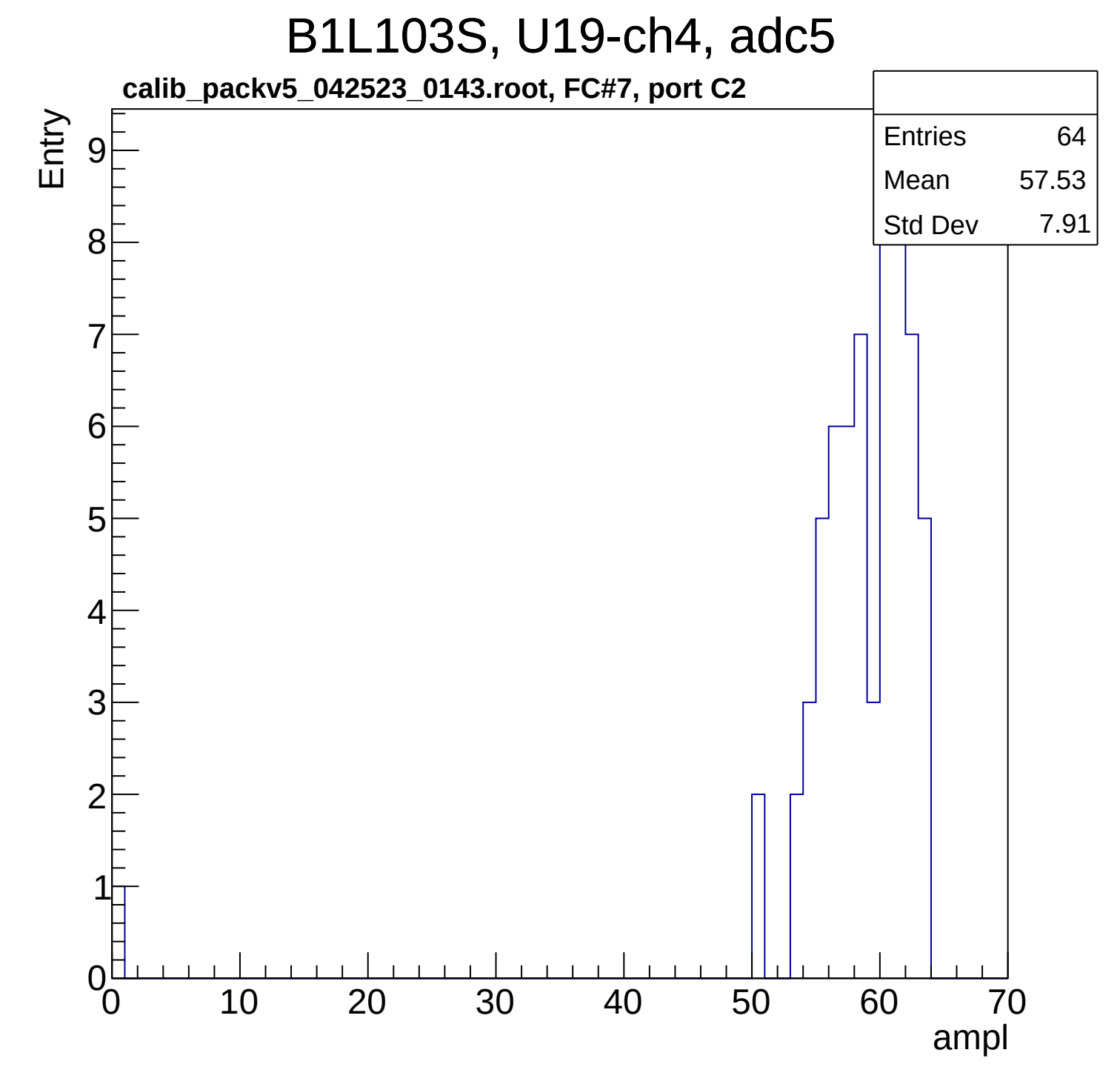
B1L103S, U19-ch4, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 57.53
Std Dev 7.91
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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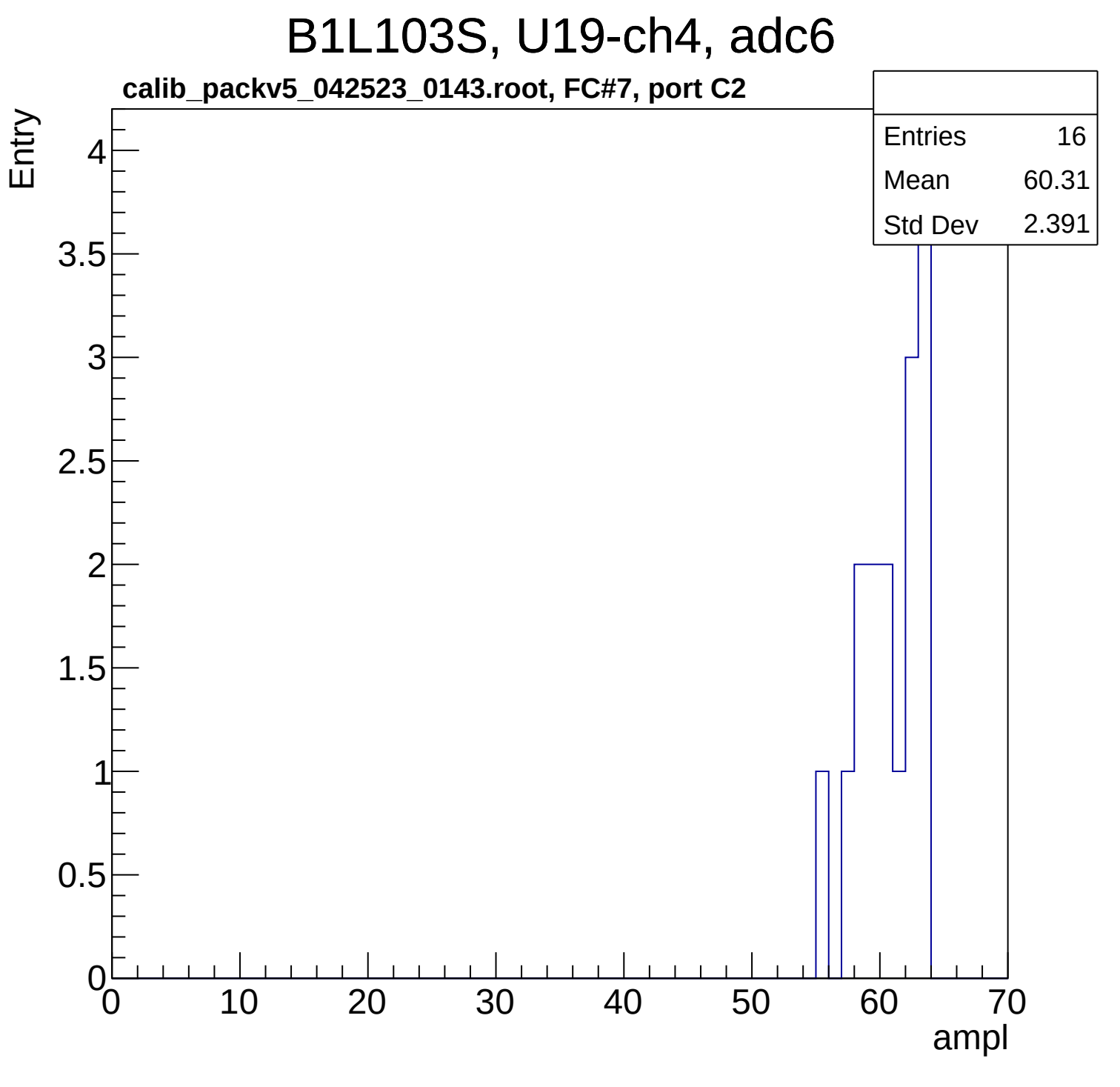
B1L103S, U19-ch4, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 60.31
Std Dev 2.391
Entry
ampl
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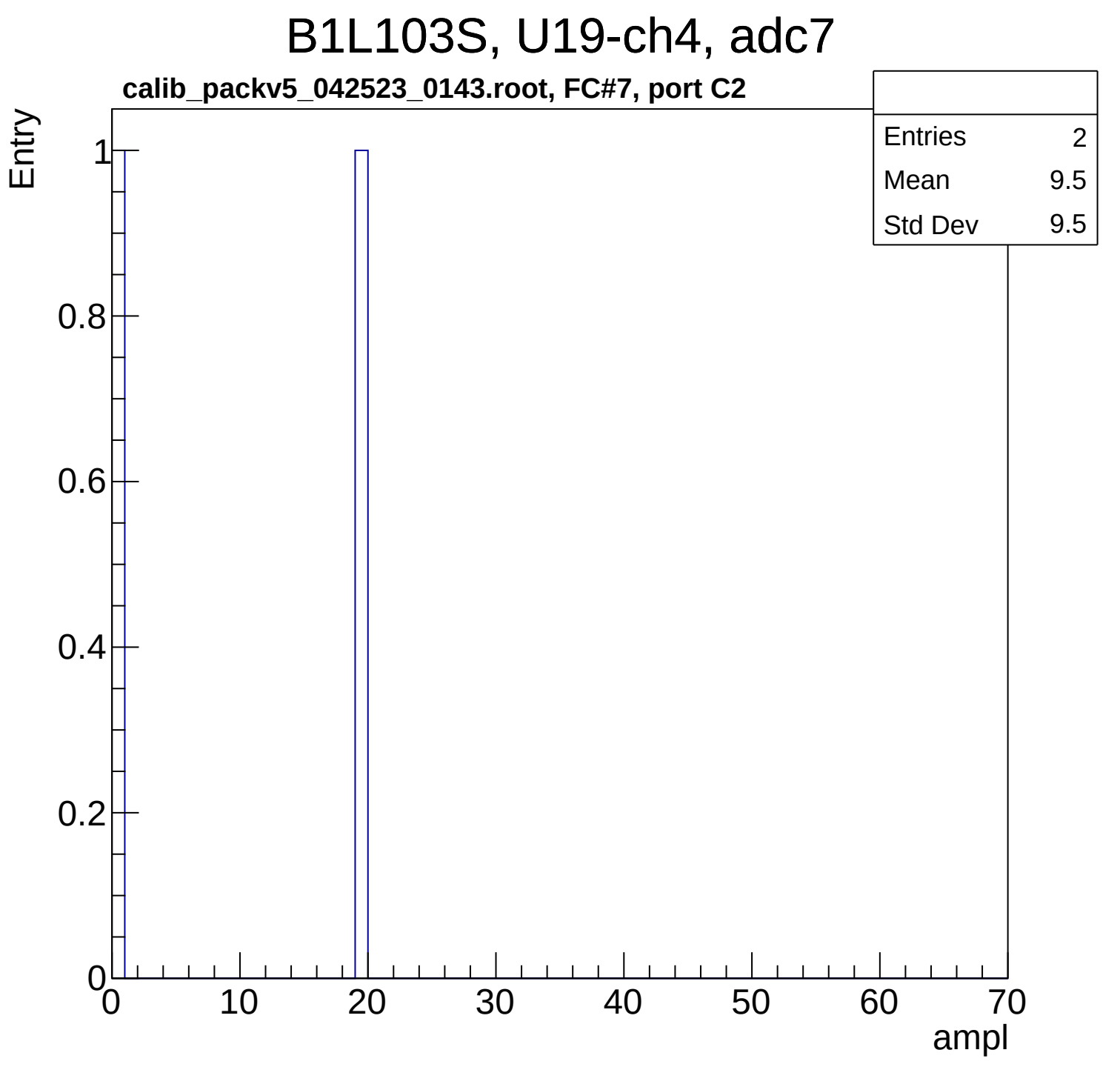
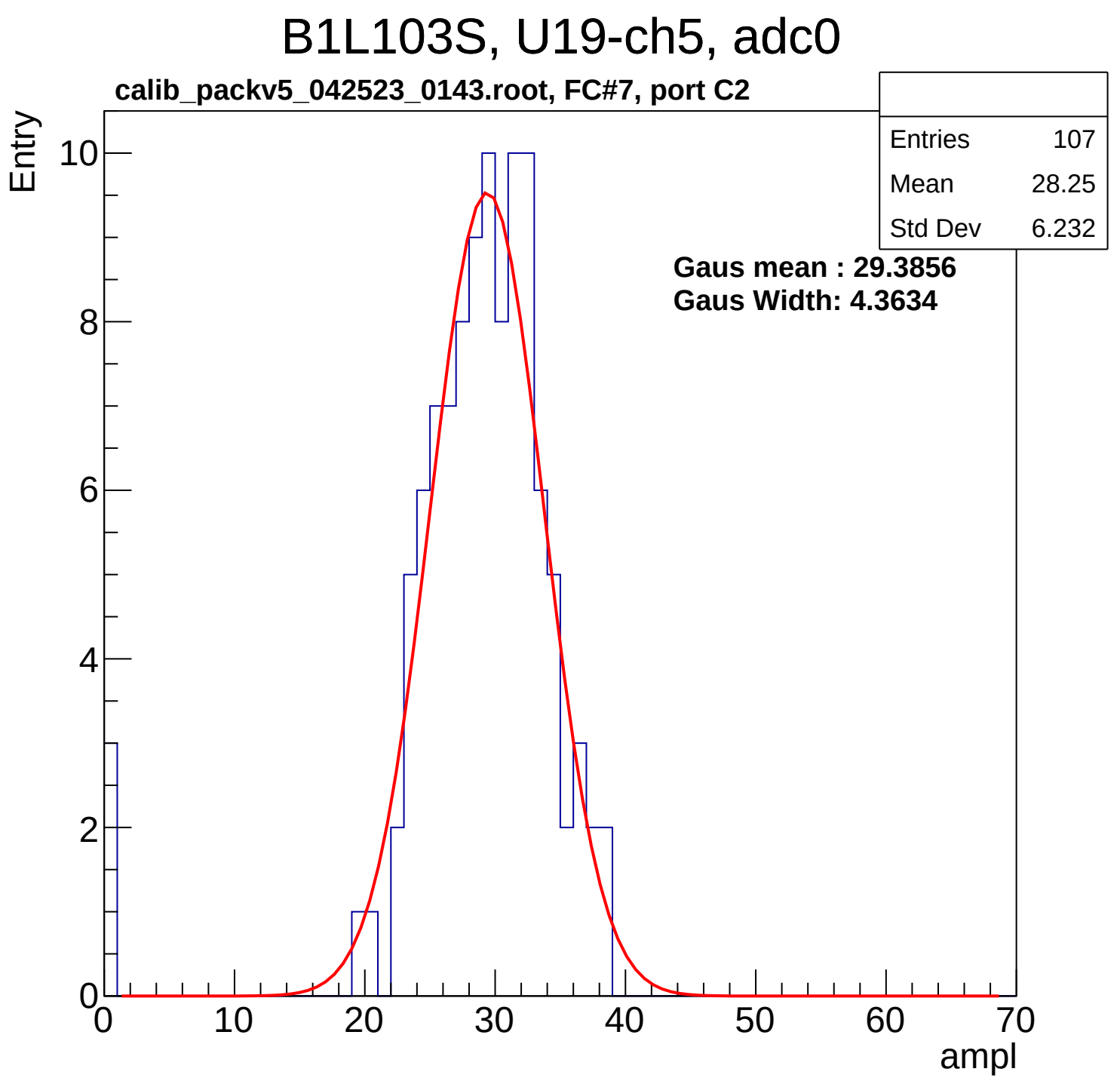
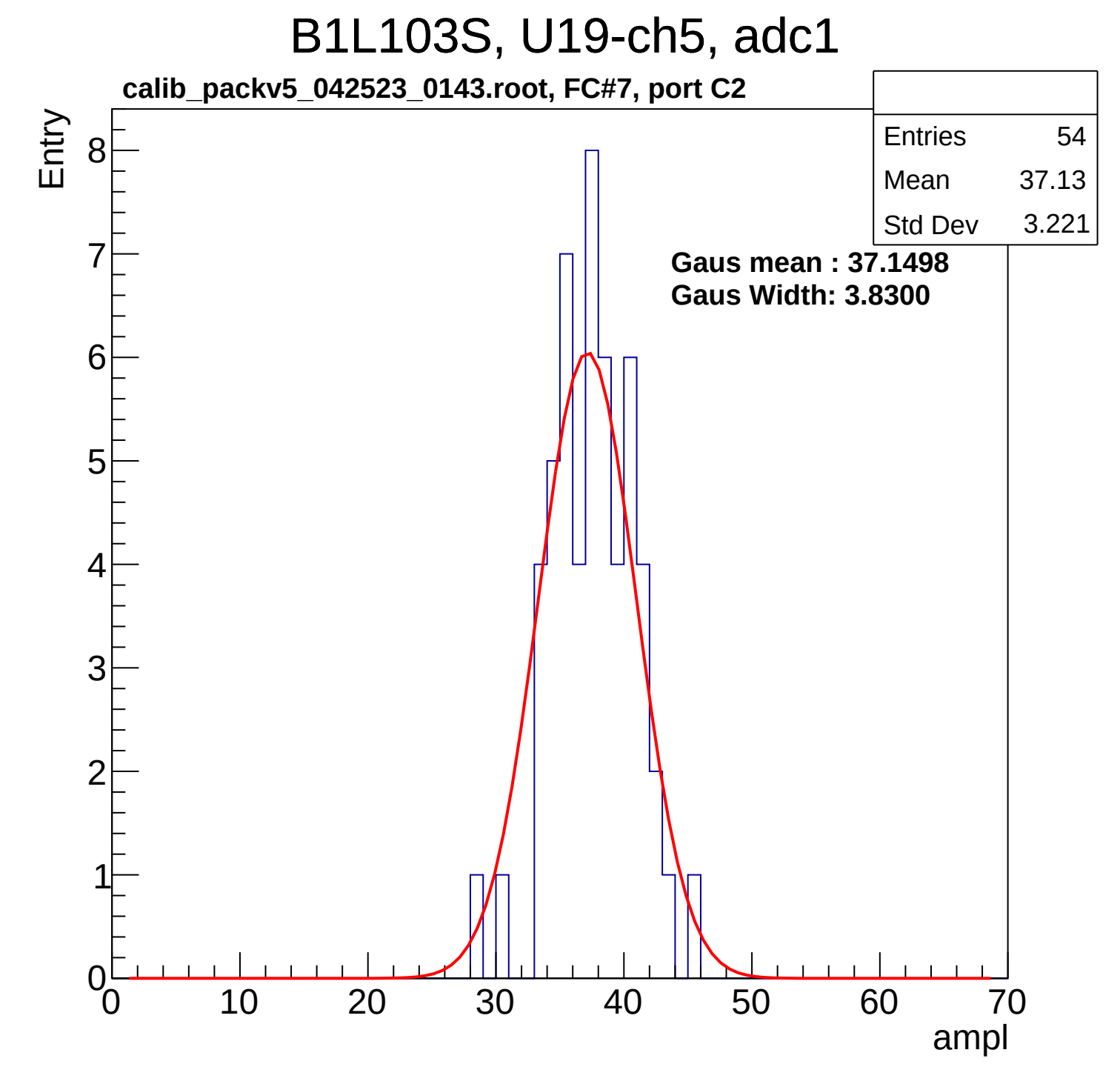
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U19-ch4, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 9.5
Std Dev 9.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U19-ch5, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 107
Mean 28.25
Std Dev 6.232
Gaus mean : 29.3856
Gaus Width: 4.3634
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch5, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 37.13
Std Dev 3.221
Gaus mean : 37.1498
Gaus Width: 3.8300
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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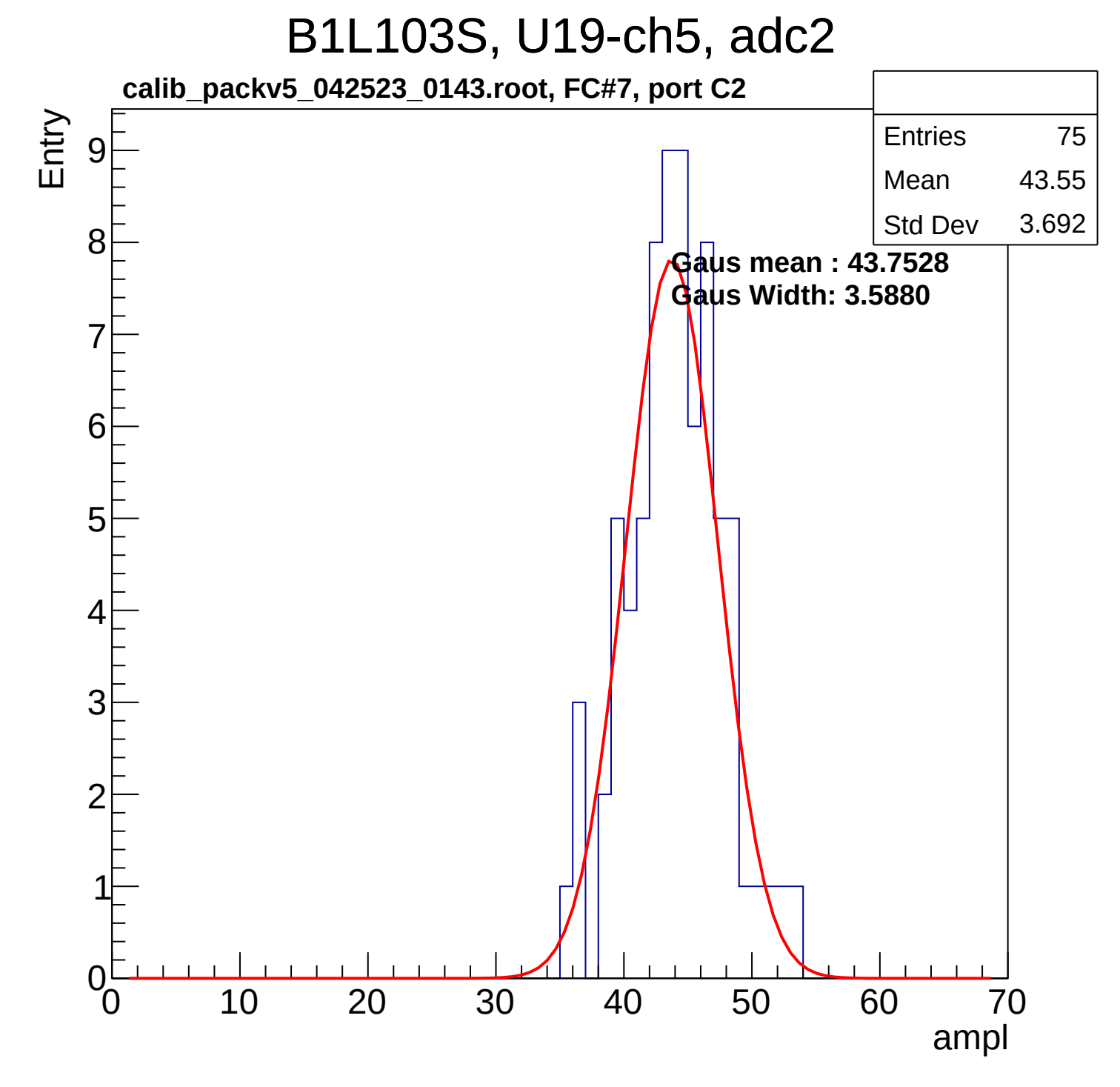
B1L103S, U19-ch5, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 43.55
Std Dev 3.692
Gaus mean : 43.7528
Gaus Width: 3.5880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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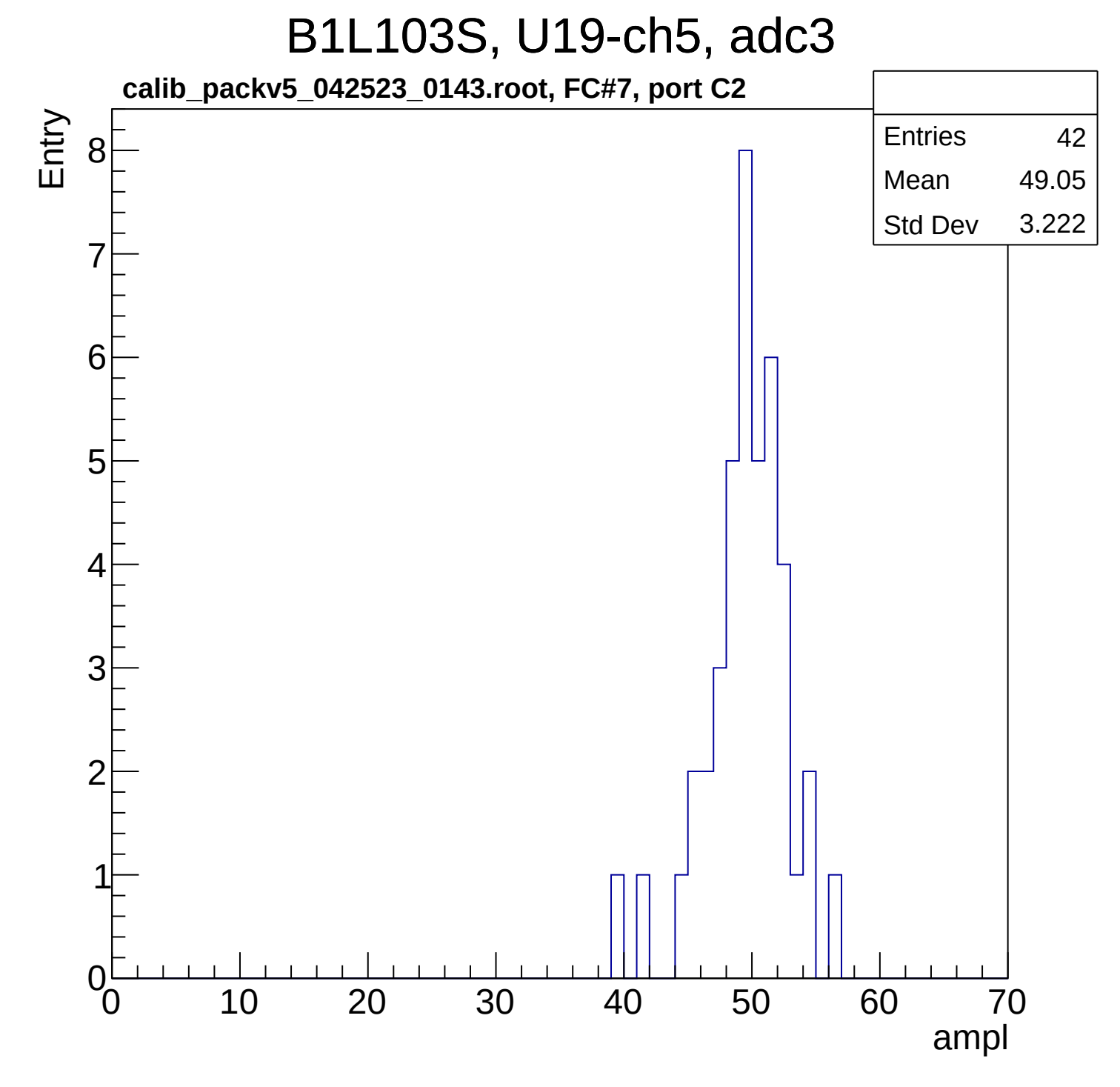
B1L103S, U19-ch5, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 49.05
Std Dev 3.222
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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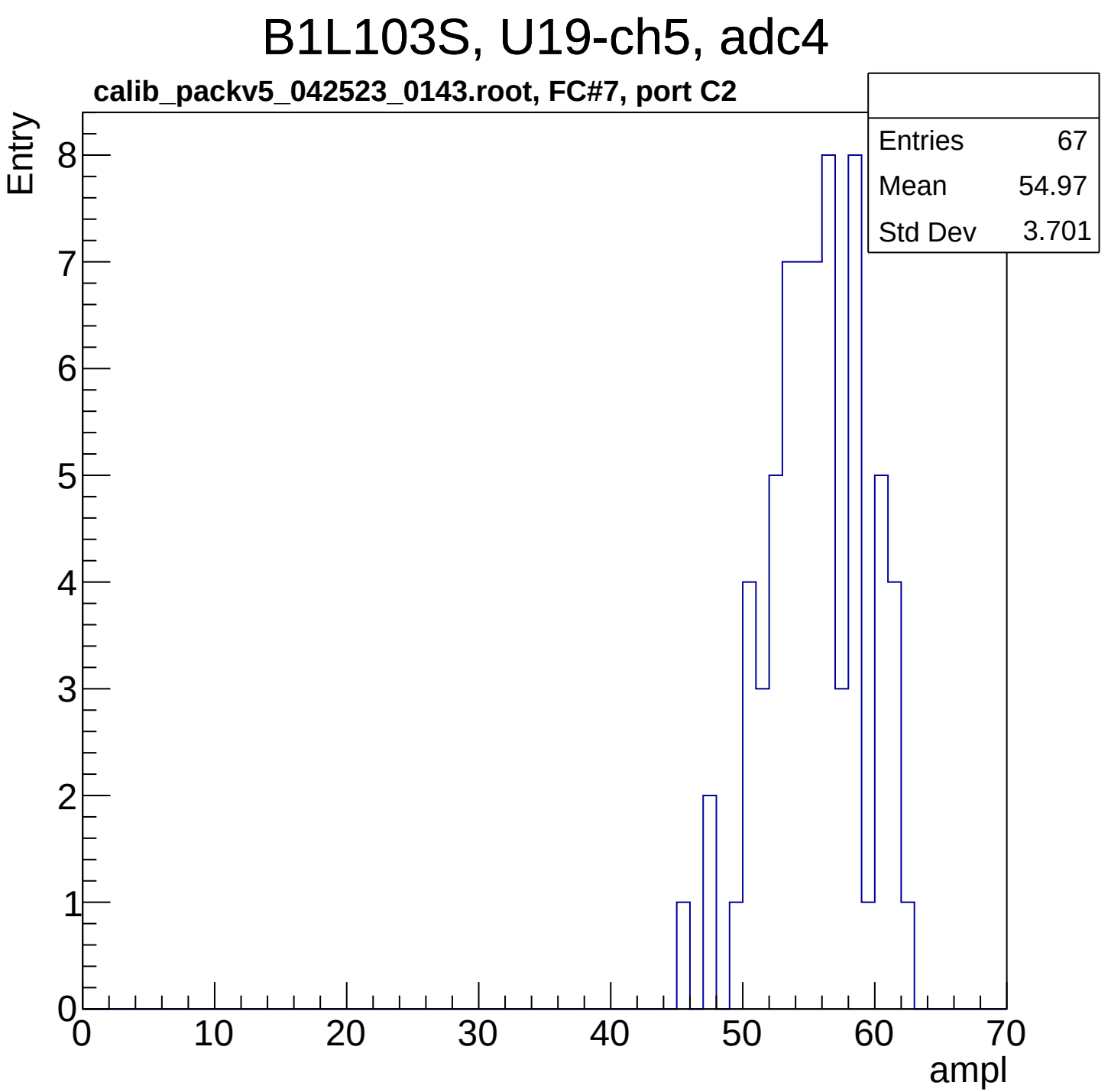
B1L103S, U19-ch5, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.97
Std Dev 3.701
Entry
ampl
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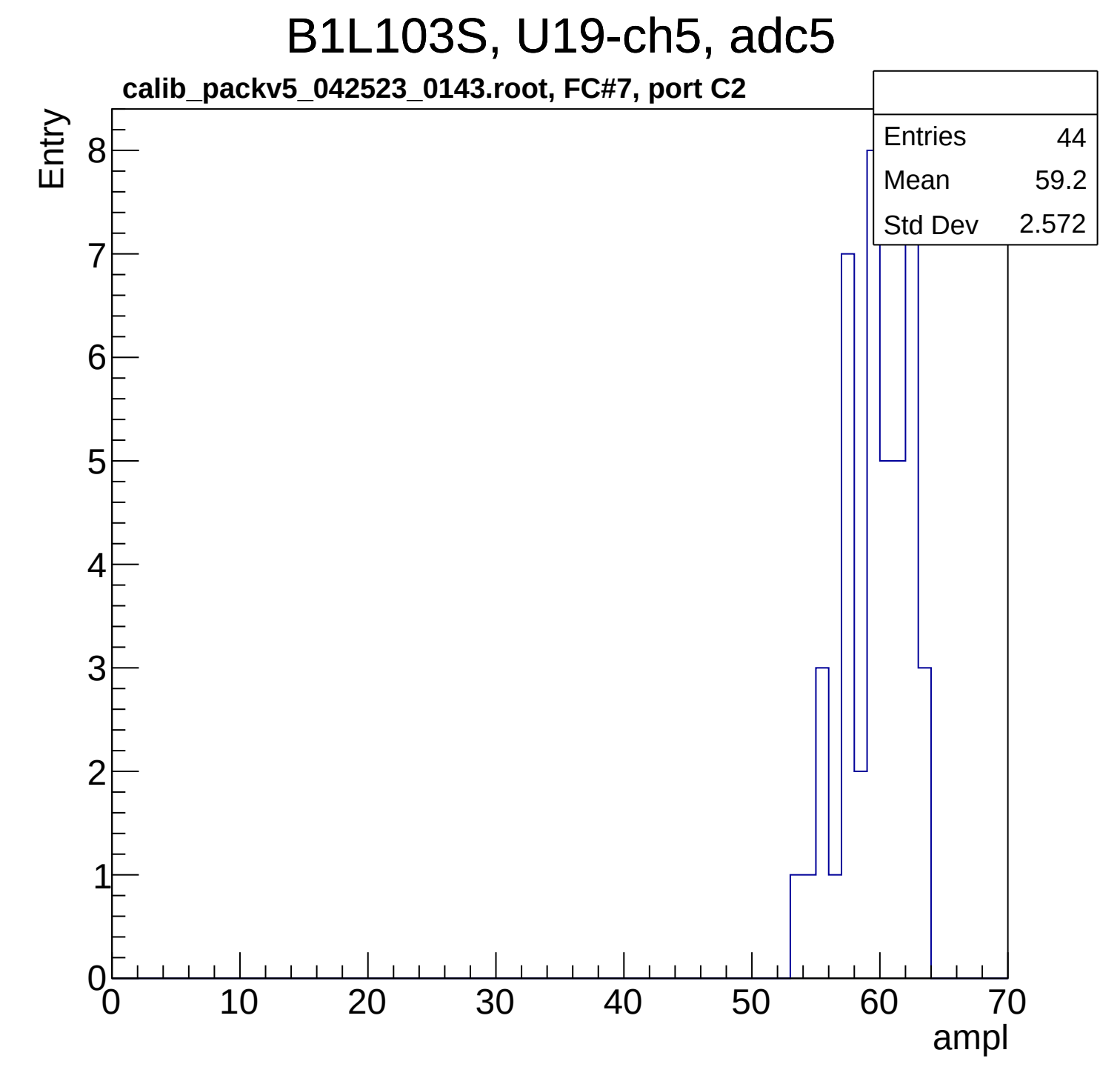
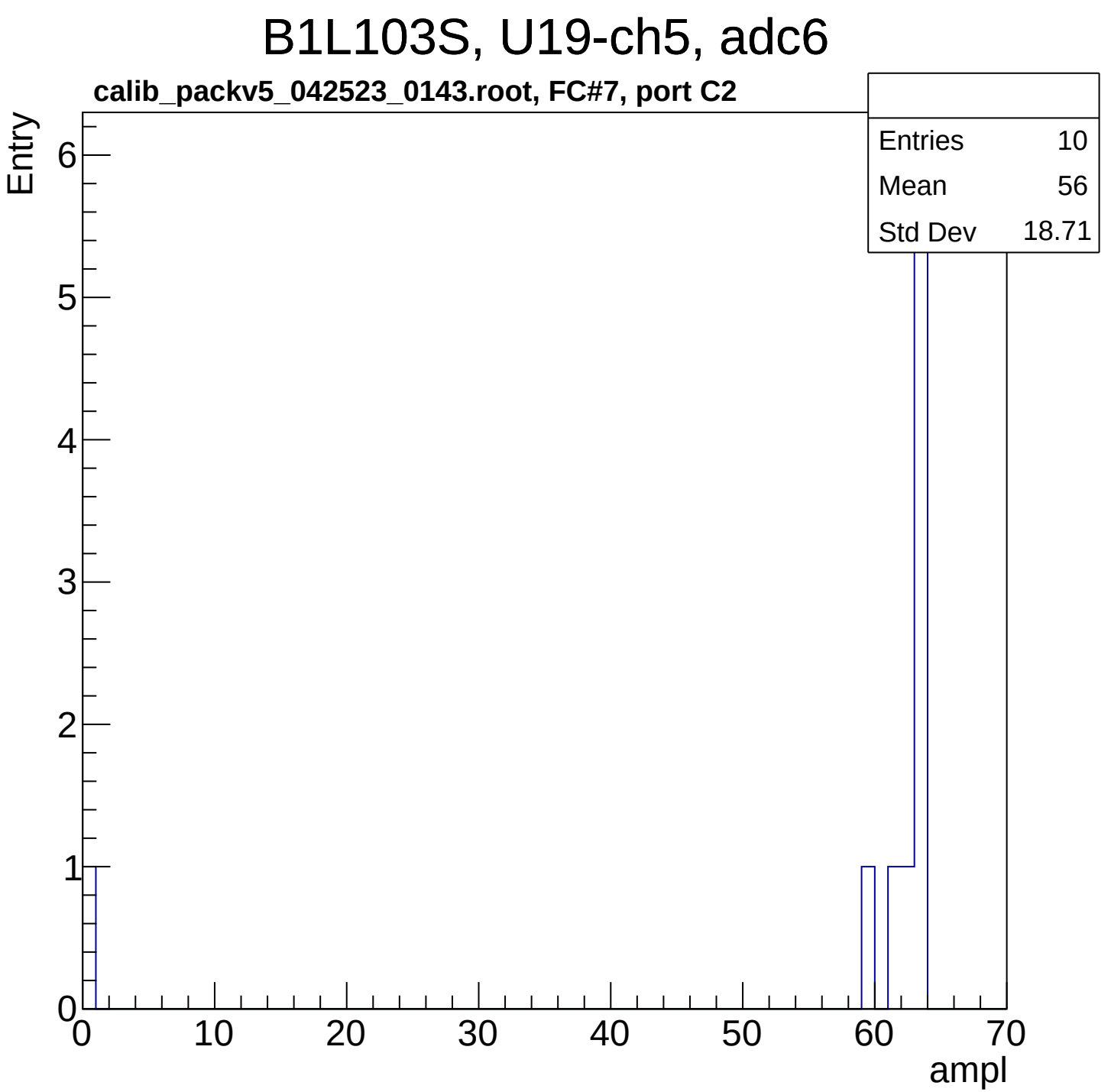
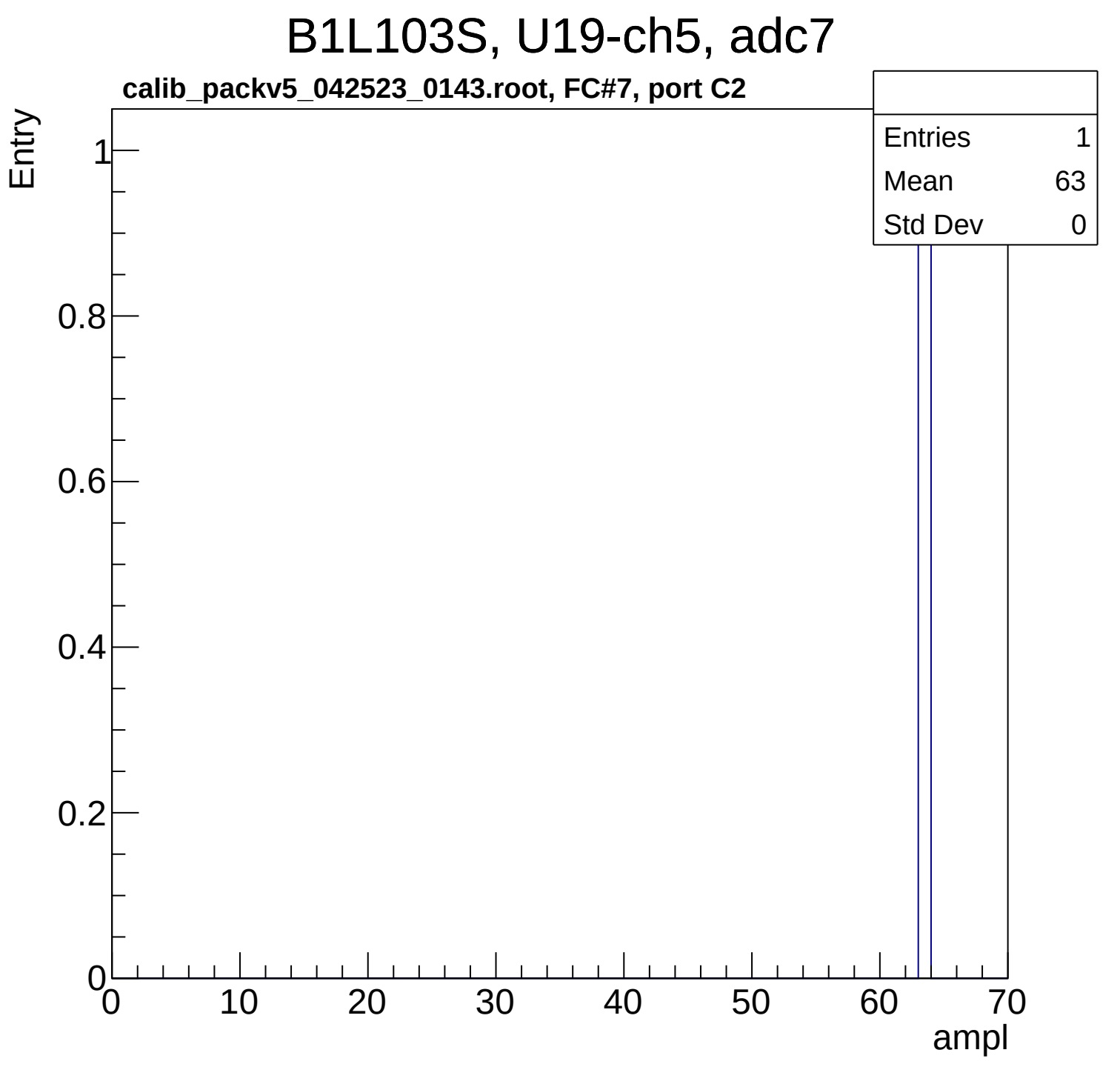
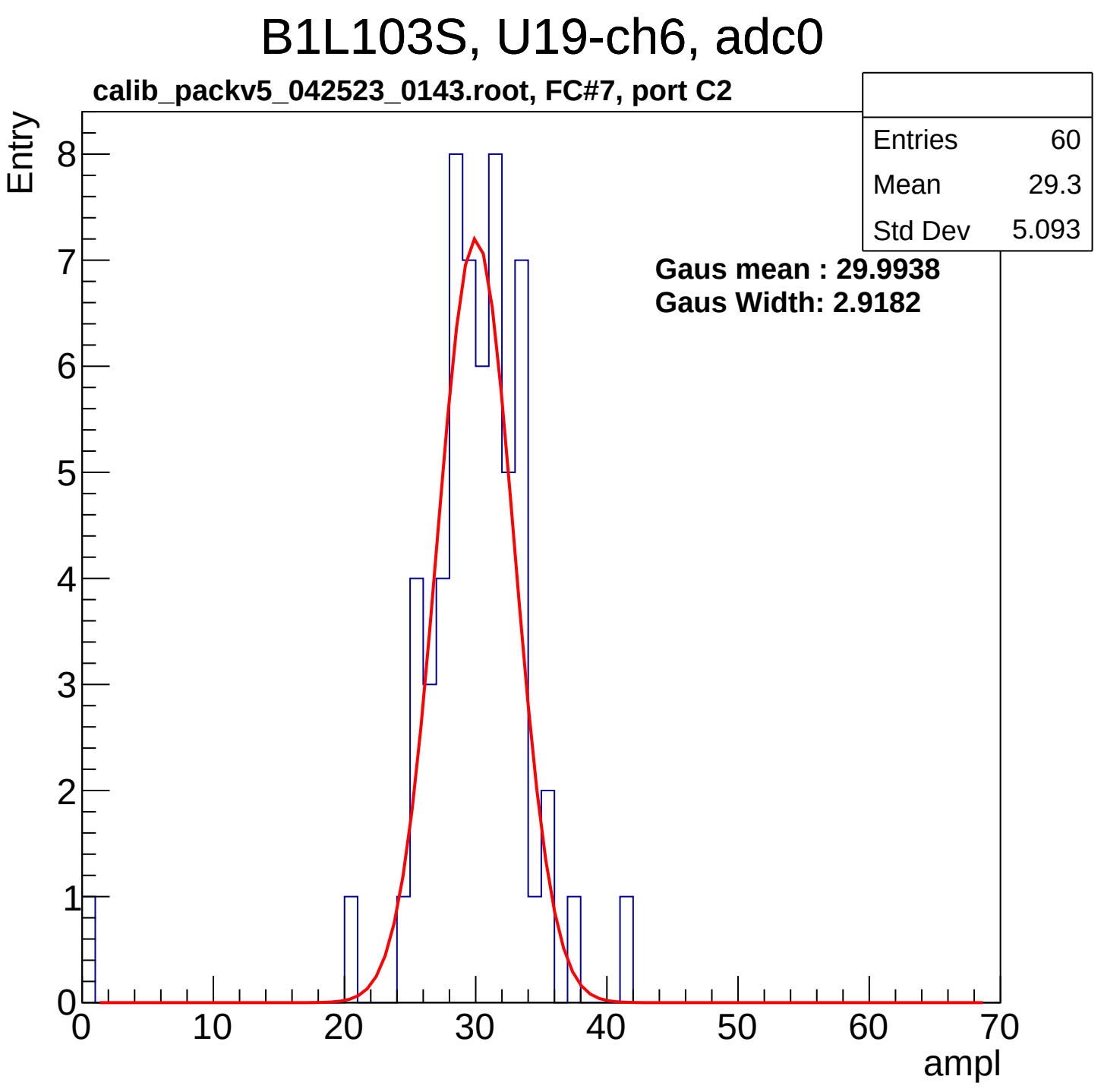
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch5, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.2
Std Dev 2.572
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch5, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56
Std Dev 18.71
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch5, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch6, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 29.3
Std Dev 5.093
Gaus mean : 29.9938
Gaus Width: 2.9182
Entry
ampl
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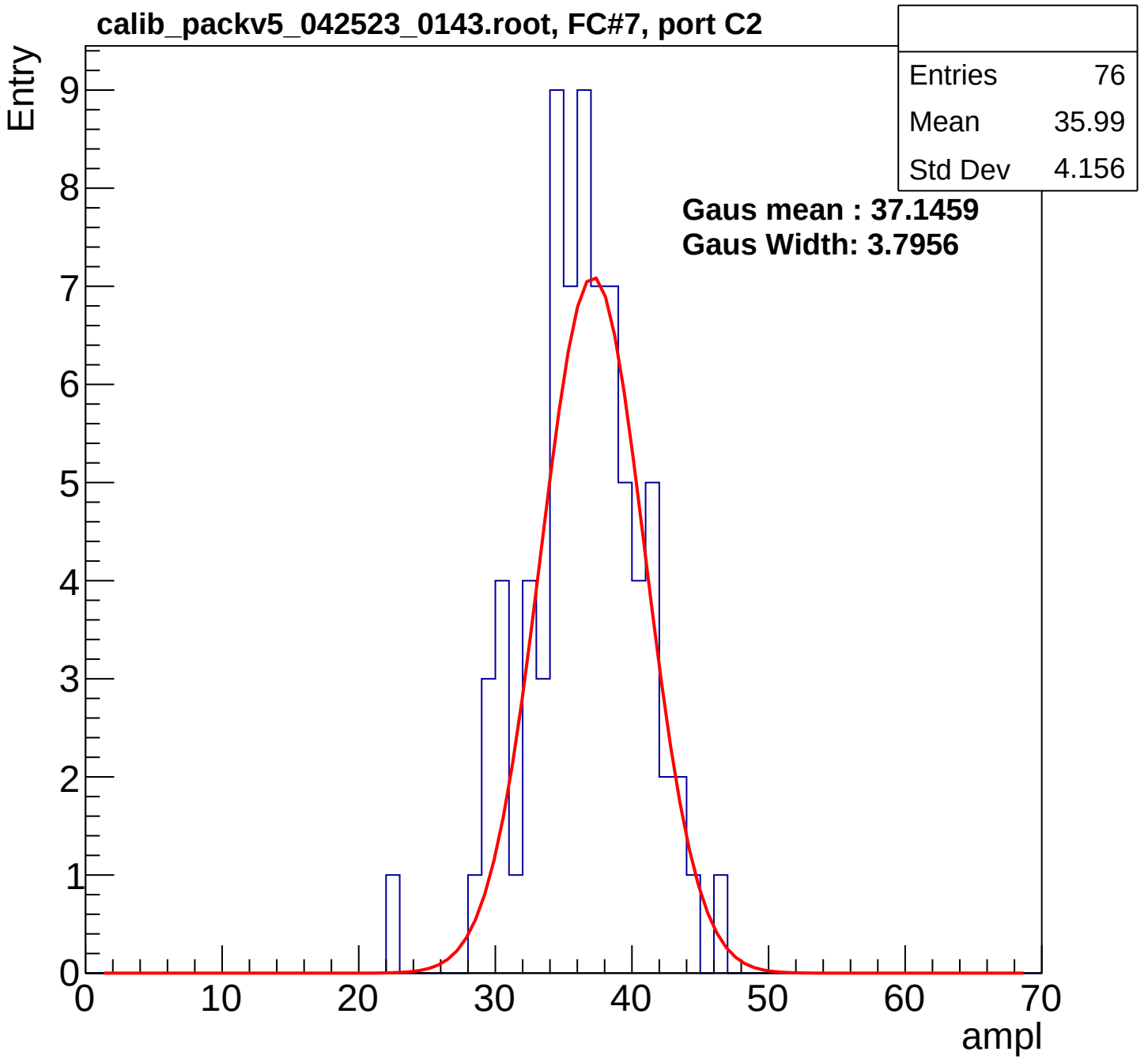
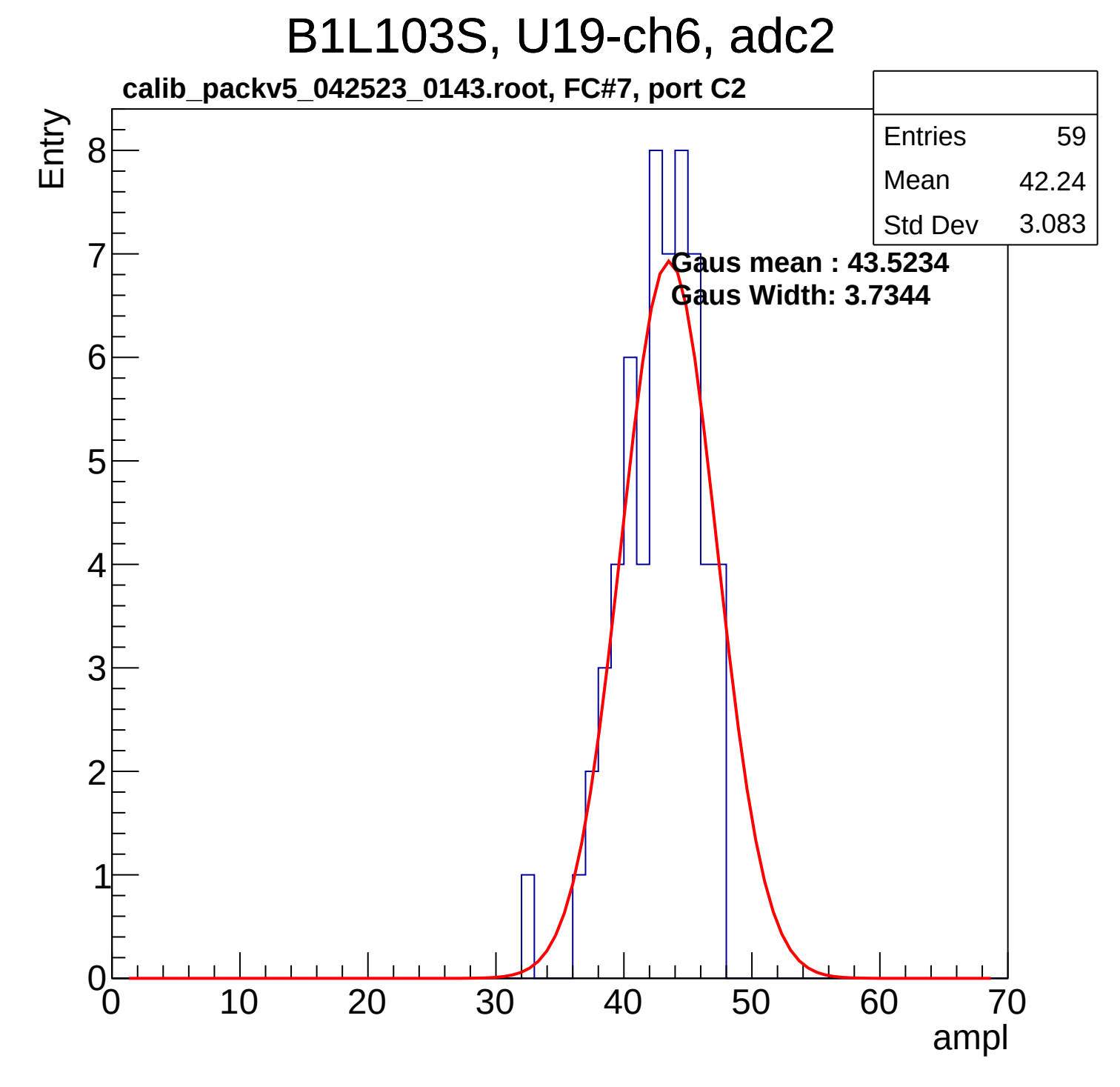
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch6, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 35.99
Std Dev 4.156
Gaus mean : 37.1459
Gaus Width: 3.7956
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch6, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42.24
Std Dev 3.083
Gaus mean : 43.5234
Gaus Width: 3.7344
Entry
ampl
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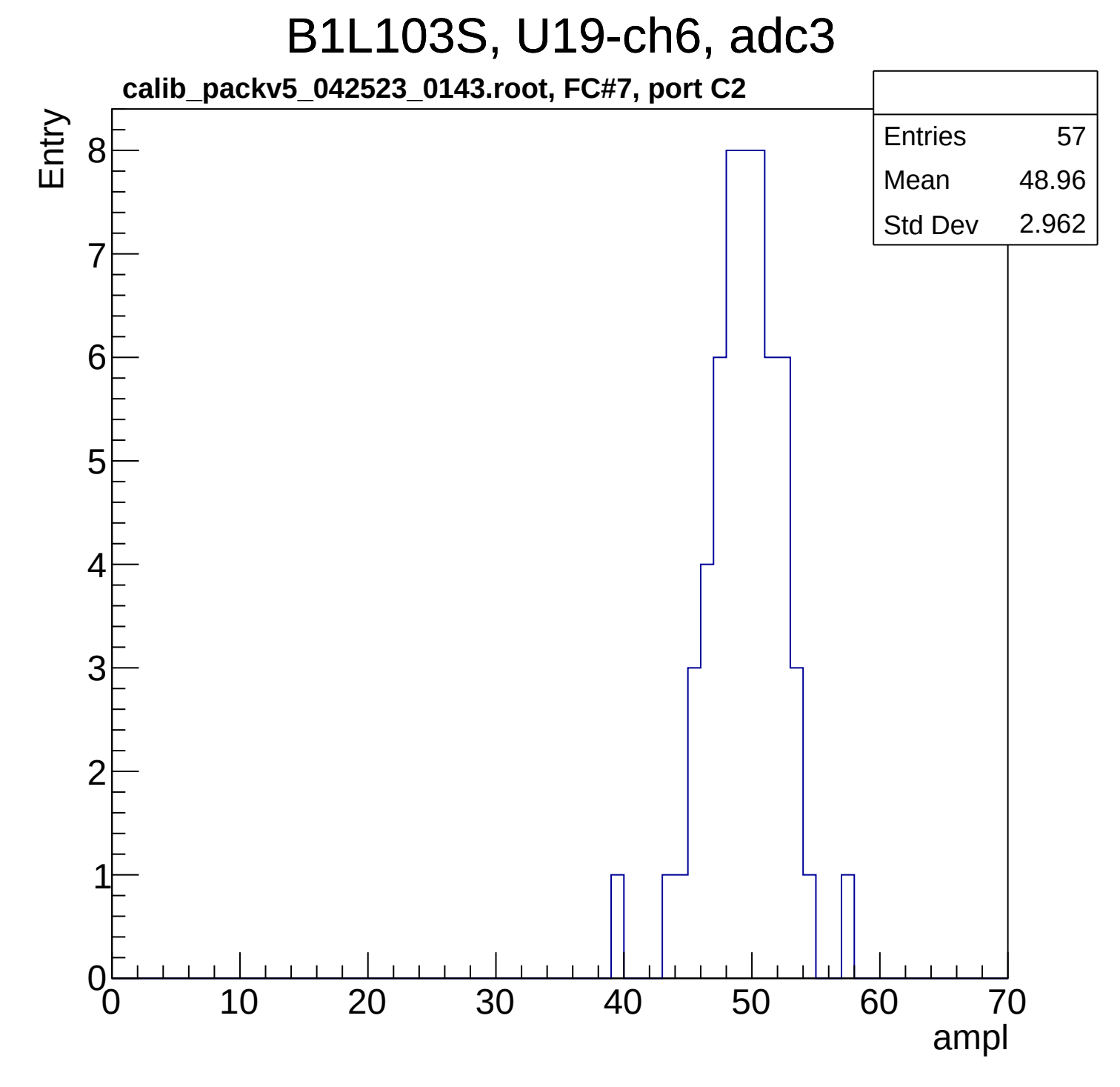
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch6, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 48.96
Std Dev 2.962
Entry
ampl
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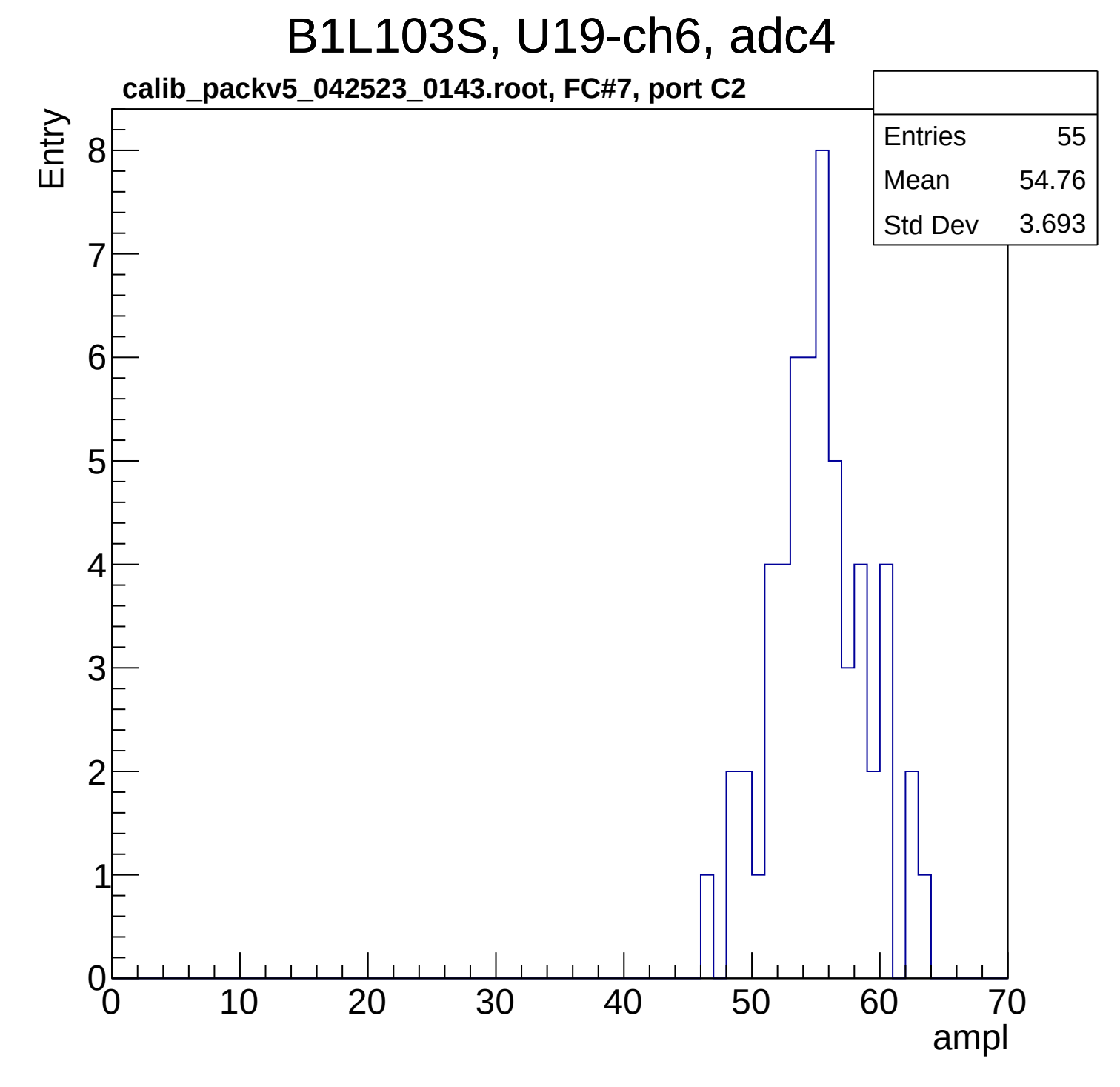
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch6, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.76
Std Dev 3.693
Entry
ampl
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2
3
4
5
6
7
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0
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20
30
40
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60
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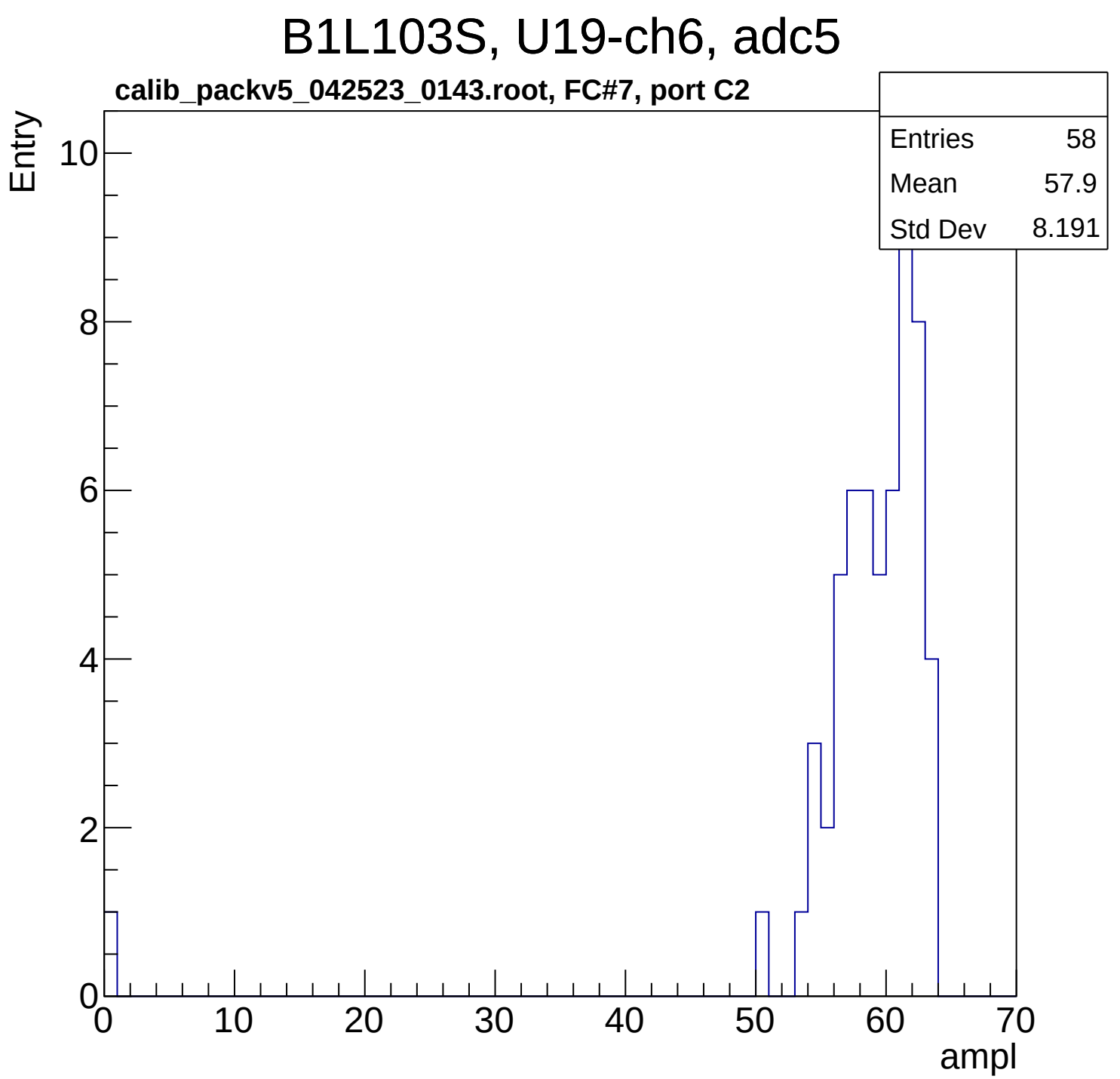
B1L103S, U19-ch6, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.9
Std Dev 8.191
Entry
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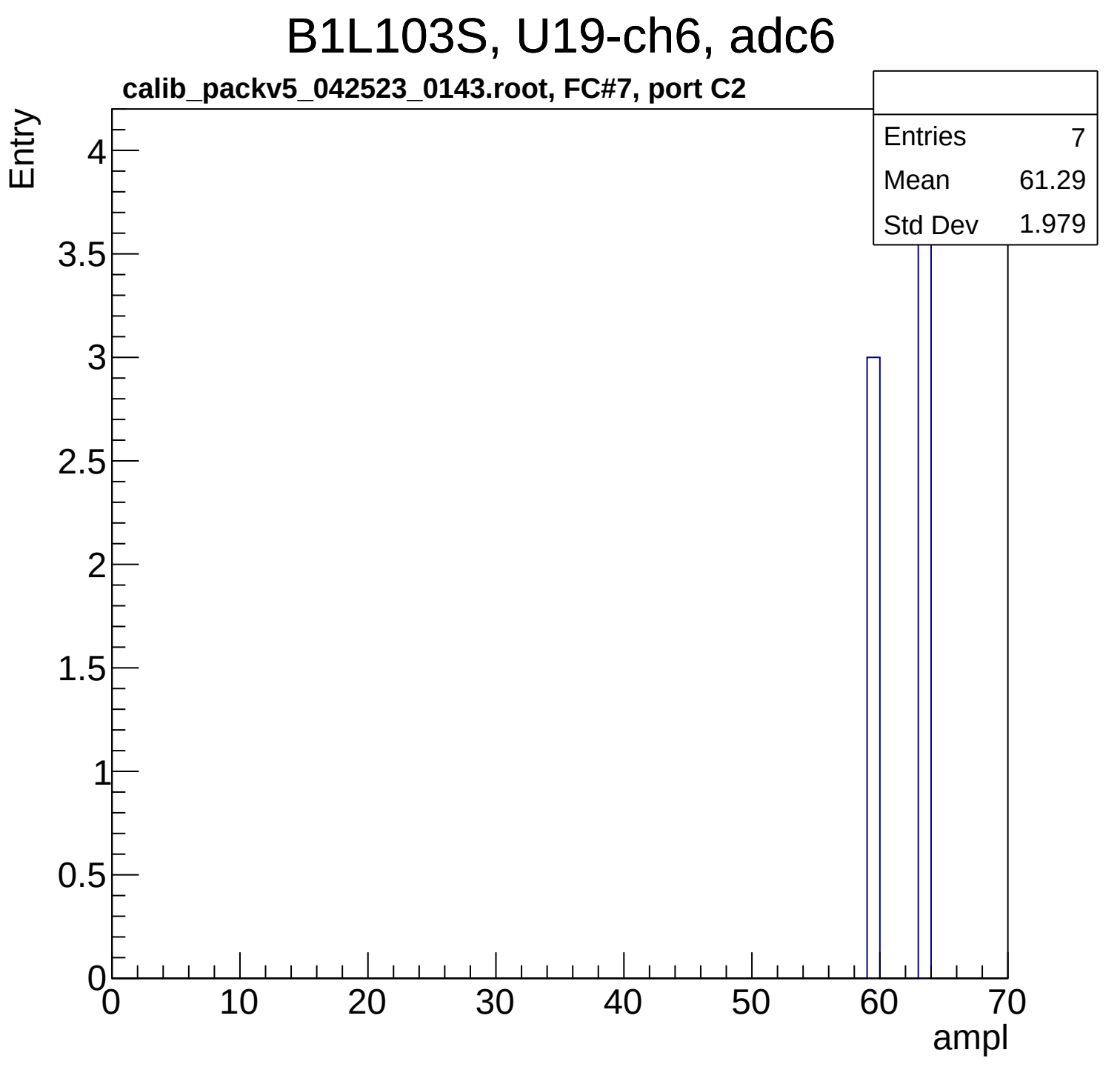
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch6, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.29
Std Dev 1.979
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
30
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50
60
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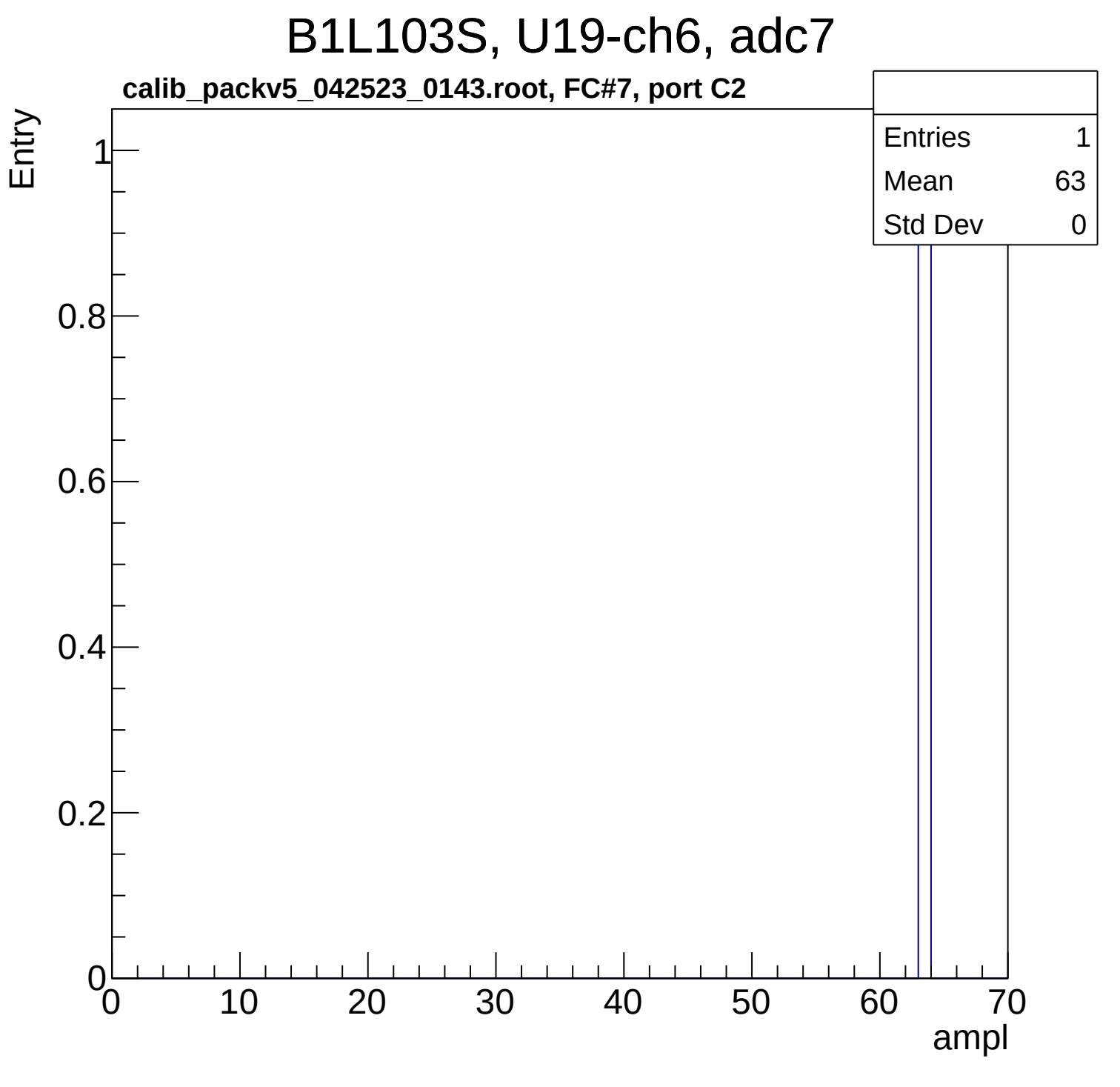
B1L103S, U19-ch6, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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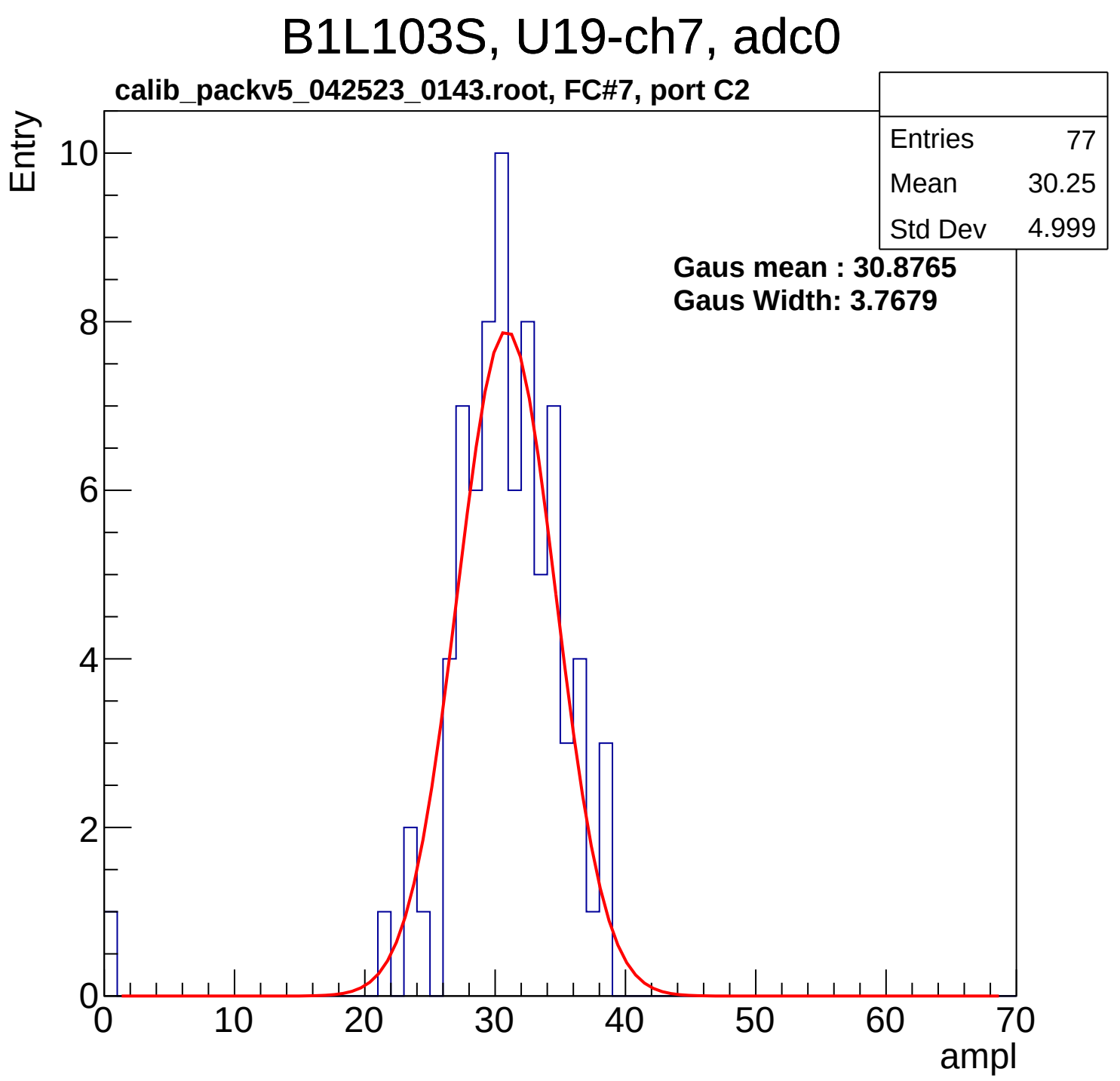
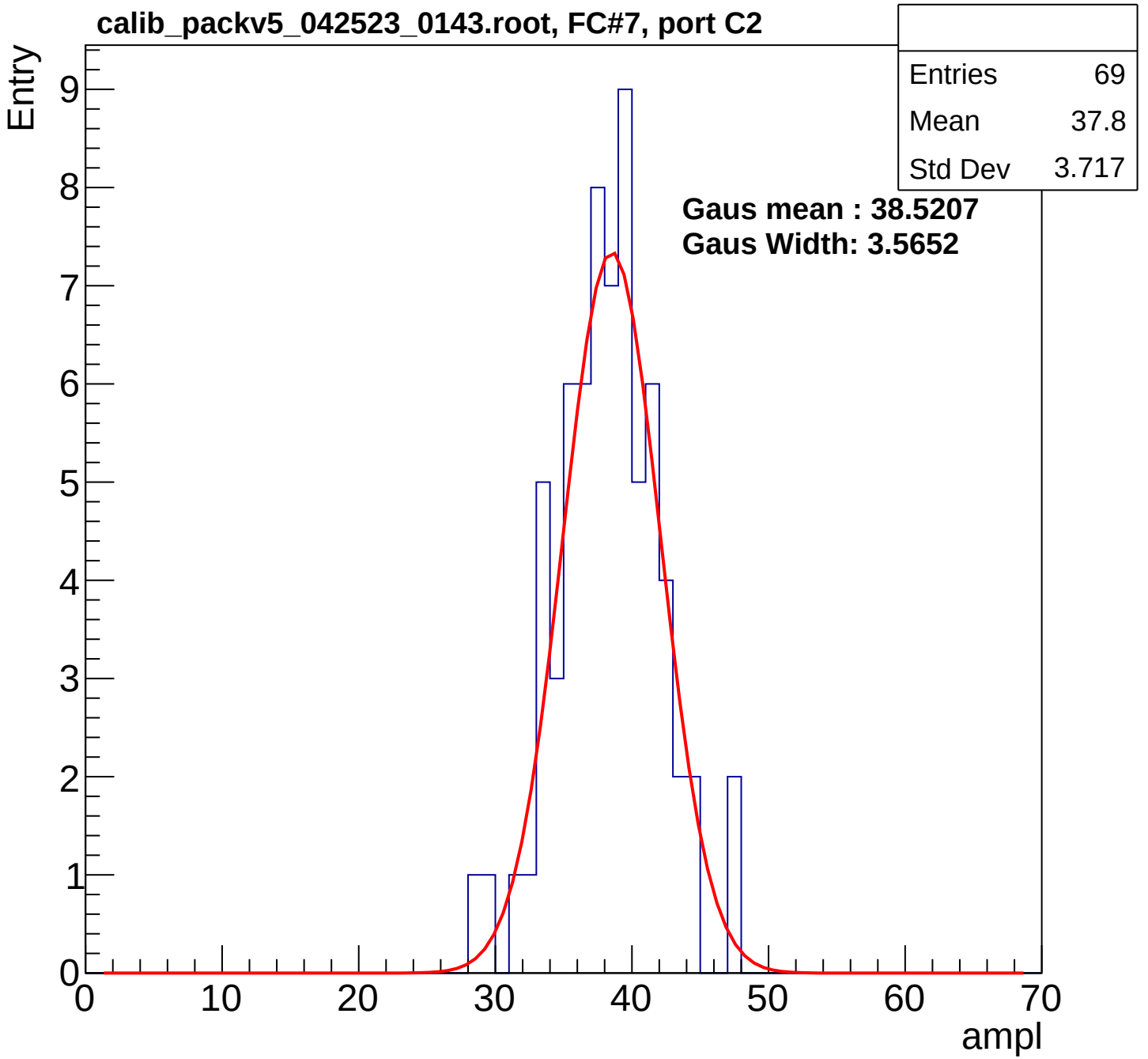
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch7, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.25
Std Dev 4.999
Gaus mean : 30.8765
Gaus Width: 3.7679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch7, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 37.8
Std Dev 3.717
Gaus mean : 38.5207
Gaus Width: 3.5652
Entry
ampl
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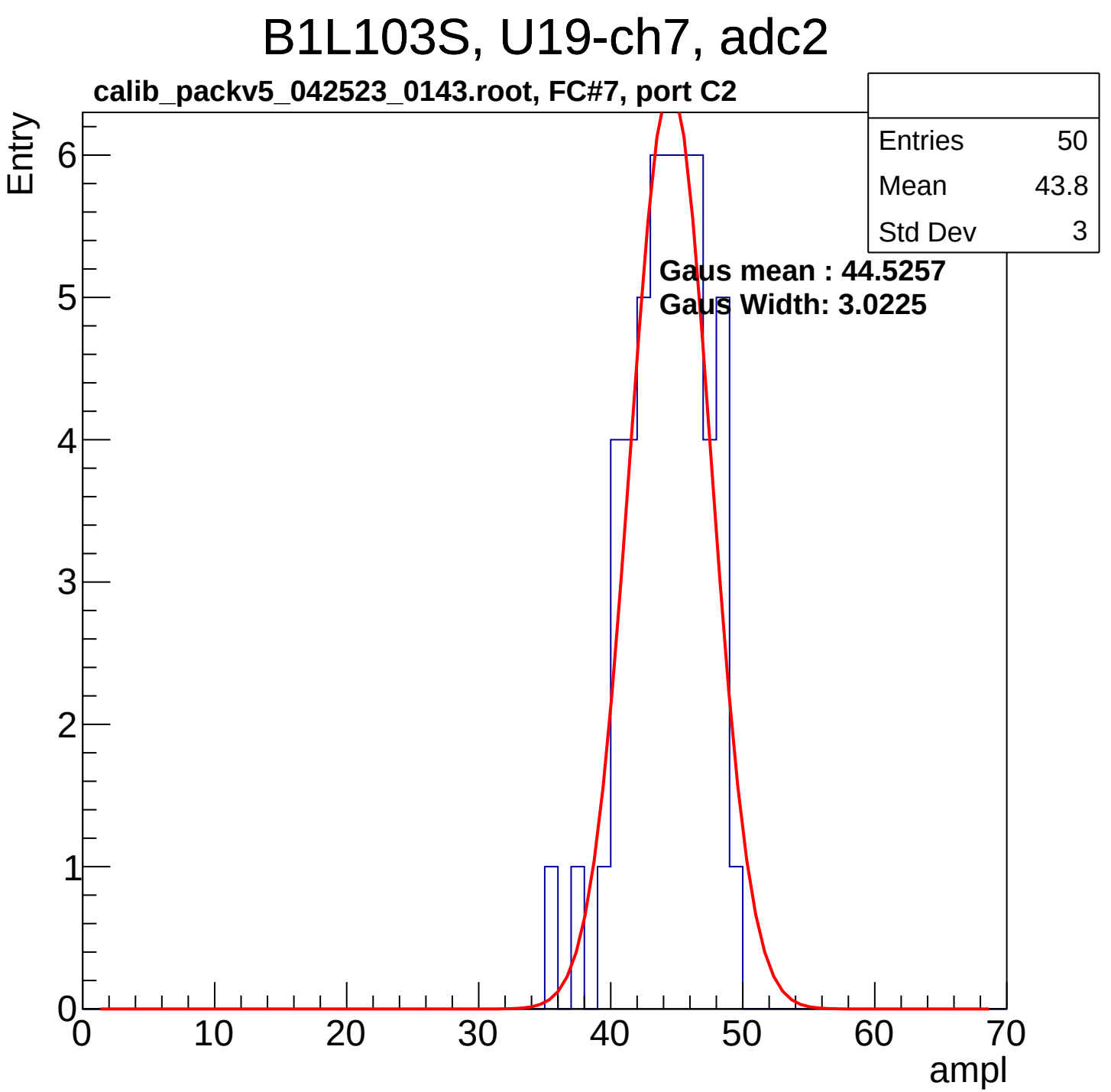
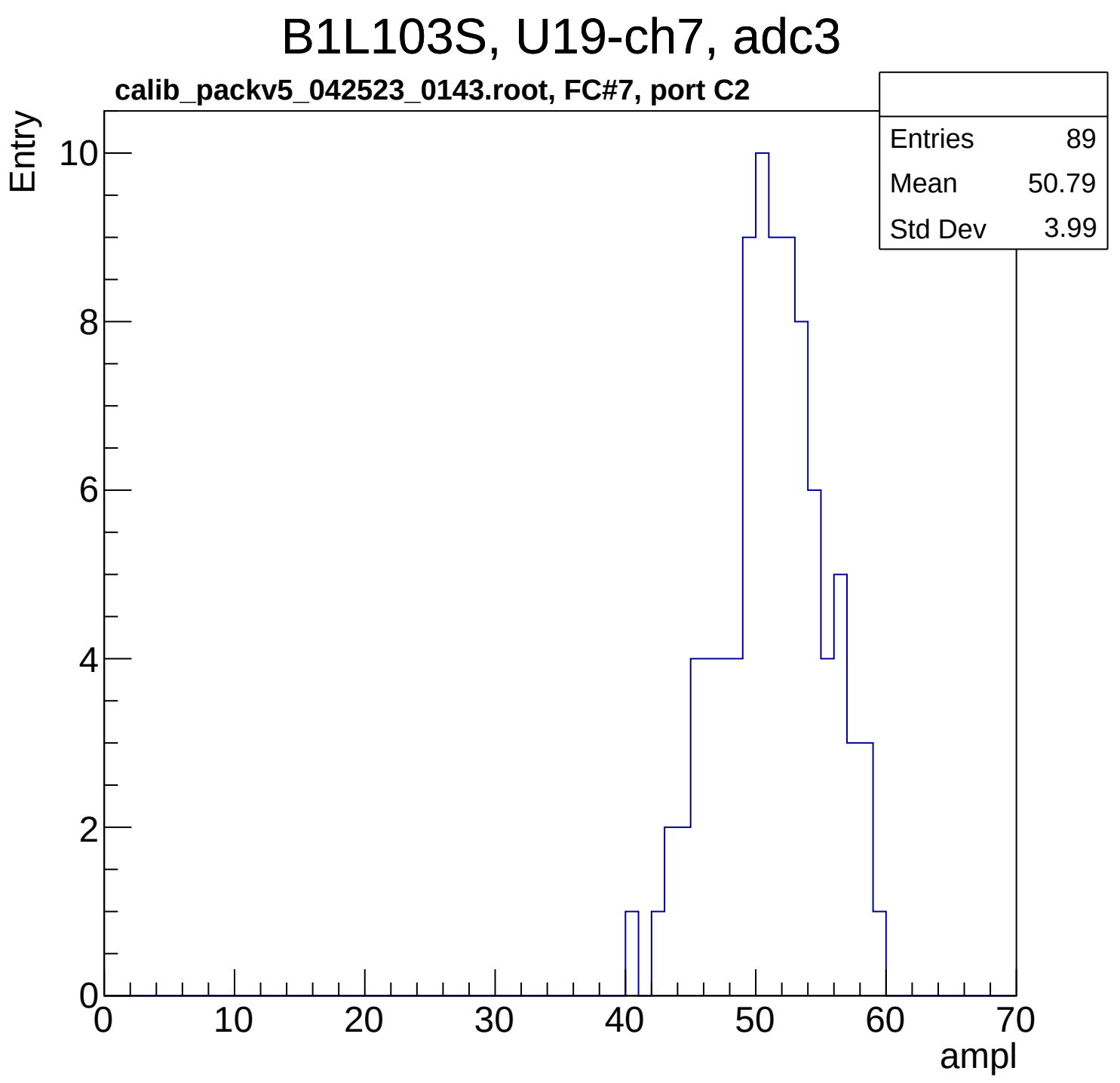
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch7, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 43.8
Std Dev 3
Gaus mean : 44.5257
Gaus Width: 3.0225
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch7, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 50.79
Std Dev 3.99
Entry
ampl
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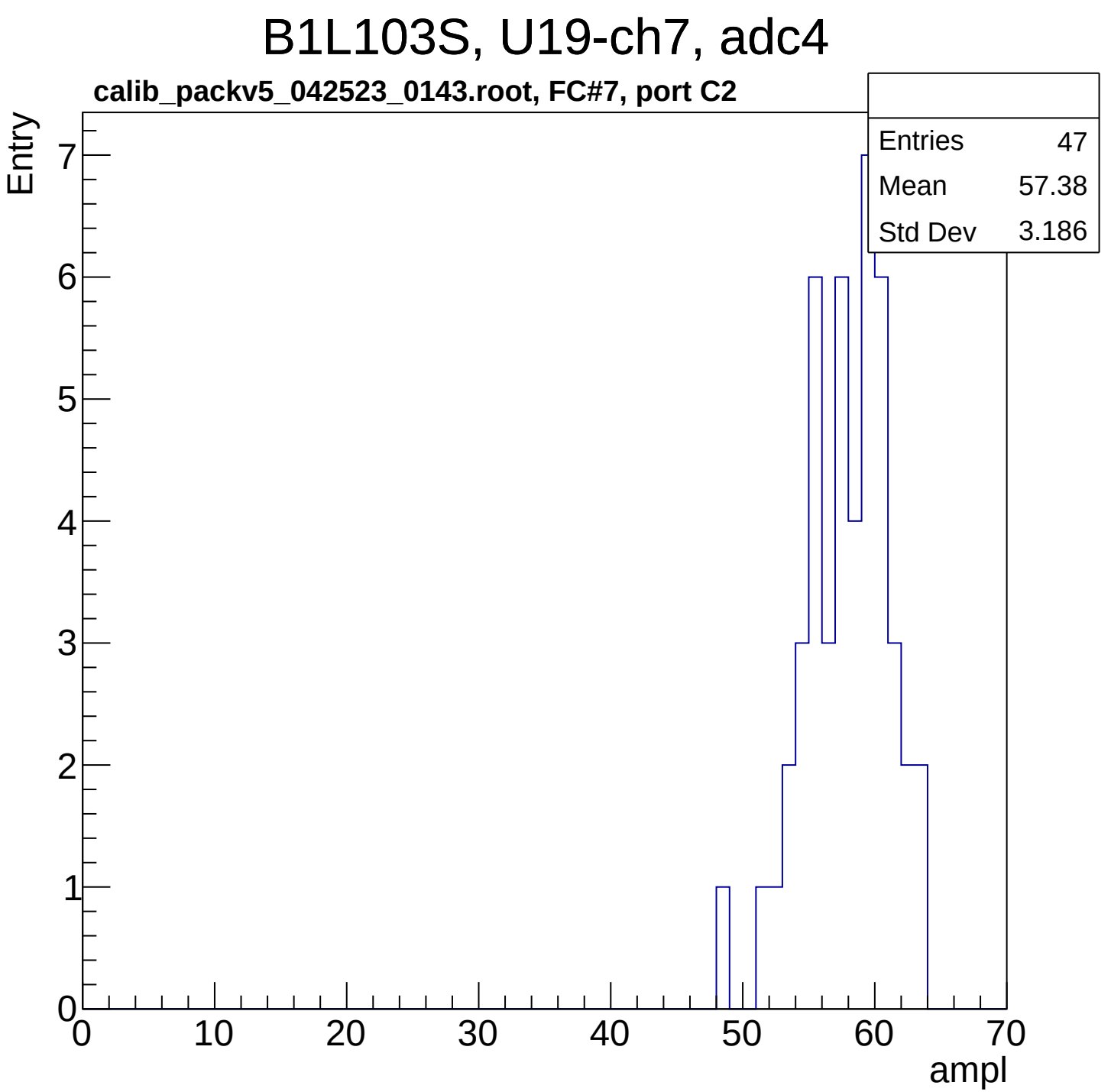
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch7, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.38
Std Dev 3.186
Entry
ampl
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3
4
5
6
7
0
10
20
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40
50
60
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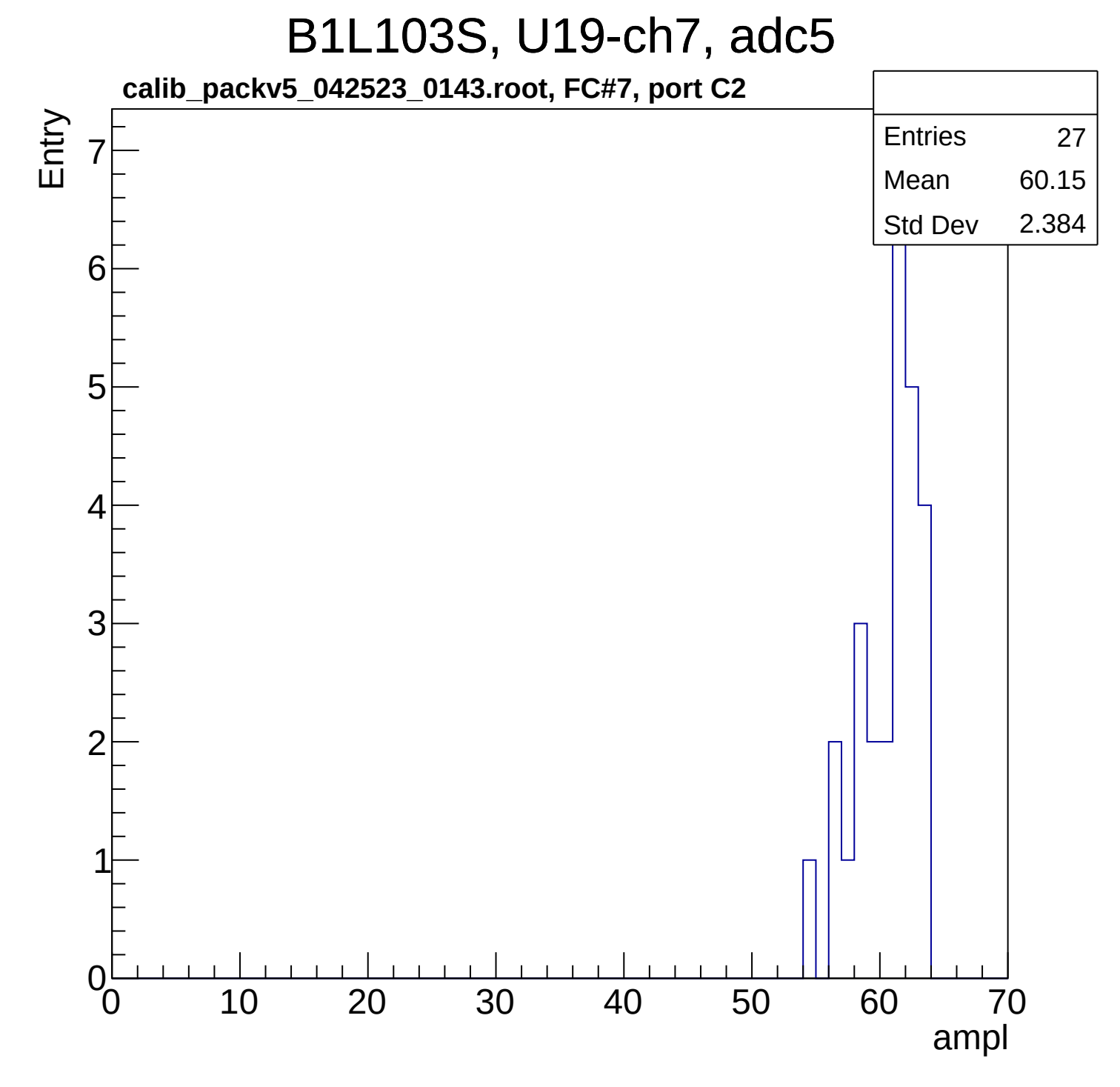
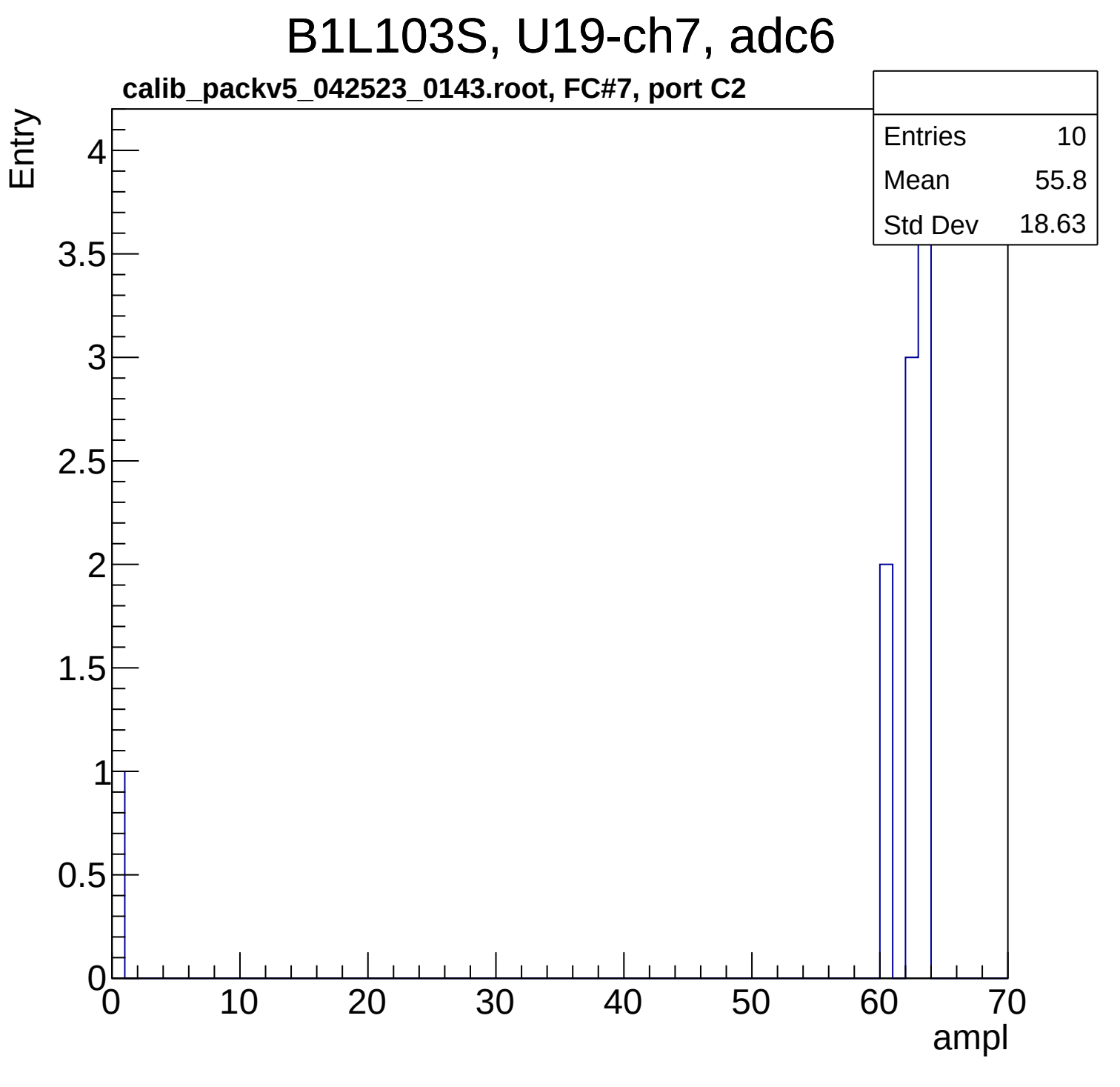

B1L103S, U19-ch7, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 27
Mean 60.15
Std Dev 2.384
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch7, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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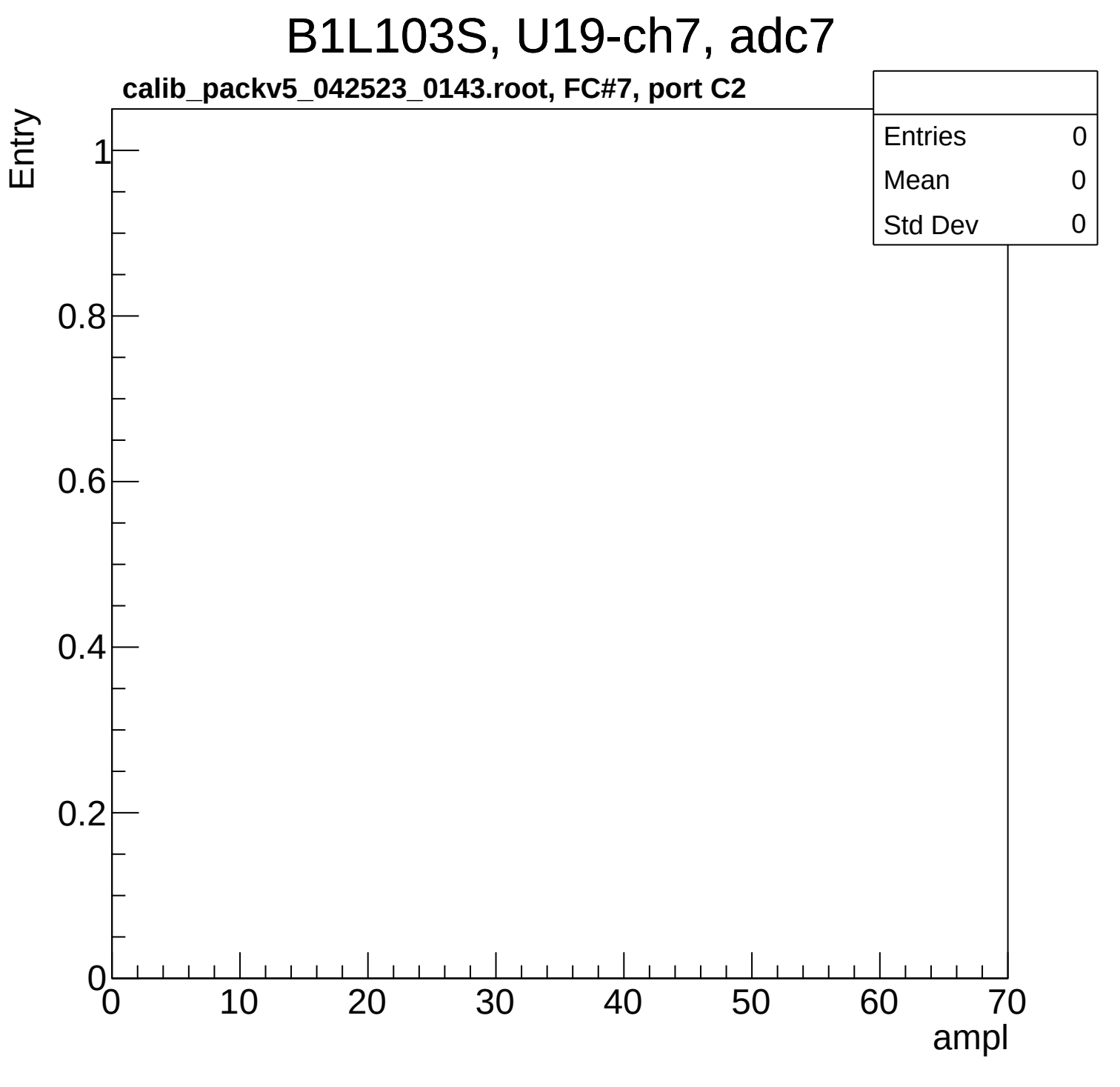
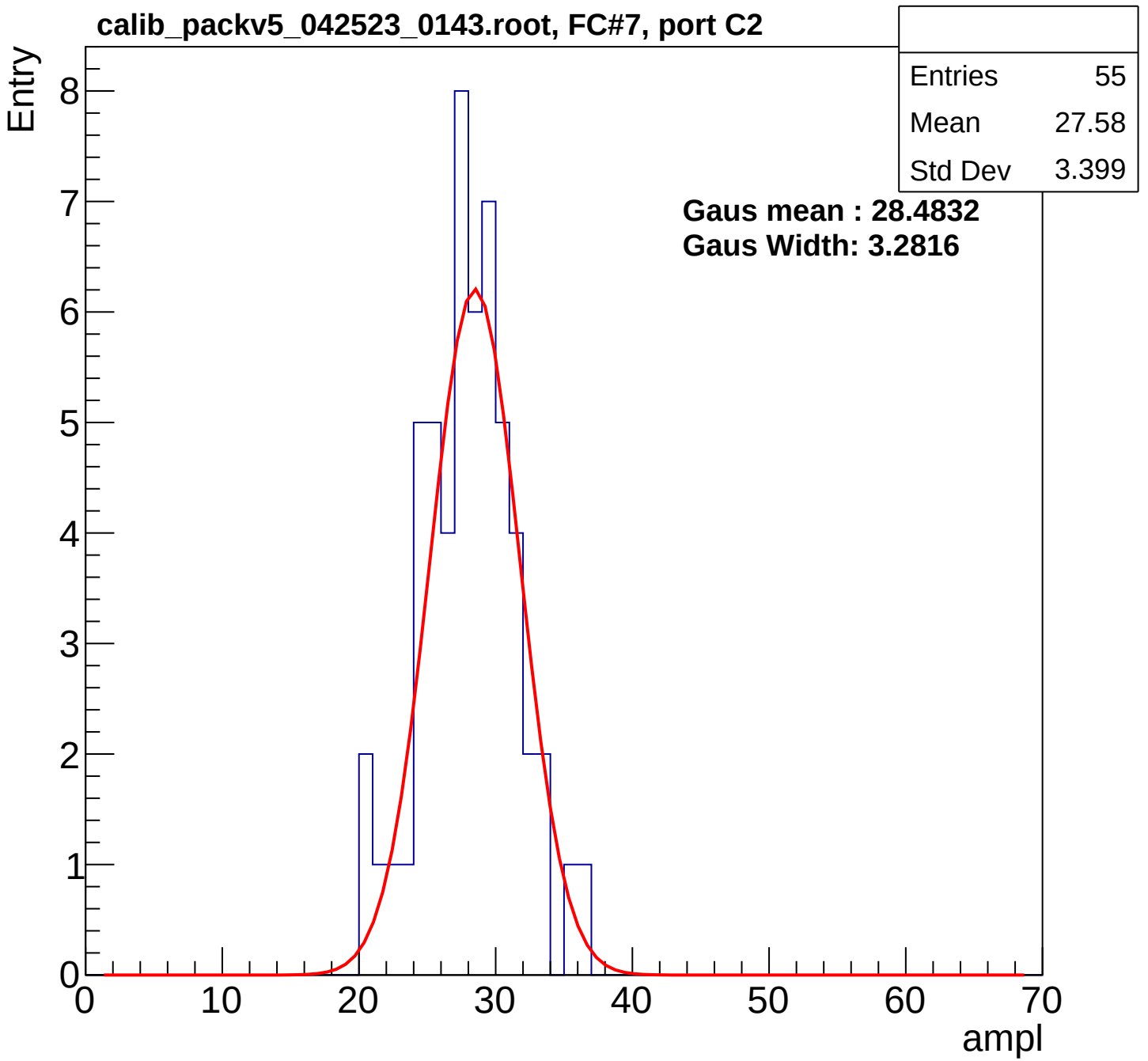

B1L103S, U19-ch7, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch8, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 27.58
Std Dev 3.399
Gaus mean : 28.4832
Gaus Width: 3.2816
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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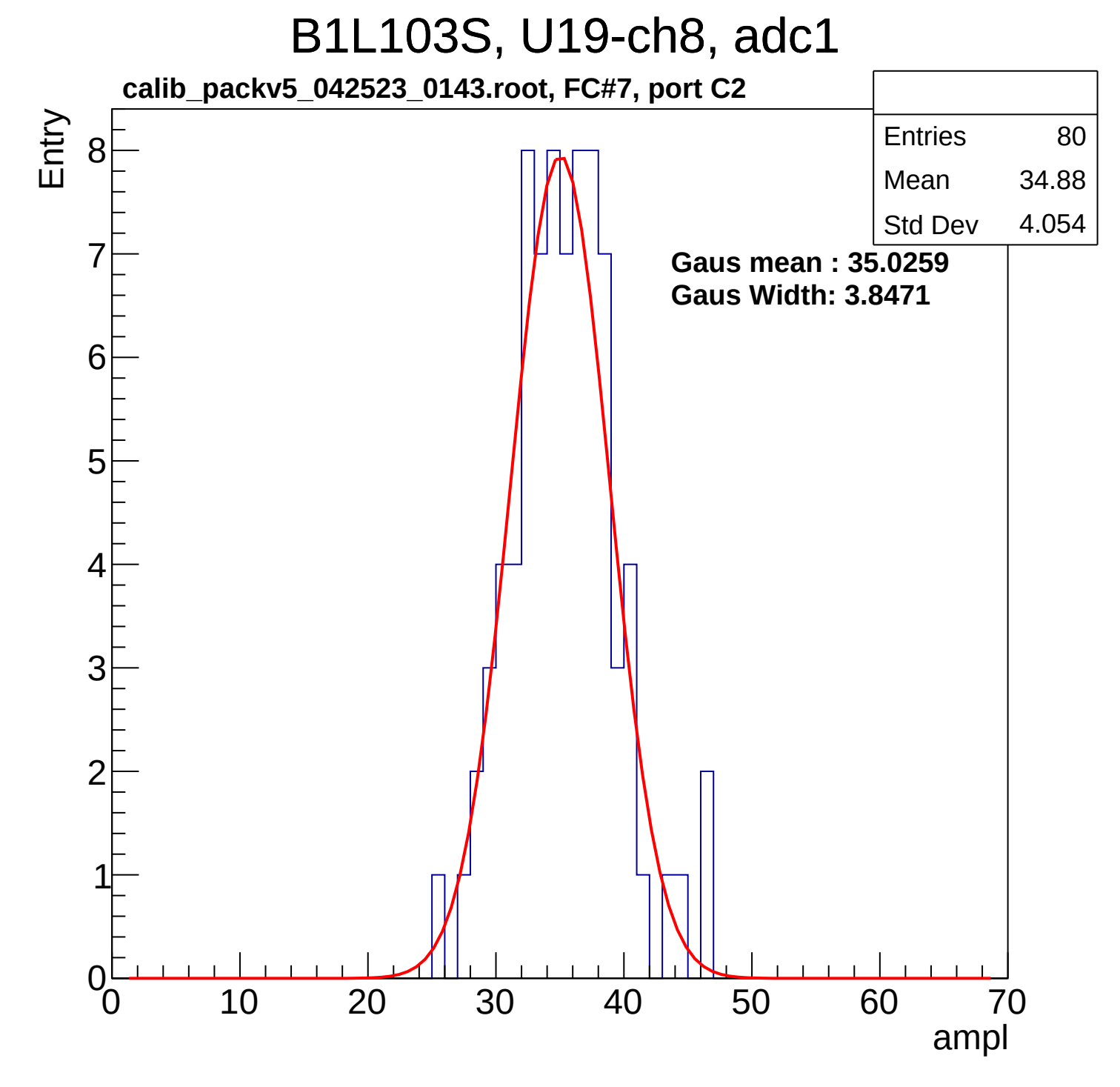
B1L103S, U19-ch8, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 34.88
Std Dev 4.054
Gaus mean : 35.0259
Gaus Width: 3.8471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
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60
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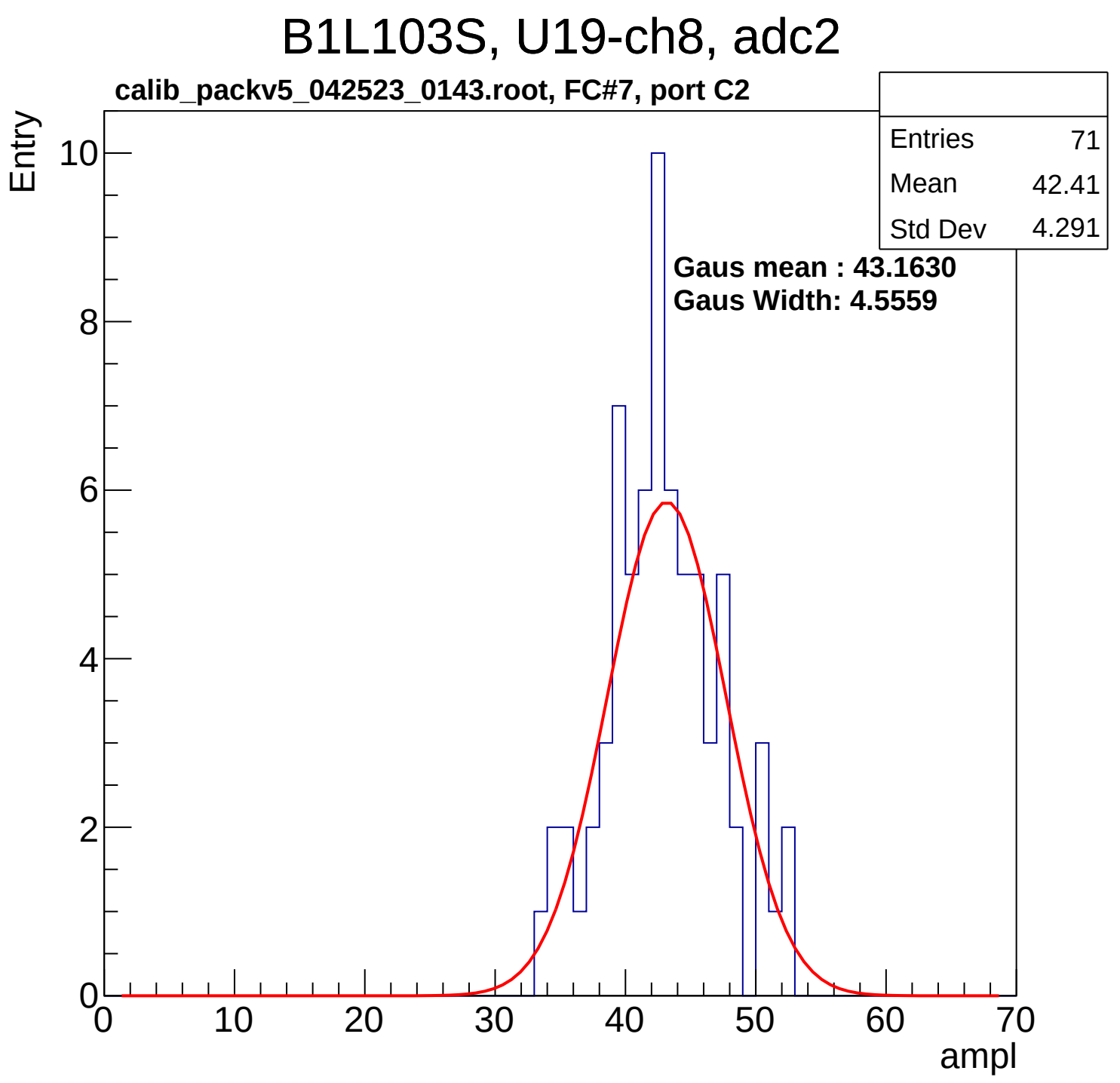
B1L103S, U19-ch8, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.41
Std Dev 4.291
Gaus mean : 43.1630
Gaus Width: 4.5559
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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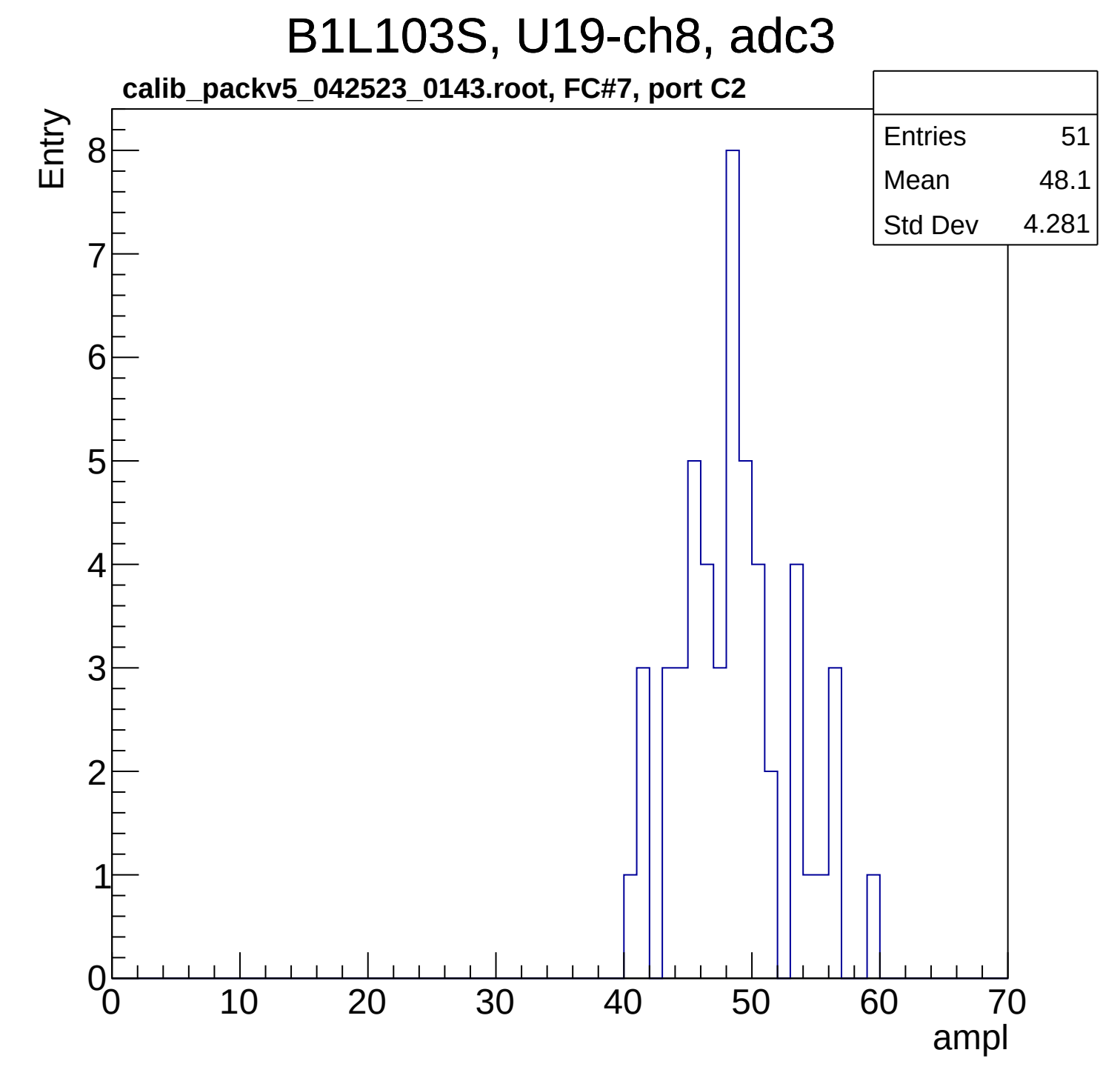
B1L103S, U19-ch8, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 48.1
Std Dev 4.281
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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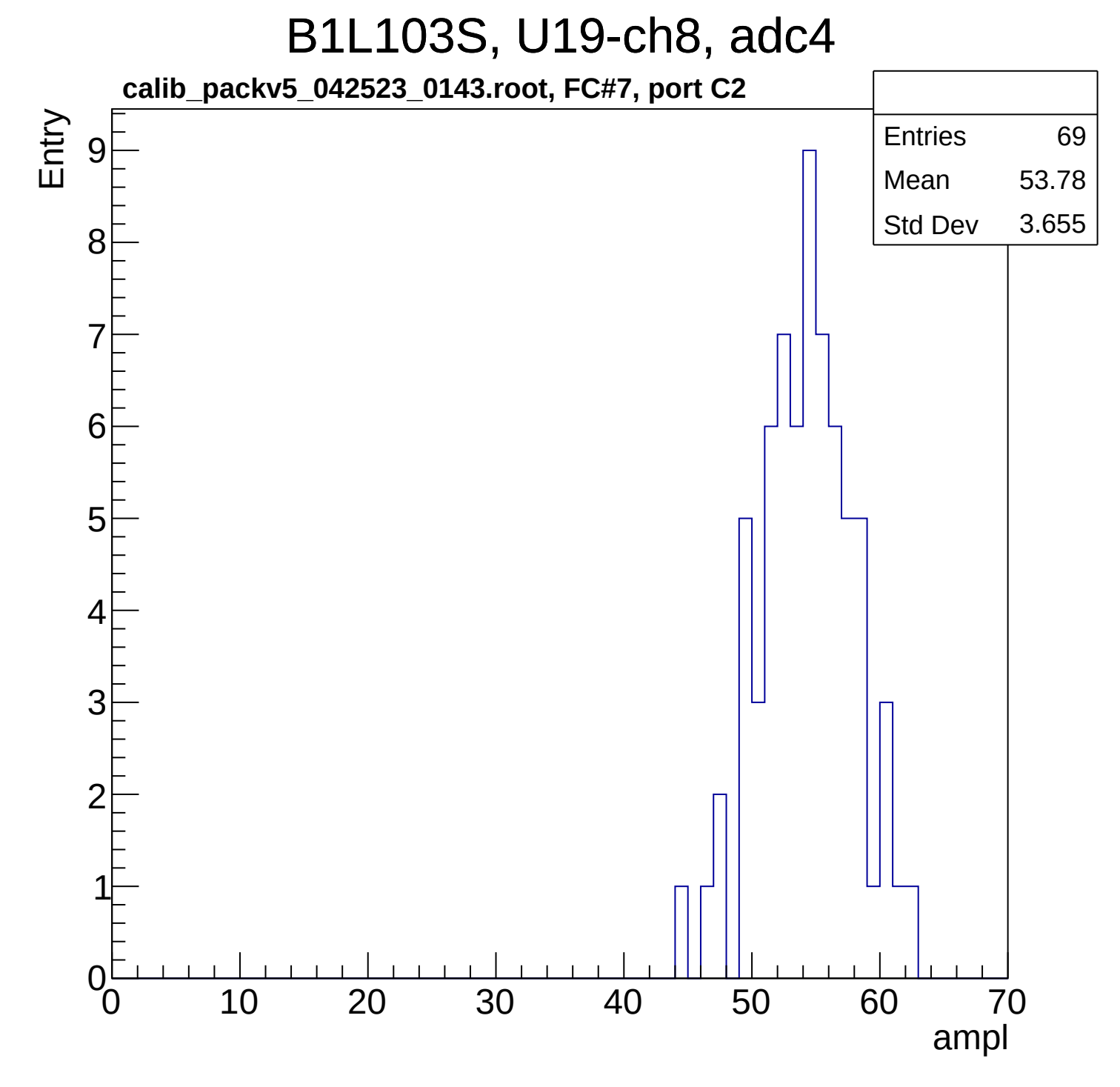
B1L103S, U19-ch8, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 53.78
Std Dev 3.655
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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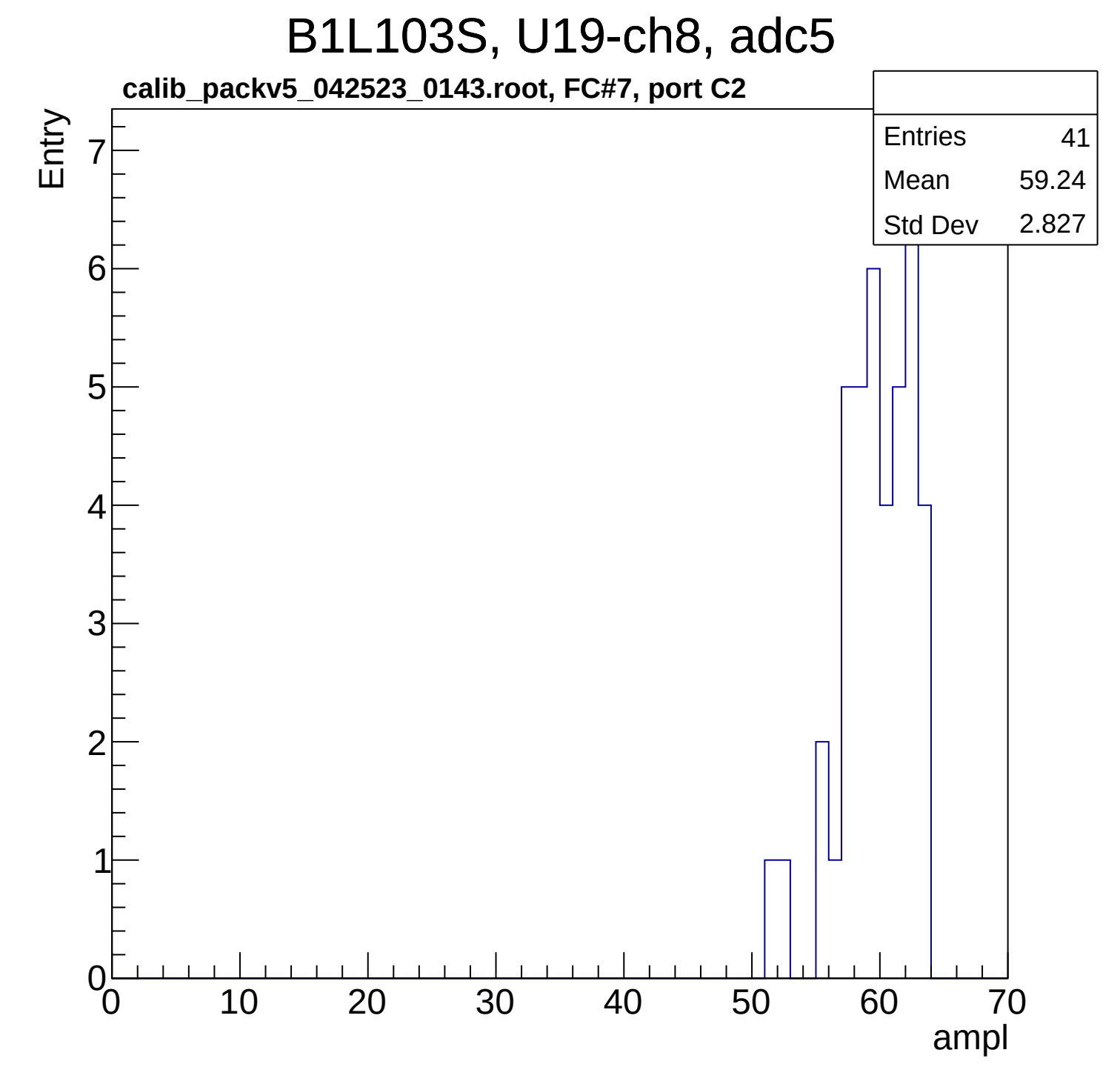
B1L103S, U19-ch8, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.24
Std Dev 2.827
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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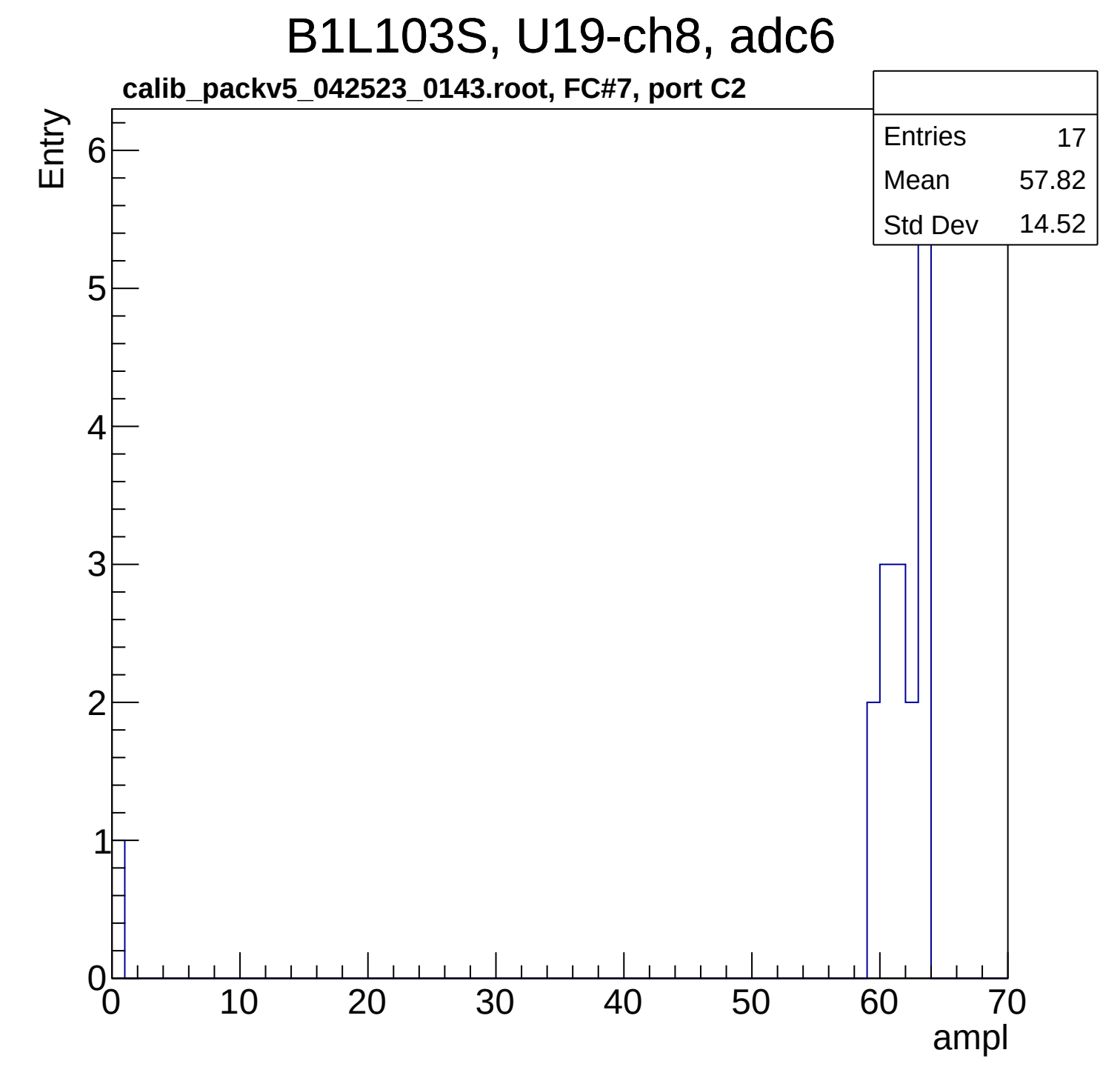
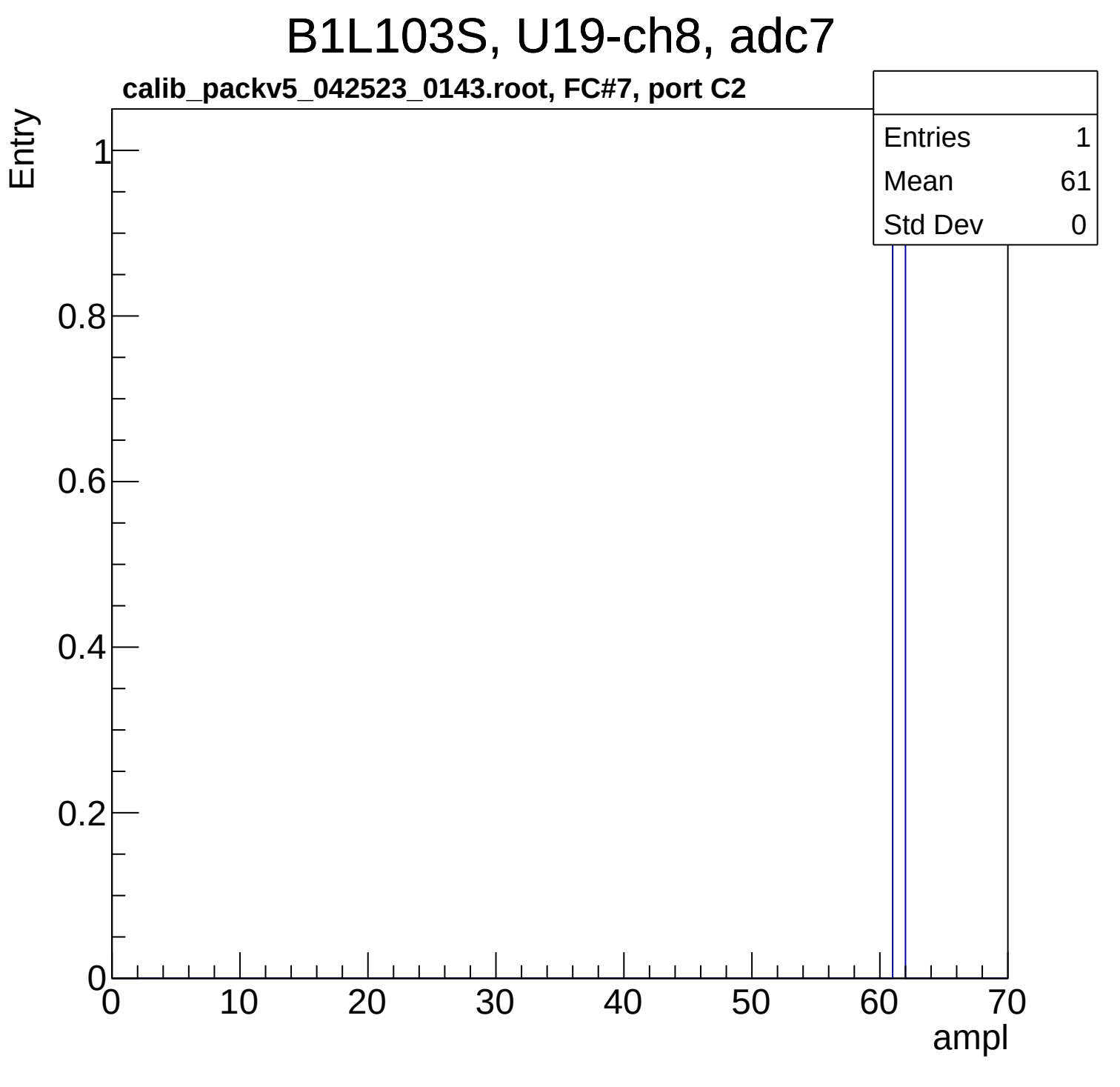
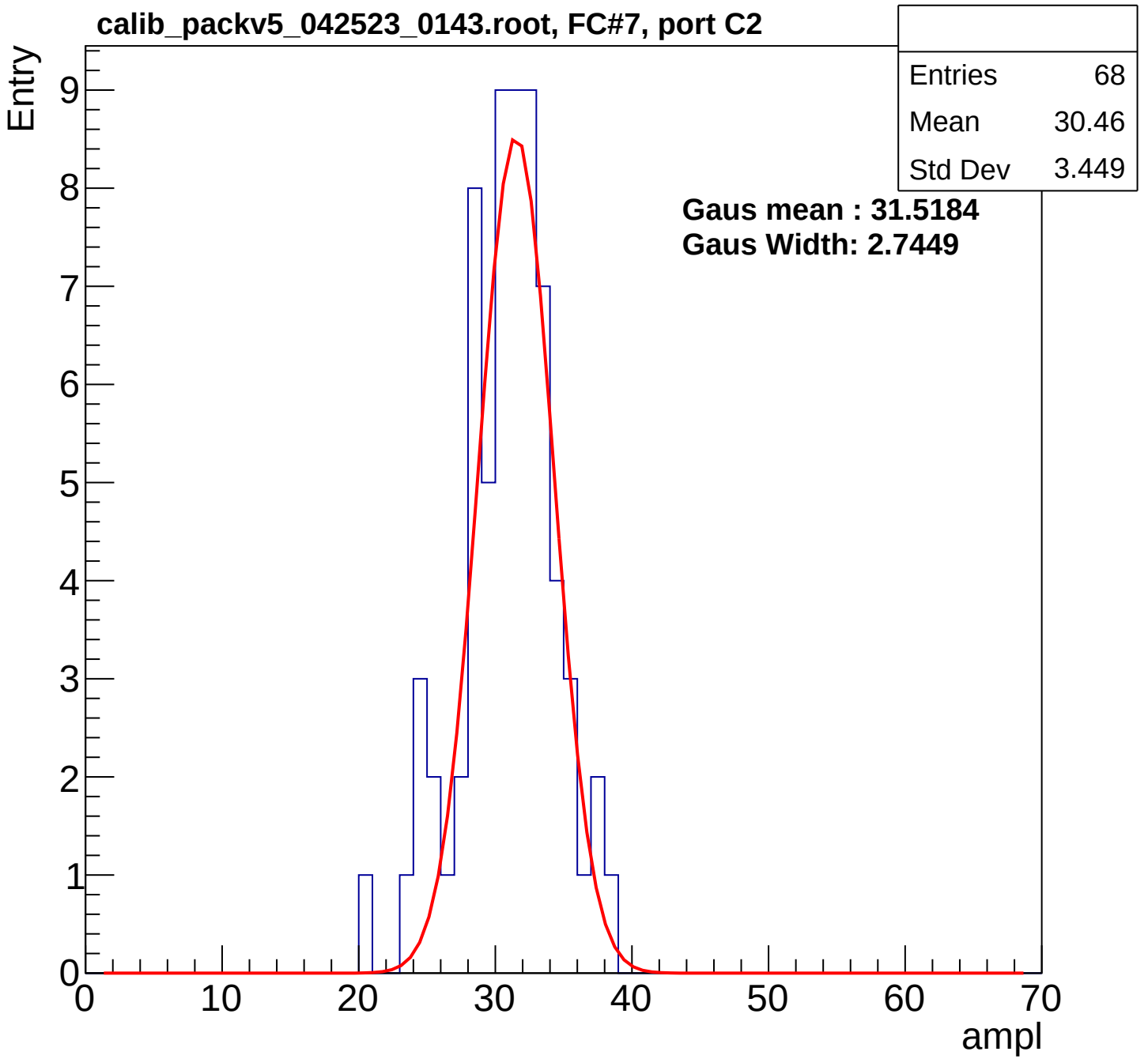

B1L103S, U19-ch8, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 57.82
Std Dev 14.52
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch8, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch9, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.46
Std Dev 3.449
Gaus mean : 31.5184
Gaus Width: 2.7449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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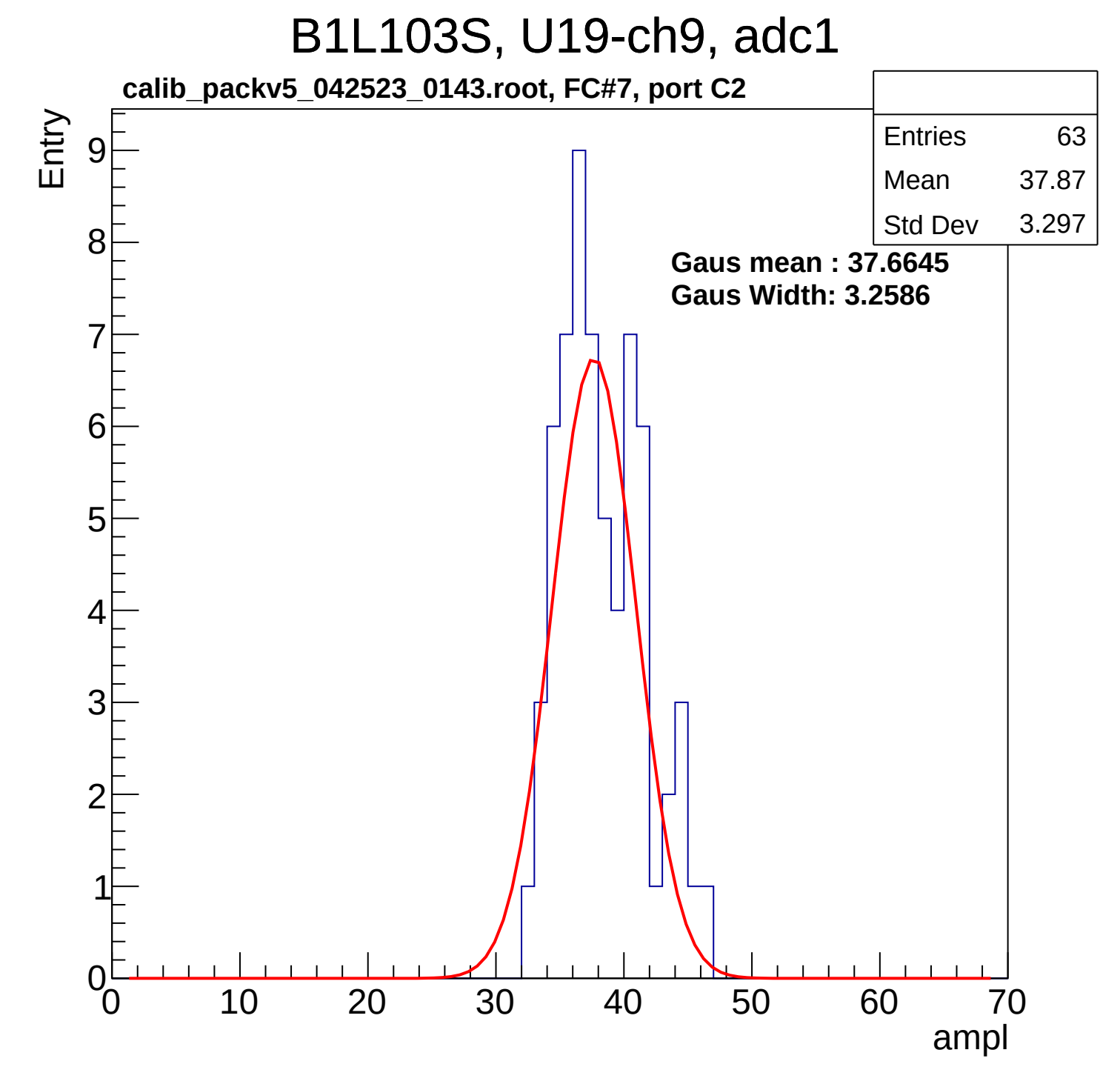
B1L103S, U19-ch9, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 37.87
Std Dev 3.297
Gaus mean : 37.6645
Gaus Width: 3.2586
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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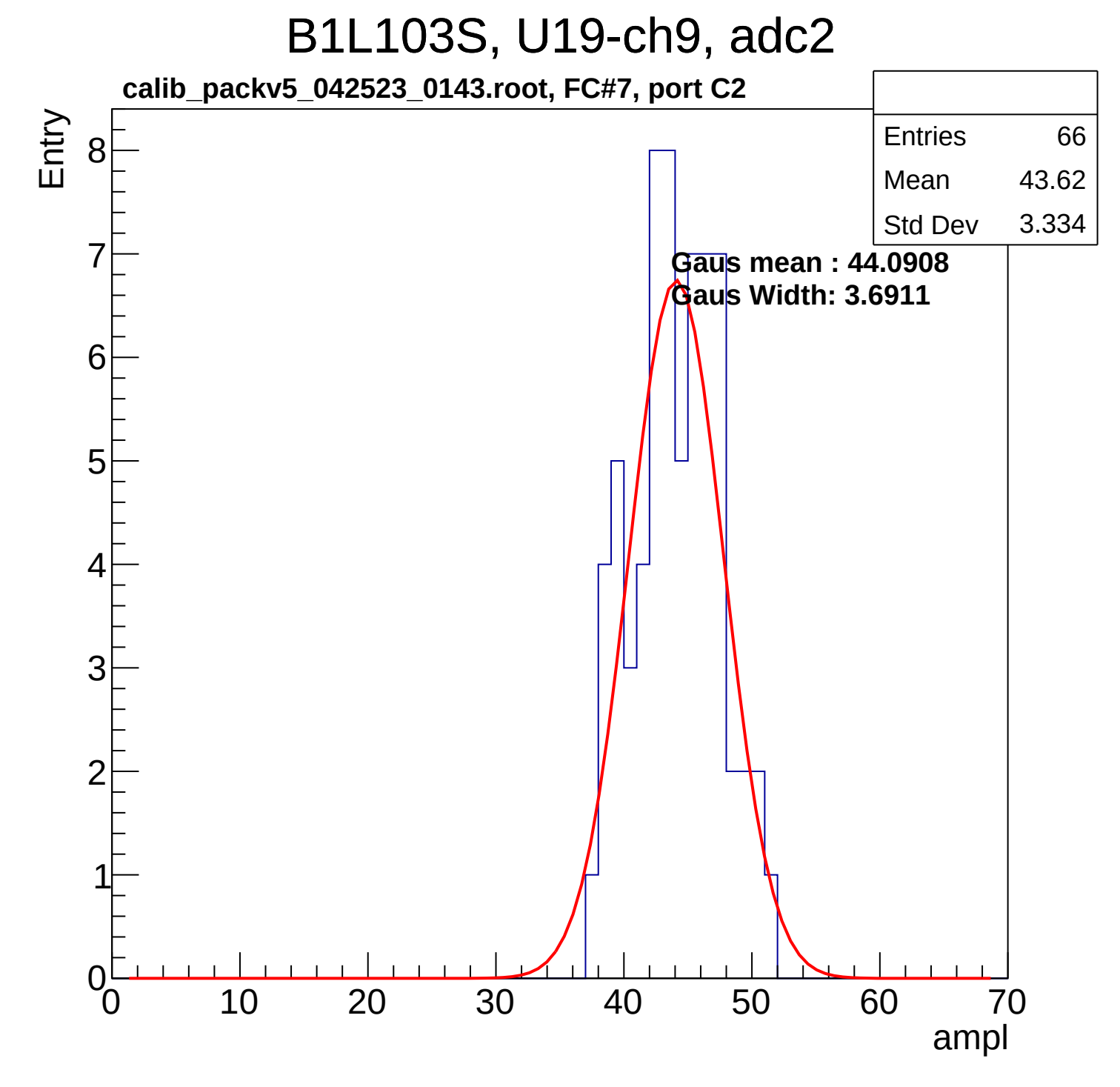
B1L103S, U19-ch9, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43.62
Std Dev 3.334
Gaus mean : 44.0908
Gaus Width: 3.6911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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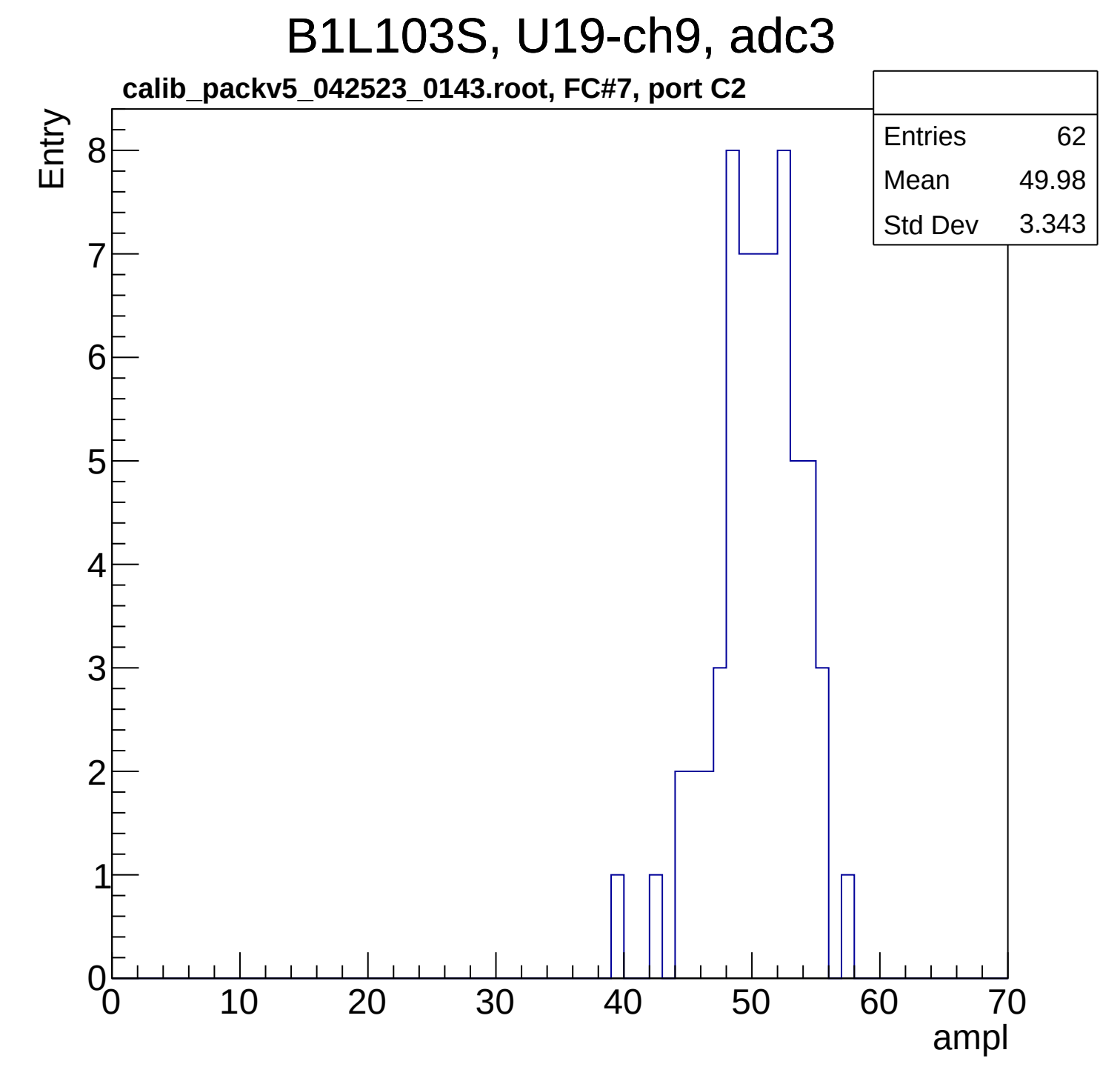
B1L103S, U19-ch9, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.98
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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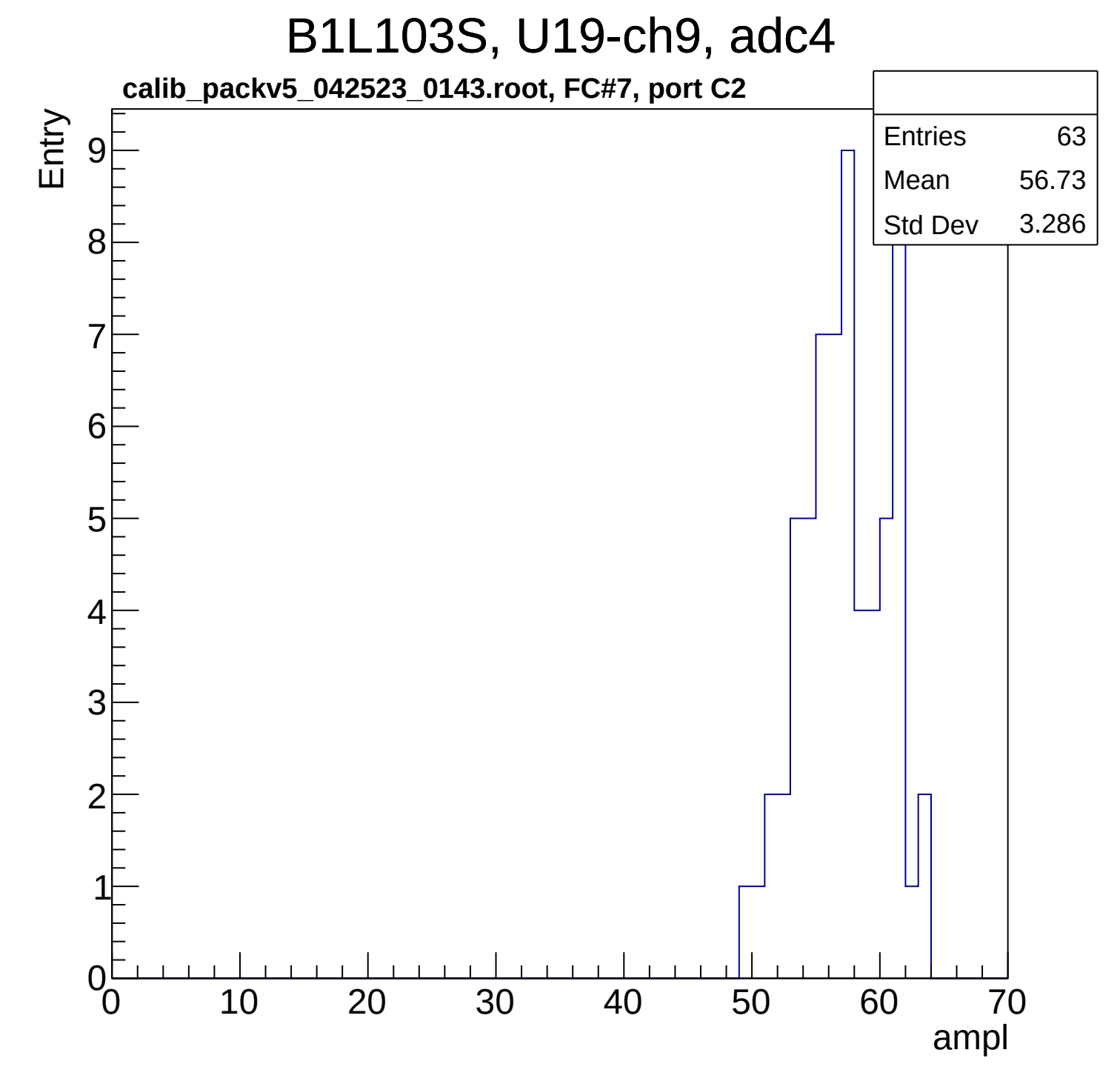
B1L103S, U19-ch9, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 56.73
Std Dev 3.286
Entry
ampl
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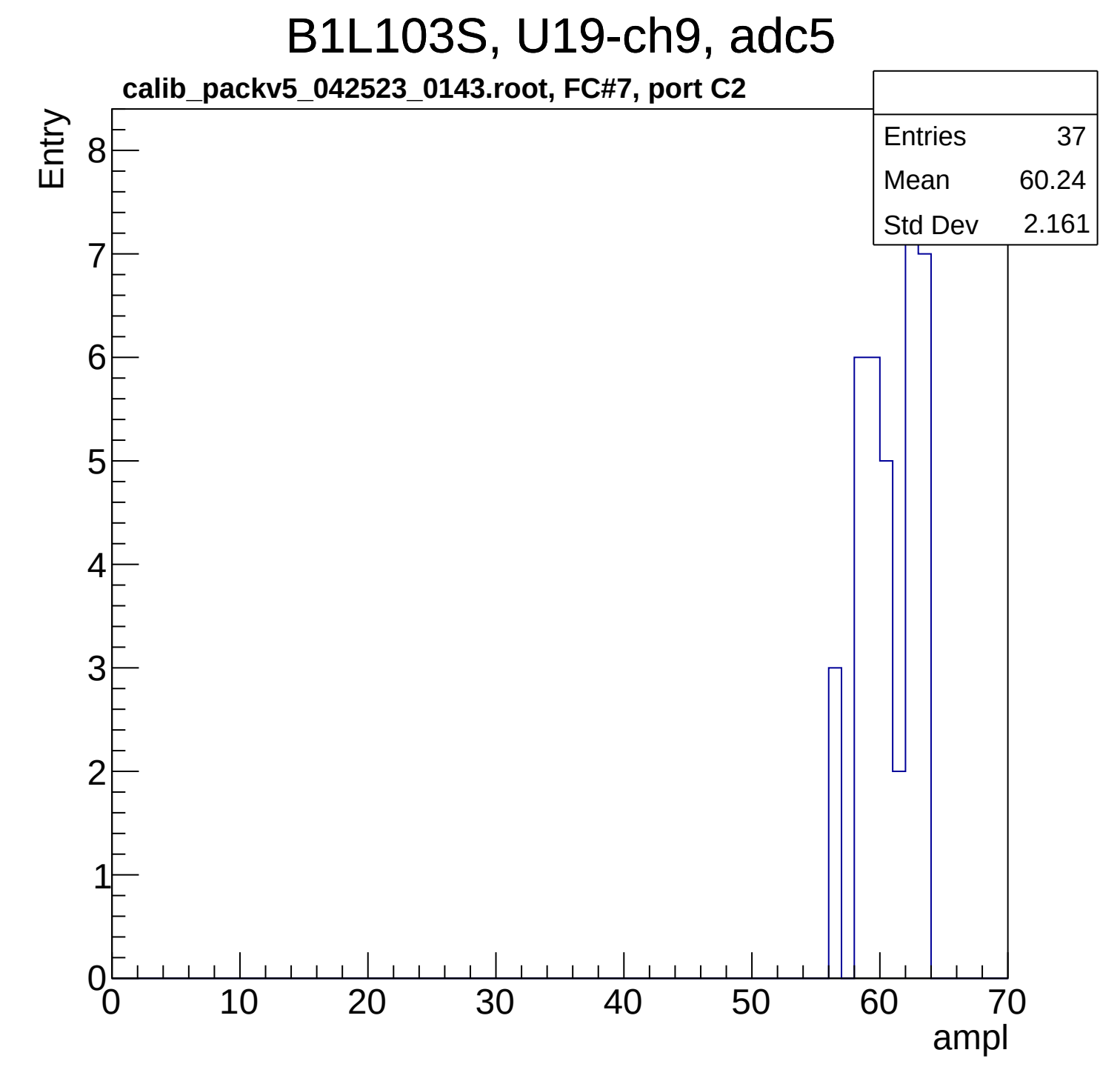
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch9, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.24
Std Dev 2.161
Entry
ampl
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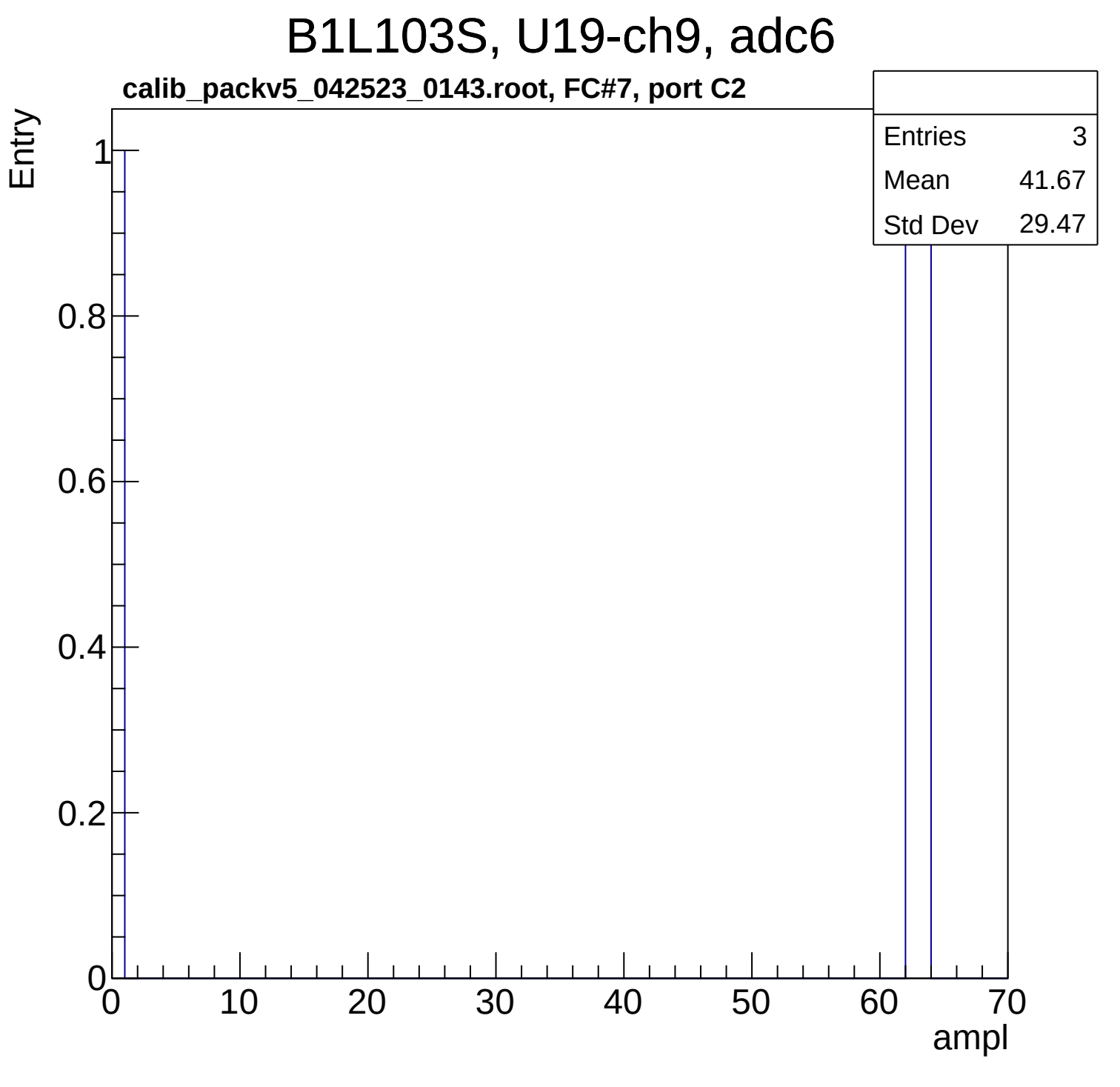
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch9, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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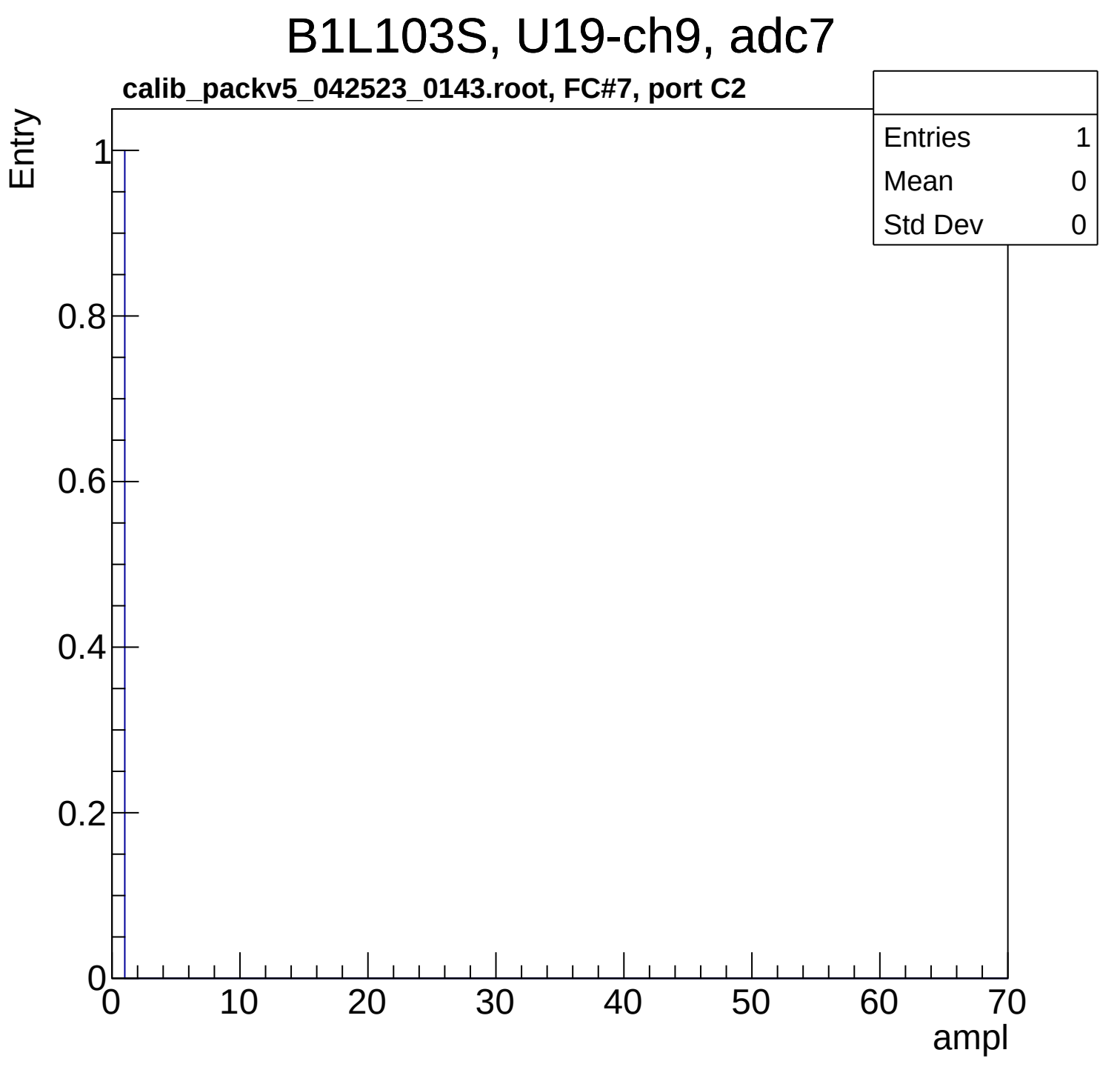
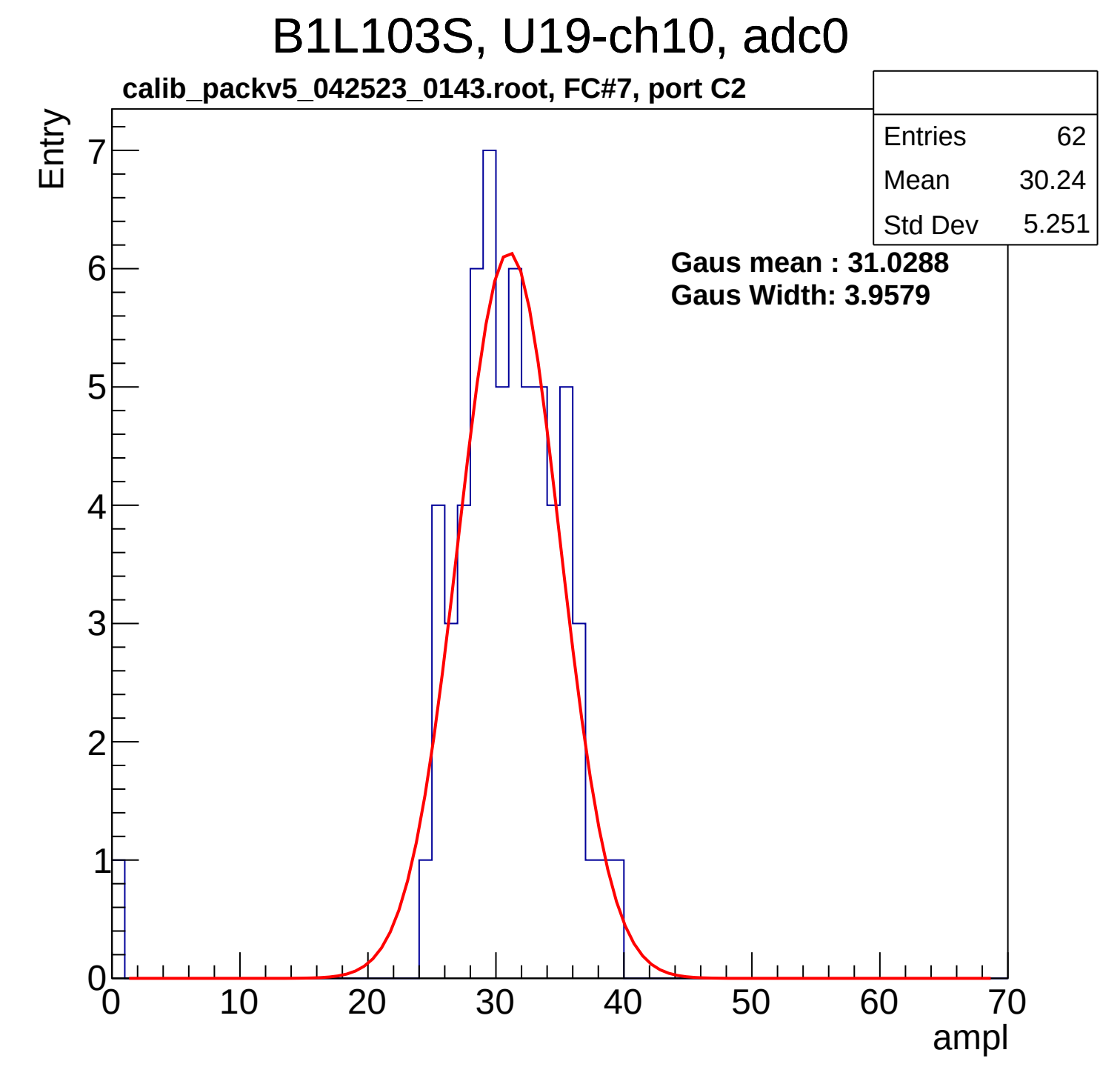
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch9, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch10, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 30.24
Std Dev 5.251
Gaus mean : 31.0288
Gaus Width: 3.9579
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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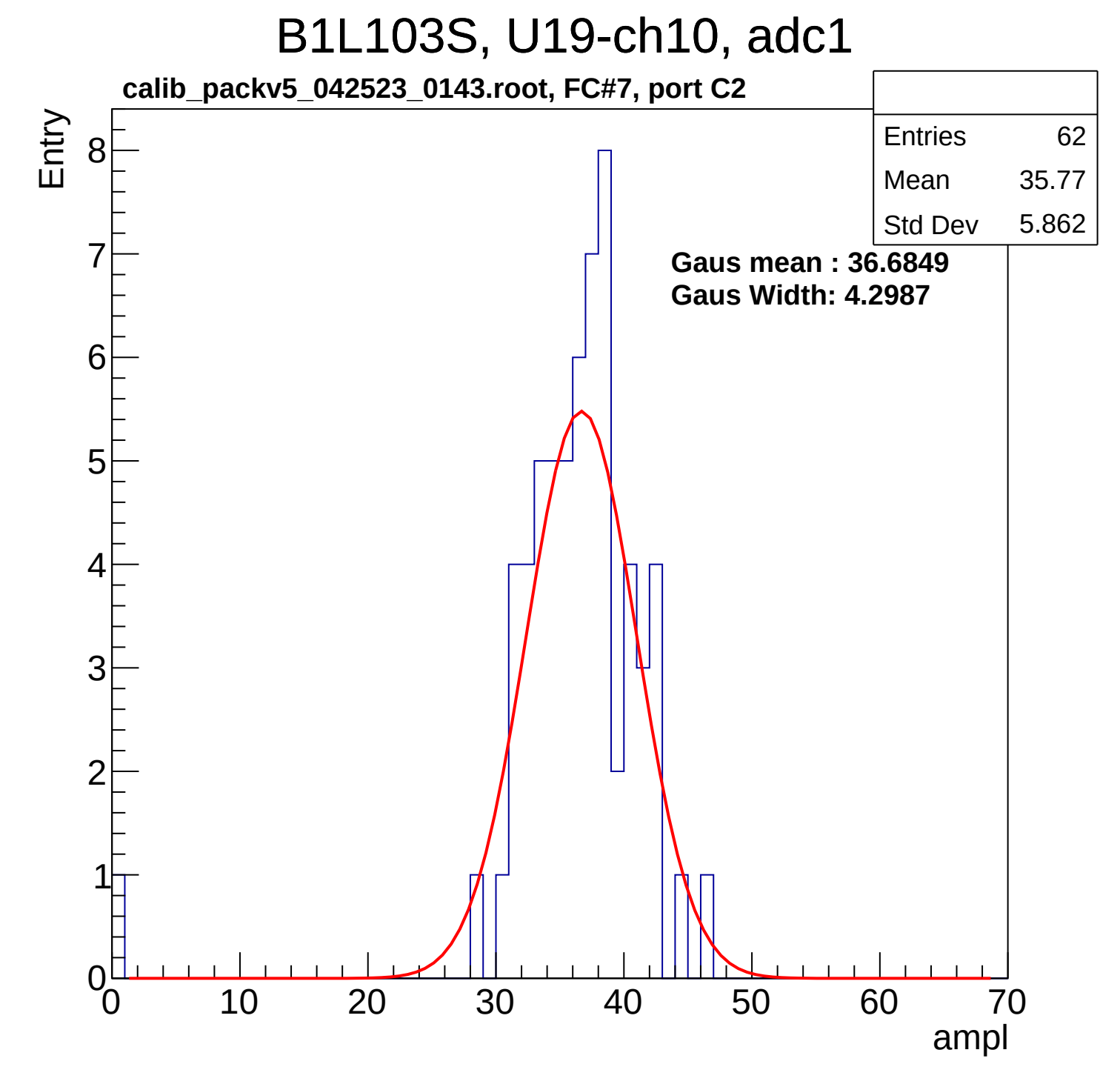
40
50
60
70


B1L103S, U19-ch10, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.77
Std Dev 5.862
Gaus mean : 36.6849
Gaus Width: 4.2987
Entry
ampl
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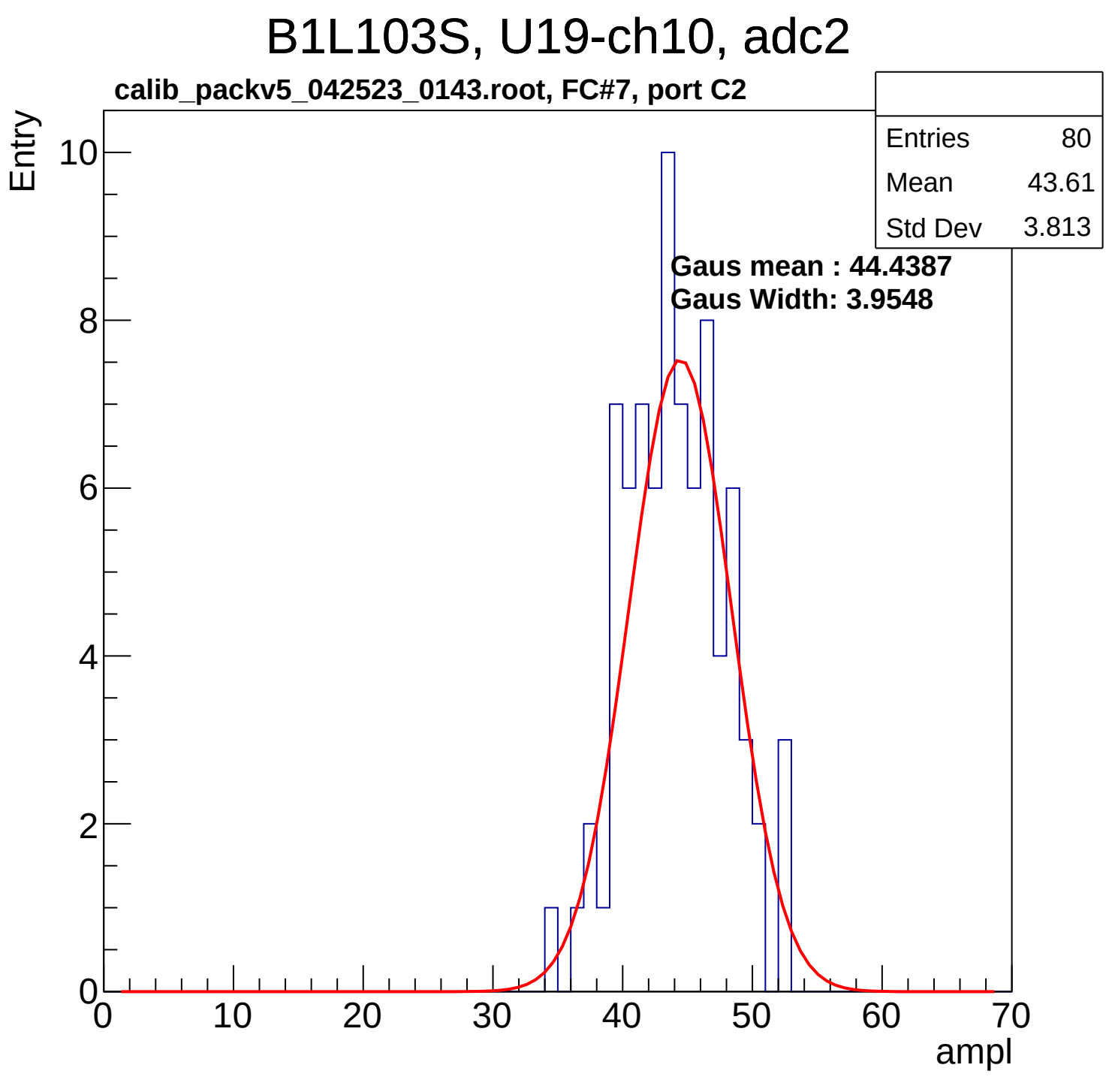
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch10, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 43.61
Std Dev 3.813
Gaus mean : 44.4387
Gaus Width: 3.9548
Entry
ampl
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2
4
6
8
10
0
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40
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60
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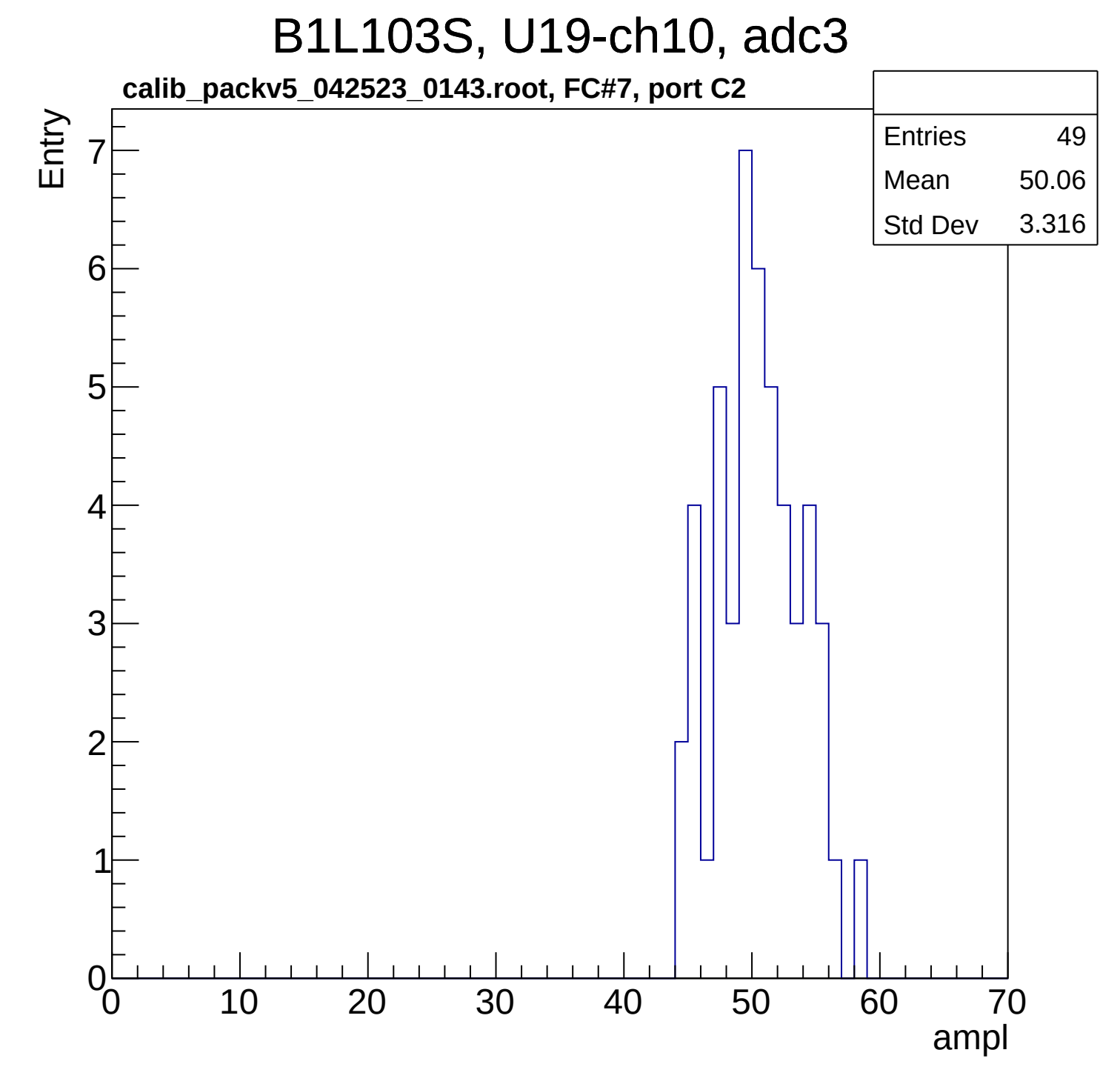
B1L103S, U19-ch10, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 50.06
Std Dev 3.316
Entry
ampl
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2
3
4
5
6
7
0
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20
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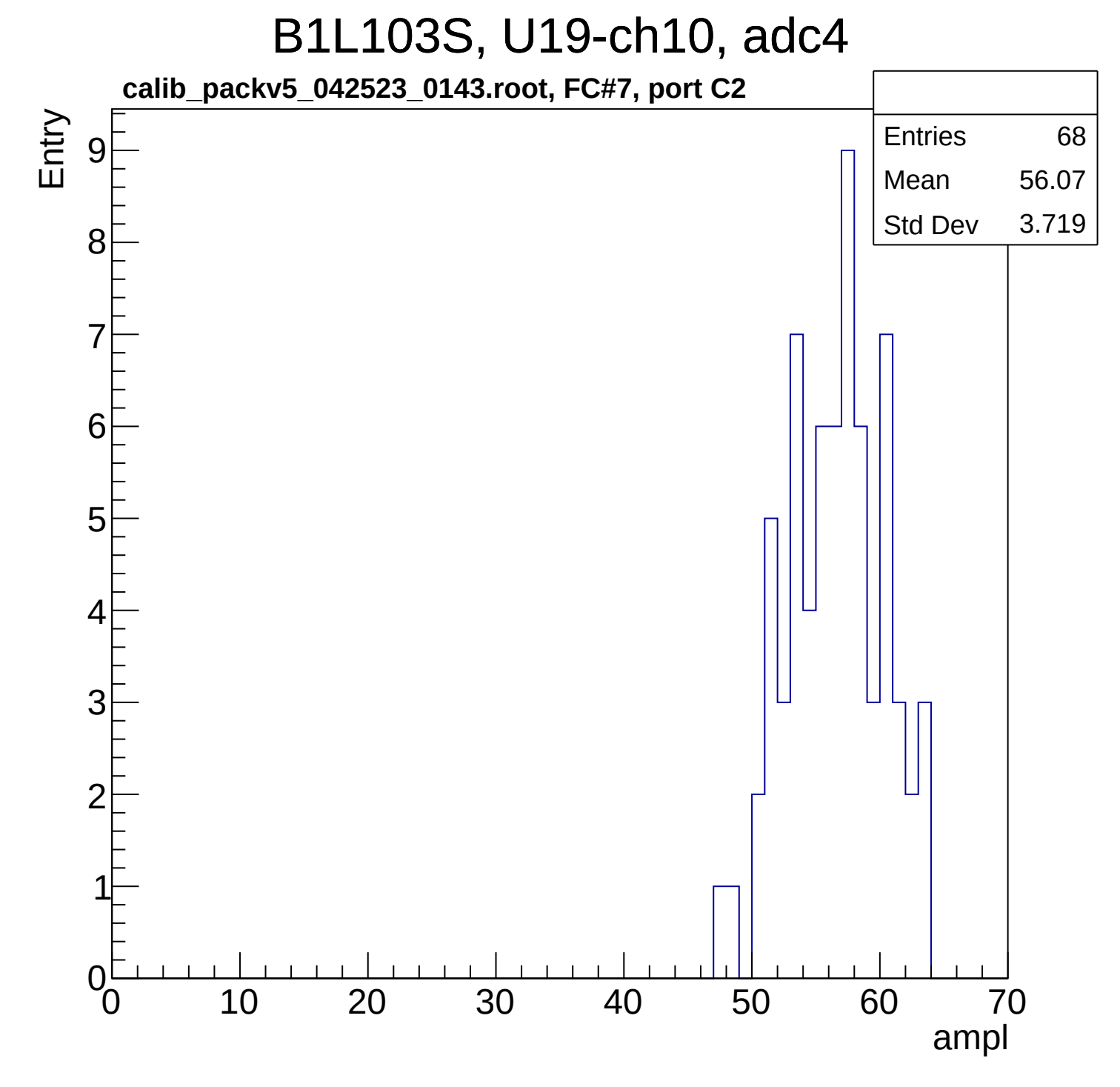
40
50
60
70


B1L103S, U19-ch10, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 56.07
Std Dev 3.719
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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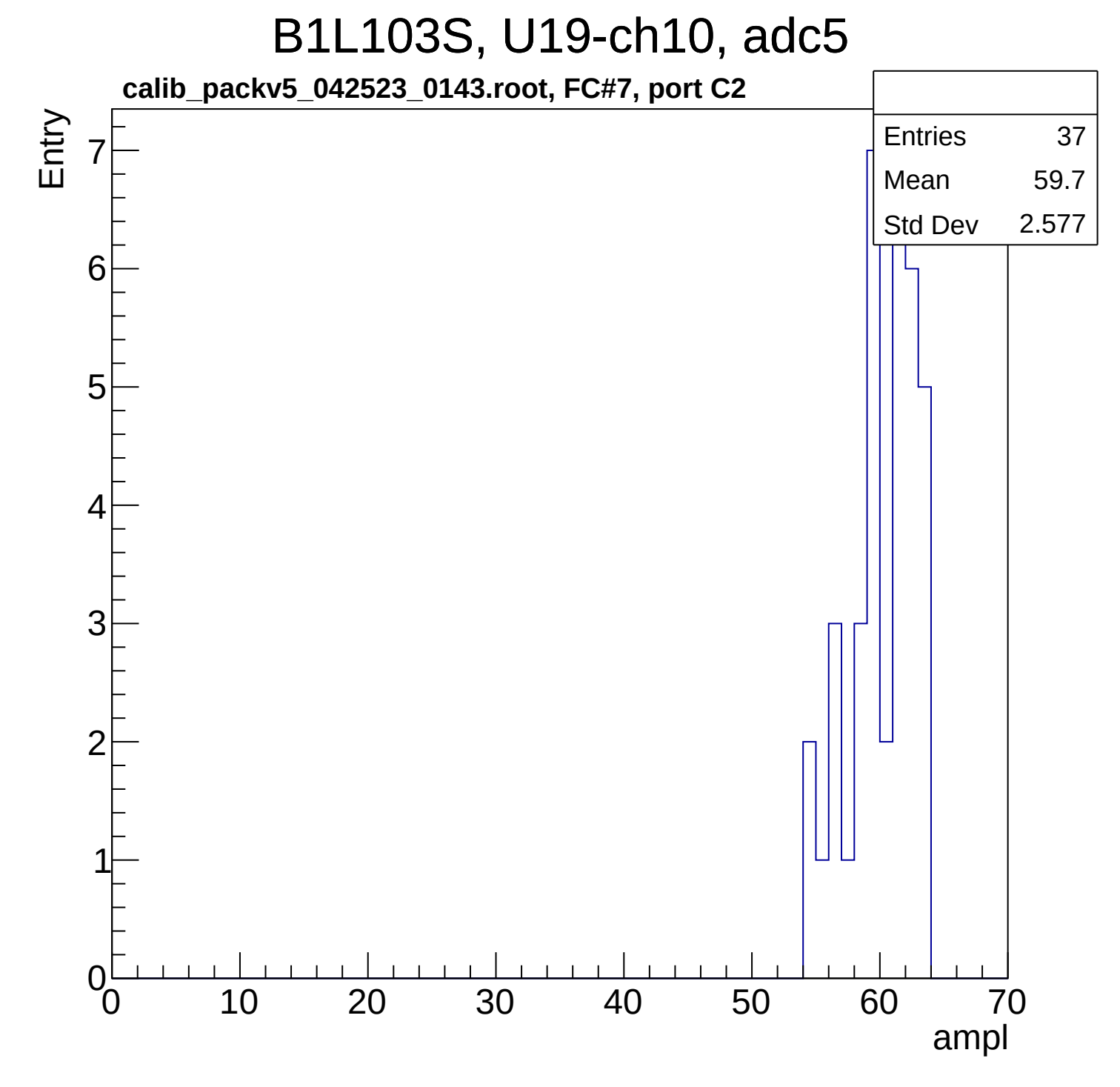
B1L103S, U19-ch10, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 59.7
Std Dev 2.577
Entry
ampl
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1
2
3
4
5
6
7
0
10
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40
50
60
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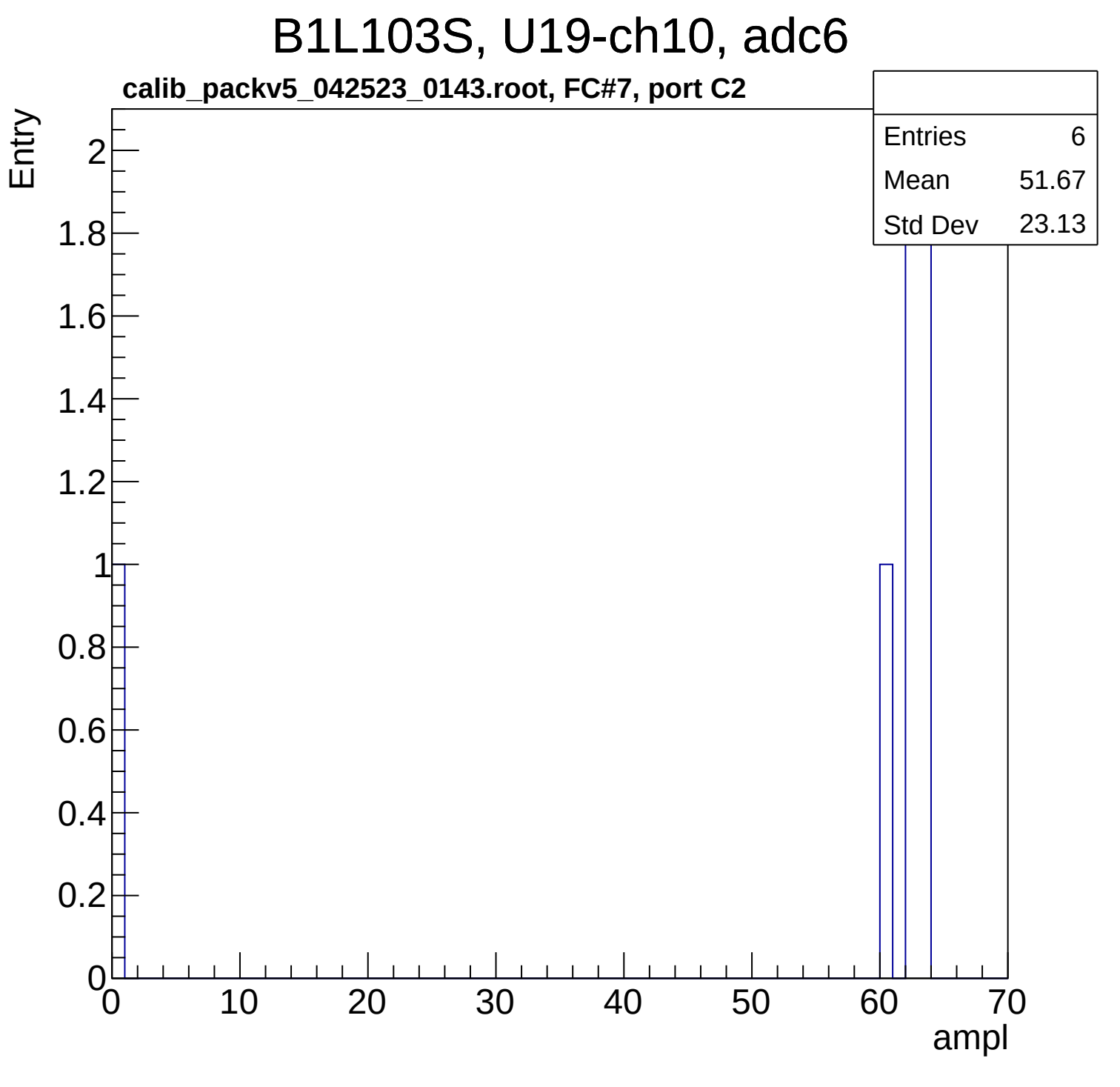
B1L103S, U19-ch10, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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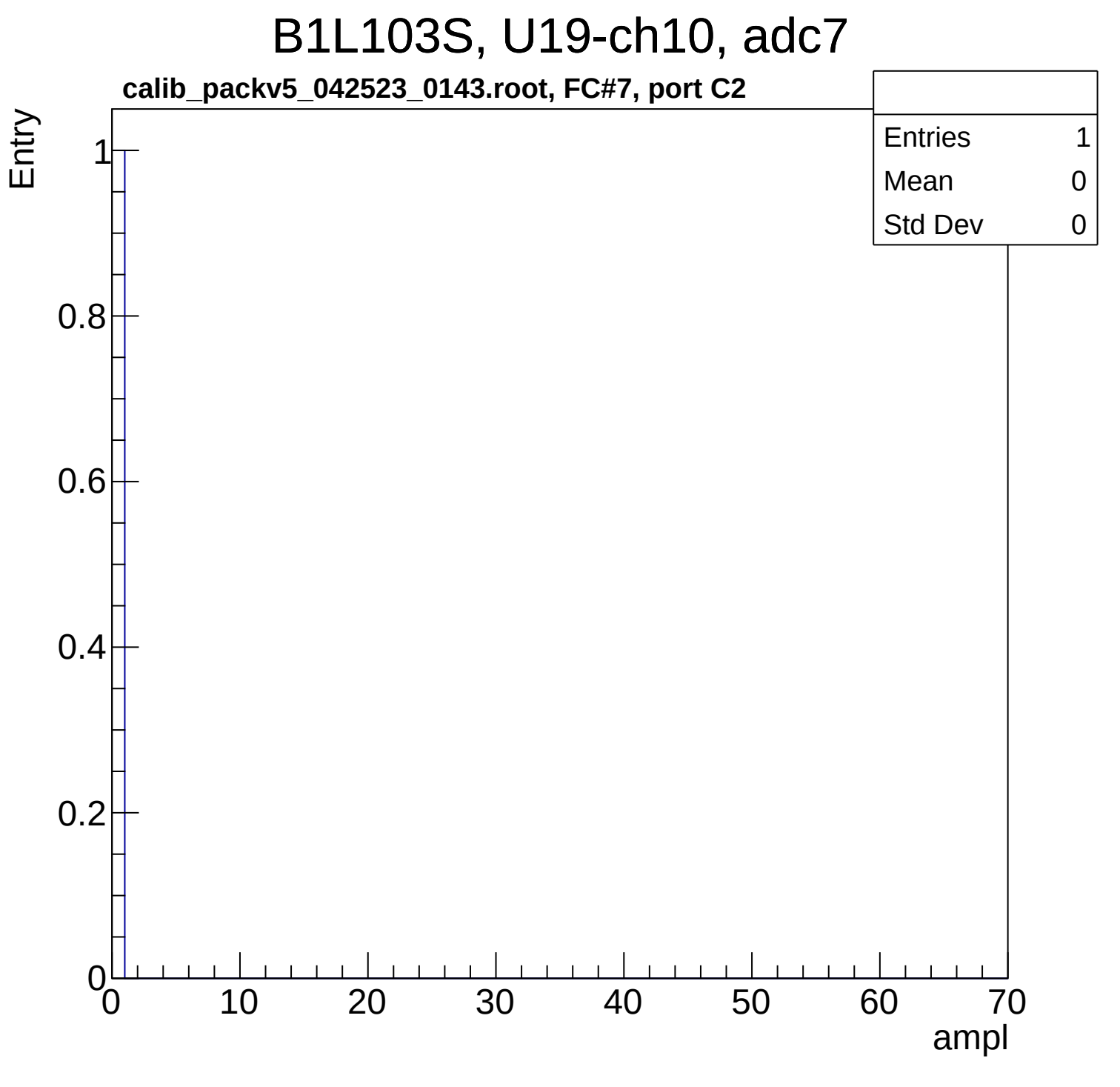
B1L103S, U19-ch10, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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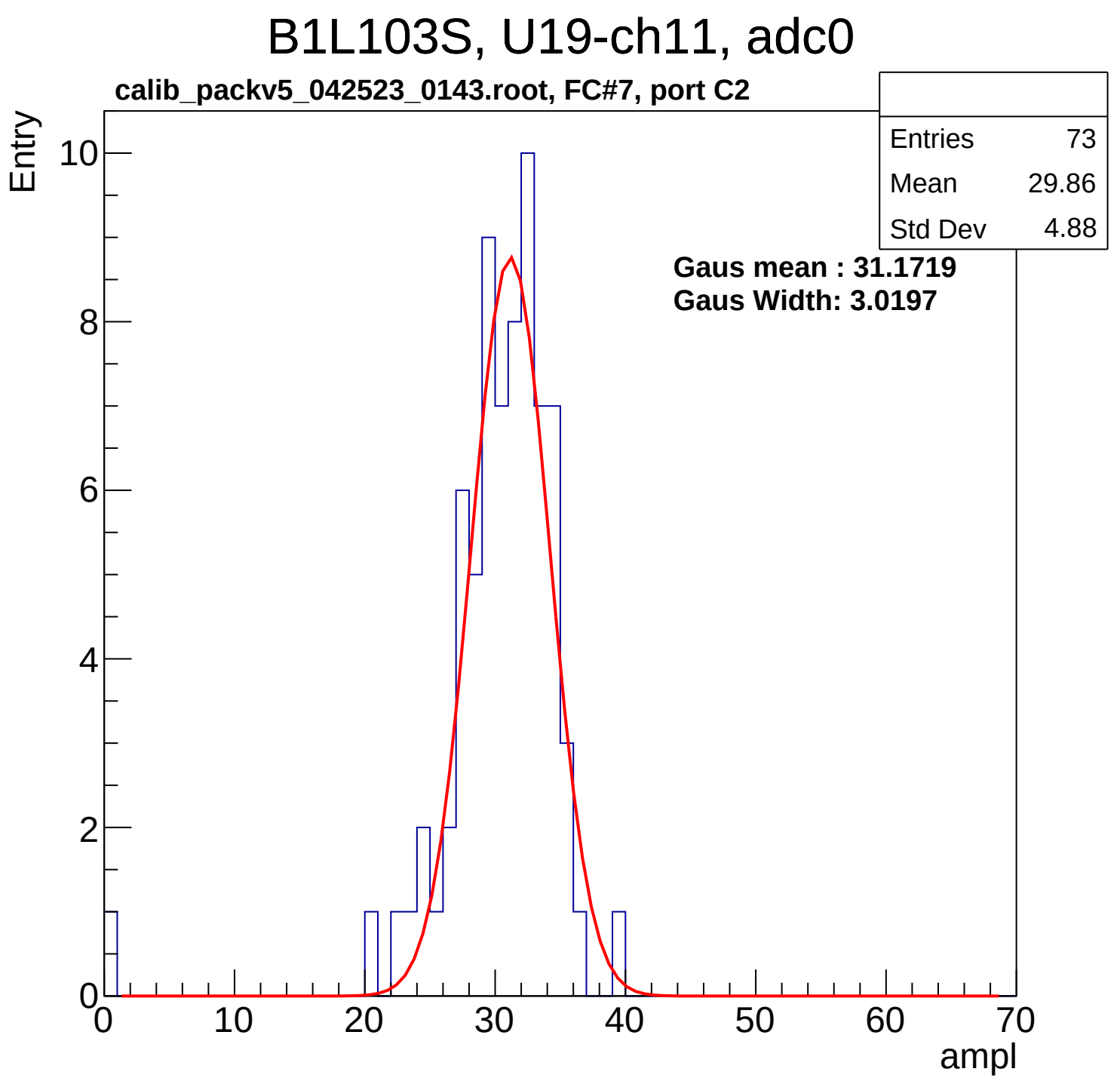
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch11, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.86
Std Dev 4.88
Gaus mean : 31.1719
Gaus Width: 3.0197
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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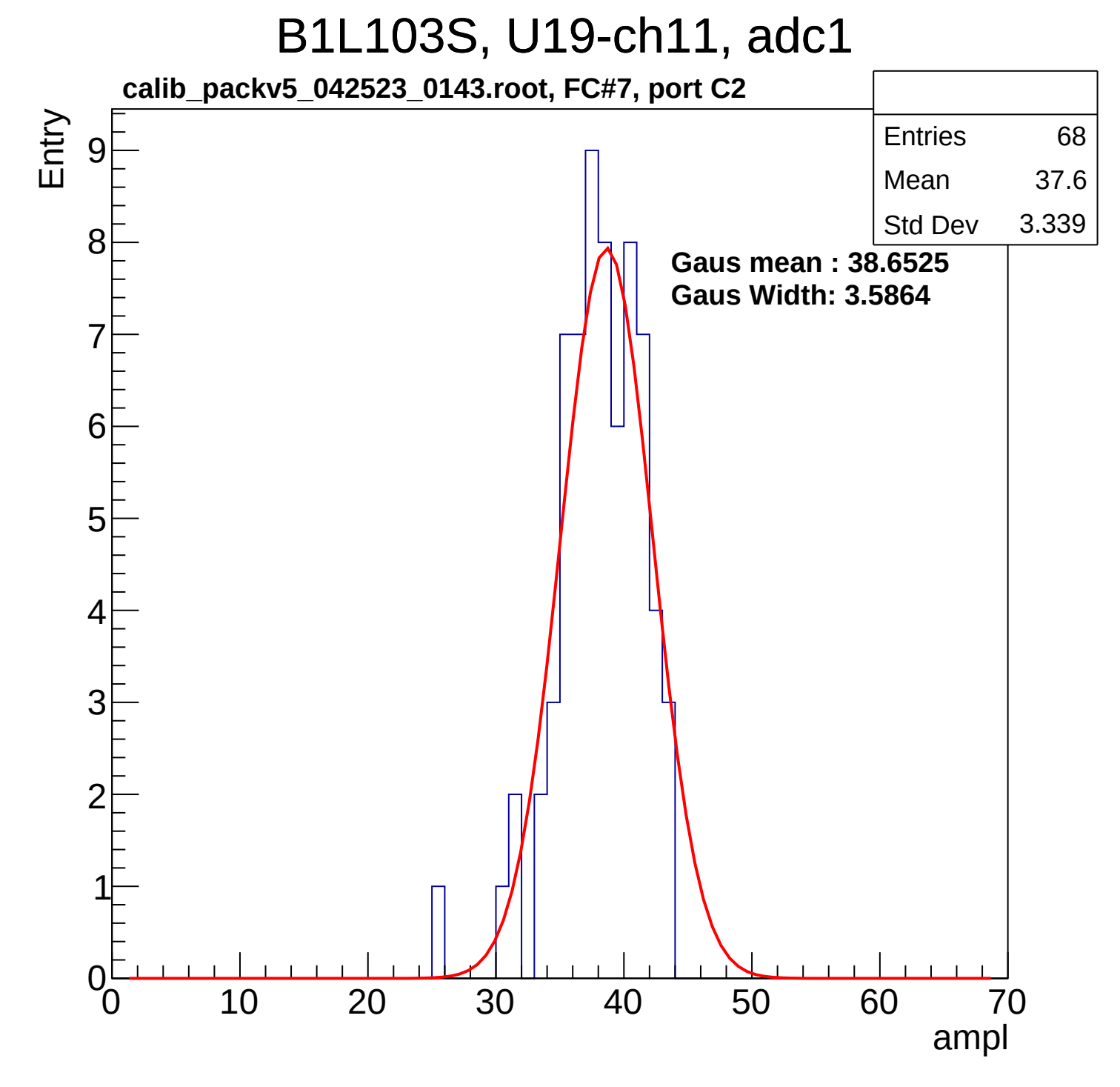
B1L103S, U19-ch11, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 37.6
Std Dev 3.339
Gaus mean : 38.6525
Gaus Width: 3.5864
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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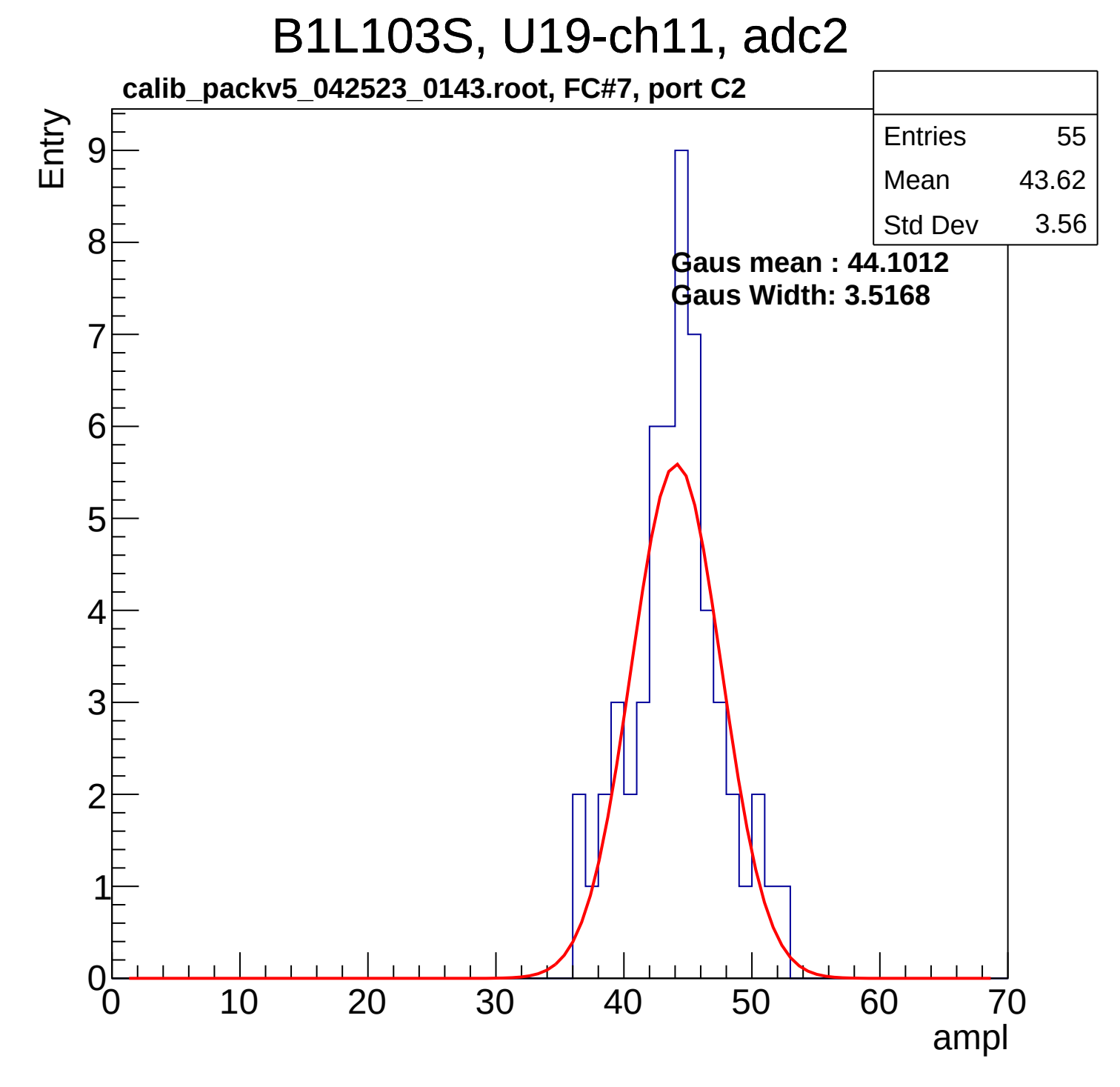
B1L103S, U19-ch11, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 43.62
Std Dev 3.56
Gaus mean : 44.1012
Gaus Width: 3.5168
Entry
ampl
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2
3
4
5
6
7
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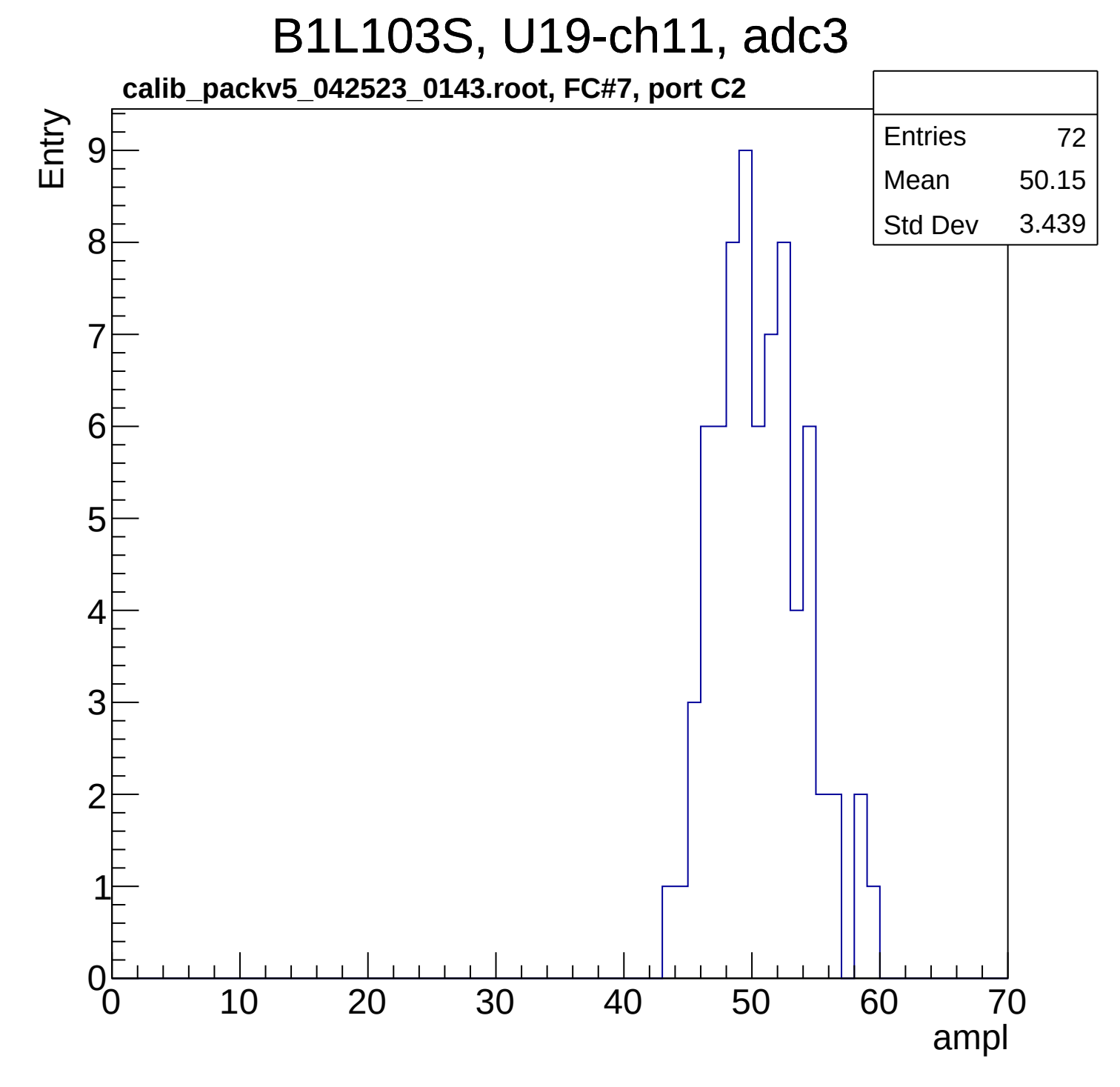
B1L103S, U19-ch11, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.15
Std Dev 3.439
Entry
ampl
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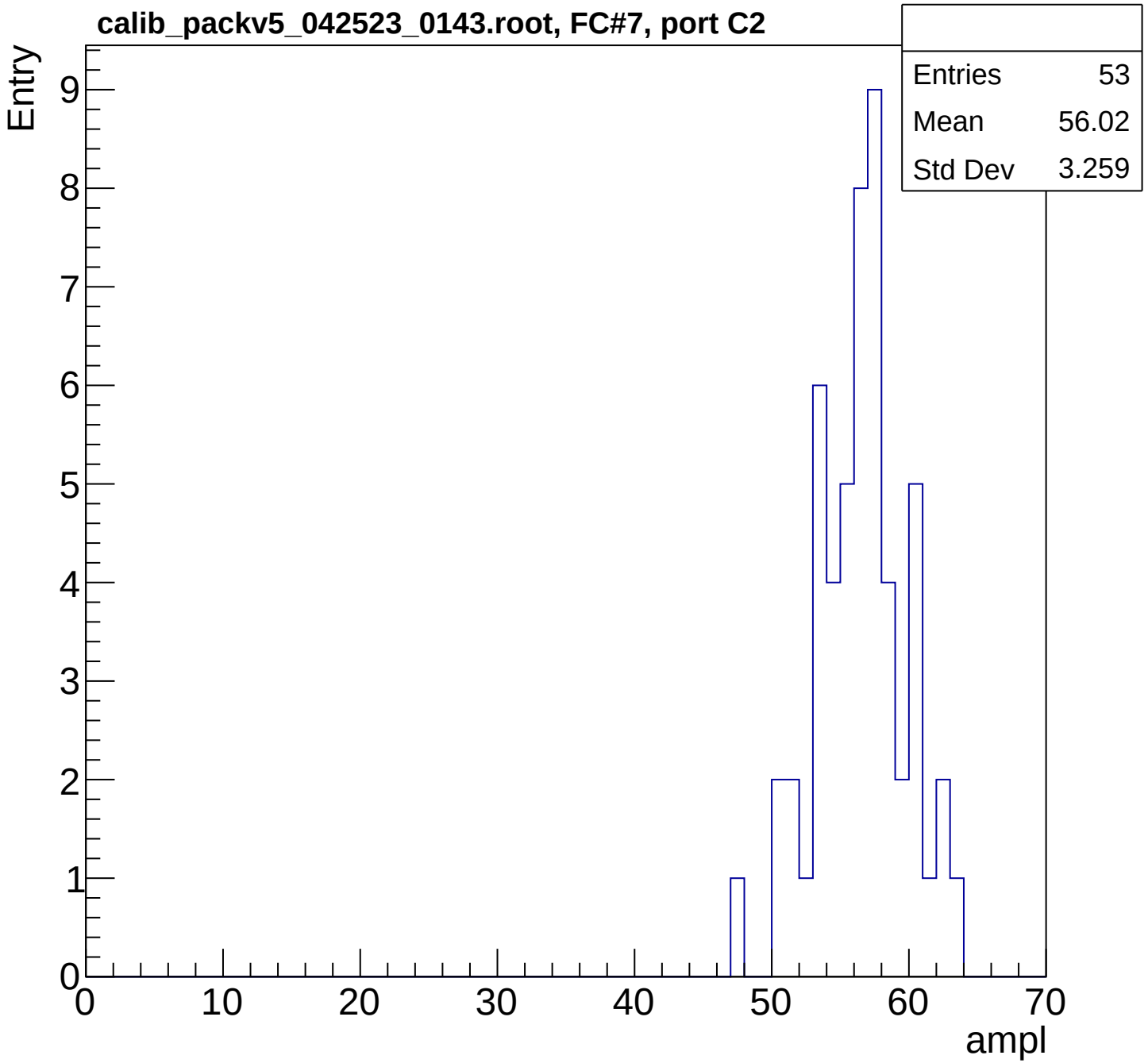
B1L103S, U19-ch11, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 56.02
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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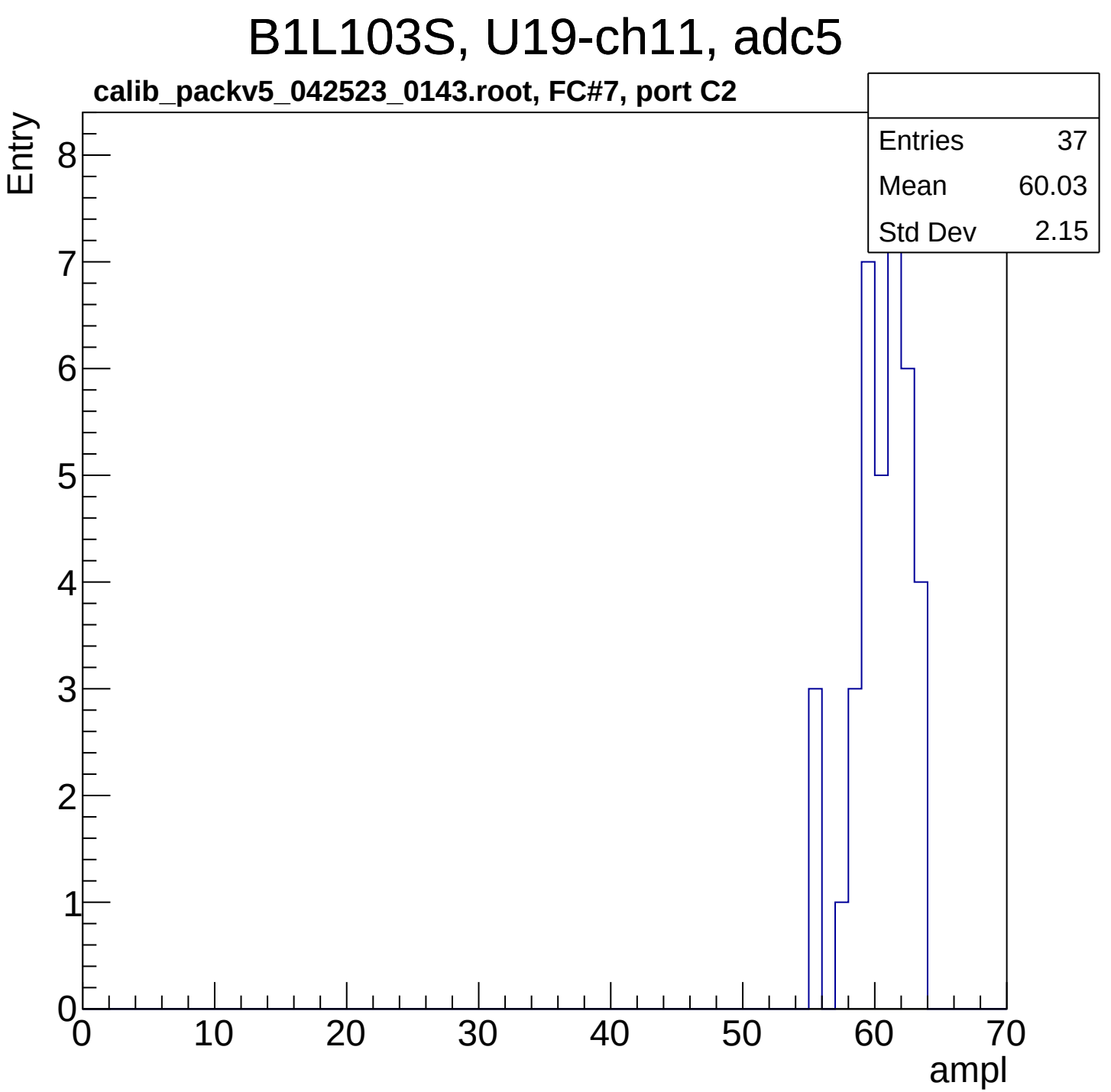
B1L103S, U19-ch11, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.03
Std Dev 2.15
Entry
0
1
2
3
4
5
6
7
8
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20
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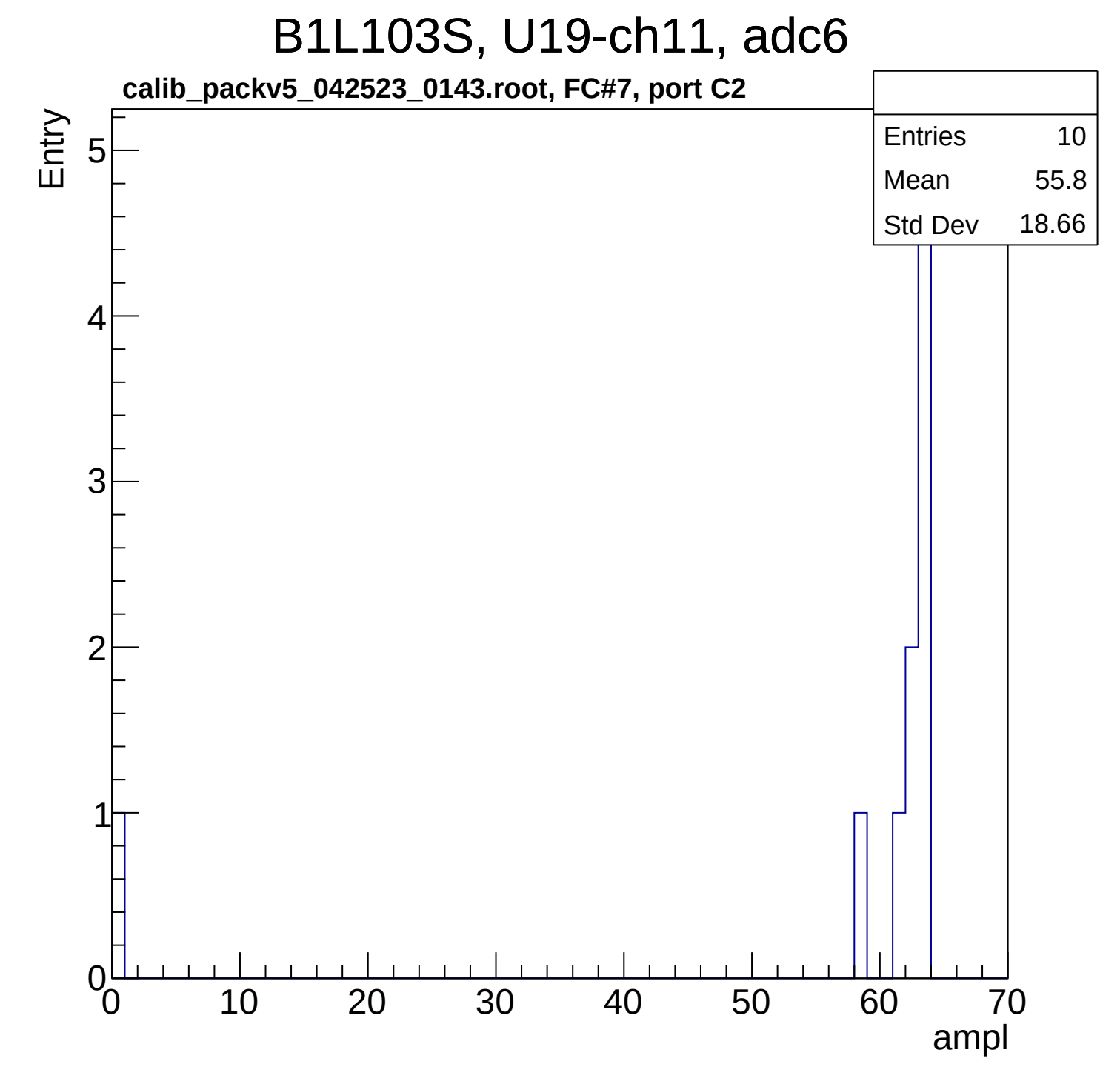
50
60
70
ampl


B1L103S, U19-ch11, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 55.8
Std Dev 18.66
Entry
0
1
2
3
4
5
0
10
20
30
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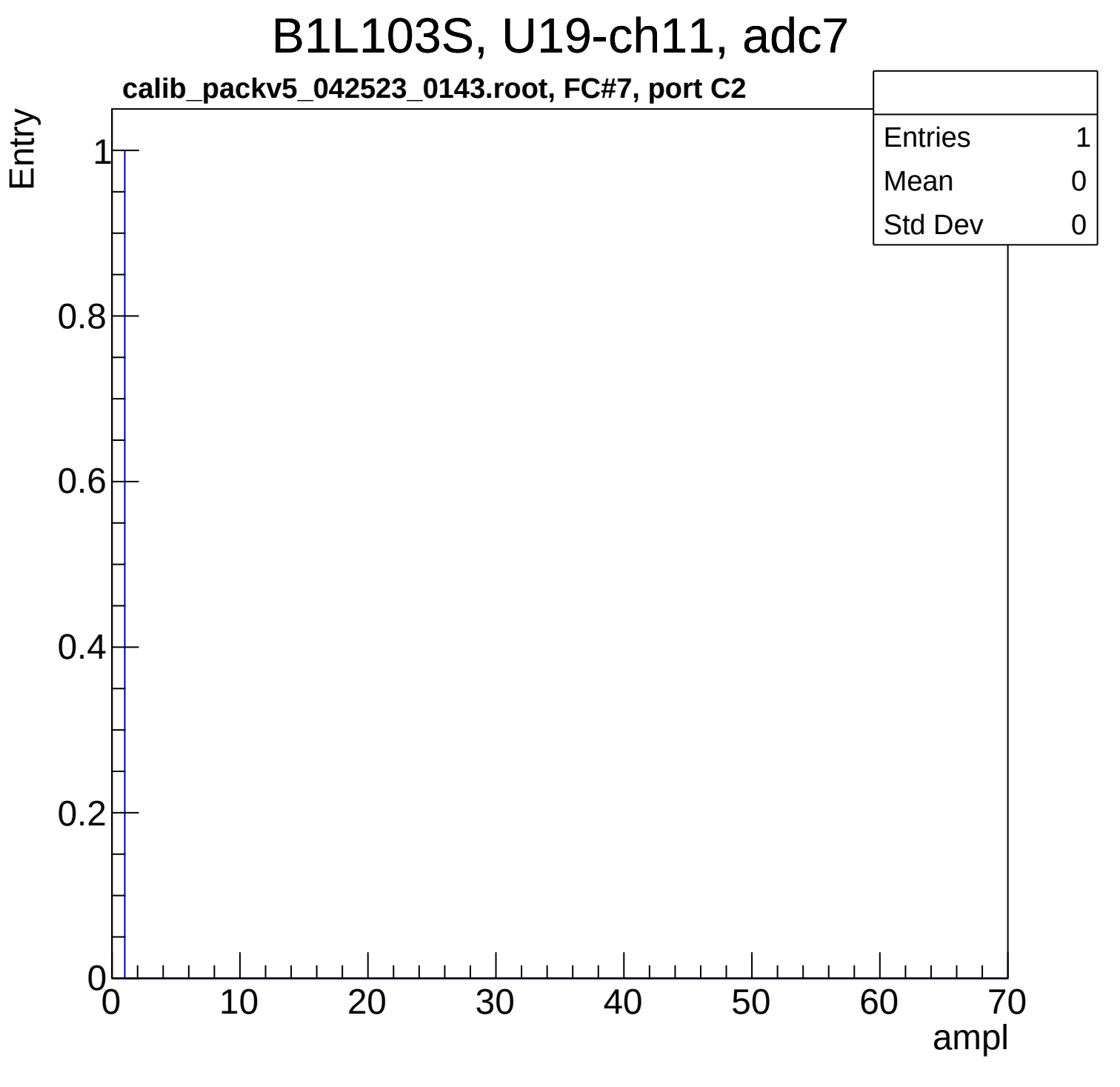
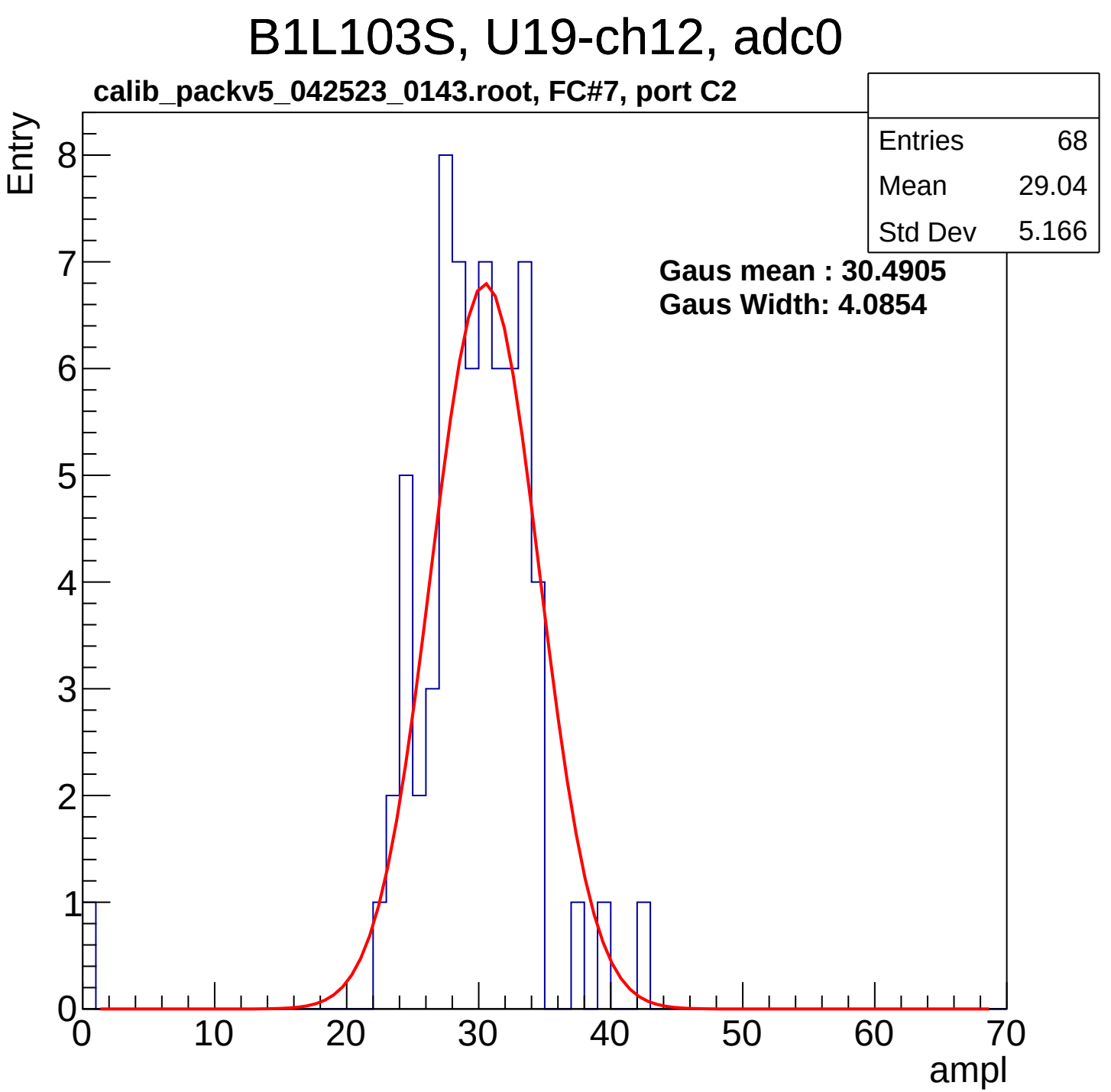
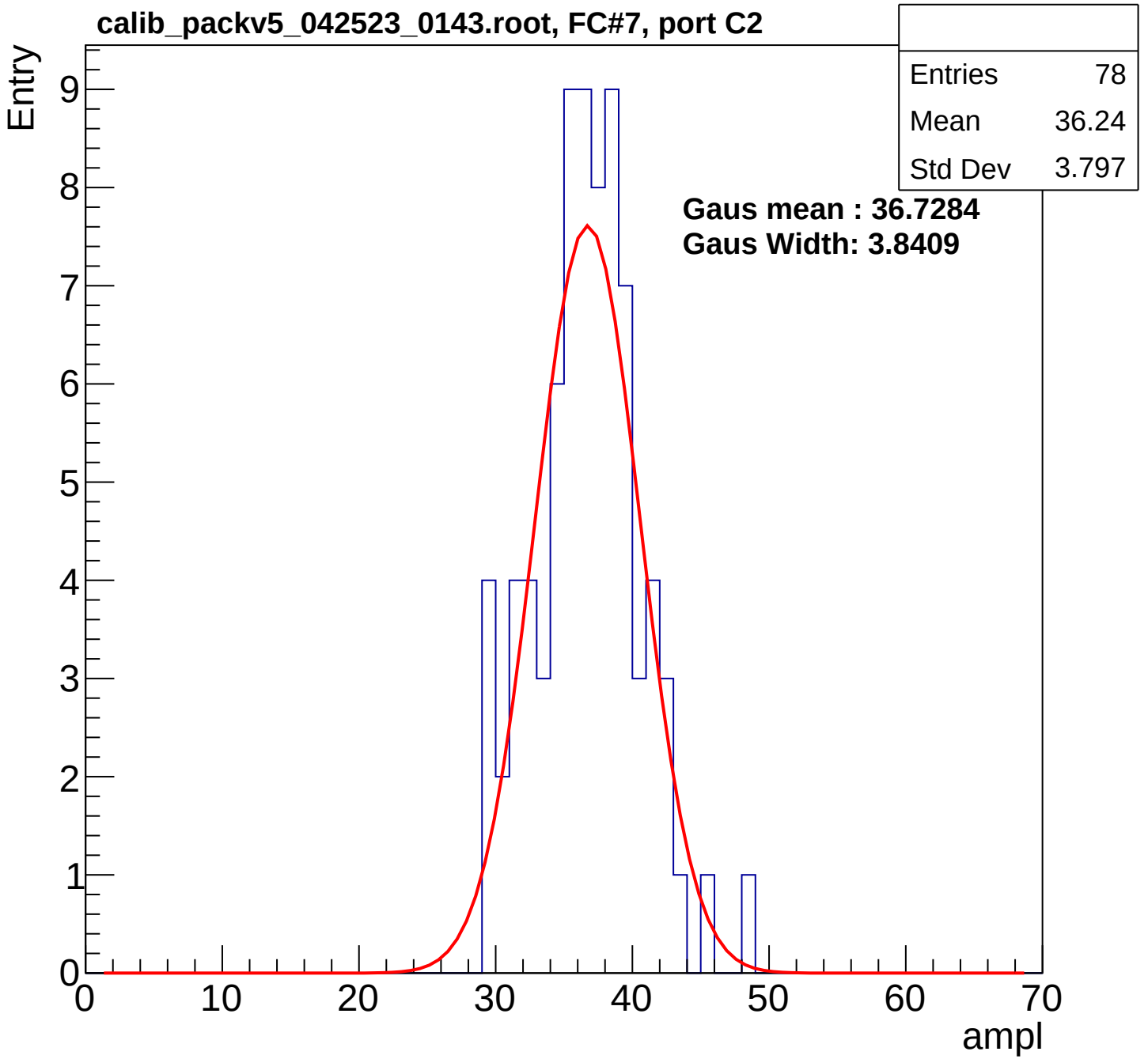
50
60
70
ampl


B1L103S, U19-ch11, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch12, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.04
Std Dev 5.166
Gaus mean : 30.4905
Gaus Width: 4.0854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch12, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.24
Std Dev 3.797
Gaus mean : 36.7284
Gaus Width: 3.8409
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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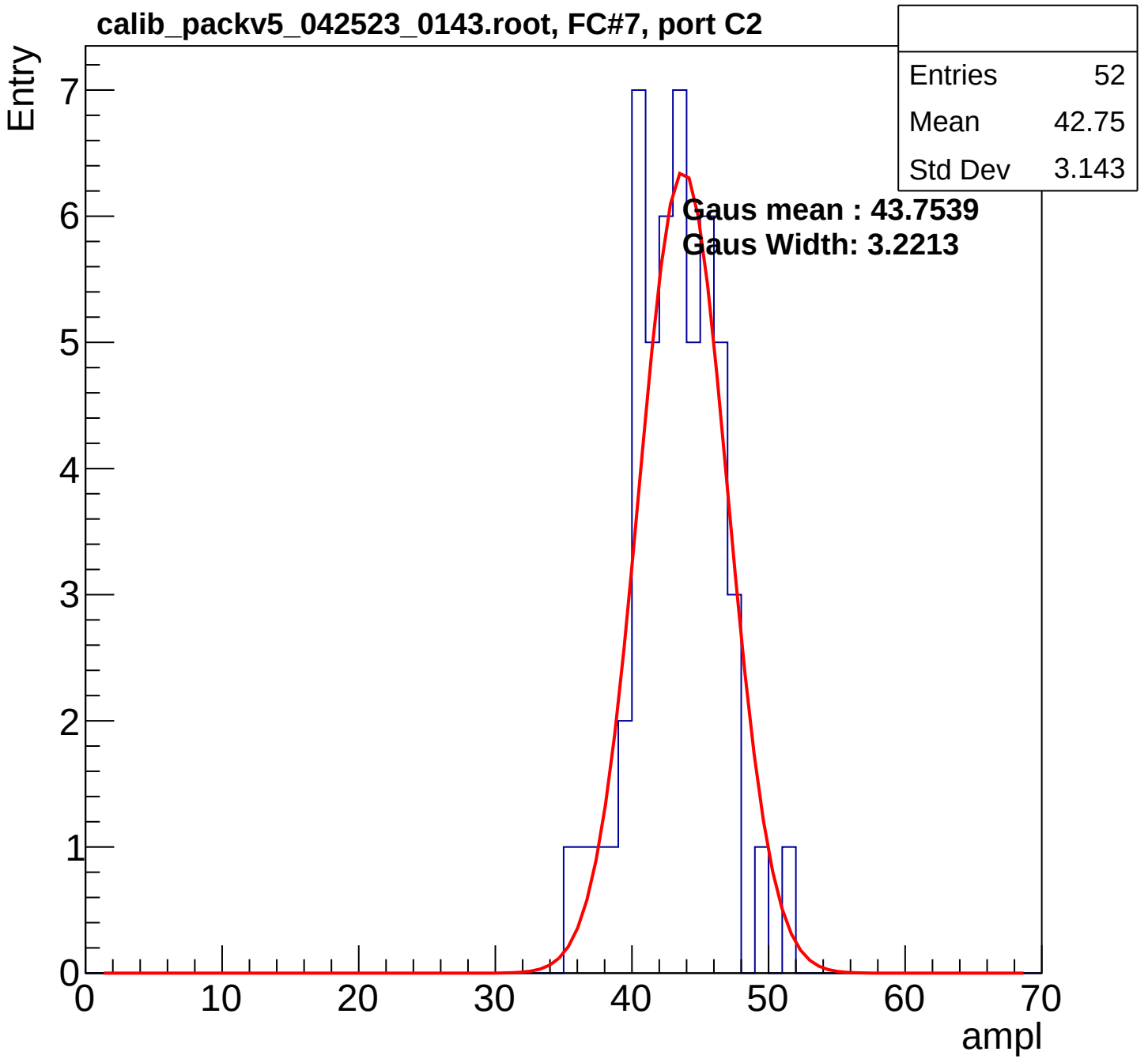
B1L103S, U19-ch12, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 42.75
Std Dev 3.143
Gaus mean : 43.7539
Gaus Width: 3.2213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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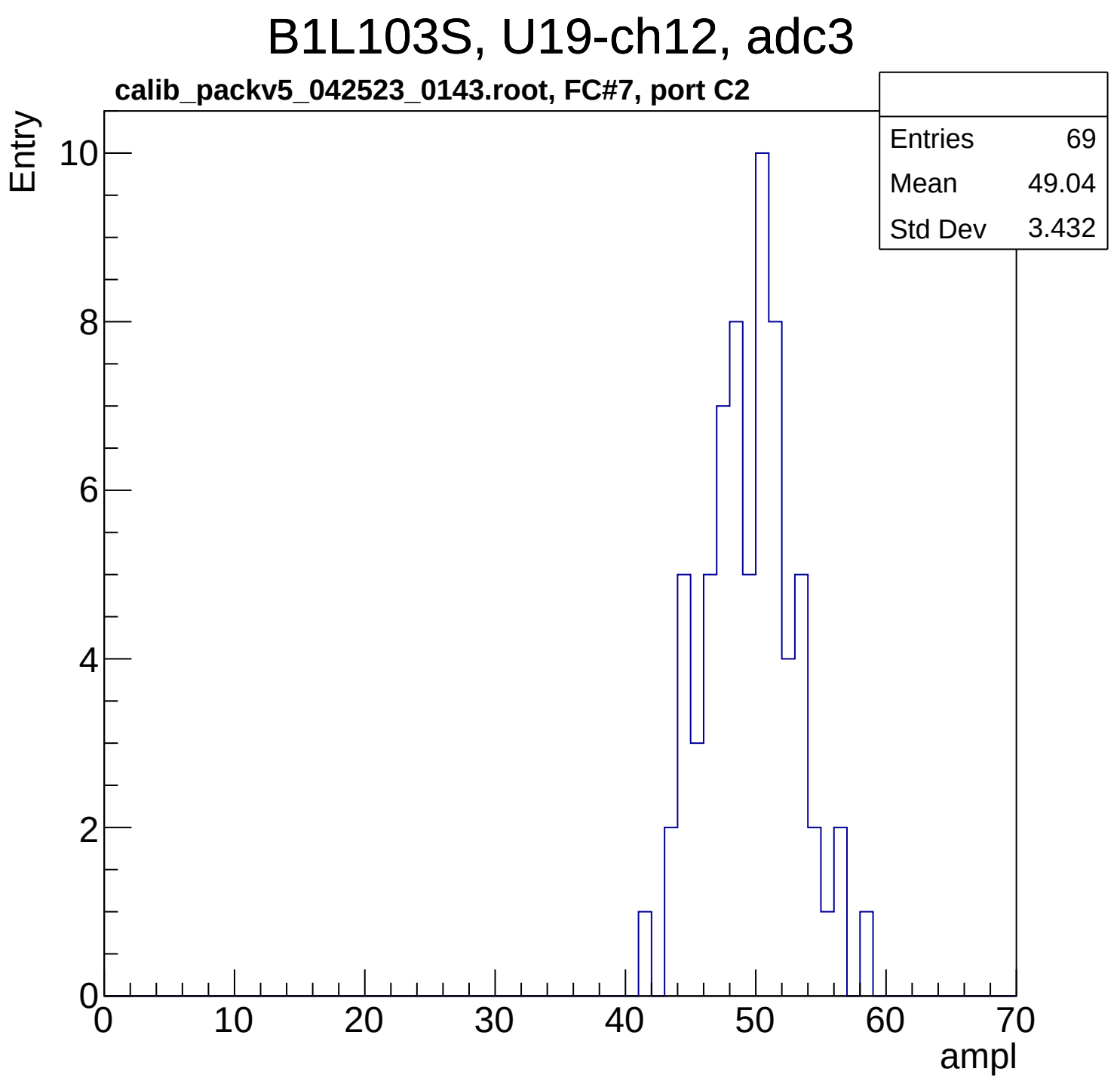
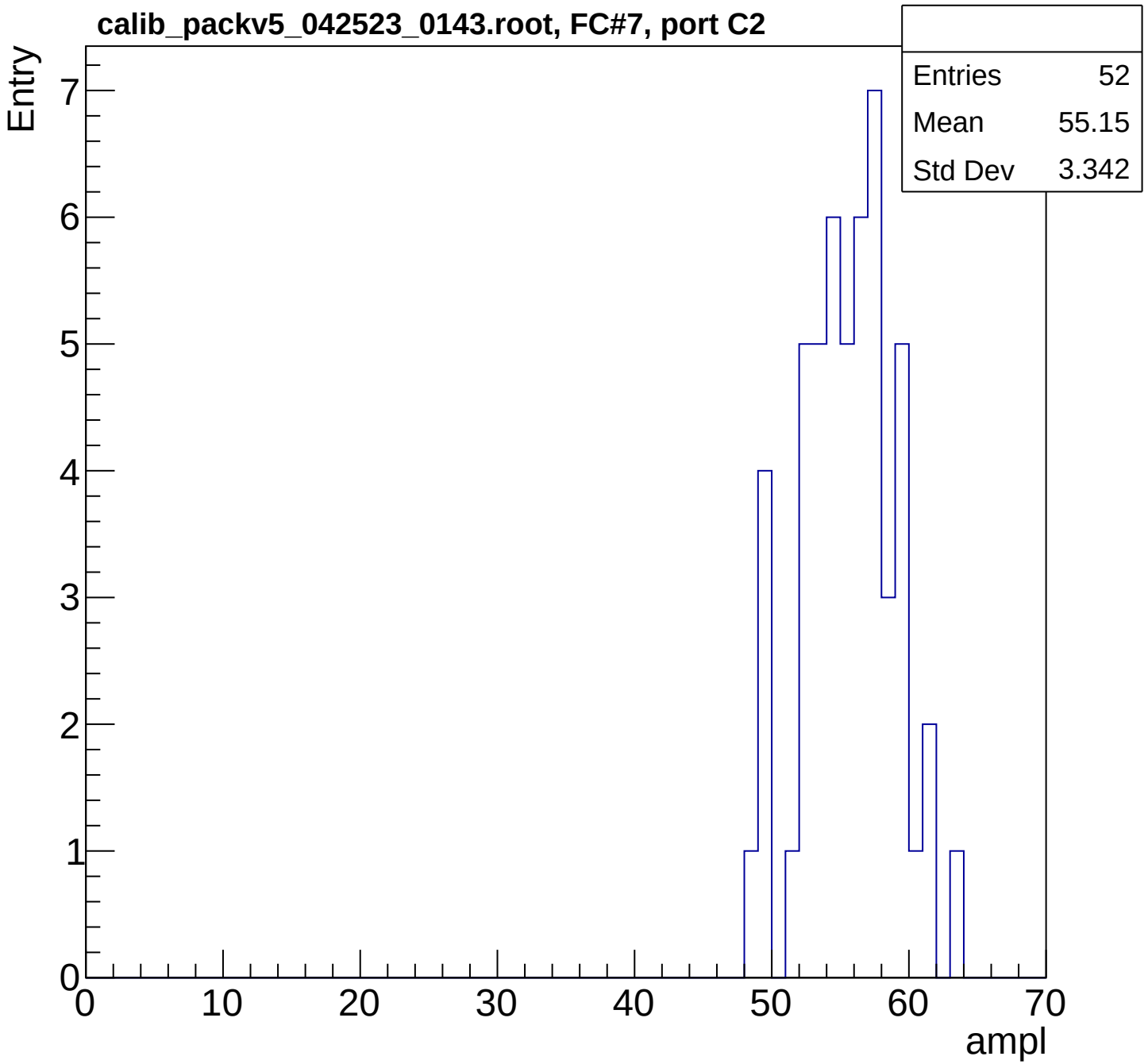

B1L103S, U19-ch12, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.04
Std Dev 3.432
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch12, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.15
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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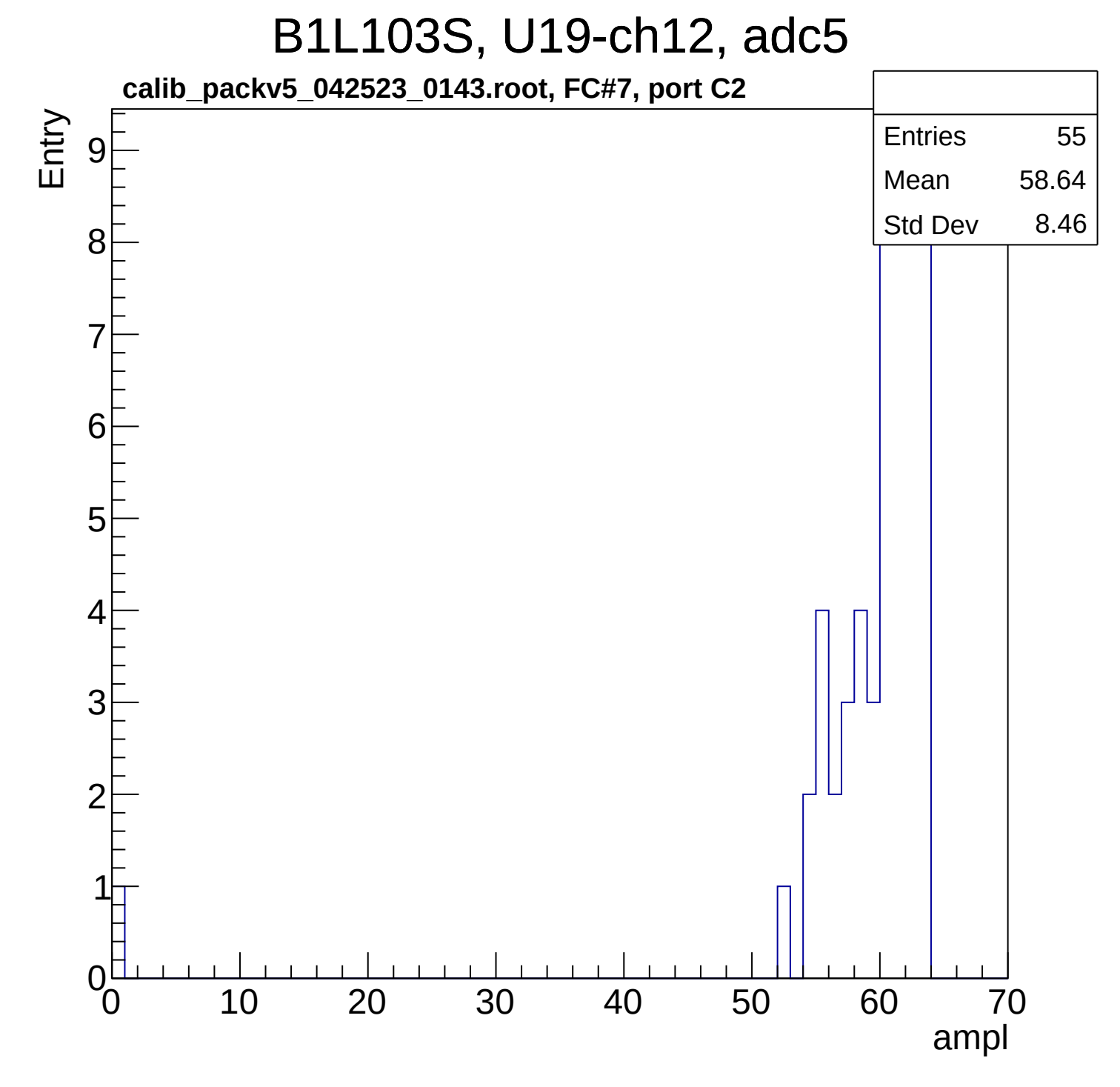
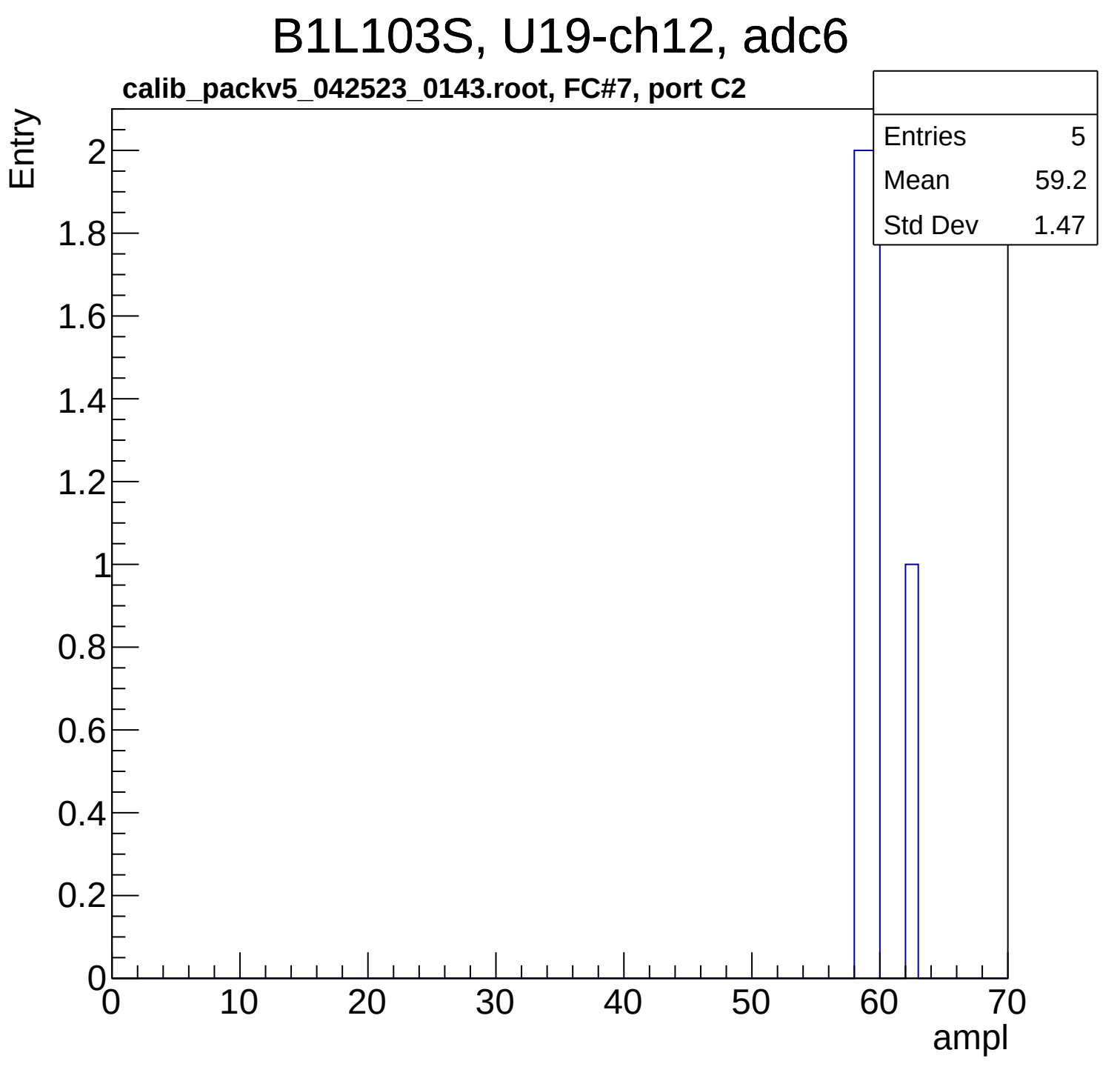
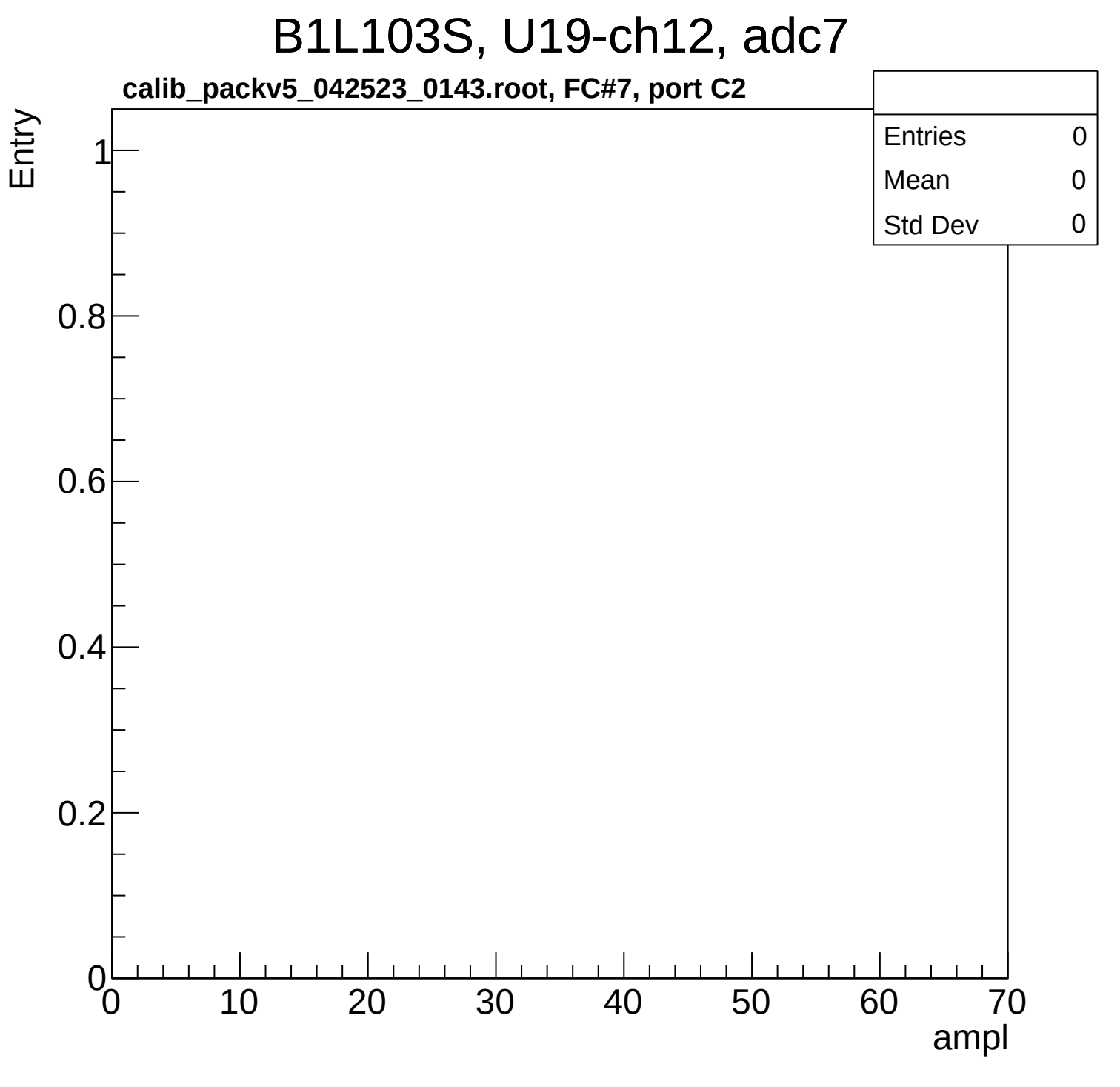
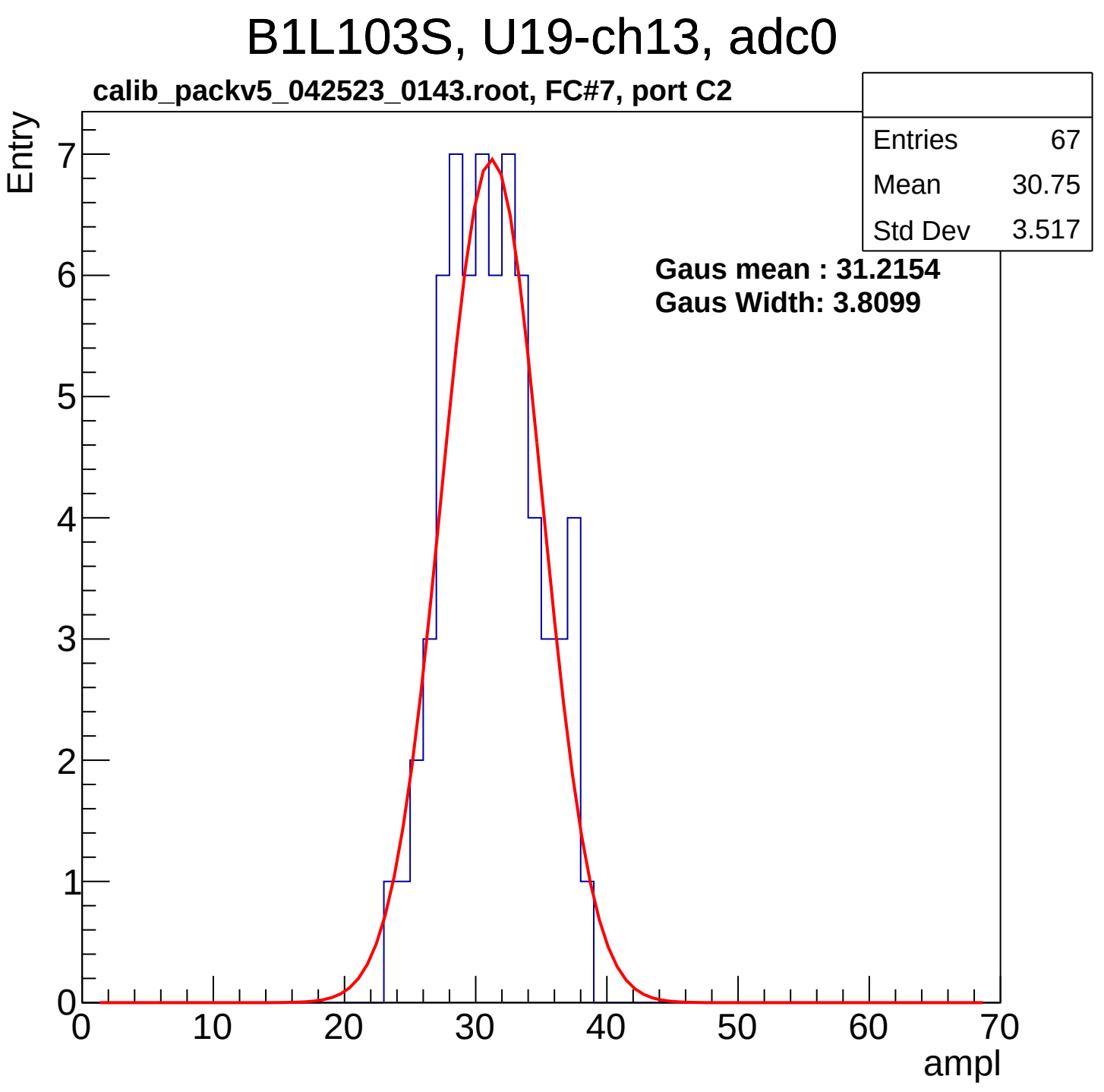
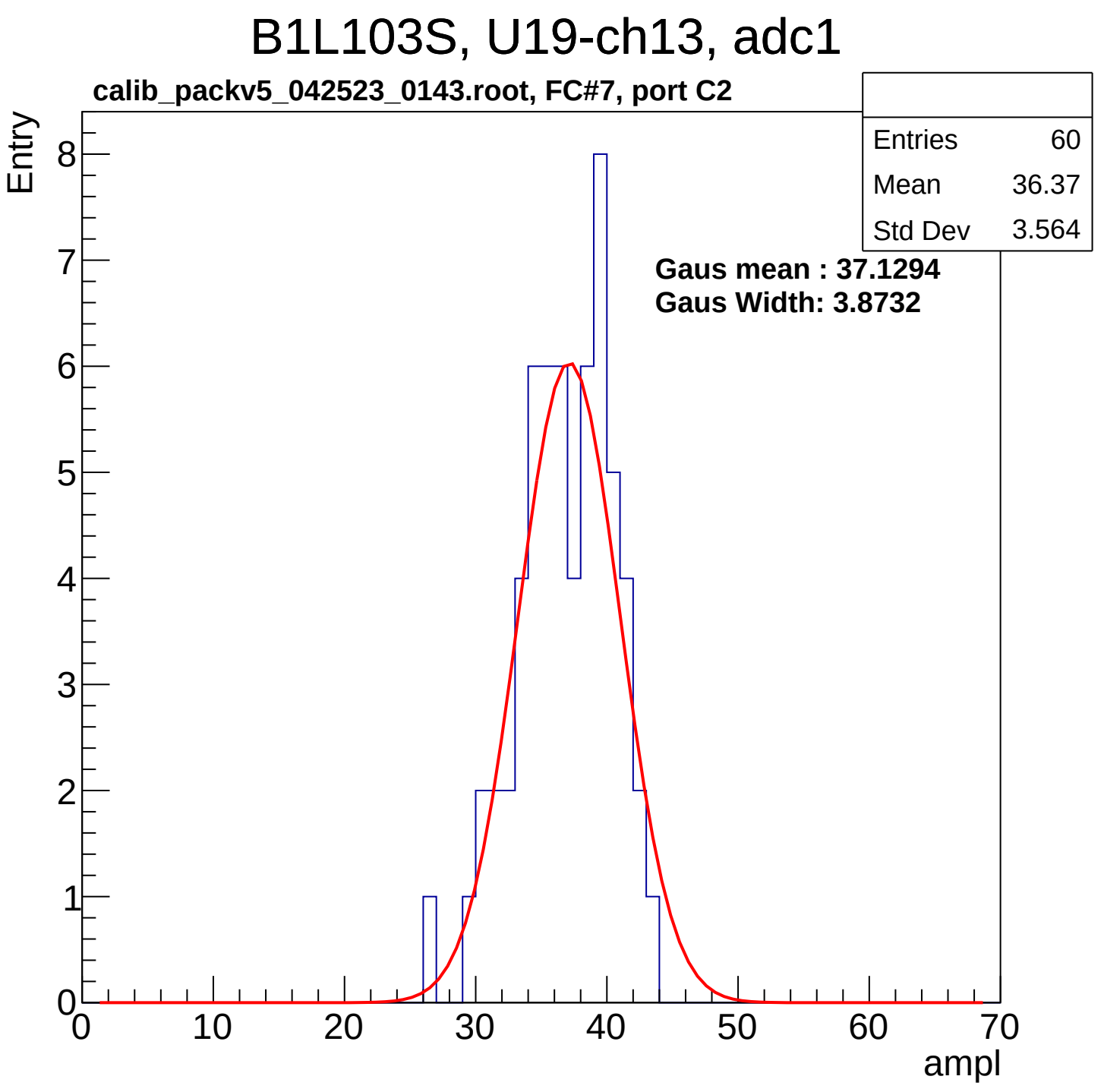

B1L103S, U19-ch12, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.64
Std Dev 8.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch12, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 59.2
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch12, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch13, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 30.75
Std Dev 3.517
Gaus mean : 31.2154
Gaus Width: 3.8099
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch13, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 36.37
Std Dev 3.564
Gaus mean : 37.1294
Gaus Width: 3.8732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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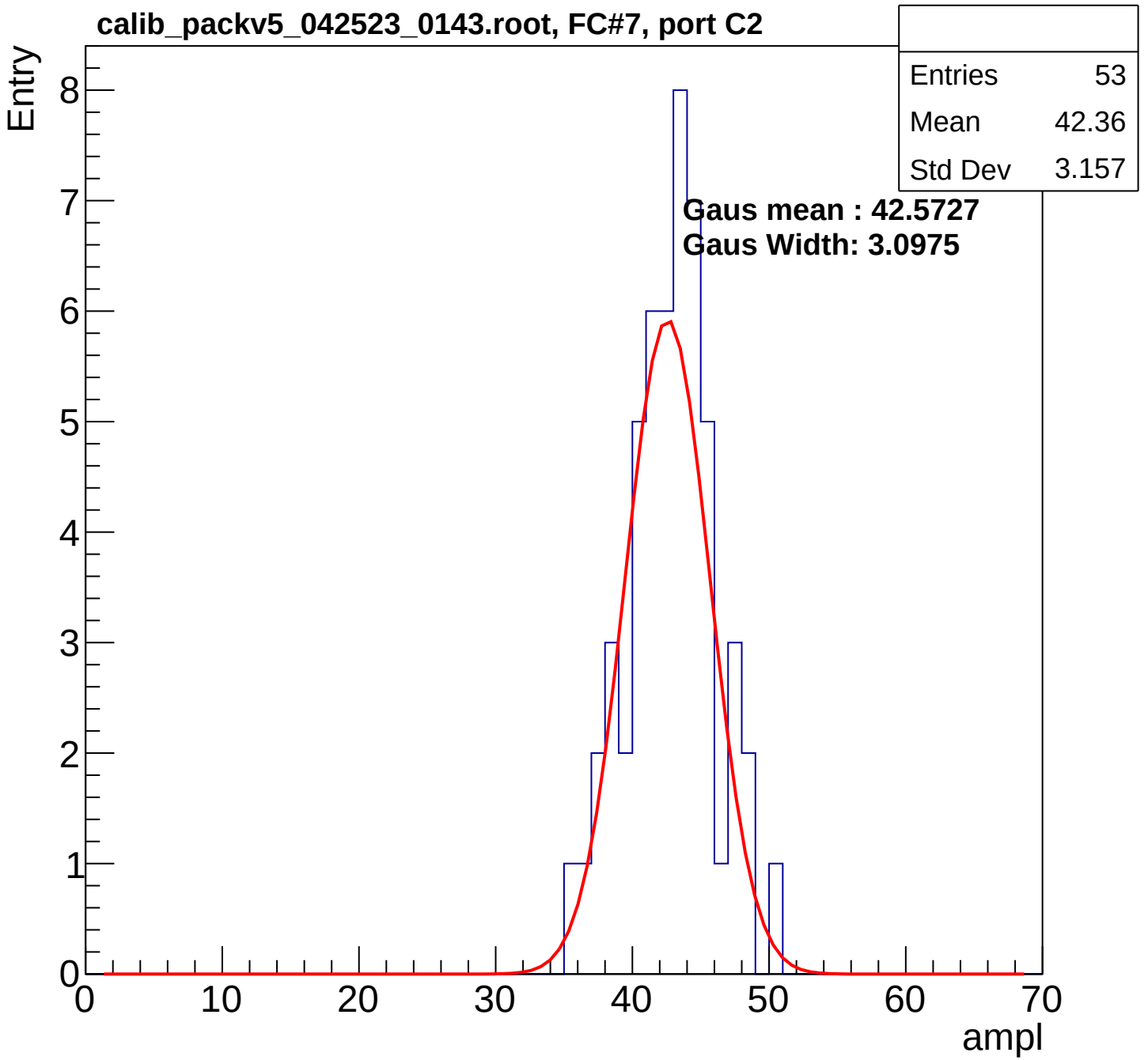
B1L103S, U19-ch13, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 42.36
Std Dev 3.157
Gaus mean : 42.5727
Gaus Width: 3.0975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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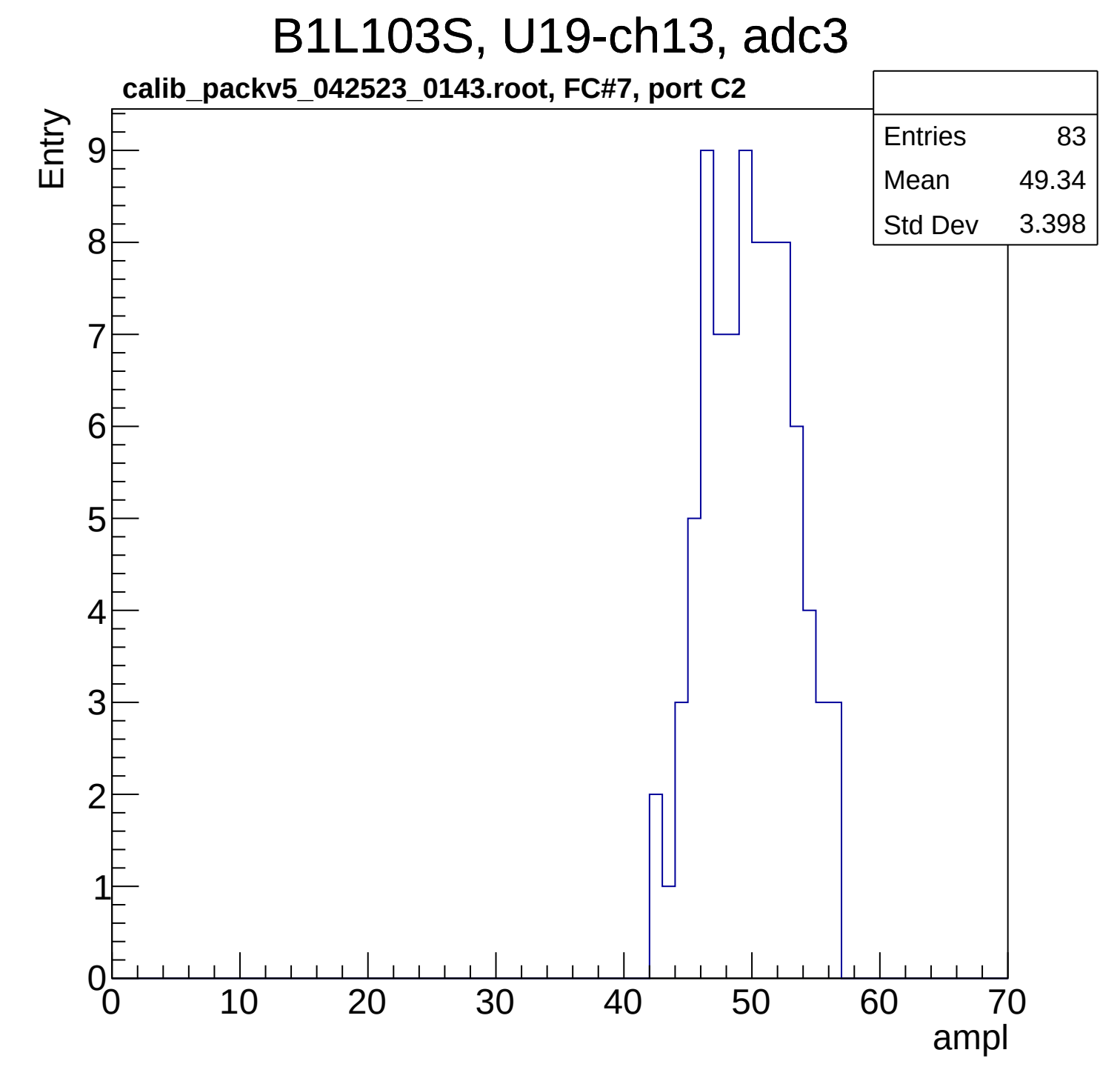
B1L103S, U19-ch13, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 49.34
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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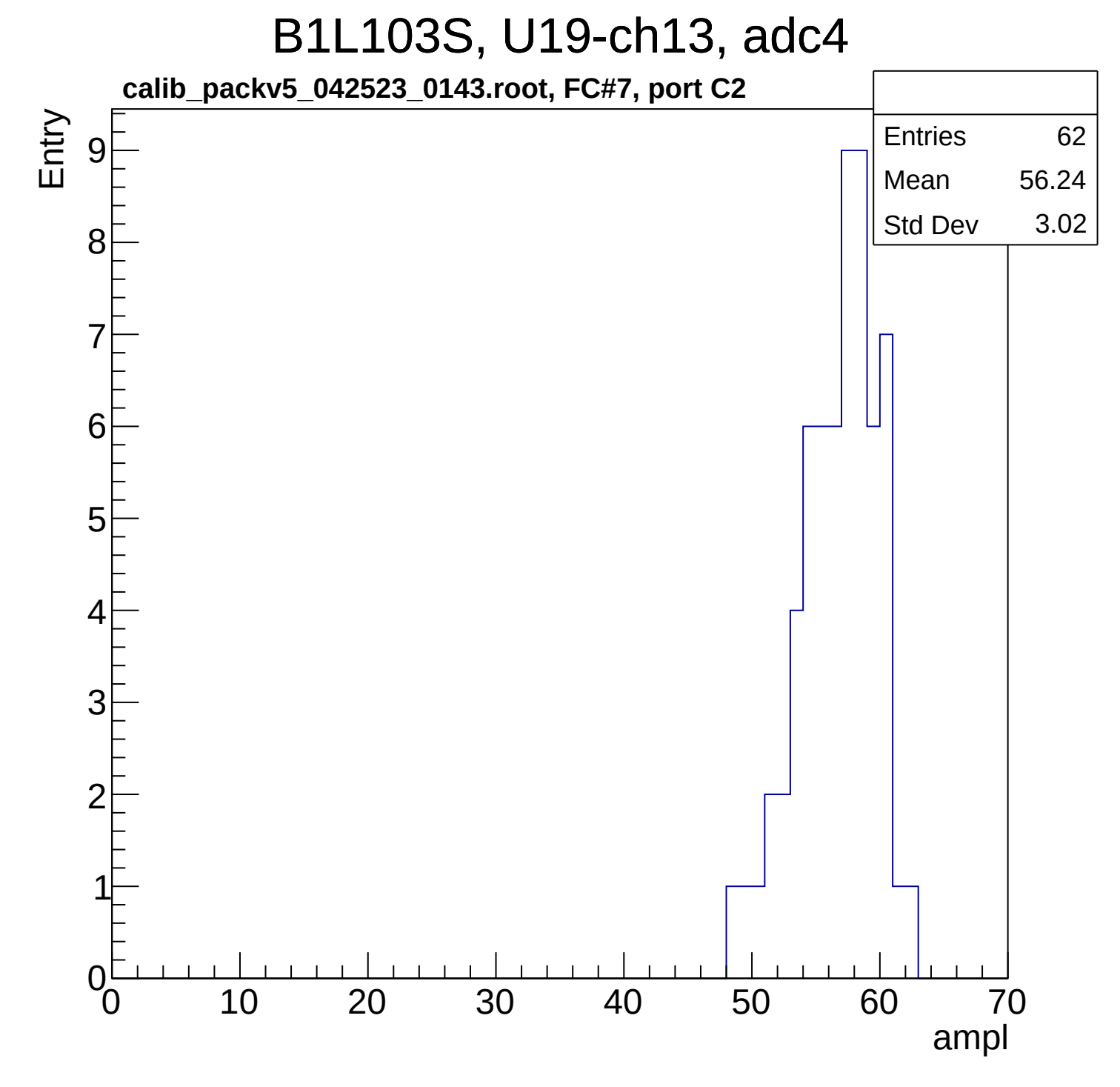
B1L103S, U19-ch13, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 56.24
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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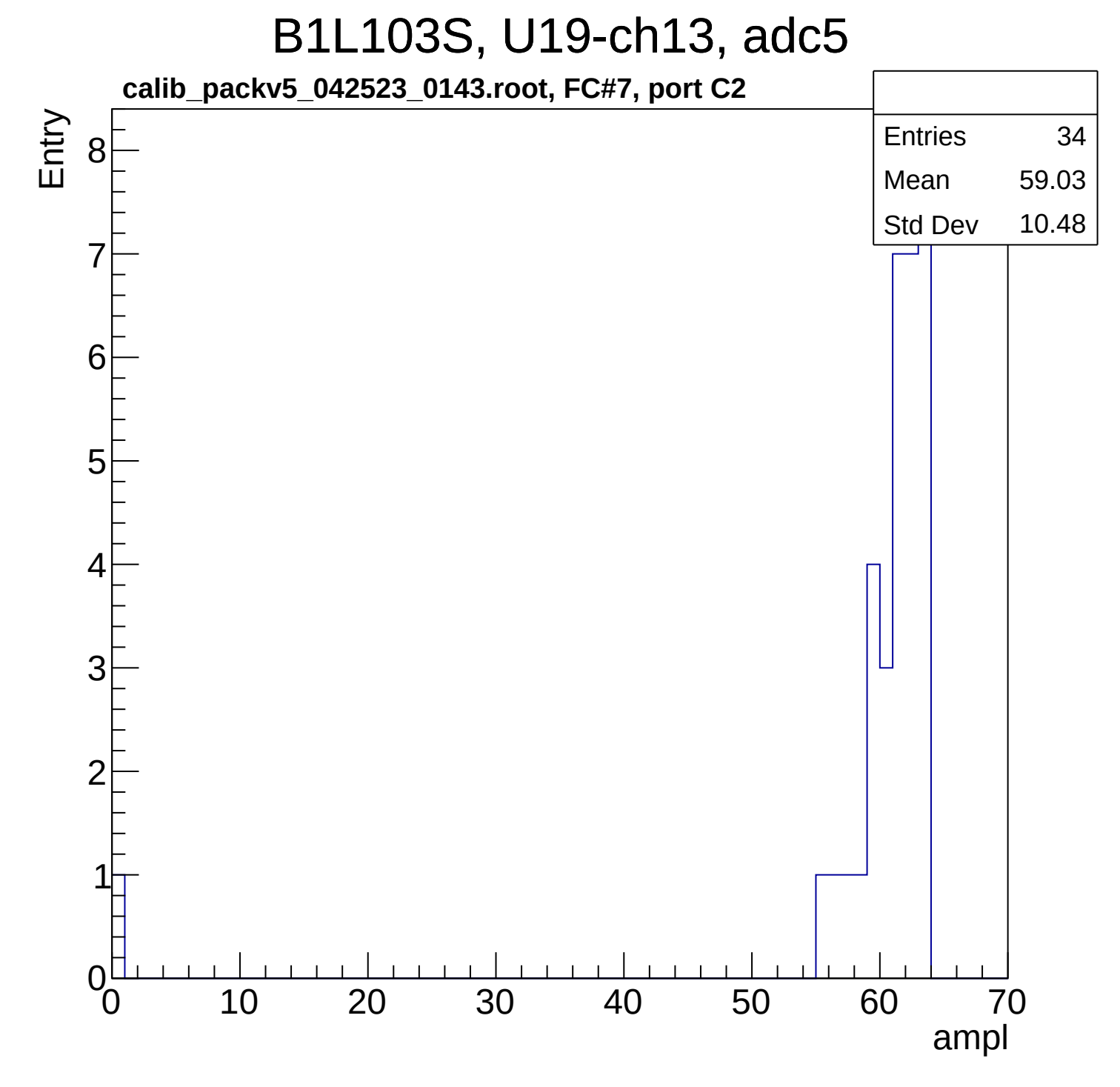
B1L103S, U19-ch13, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59.03
Std Dev 10.48
Entry
ampl
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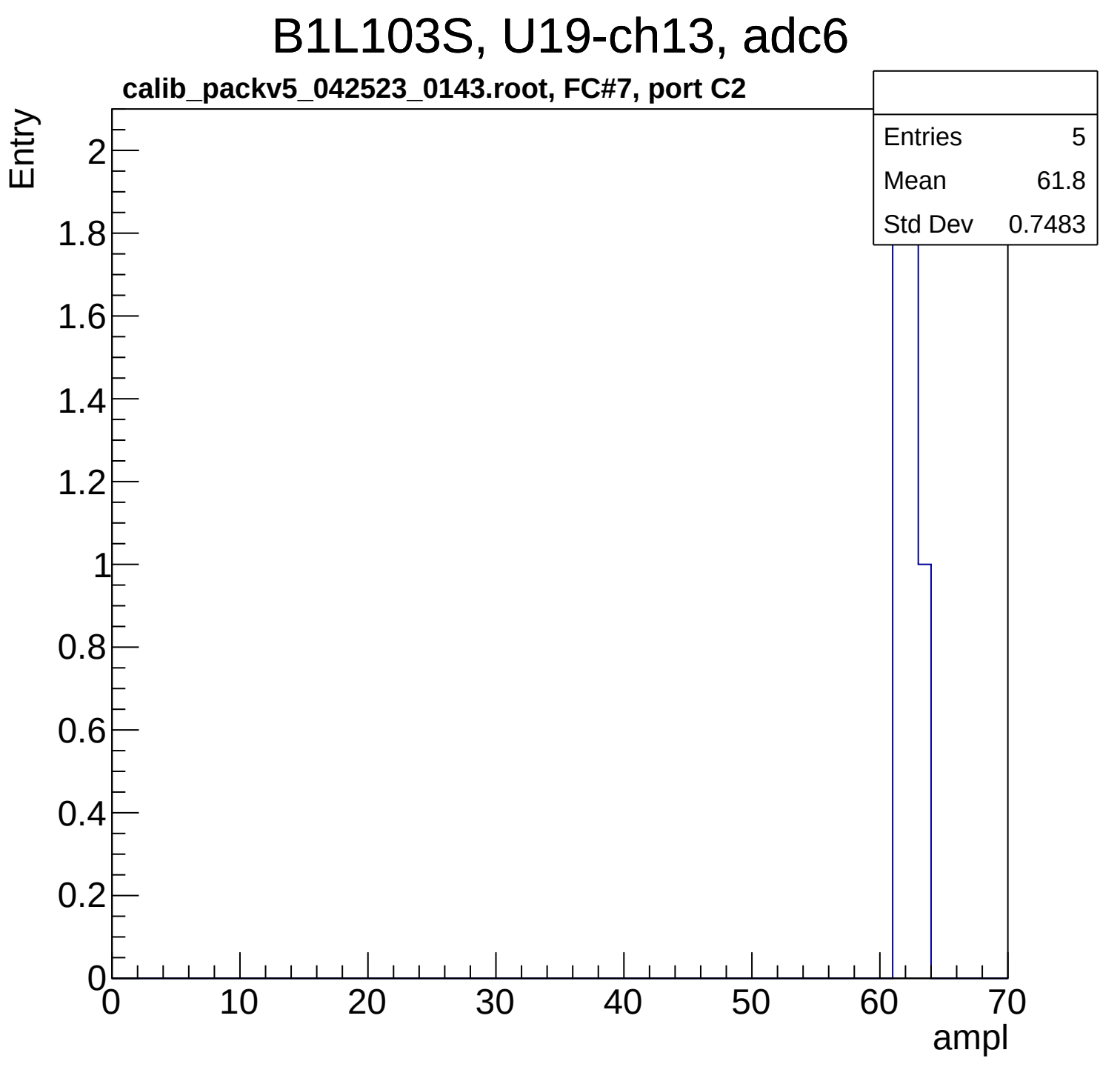
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch13, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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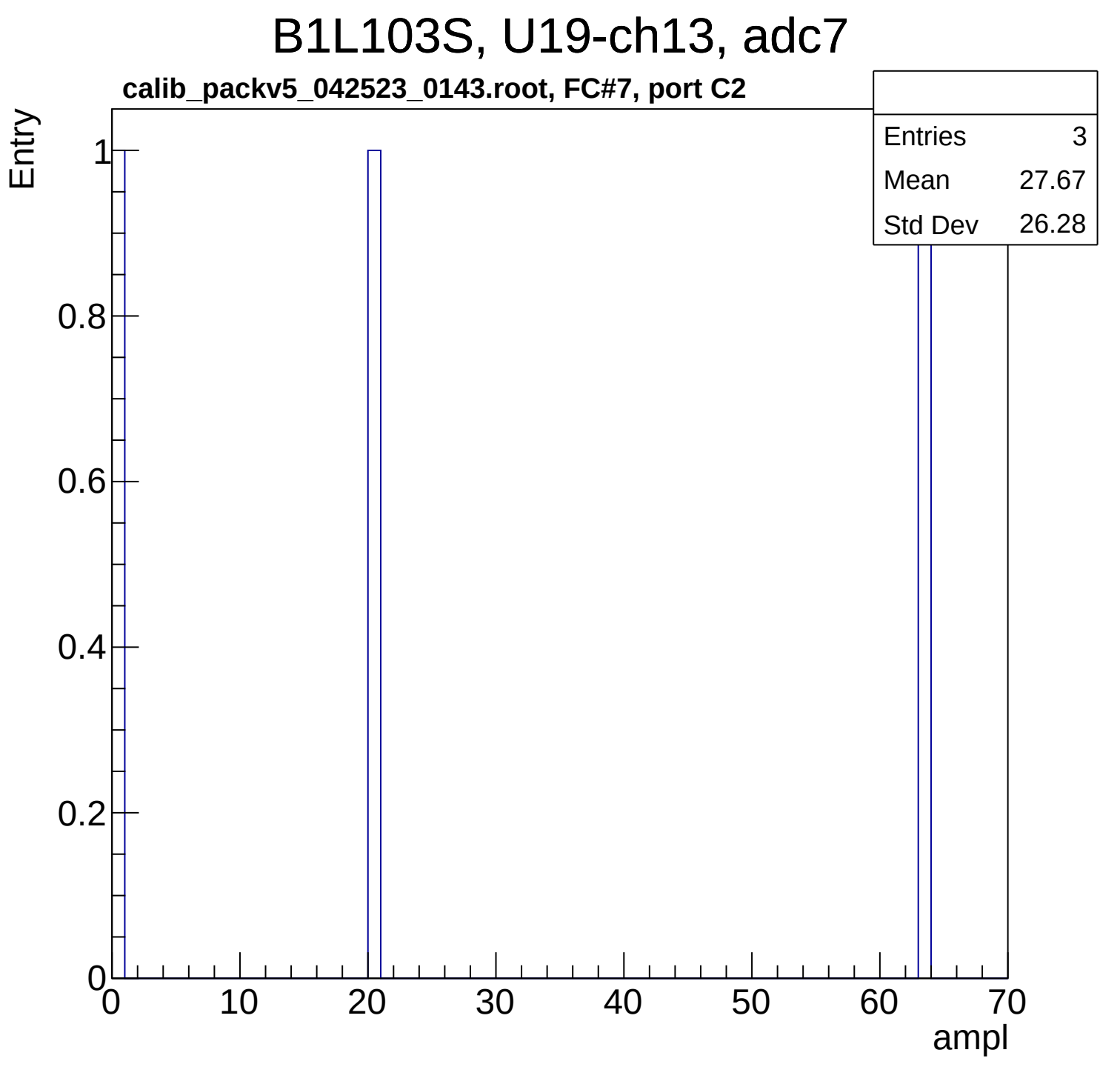
B1L103S, U19-ch13, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 27.67
Std Dev 26.28
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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20
30
40
50
60
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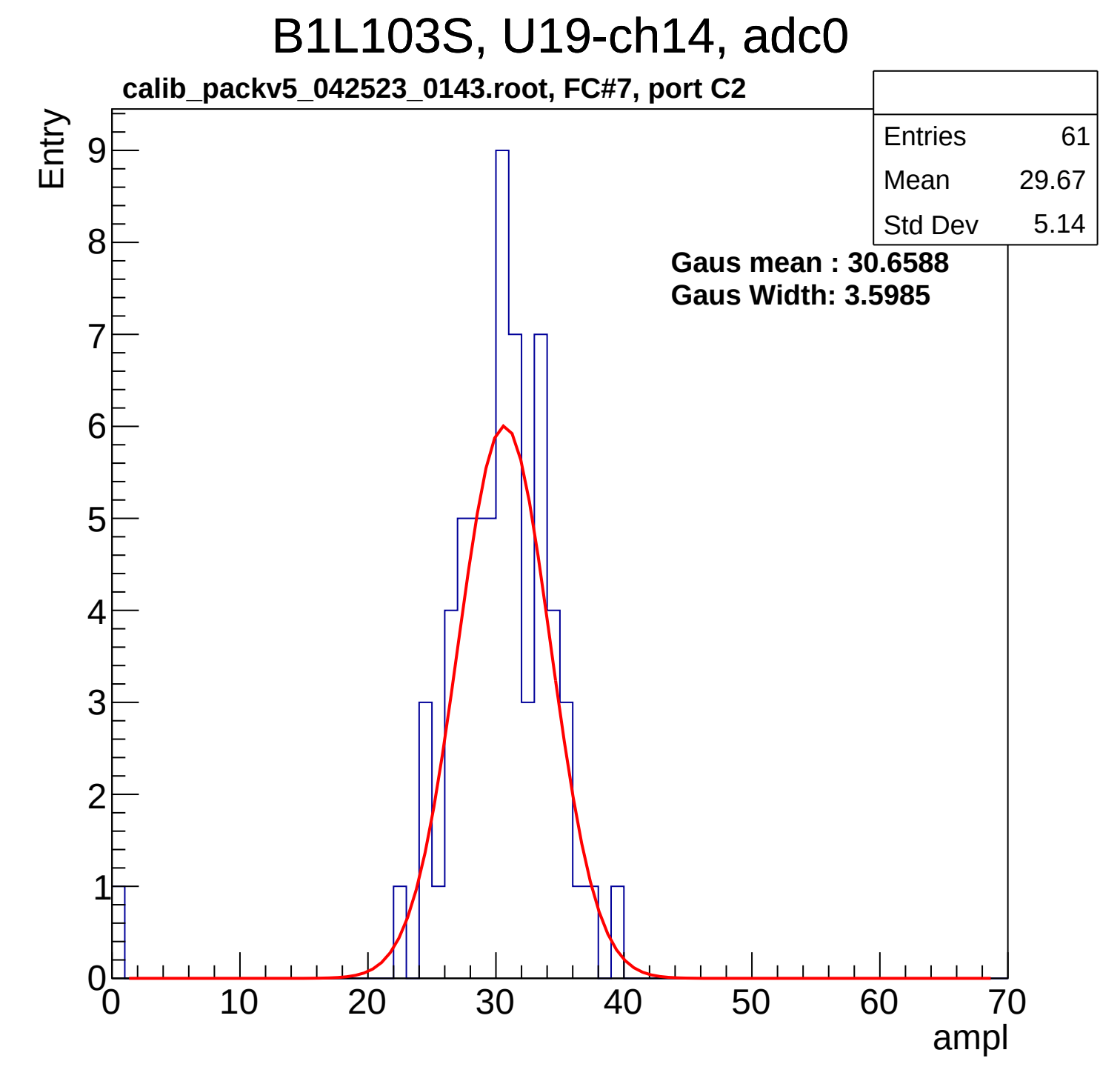
B1L103S, U19-ch14, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 29.67
Std Dev 5.14
Gaus mean : 30.6588
Gaus Width: 3.5985
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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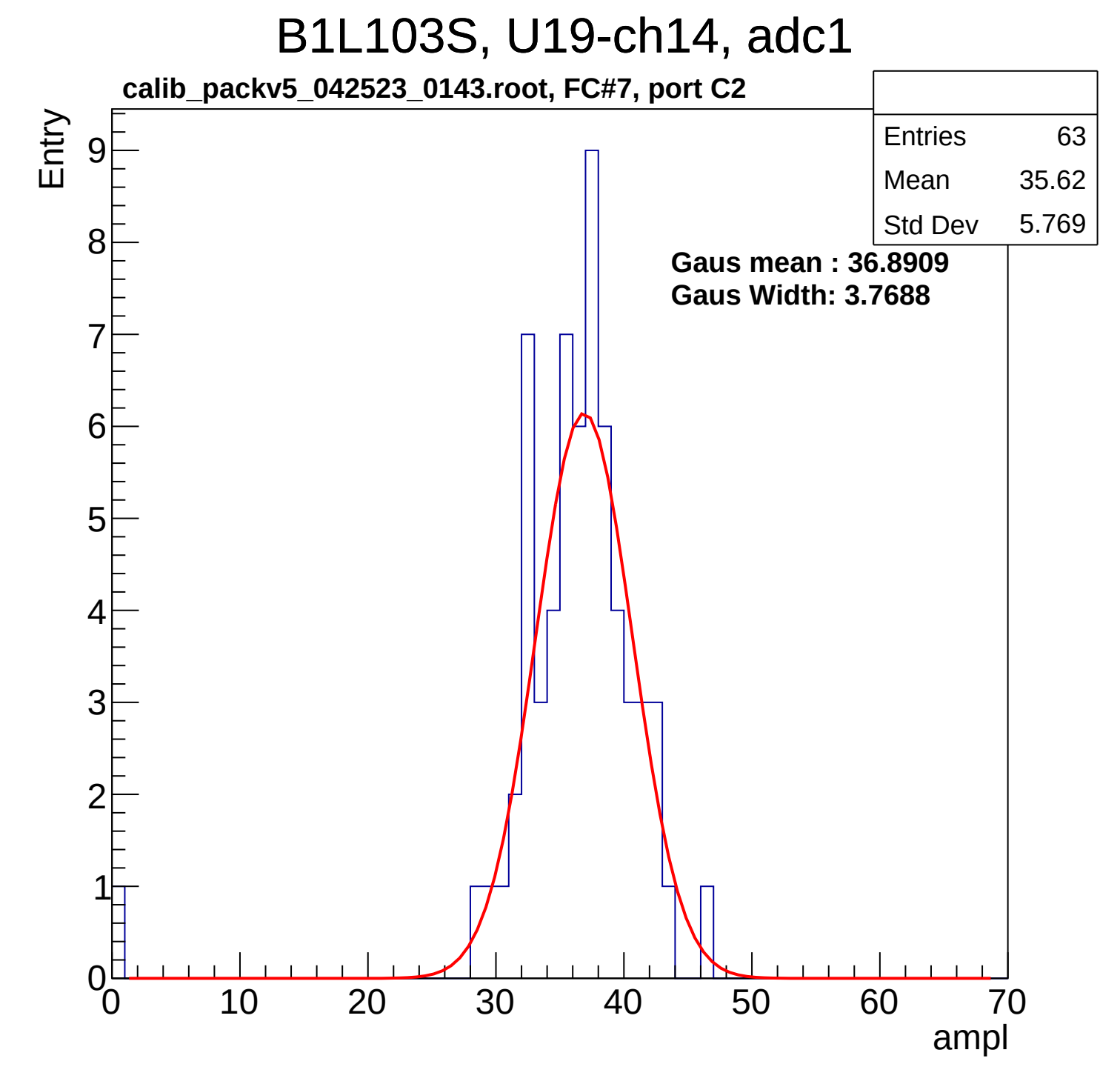
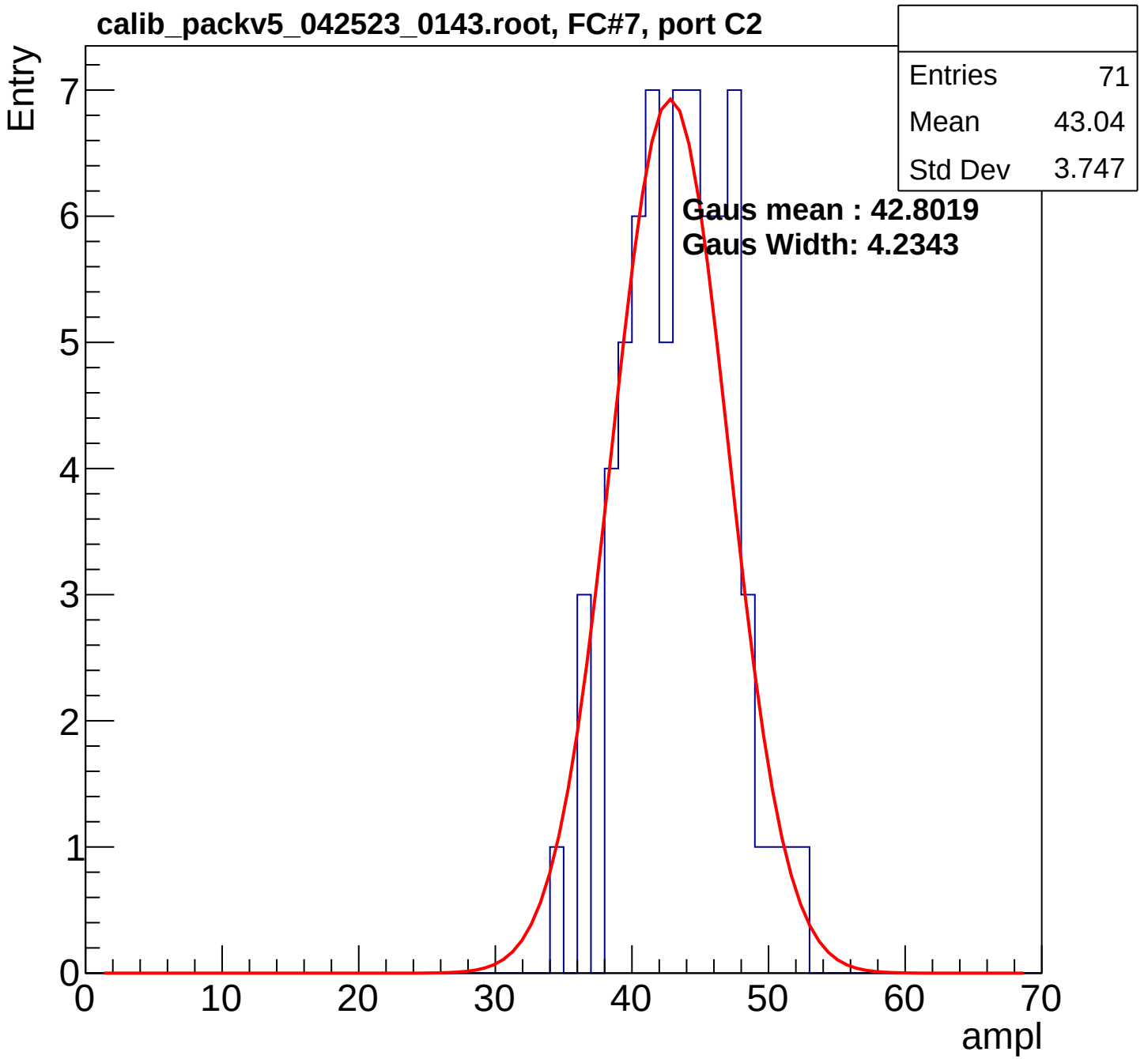

B1L103S, U19-ch14, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.62
Std Dev 5.769
Gaus mean : 36.8909
Gaus Width: 3.7688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch14, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 43.04
Std Dev 3.747
Gaus mean : 42.8019
Gaus Width: 4.2343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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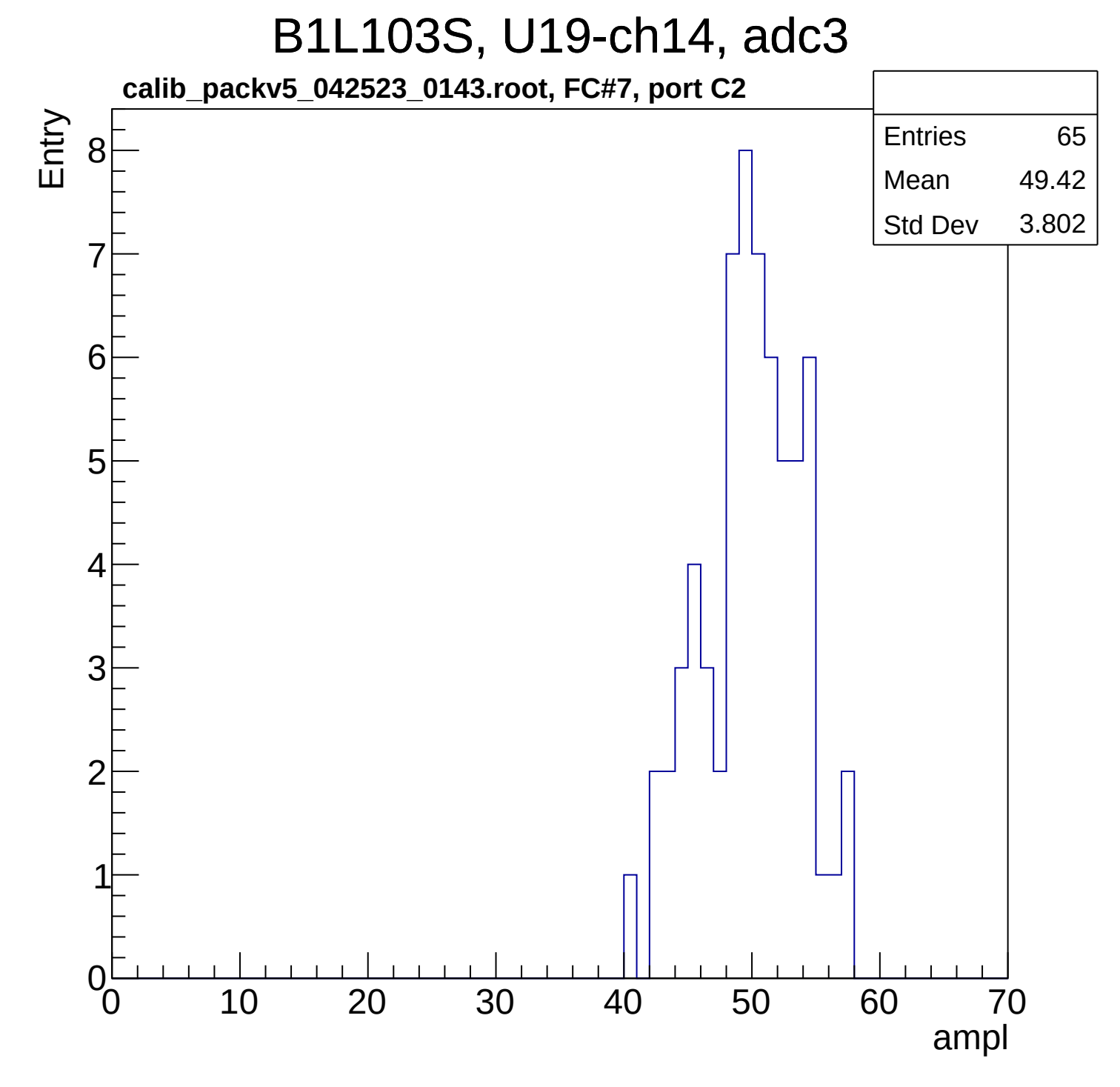
B1L103S, U19-ch14, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 49.42
Std Dev 3.802
Entry
ampl
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2
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4
5
6
7
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0
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60
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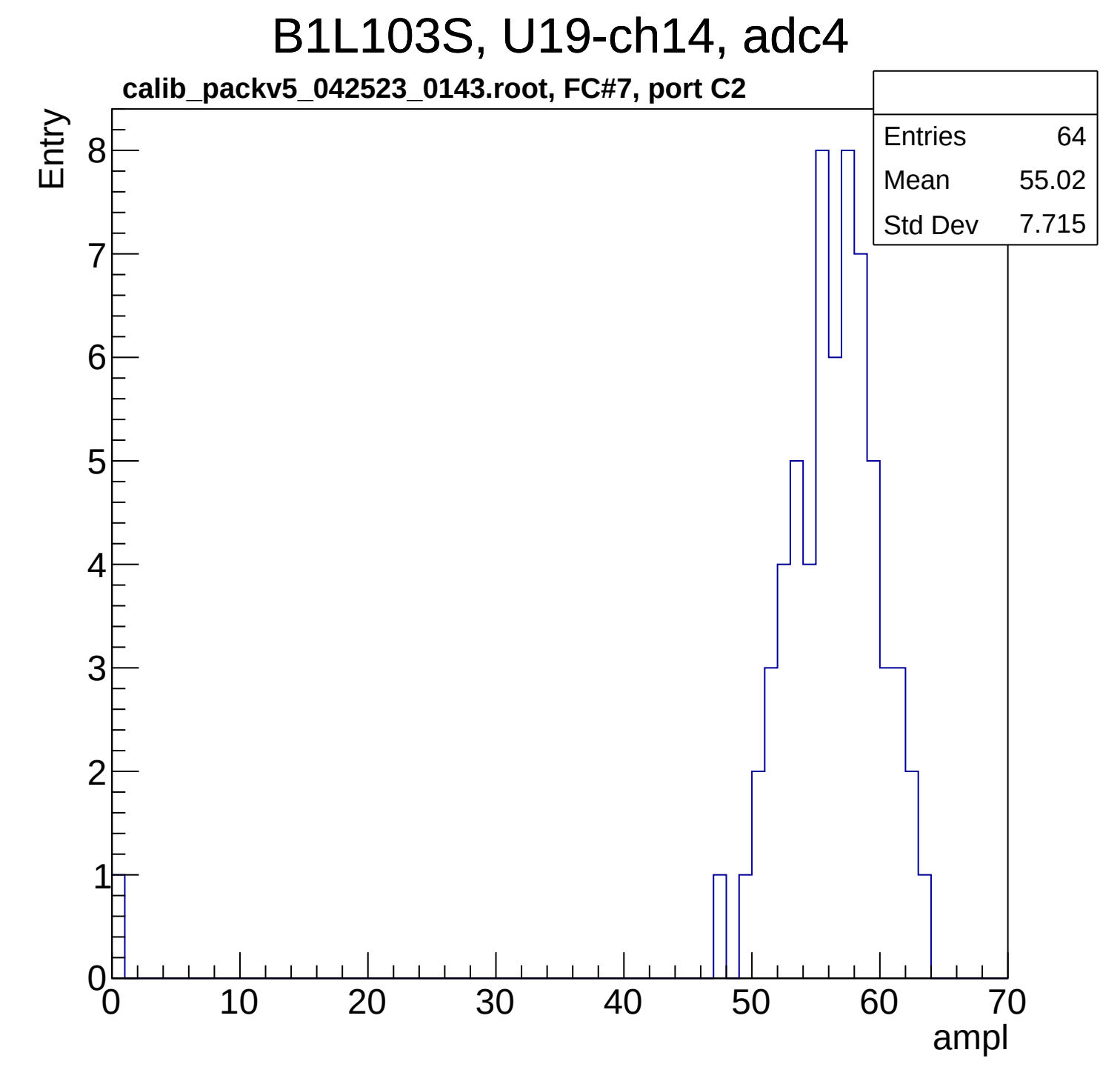
B1L103S, U19-ch14, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.02
Std Dev 7.715
Entry
ampl
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3
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5
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7
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0
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40
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60
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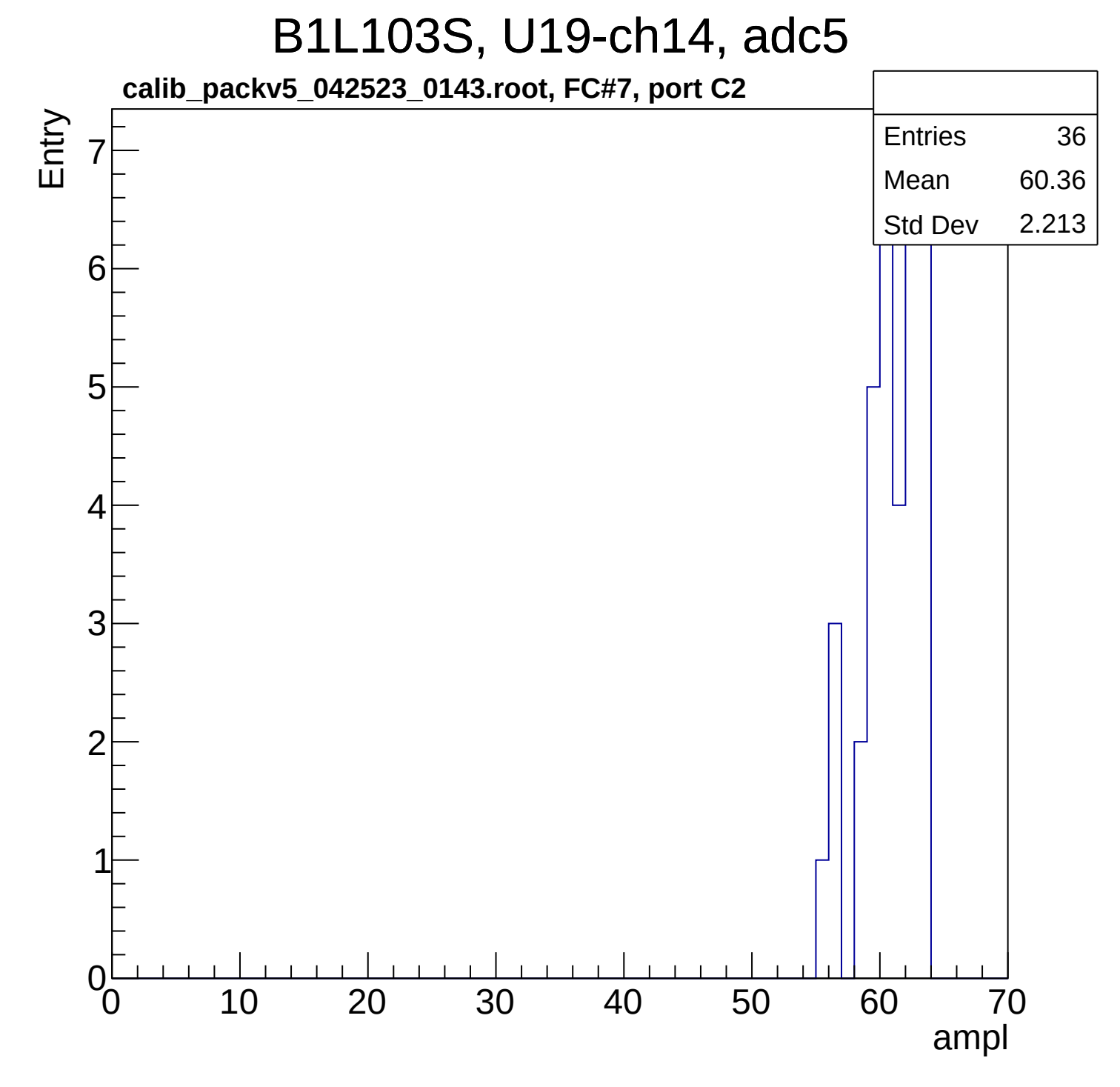
B1L103S, U19-ch14, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 60.36
Std Dev 2.213
Entry
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1
2
3
4
5
6
7
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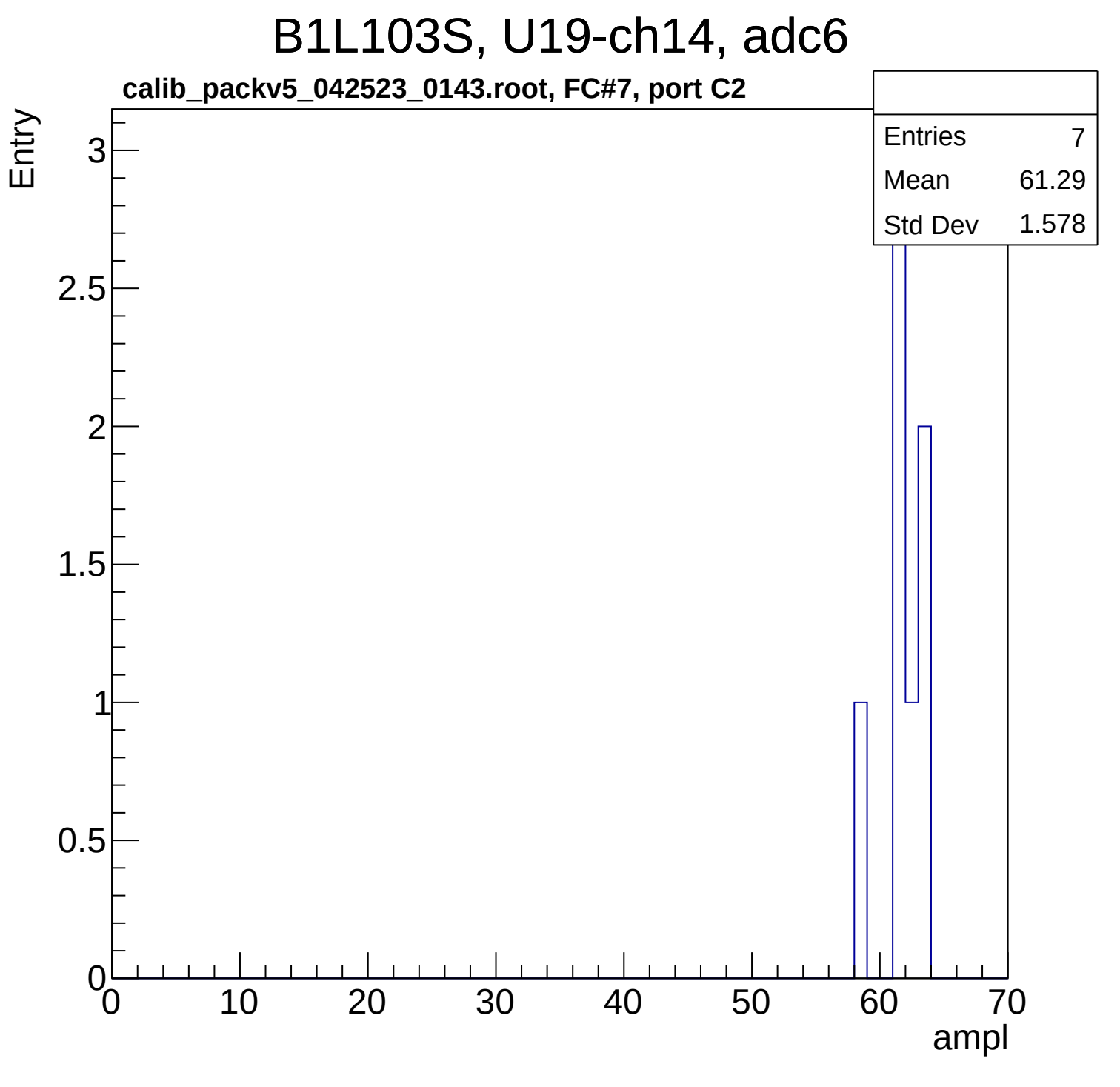
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch14, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.29
Std Dev 1.578
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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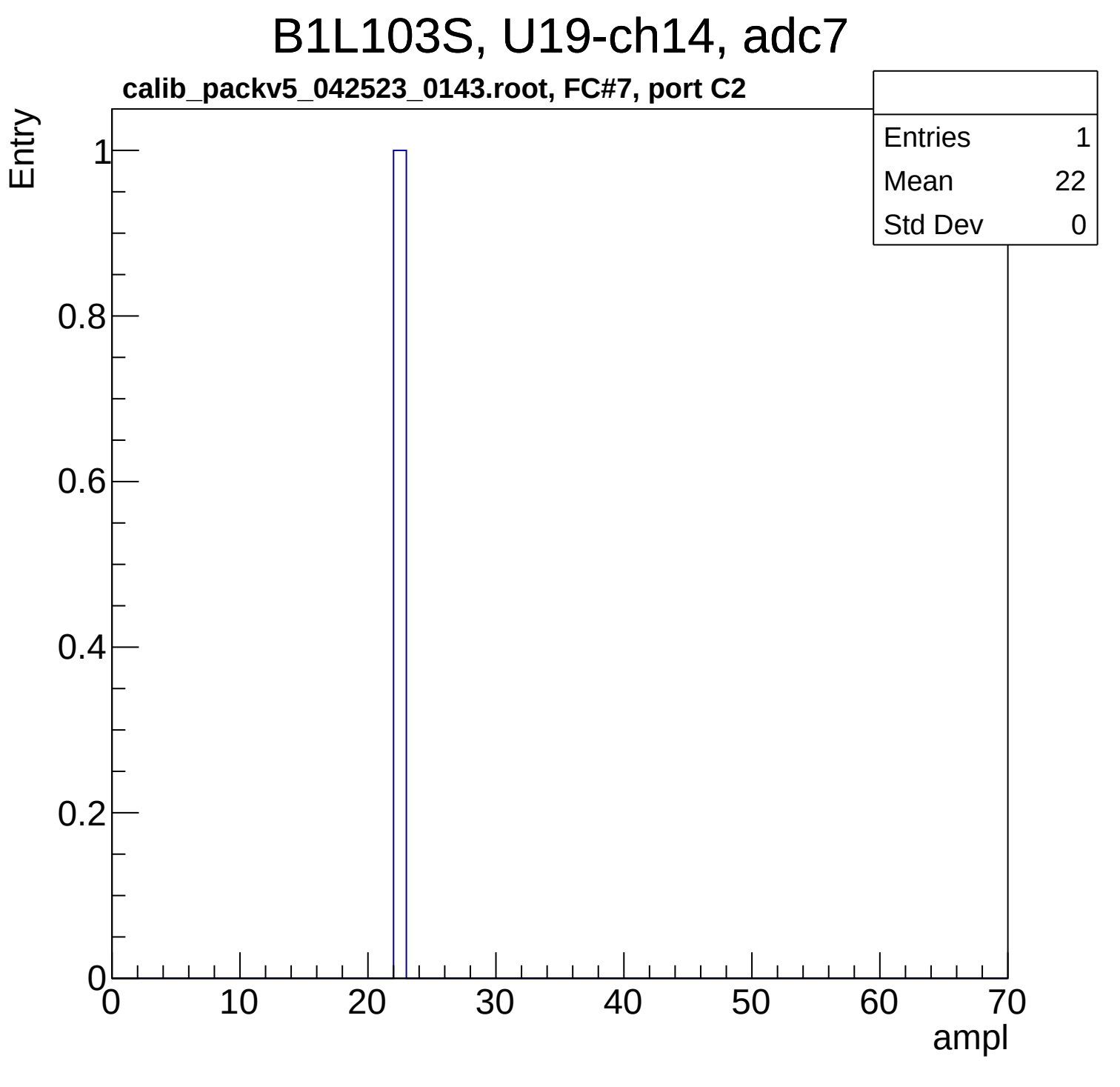
40
50
60
70


B1L103S, U19-ch14, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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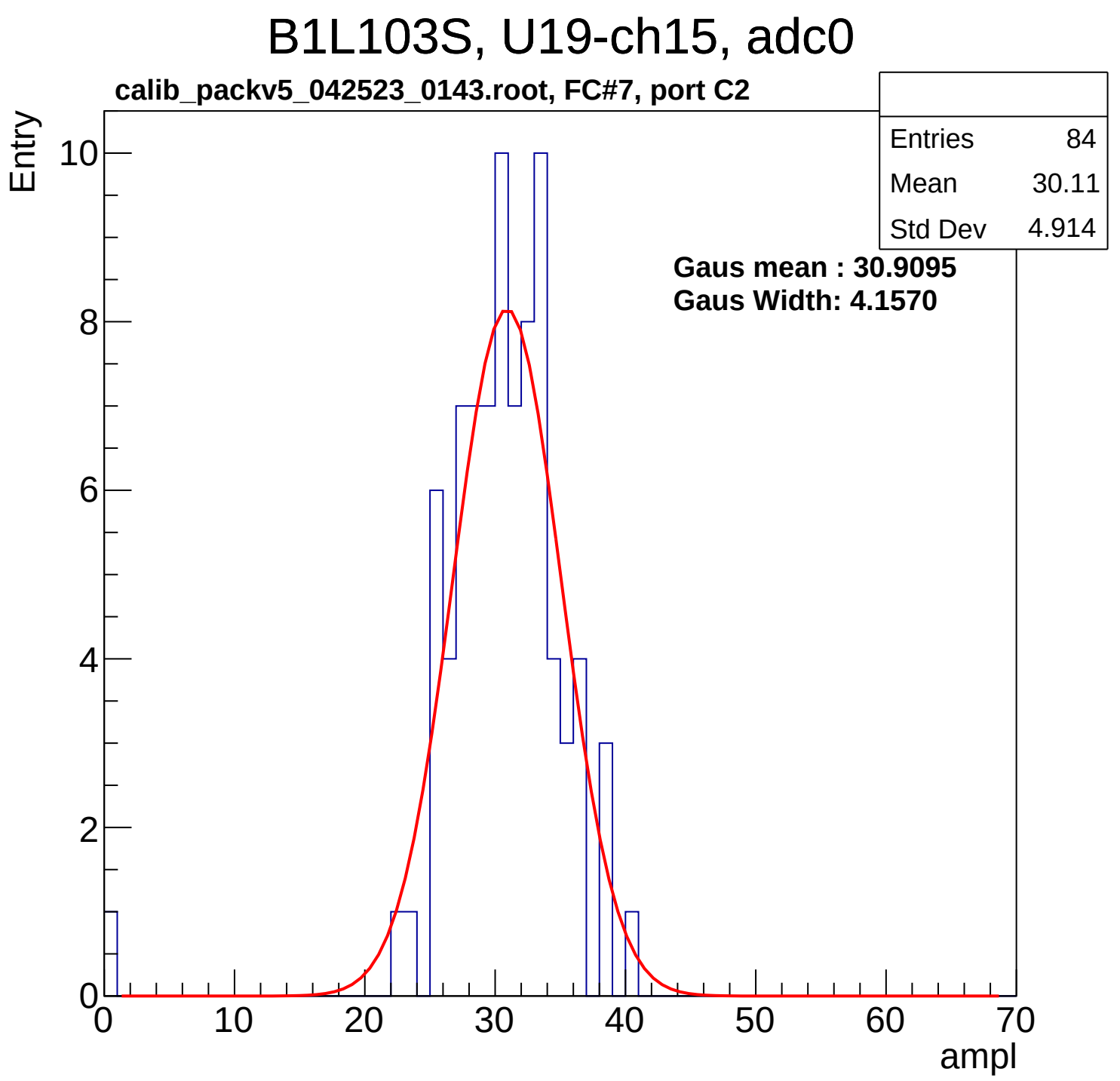
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch15, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 30.11
Std Dev 4.914
Gaus mean : 30.9095
Gaus Width: 4.1570
Entry
ampl
0
2
4
6
8
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0
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20
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40
50
60
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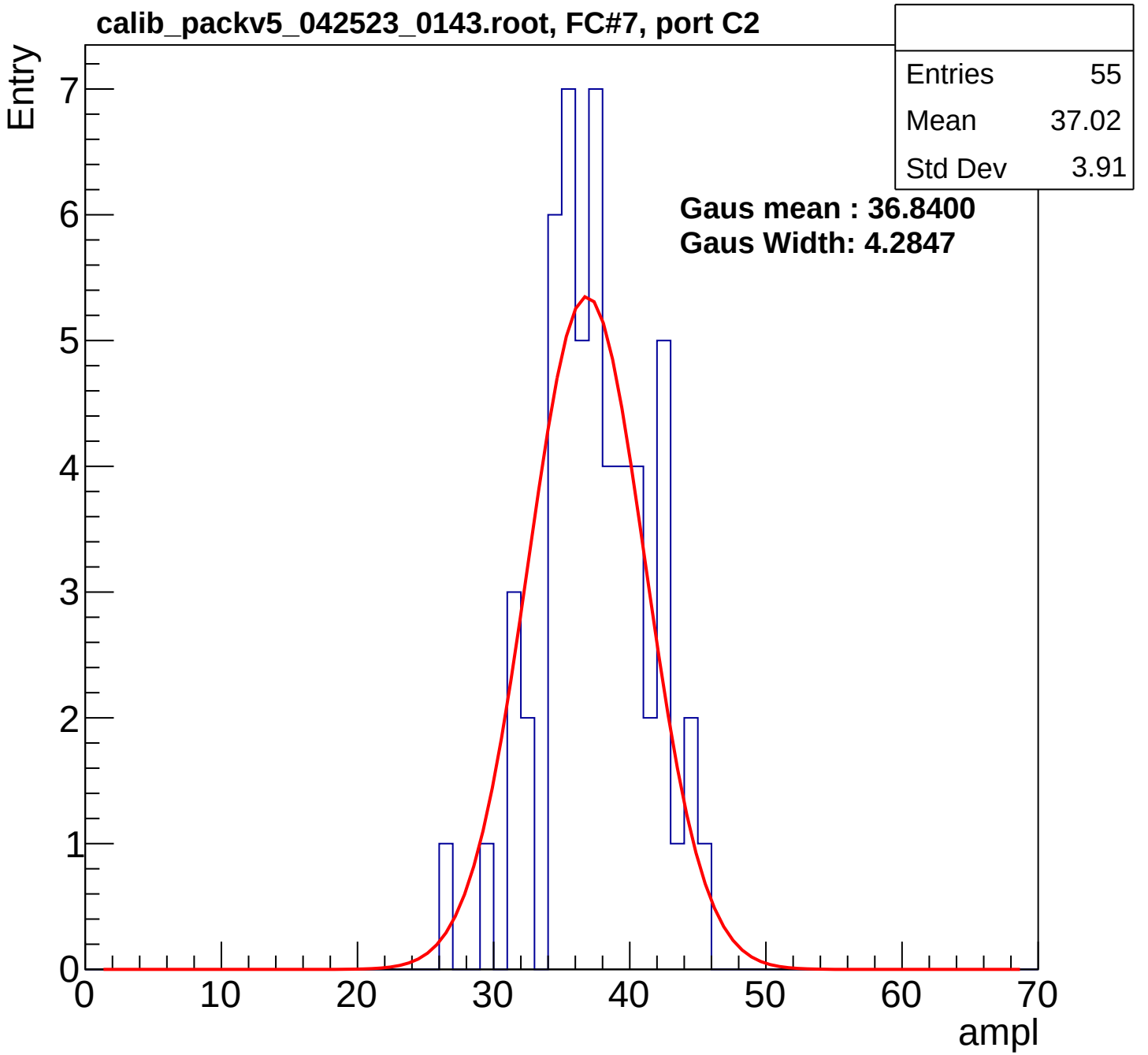
B1L103S, U19-ch15, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 37.02
Std Dev 3.91
Gaus mean : 36.8400
Gaus Width: 4.2847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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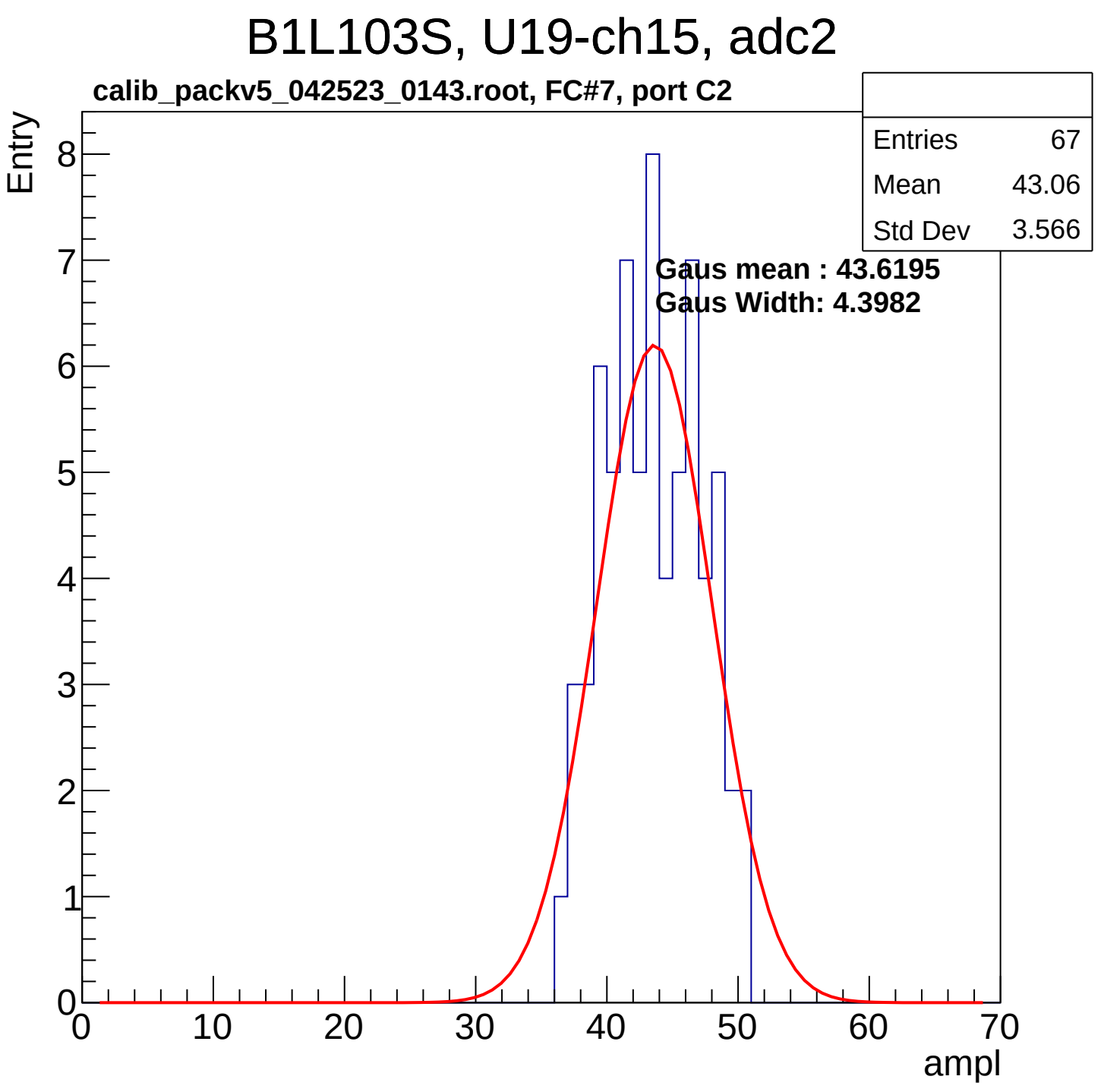
B1L103S, U19-ch15, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 43.06
Std Dev 3.566
Gaus mean : 43.6195
Gaus Width: 4.3982
Entry
ampl
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2
3
4
5
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0
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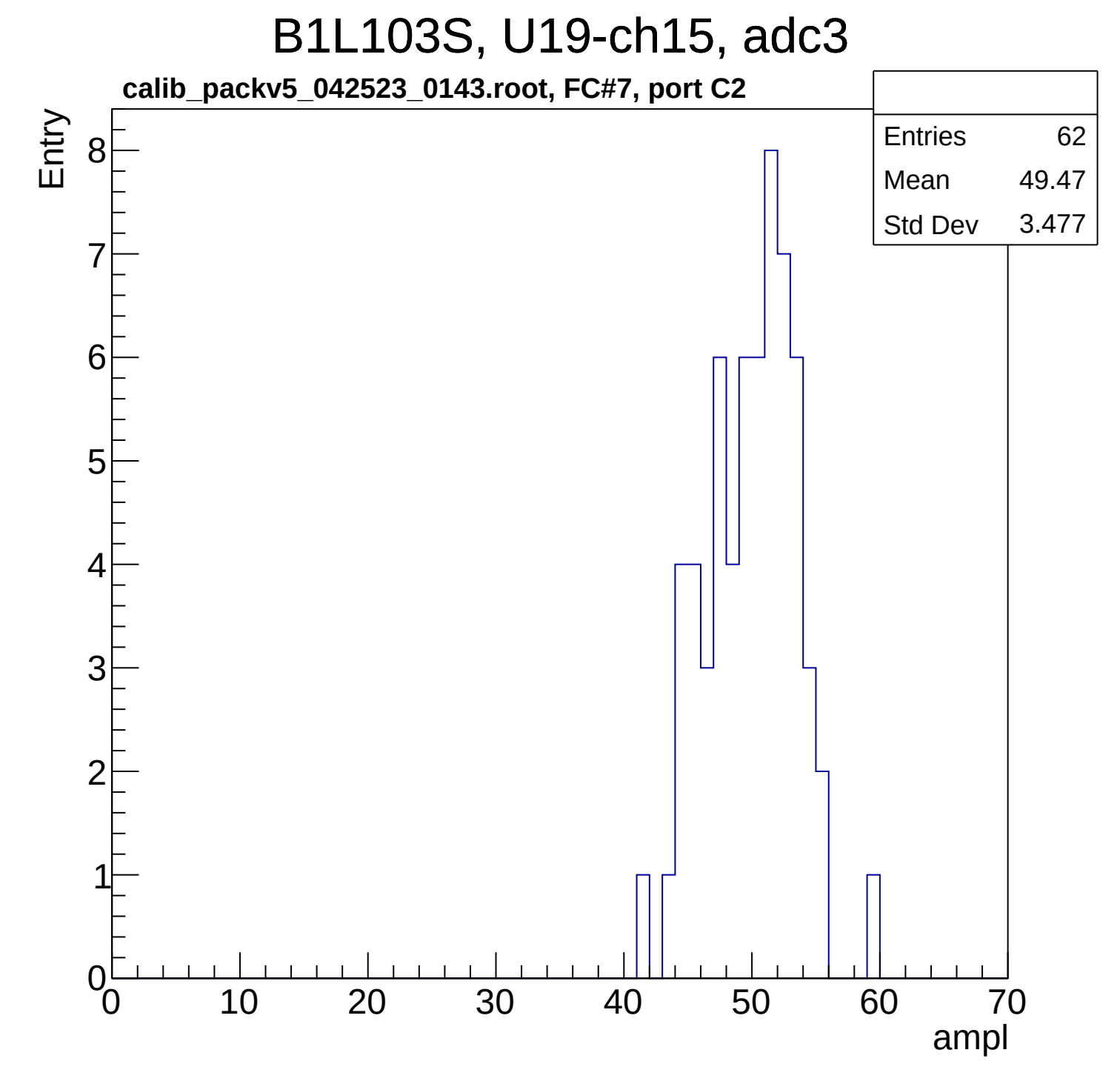
B1L103S, U19-ch15, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.47
Std Dev 3.477
Entry
ampl
0
1
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5
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0
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60
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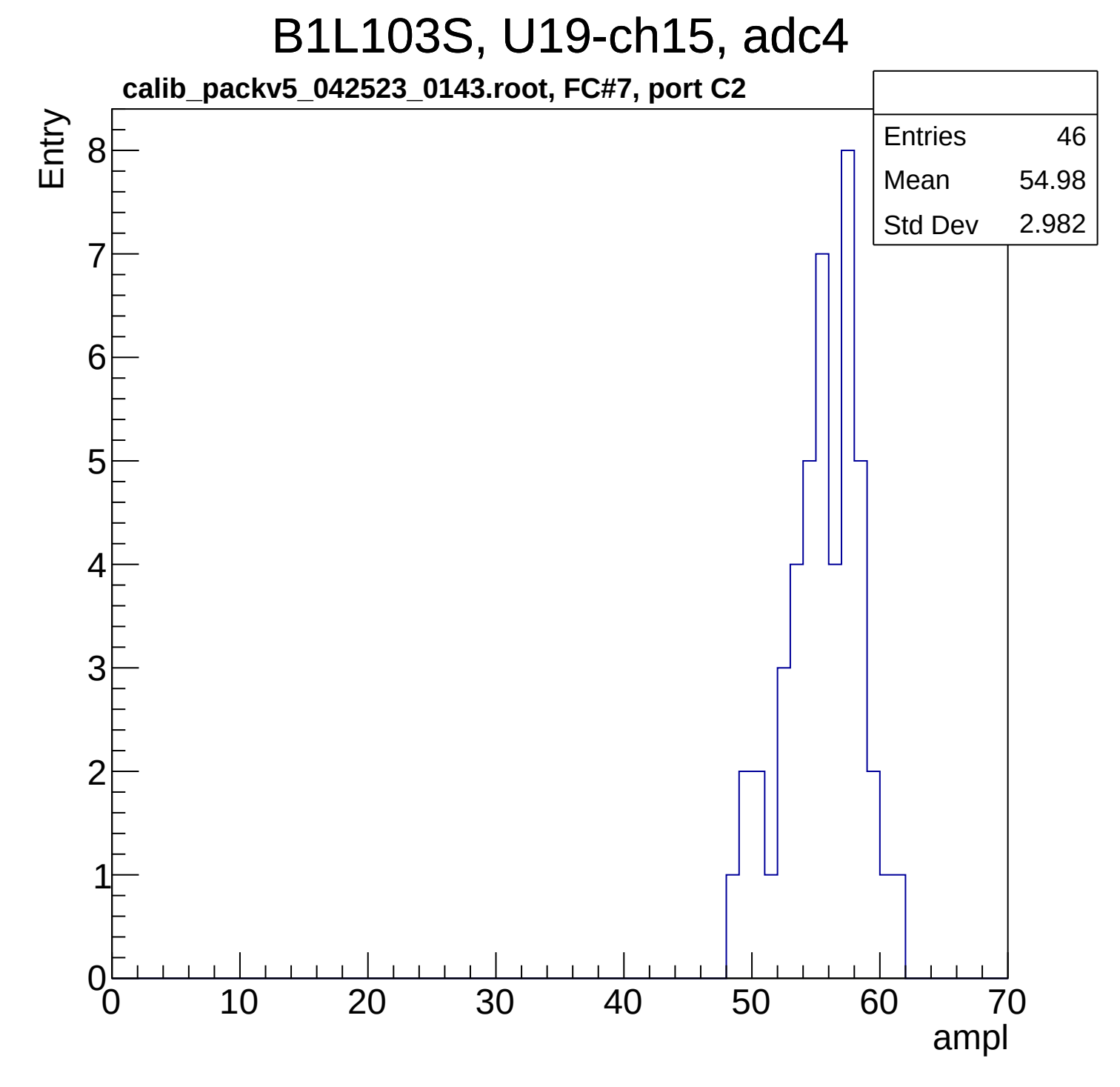
B1L103S, U19-ch15, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 54.98
Std Dev 2.982
Entry
0
1
2
3
4
5
6
7
8
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10
20
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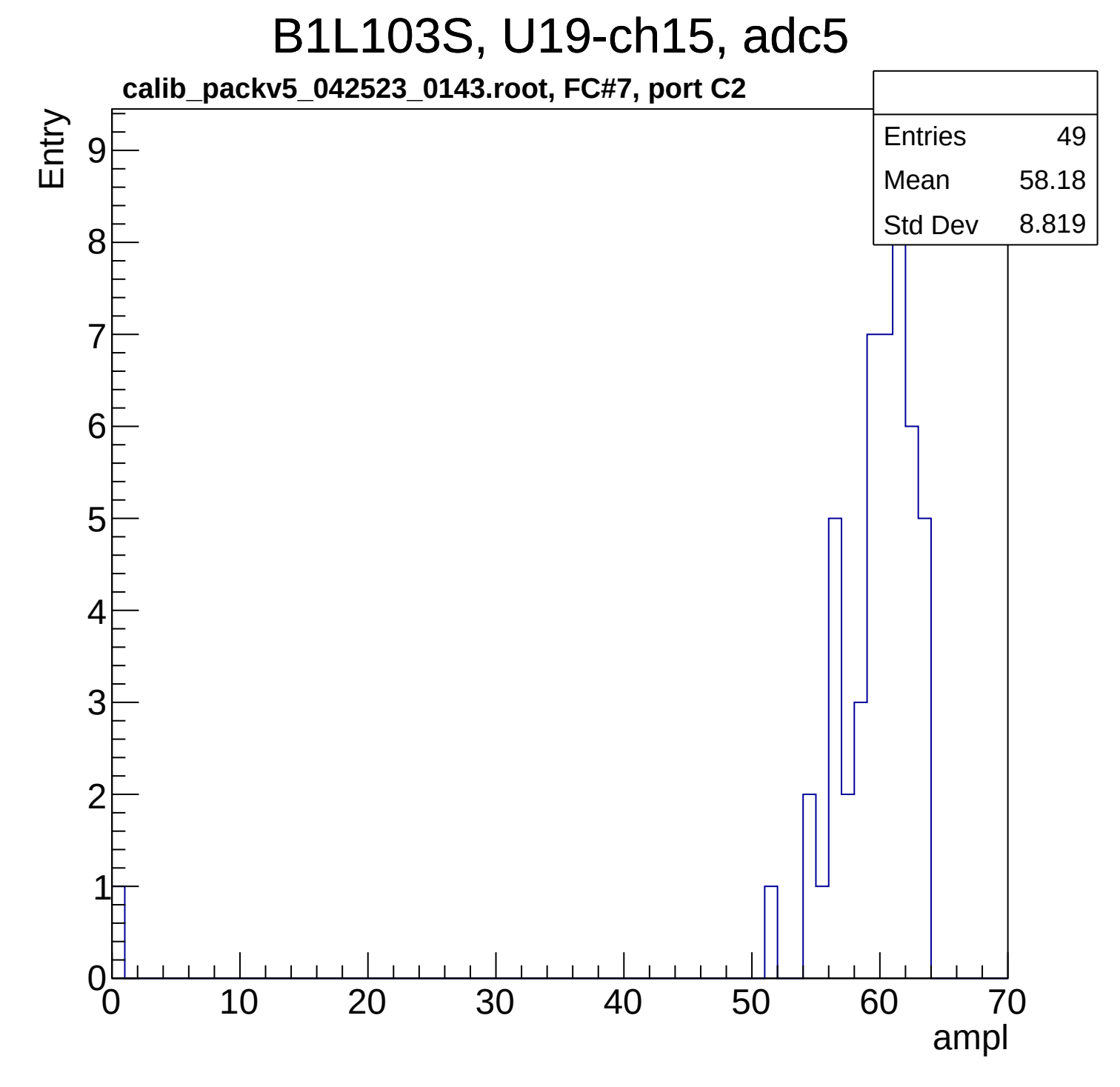
50
60
70
ampl


B1L103S, U19-ch15, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.18
Std Dev 8.819
Entry
ampl
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4
5
6
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8
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0
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40
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60
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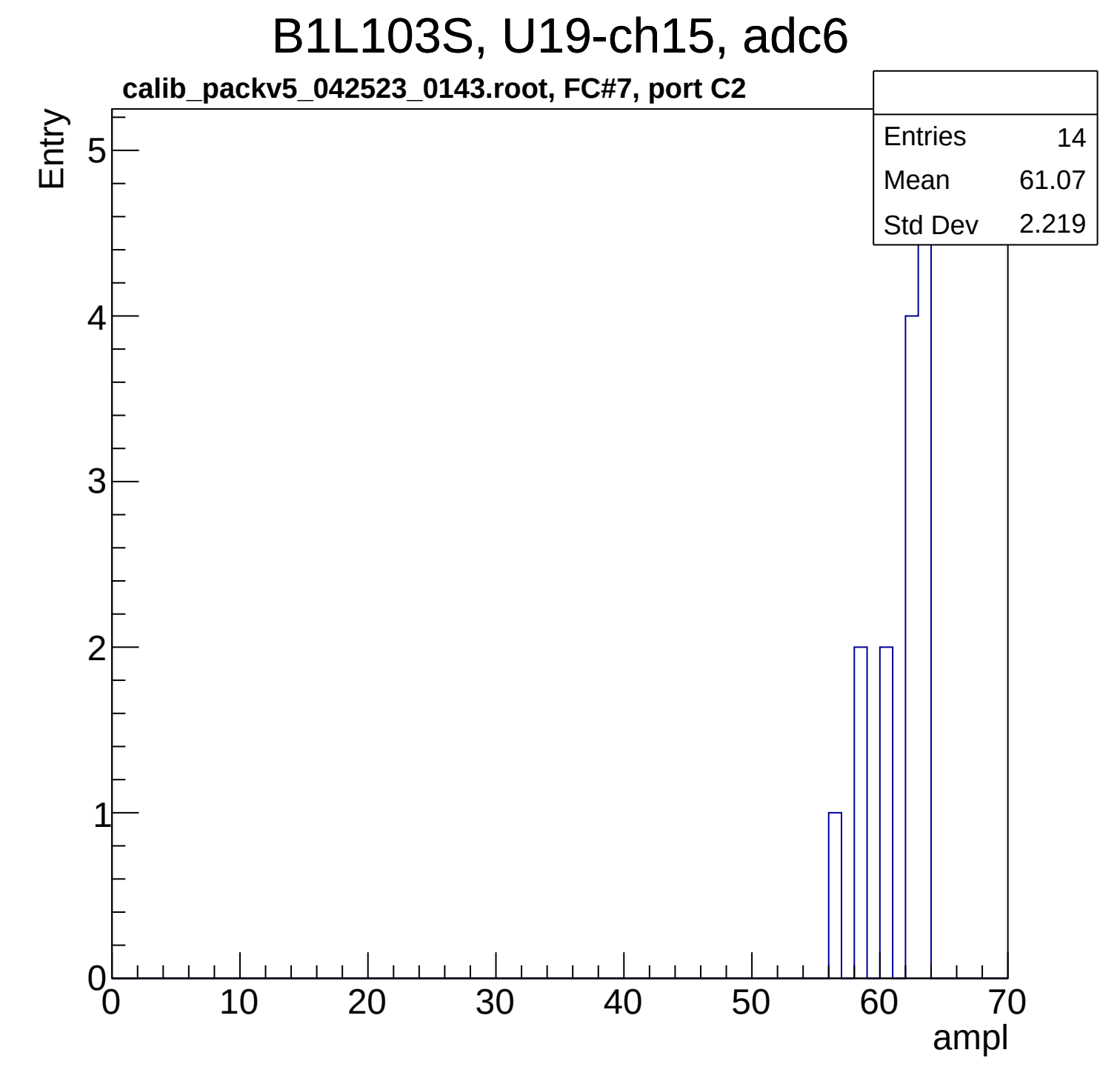
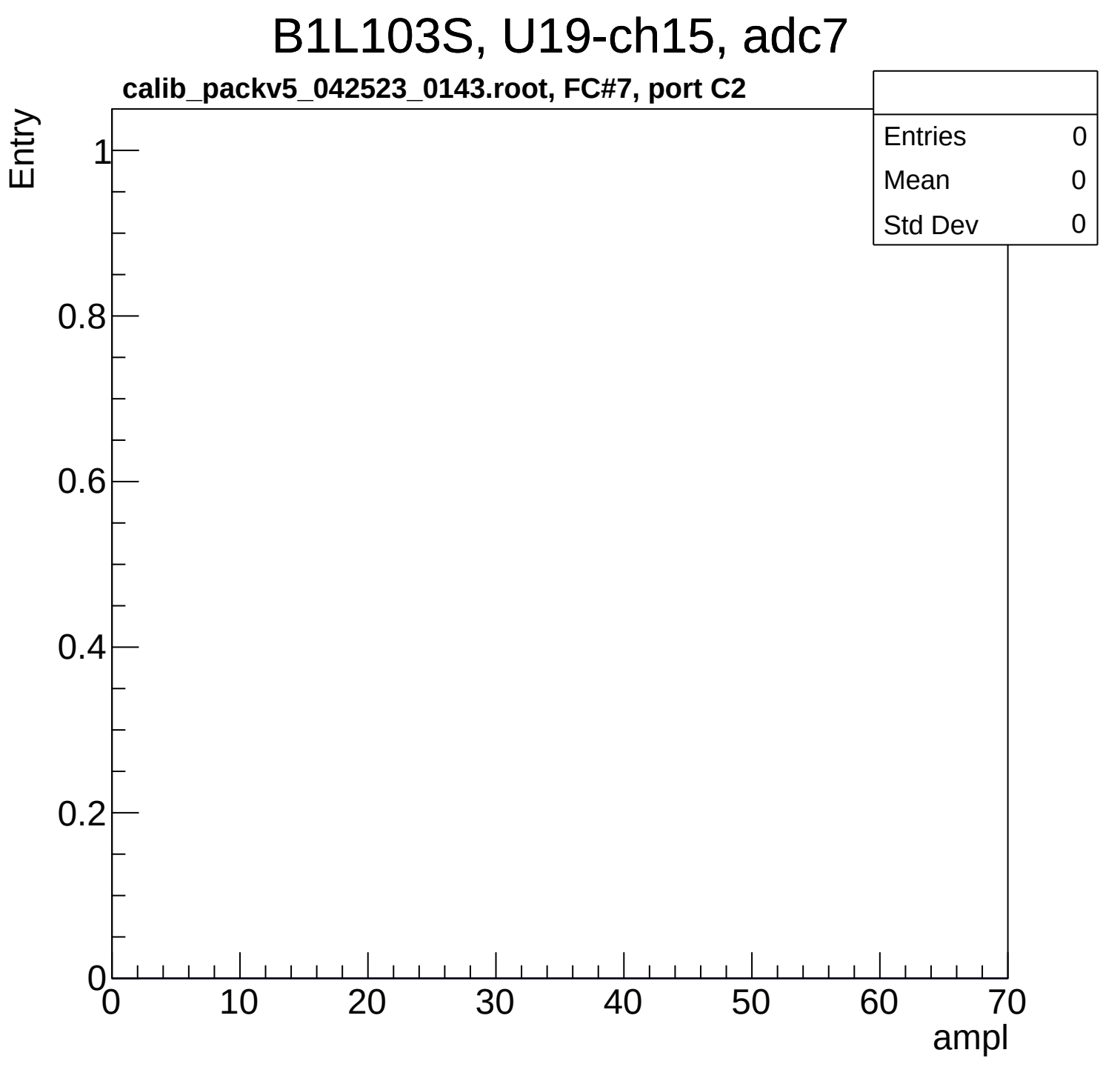

B1L103S, U19-ch15, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 61.07
Std Dev 2.219
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch15, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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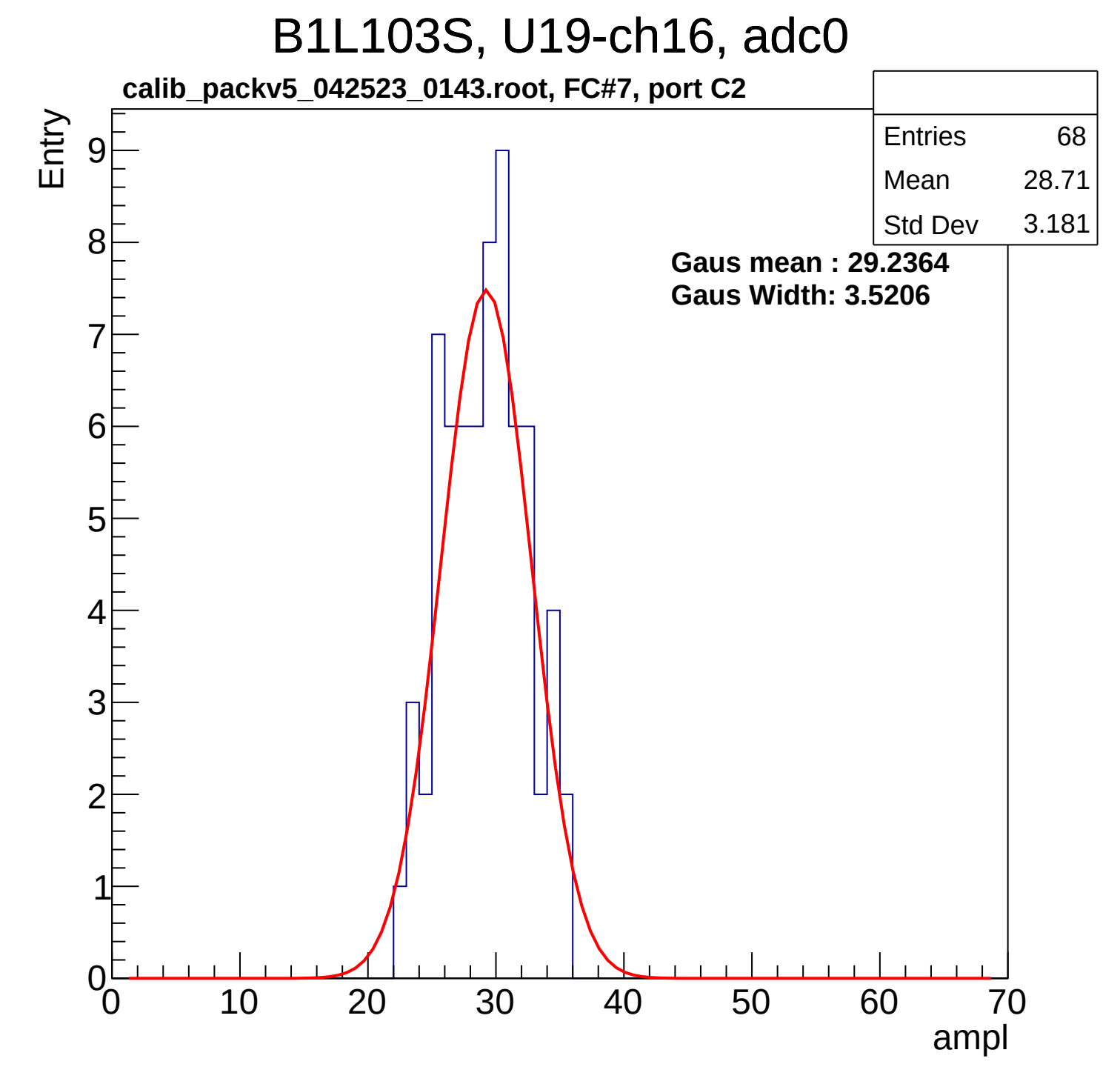
30
40
50
60
70
ampl


B1L103S, U19-ch16, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 28.71
Std Dev 3.181
Gaus mean : 29.2364
Gaus Width: 3.5206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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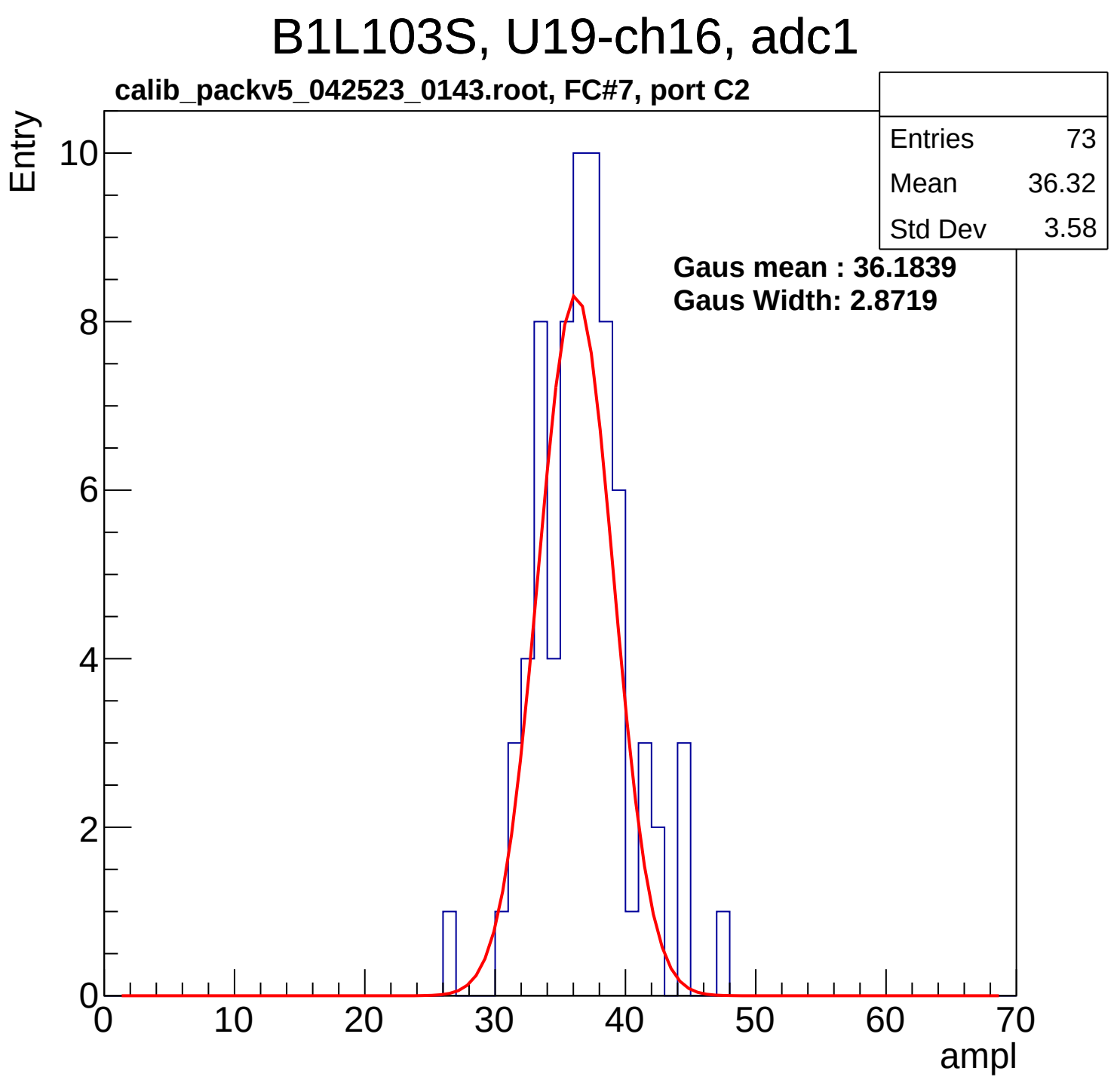
B1L103S, U19-ch16, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.32
Std Dev 3.58
Gaus mean : 36.1839
Gaus Width: 2.8719
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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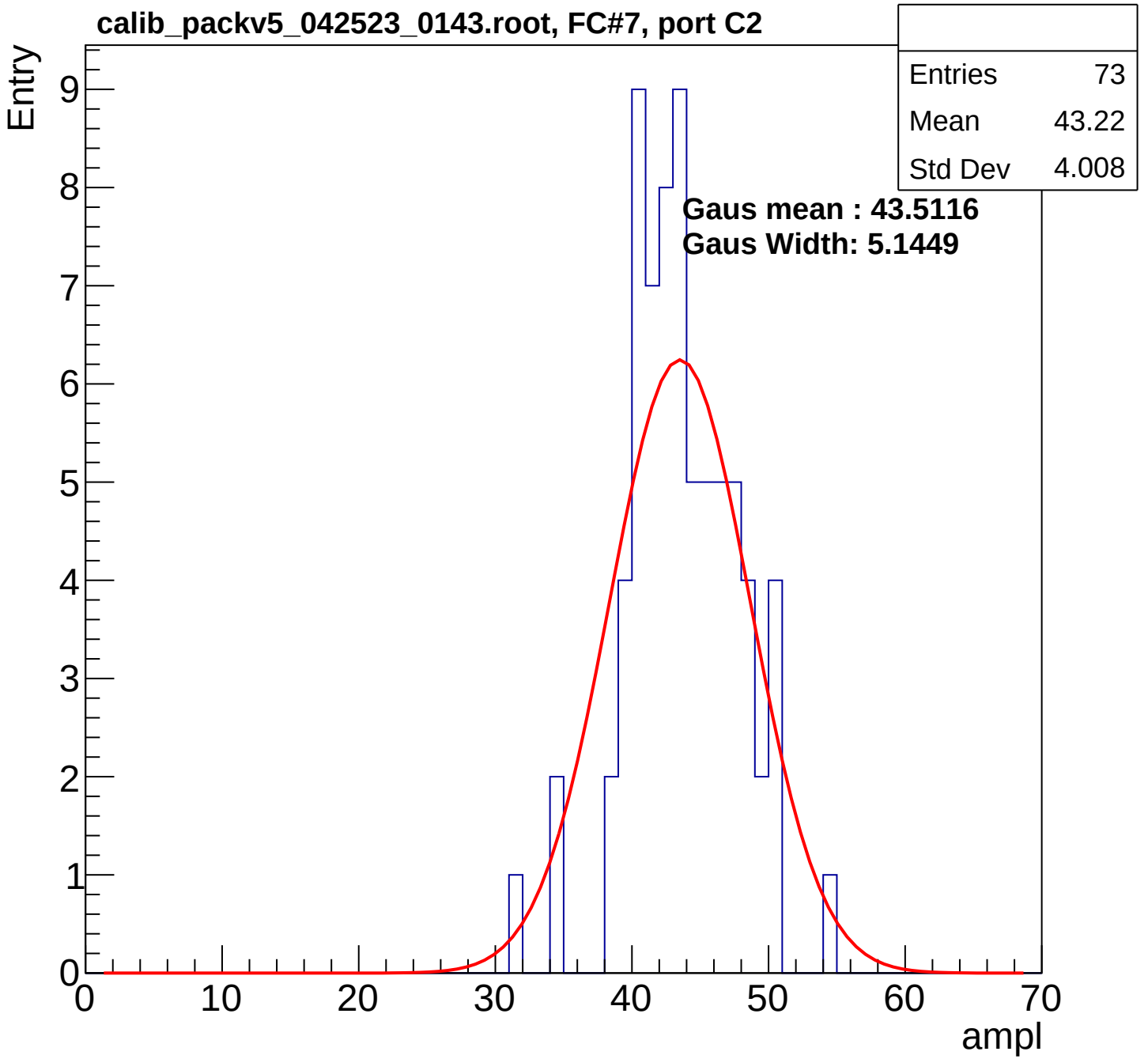
B1L103S, U19-ch16, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 43.22
Std Dev 4.008
Gaus mean : 43.5116
Gaus Width: 5.1449
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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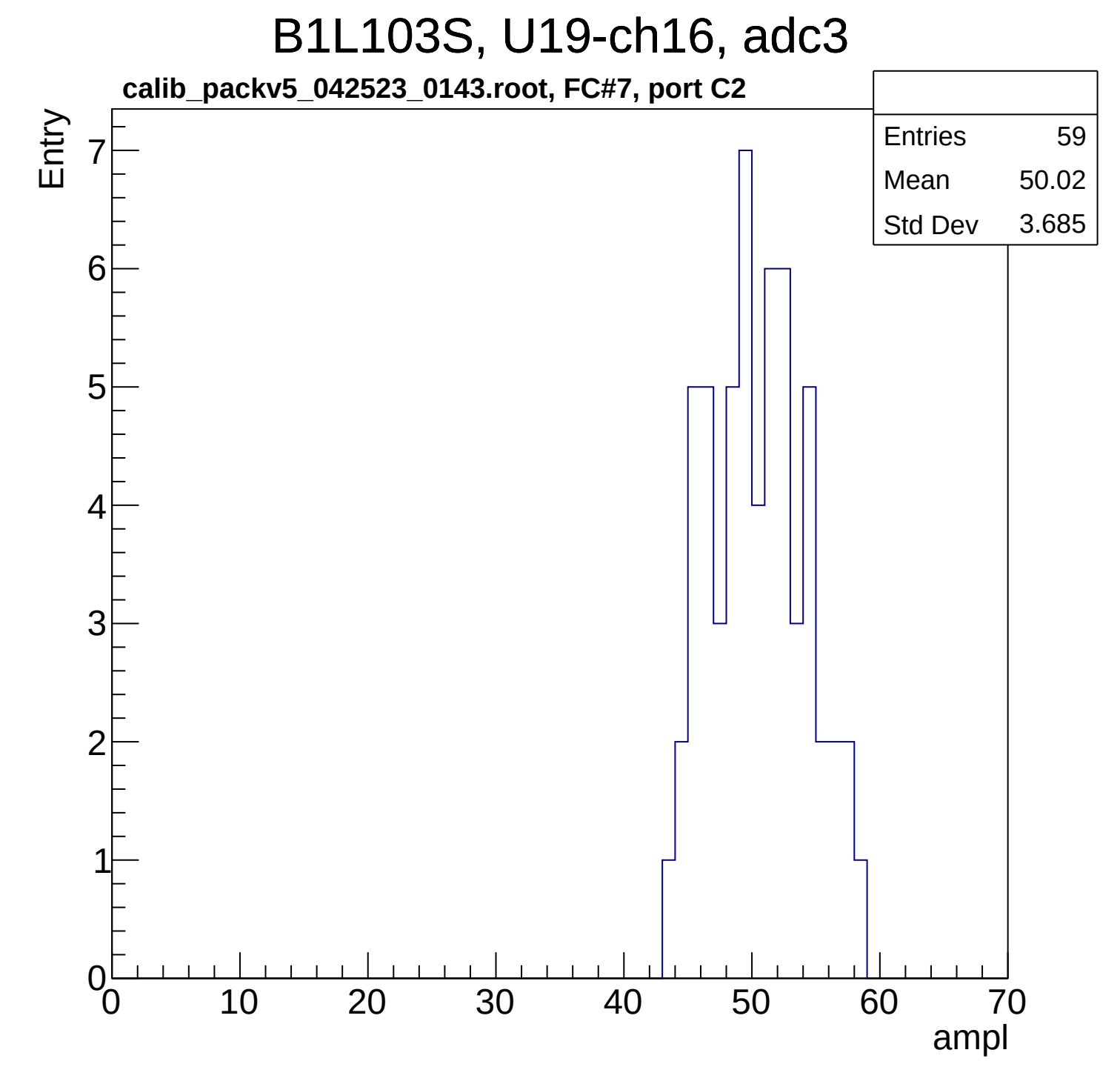
B1L103S, U19-ch16, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 50.02
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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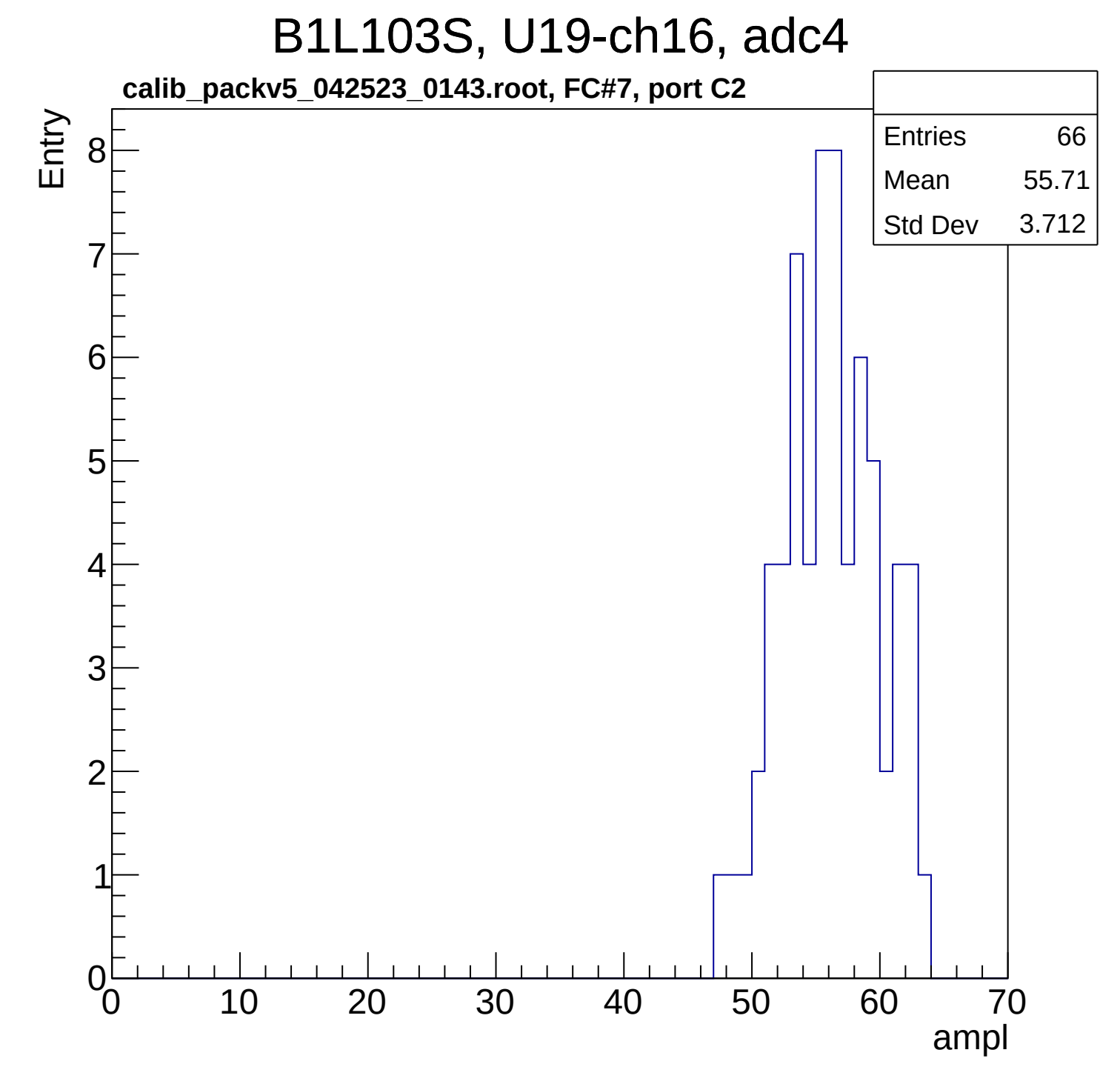
B1L103S, U19-ch16, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 55.71
Std Dev 3.712
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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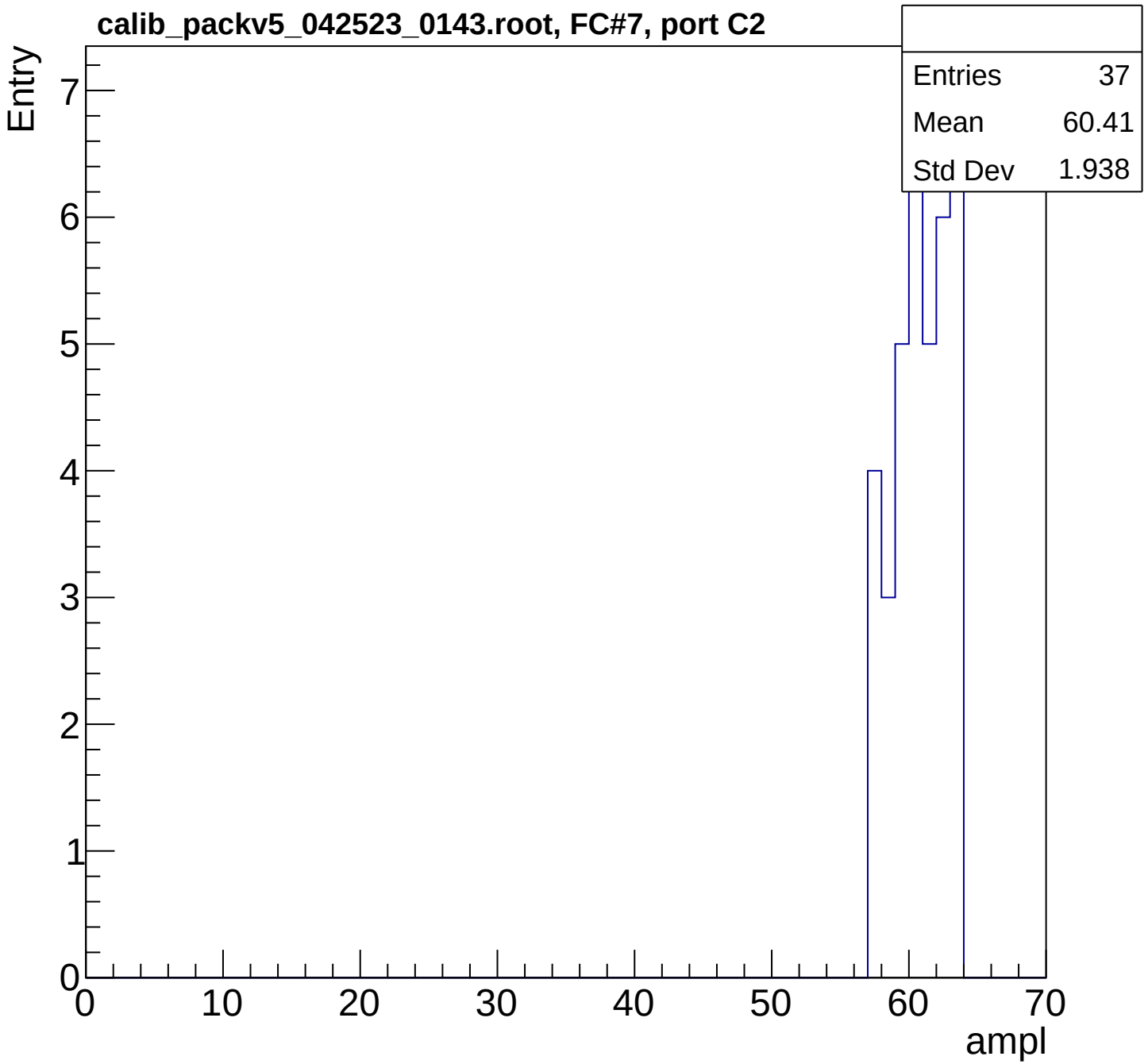
B1L103S, U19-ch16, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.41
Std Dev 1.938
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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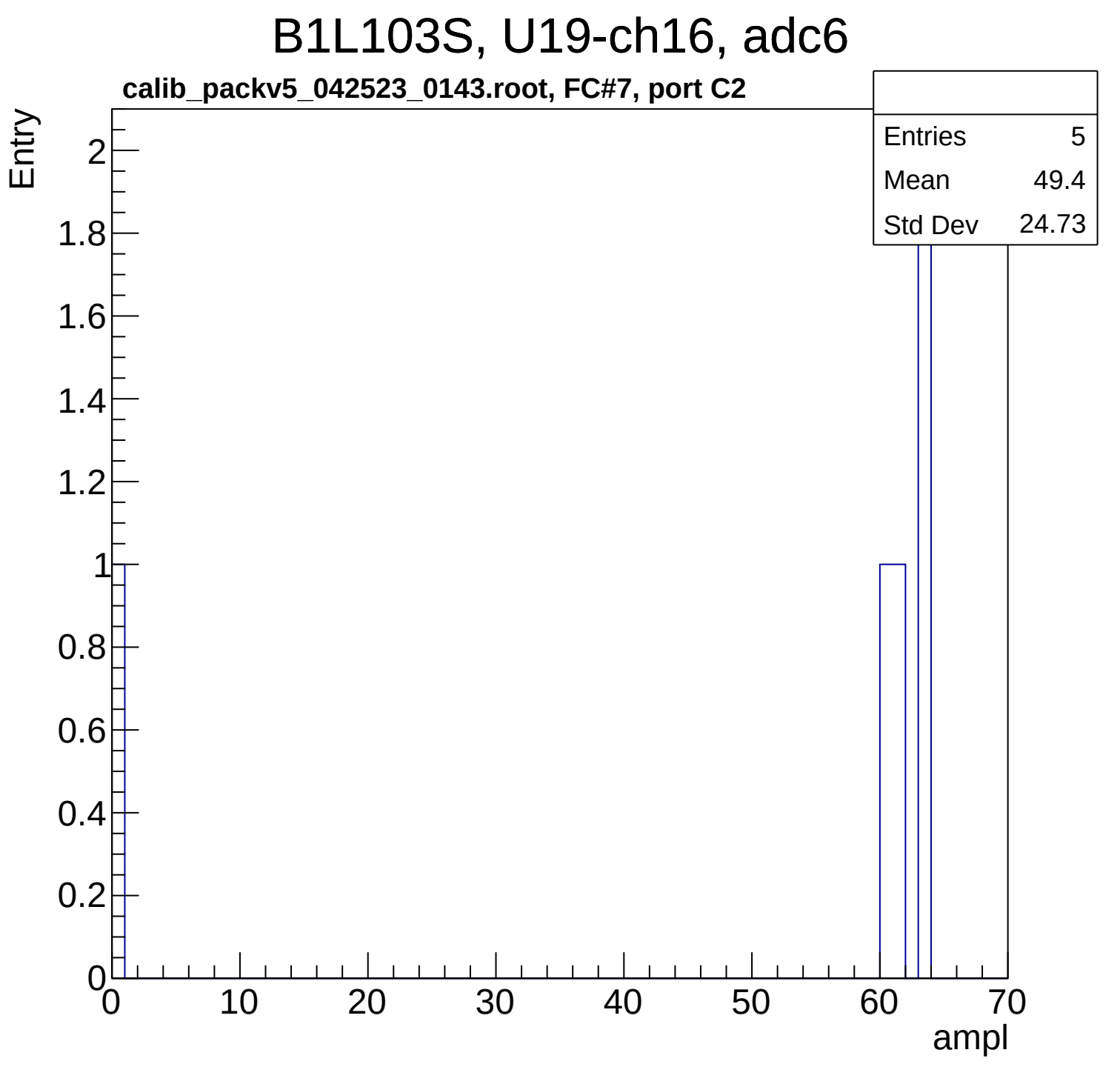
B1L103S, U19-ch16, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.4
Std Dev 24.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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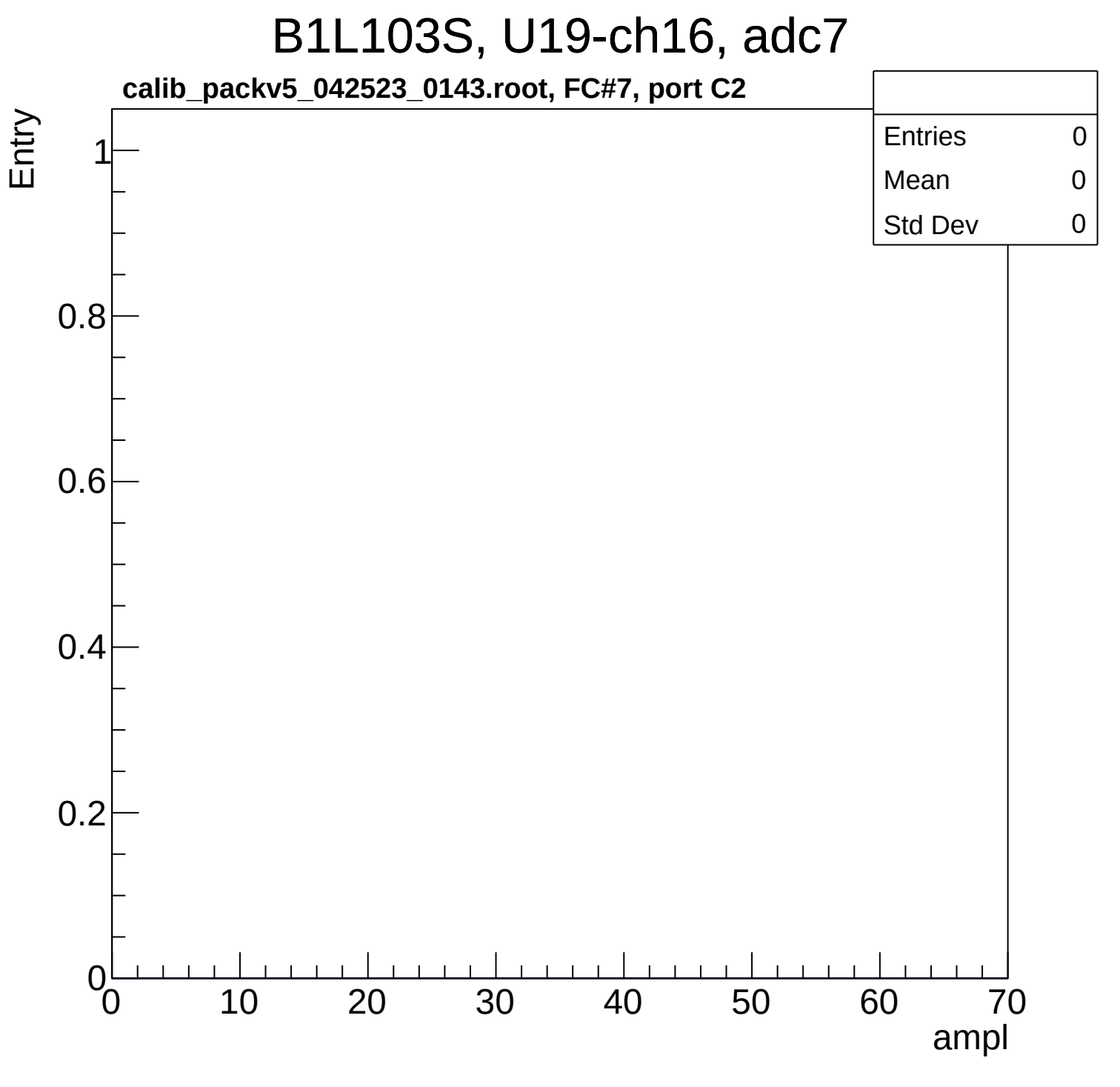
B1L103S, U19-ch16, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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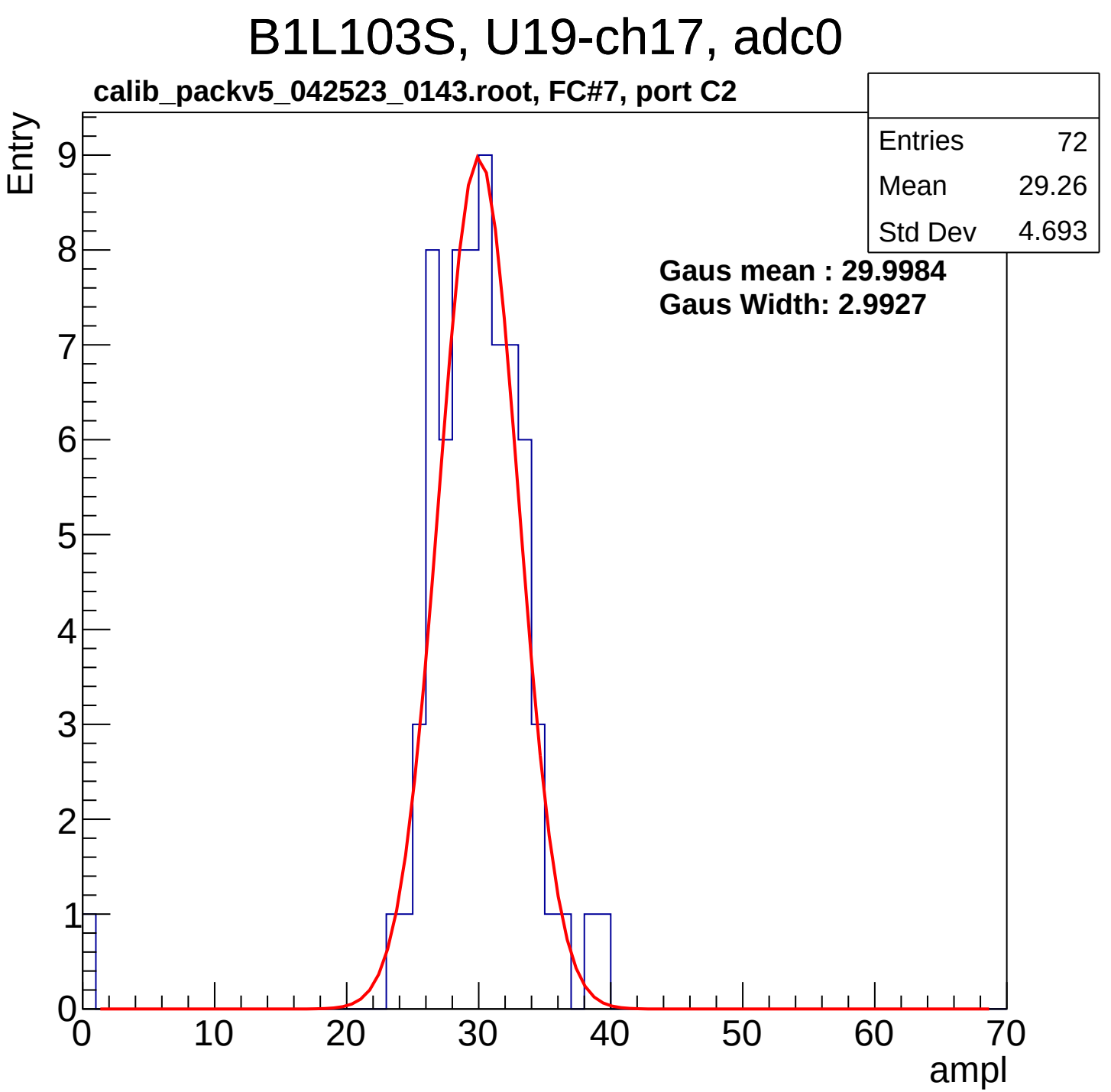
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch17, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 29.26
Std Dev 4.693
Gaus mean : 29.9984
Gaus Width: 2.9927
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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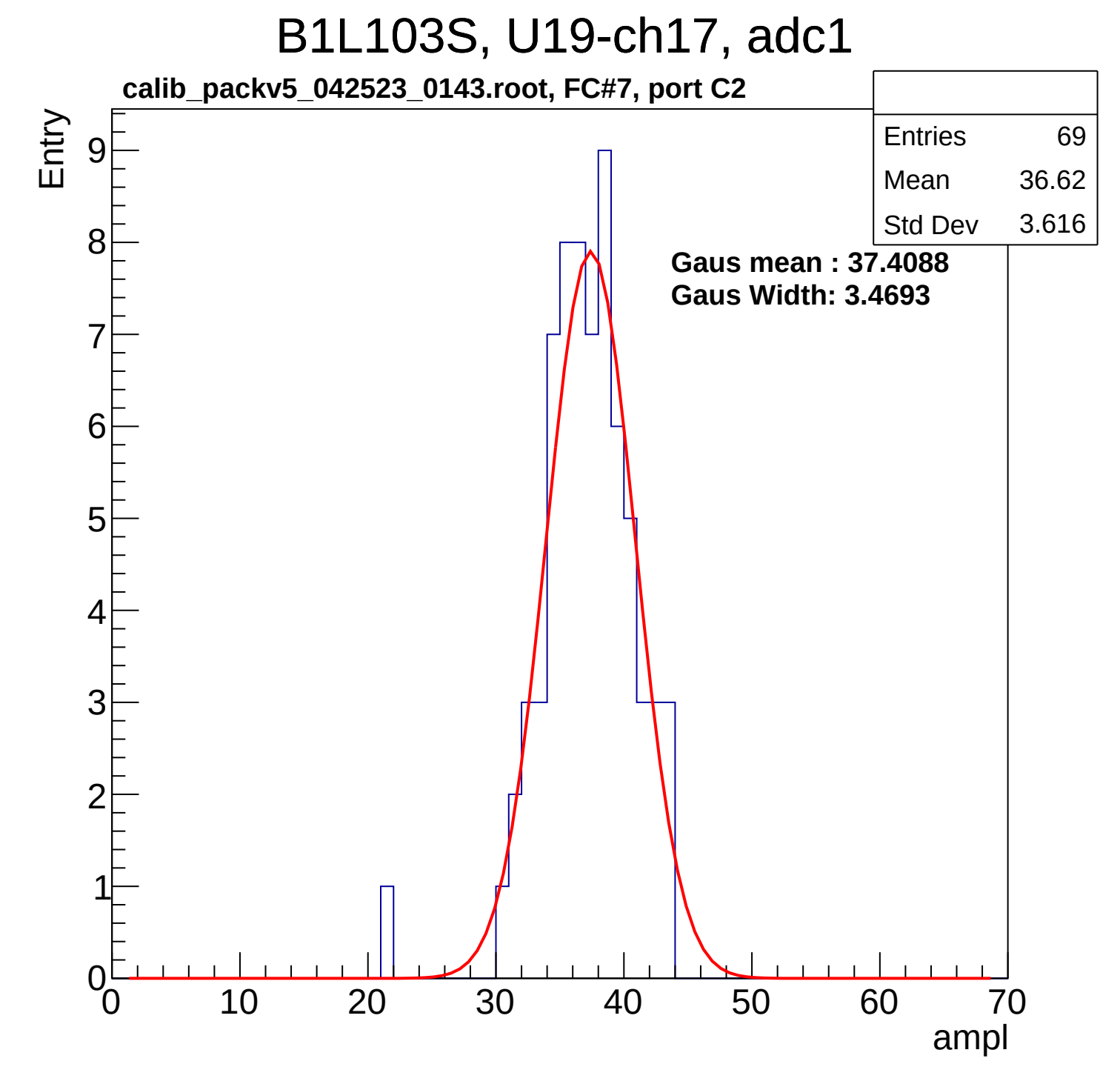
B1L103S, U19-ch17, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.62
Std Dev 3.616
Gaus mean : 37.4088
Gaus Width: 3.4693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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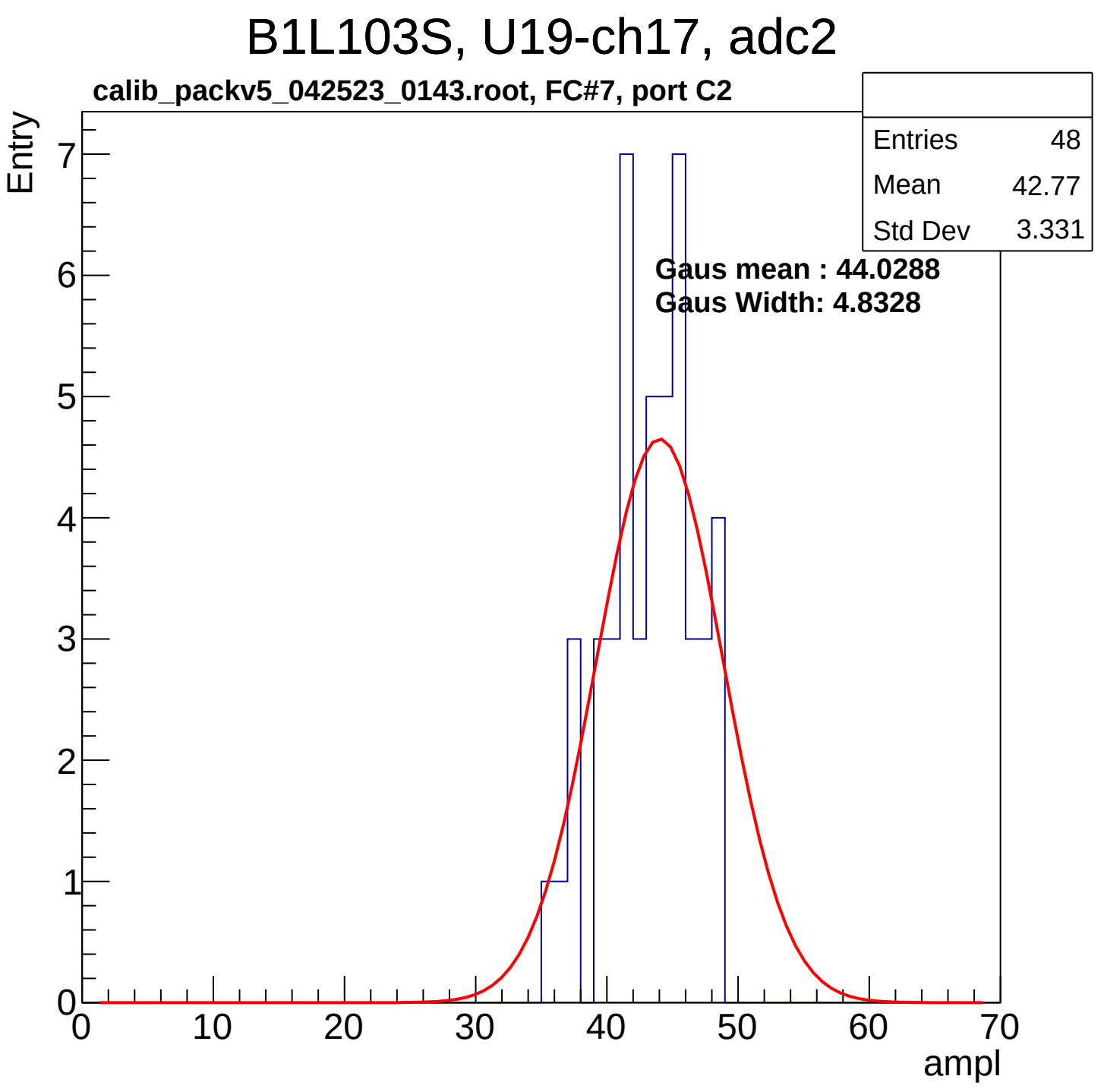
B1L103S, U19-ch17, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 42.77
Std Dev 3.331
Gaus mean : 44.0288
Gaus Width: 4.8328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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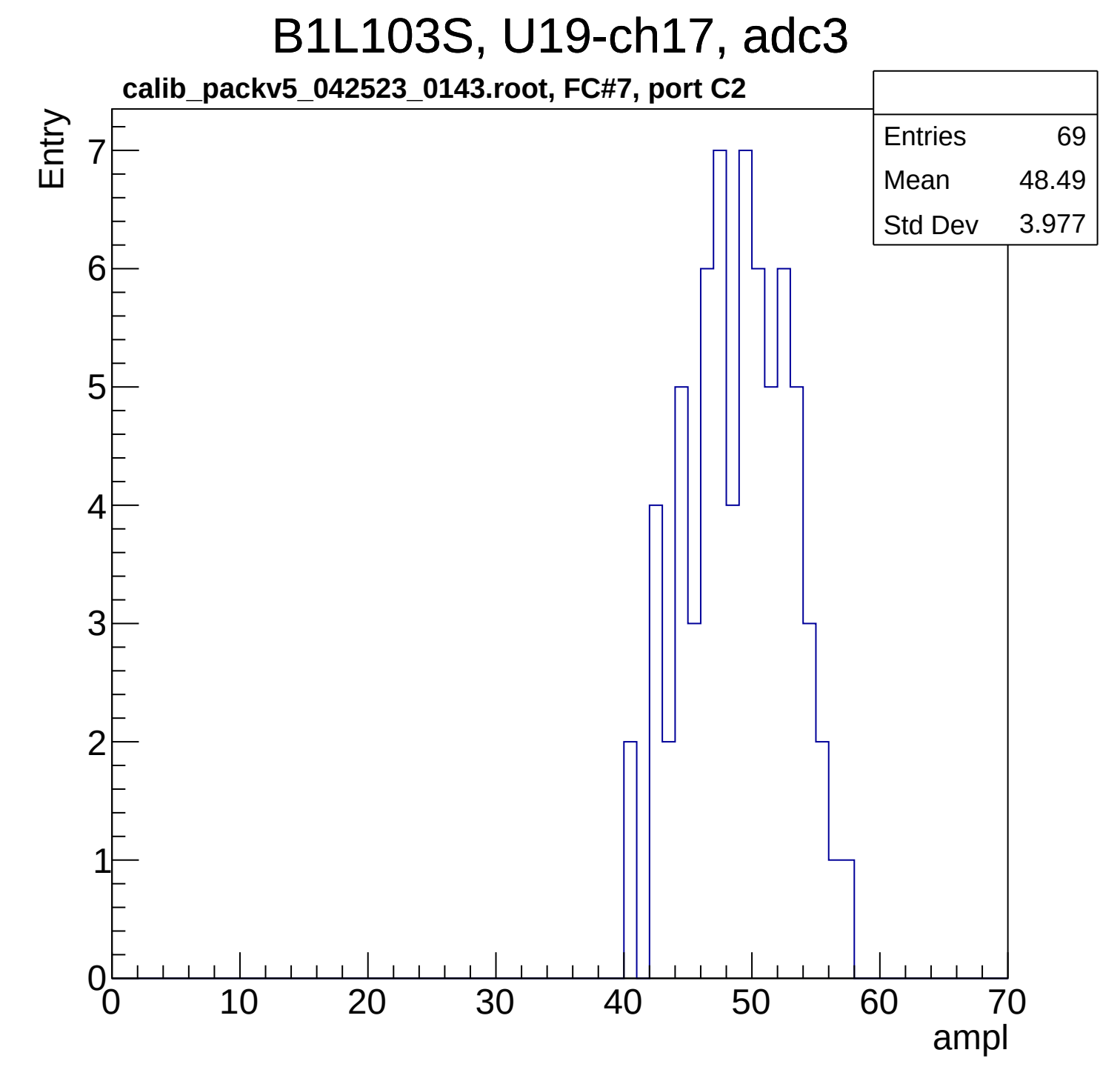
B1L103S, U19-ch17, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 48.49
Std Dev 3.977
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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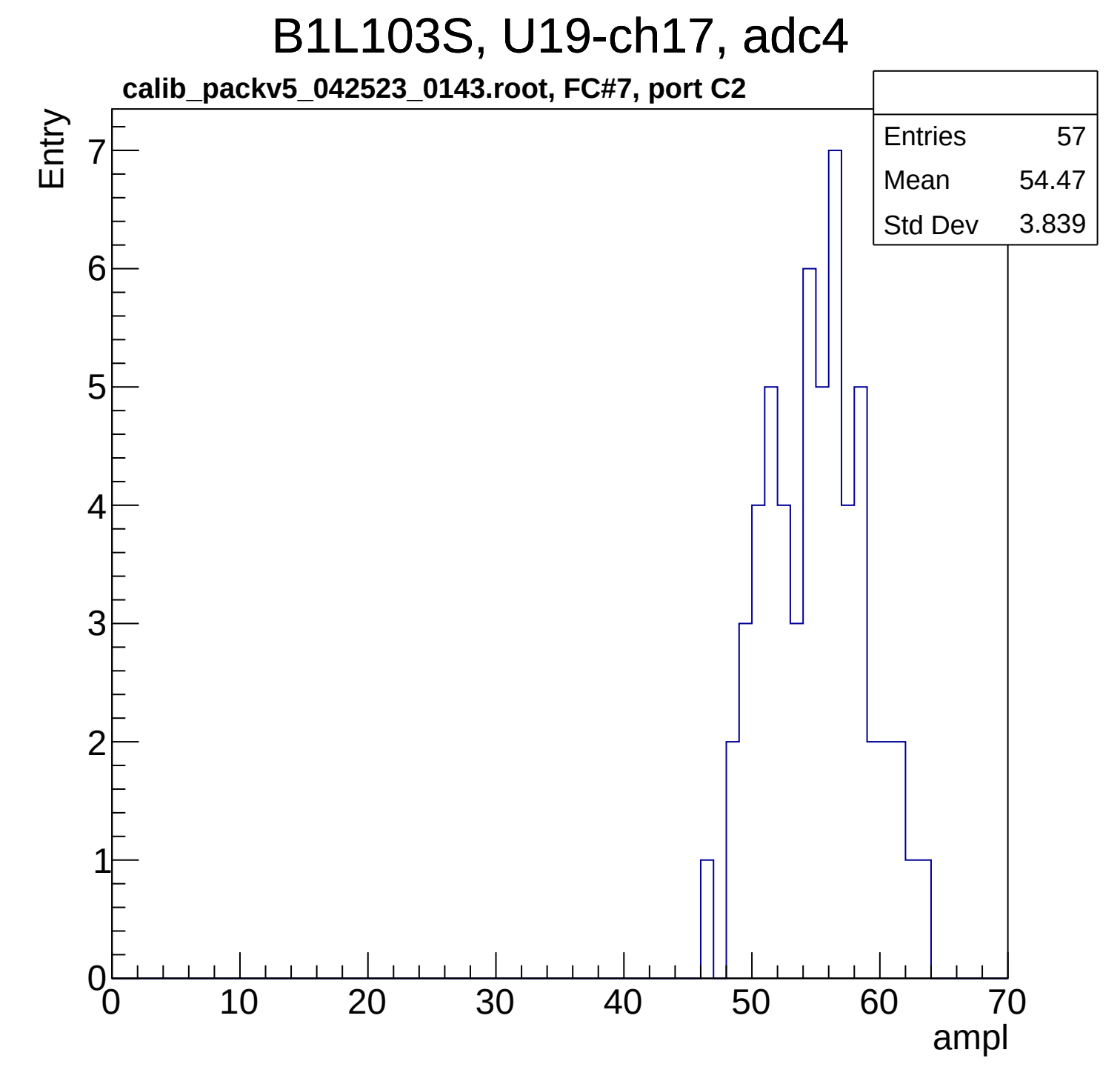
B1L103S, U19-ch17, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 54.47
Std Dev 3.839
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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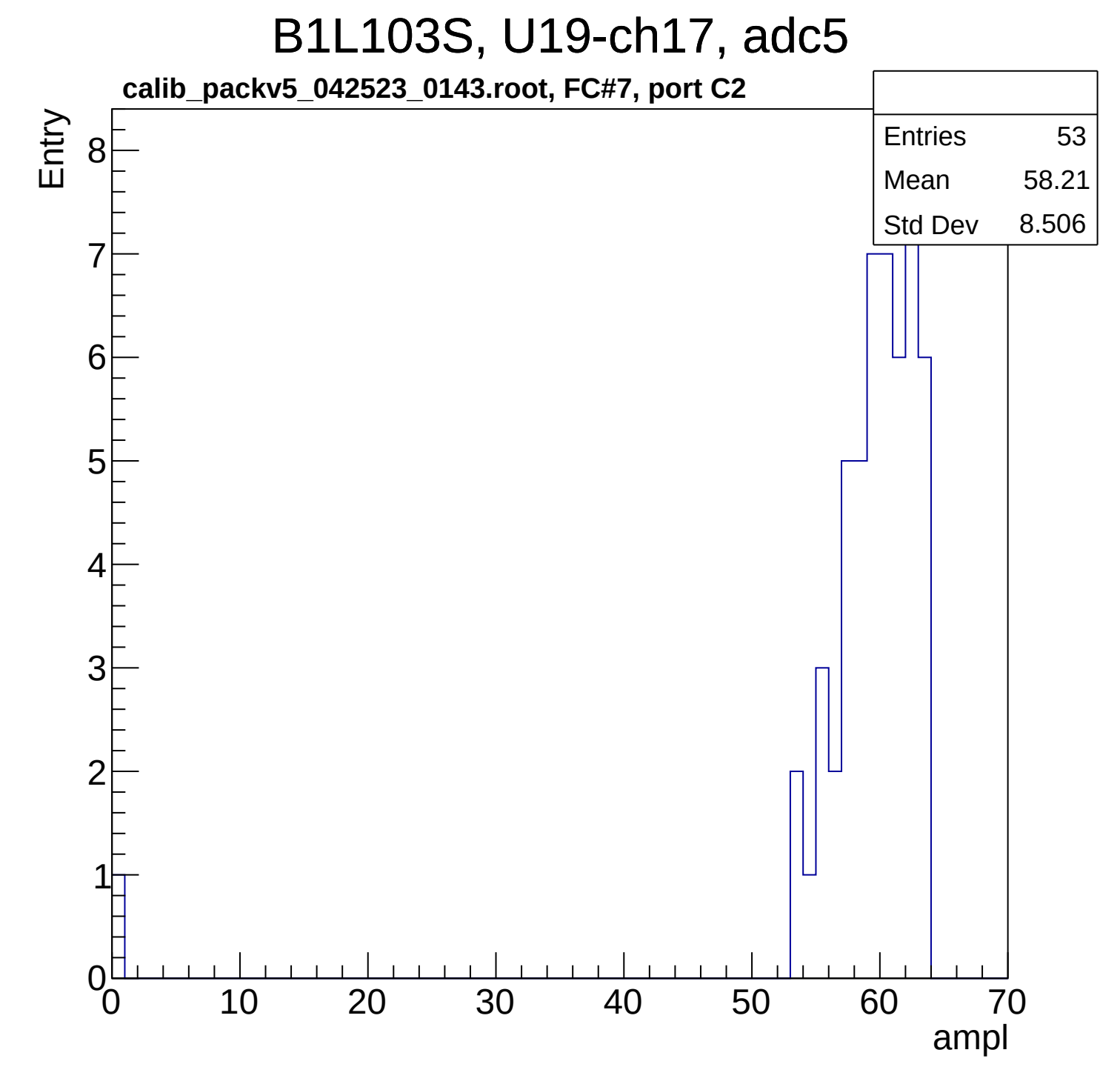
B1L103S, U19-ch17, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.21
Std Dev 8.506
Entry
ampl
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2
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4
5
6
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0
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60
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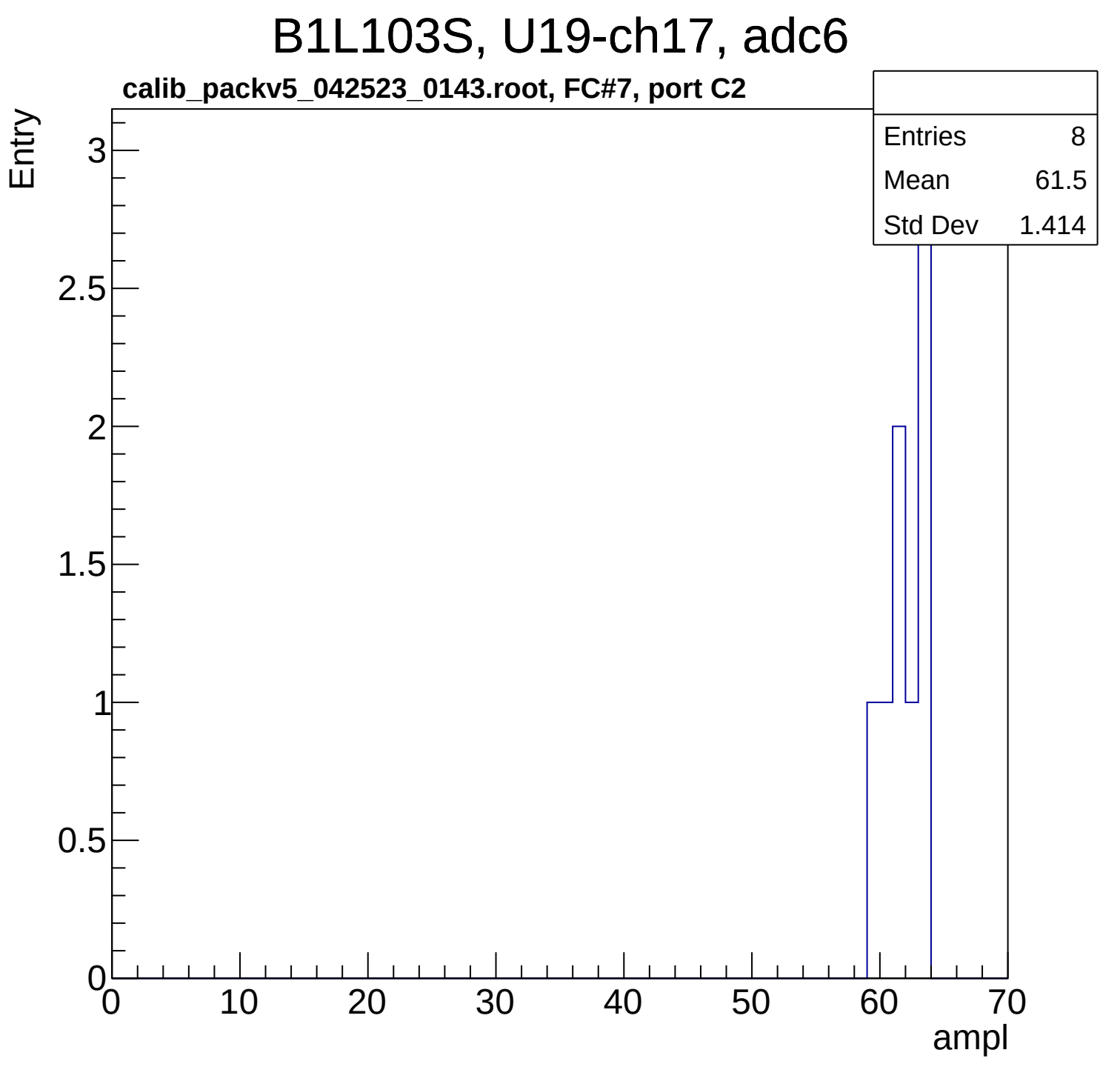
B1L103S, U19-ch17, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.5
Std Dev 1.414
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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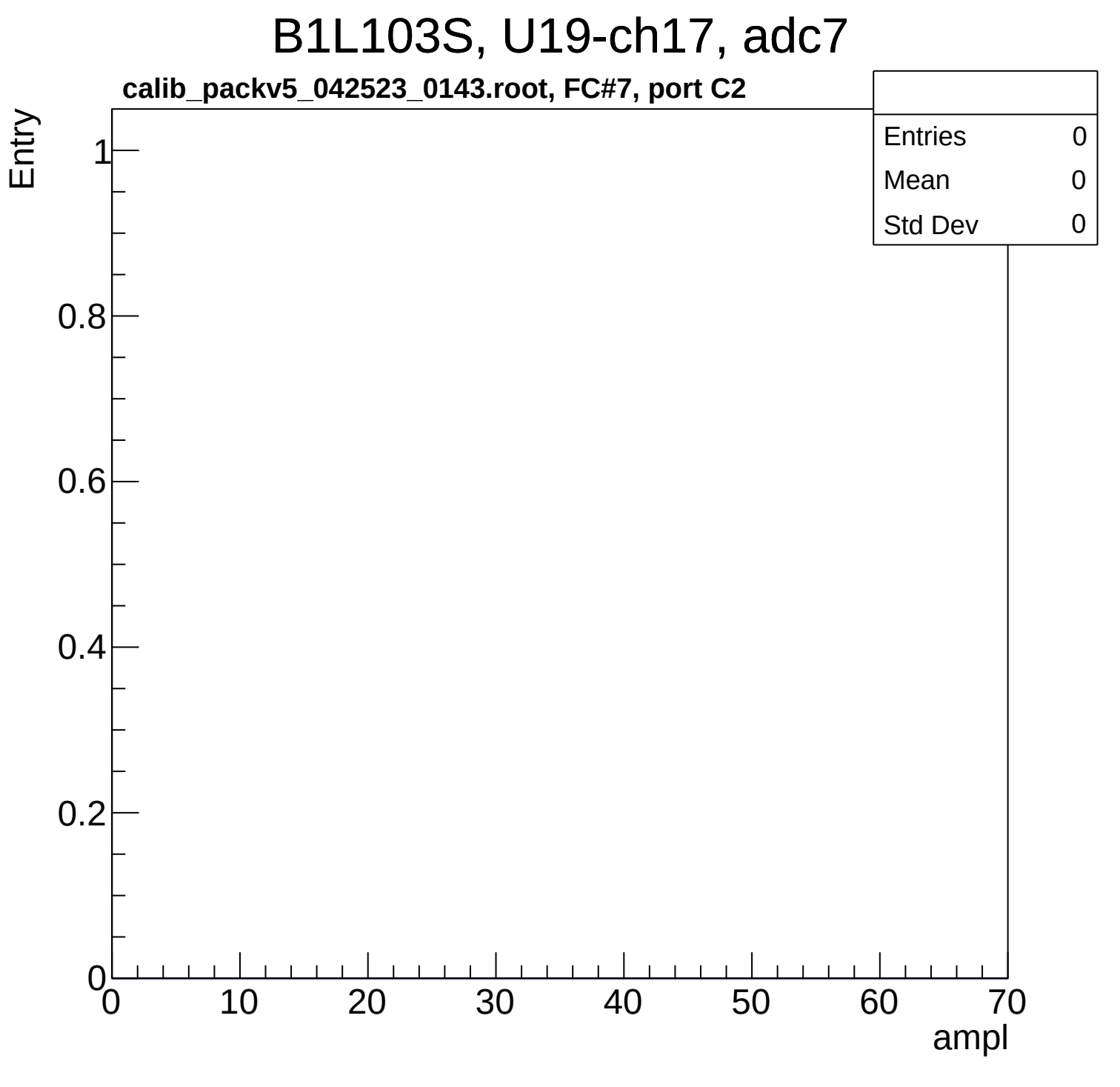
40
50
60
70


B1L103S, U19-ch17, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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0
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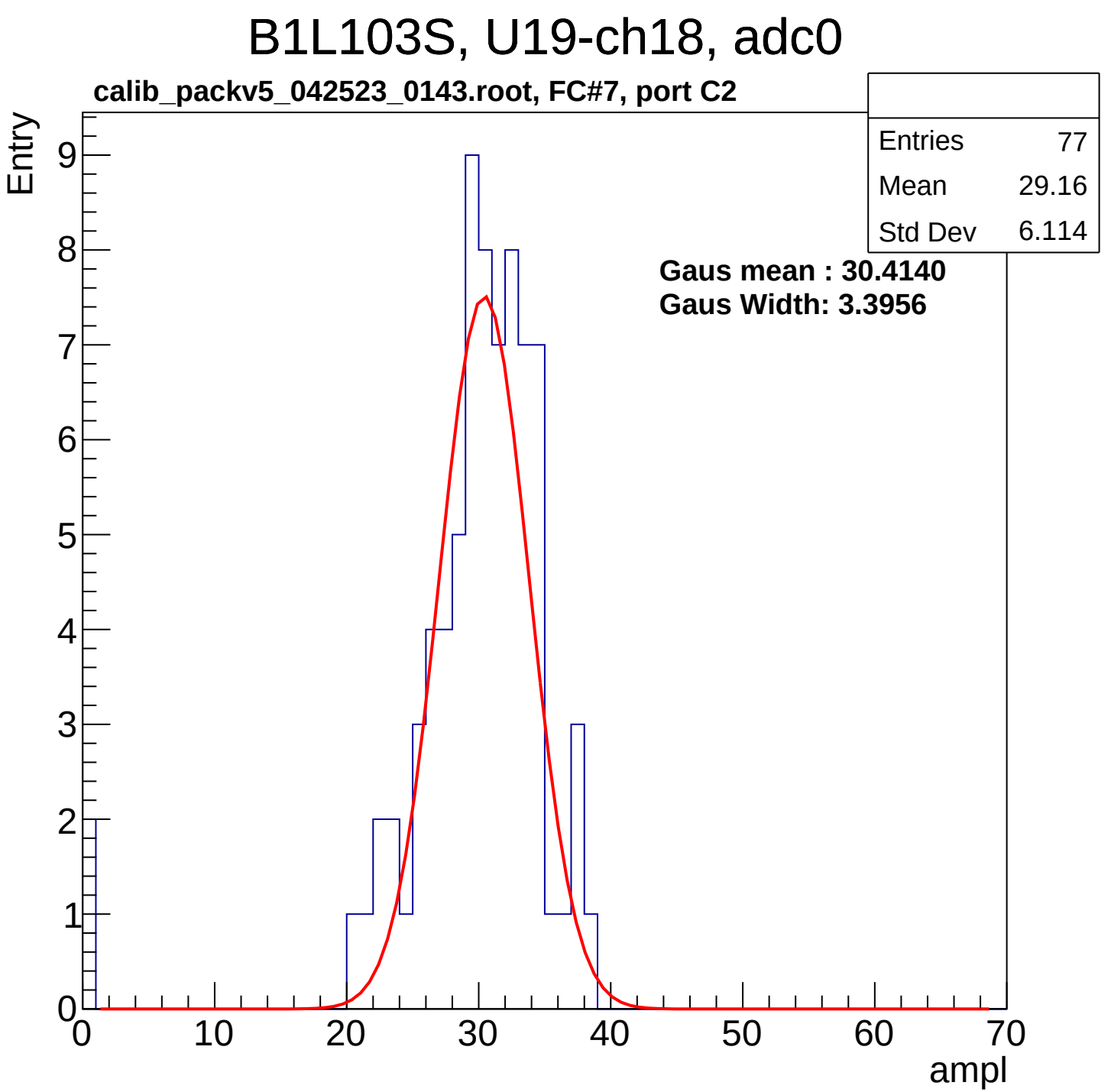
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch18, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 29.16
Std Dev 6.114
Gaus mean : 30.4140
Gaus Width: 3.3956
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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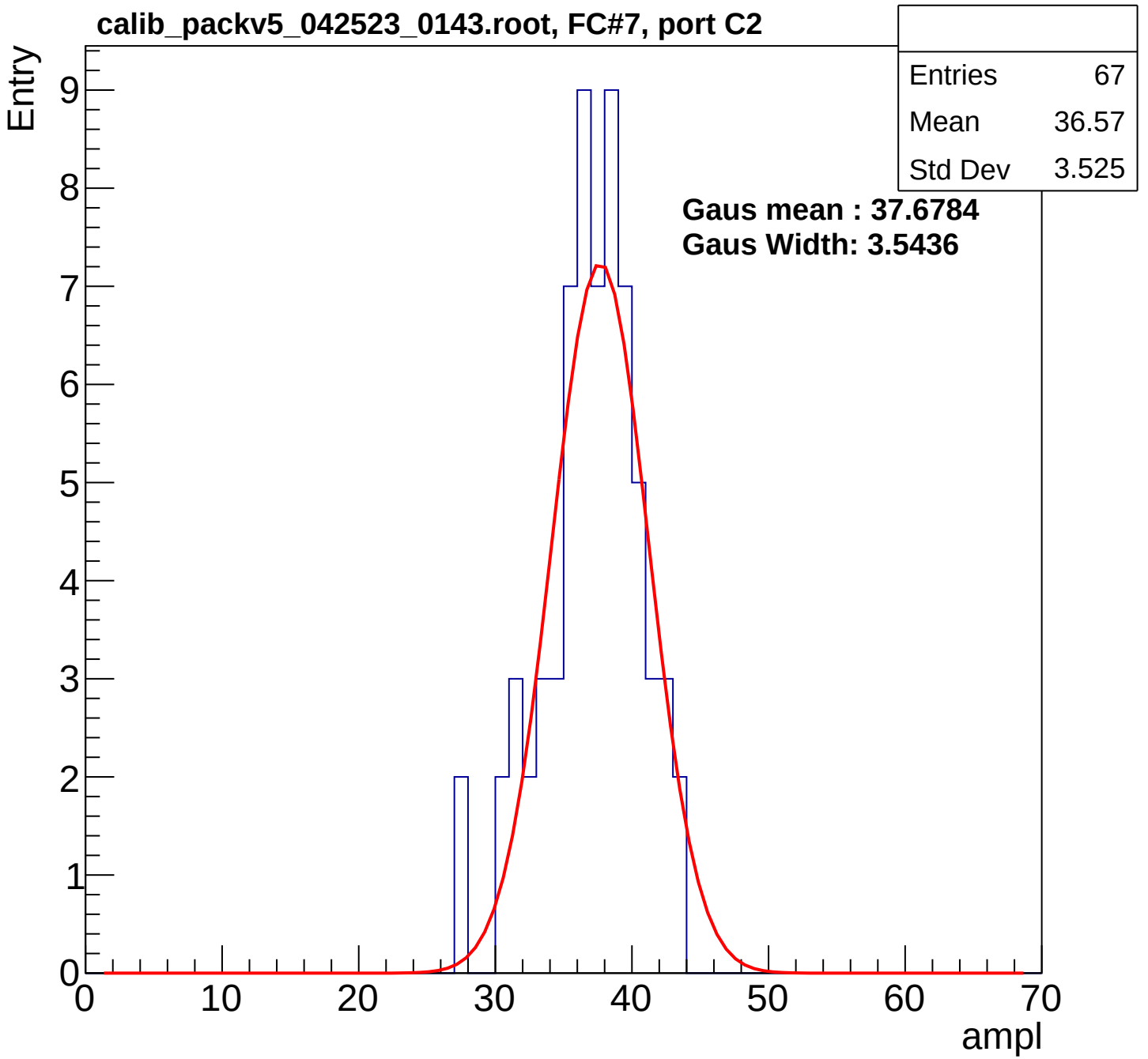
60
70


B1L103S, U19-ch18, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.57
Std Dev 3.525
Gaus mean : 37.6784
Gaus Width: 3.5436
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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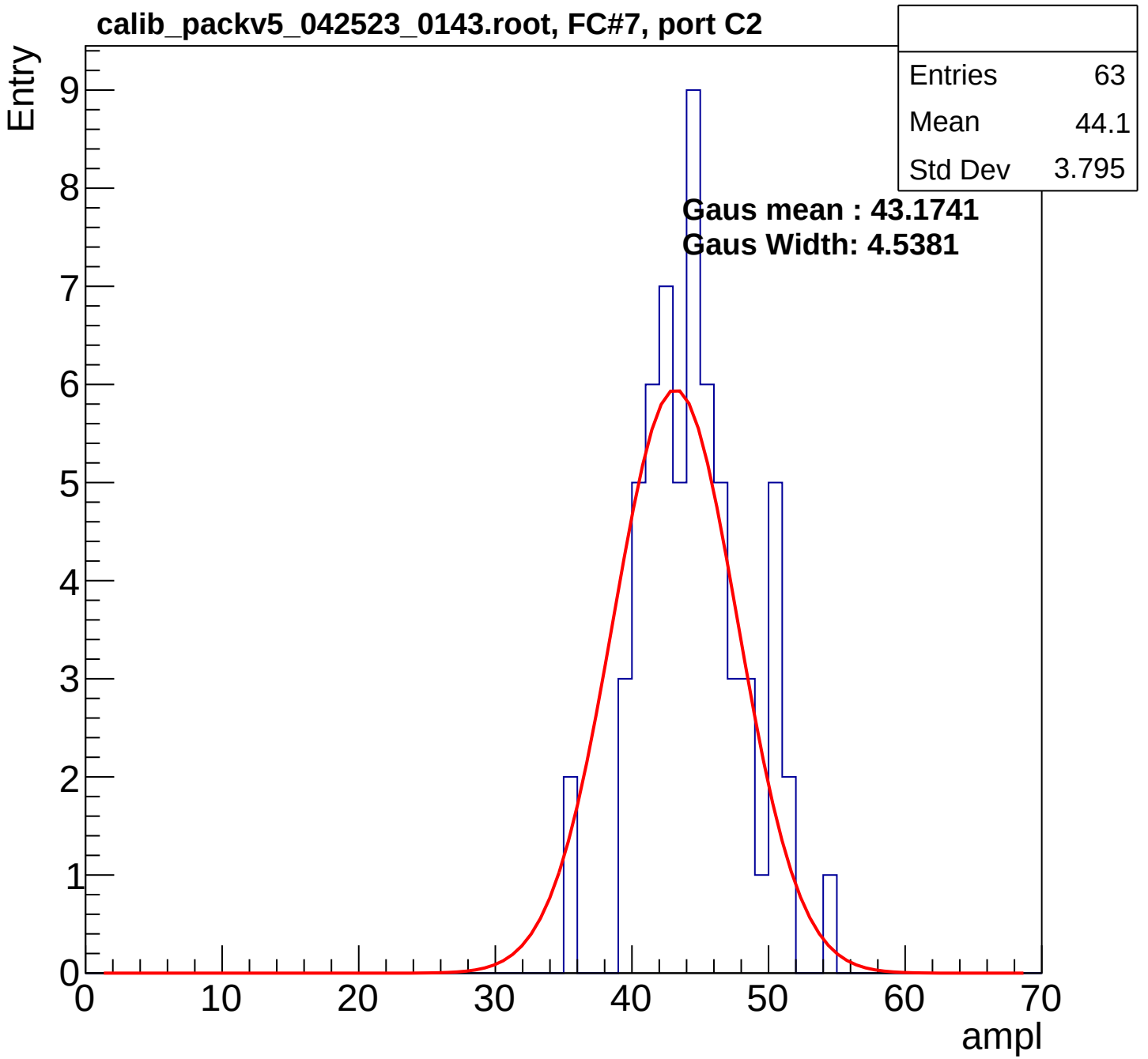
B1L103S, U19-ch18, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 44.1
Std Dev 3.795
Gaus mean : 43.1741
Gaus Width: 4.5381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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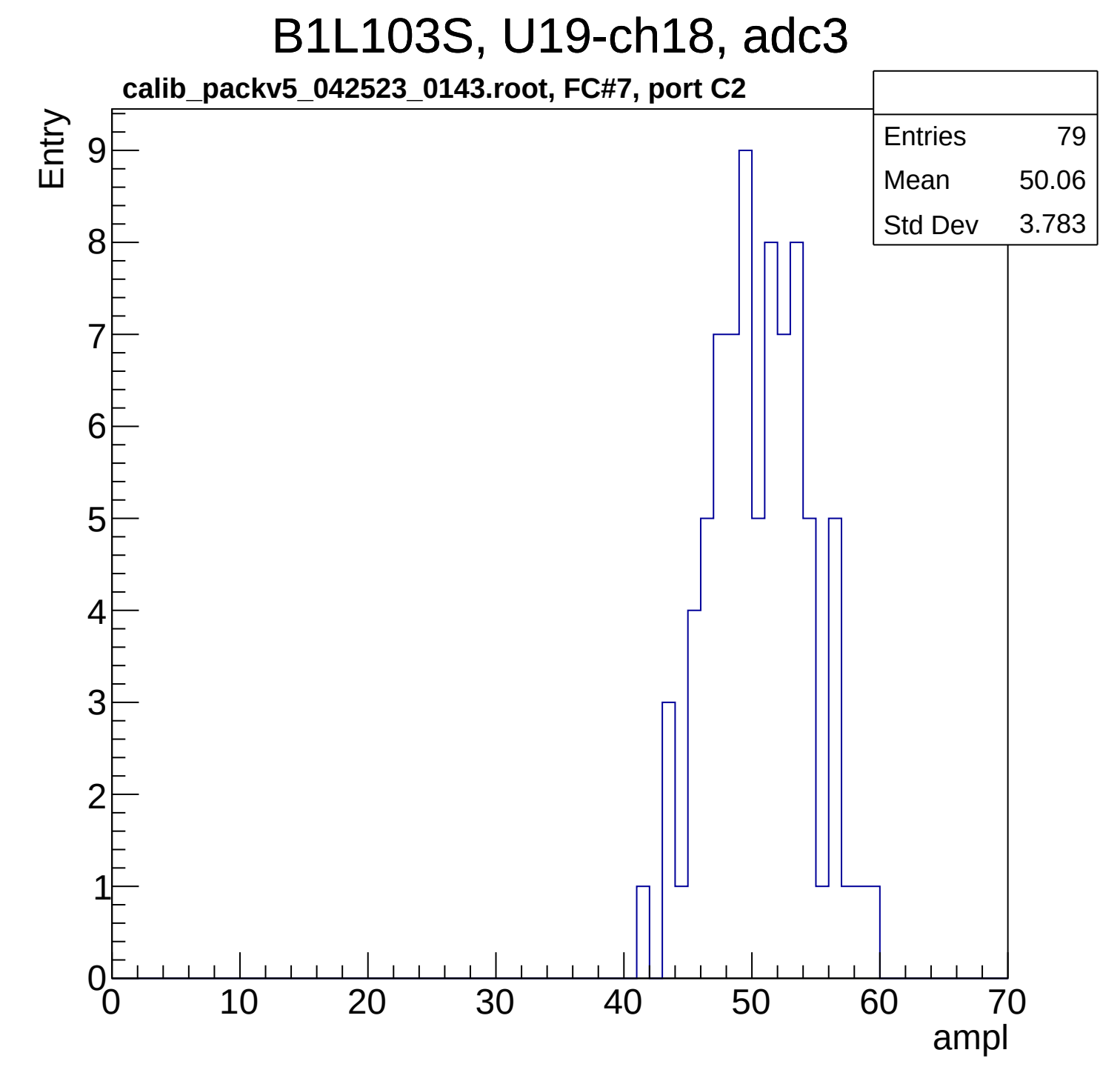
B1L103S, U19-ch18, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 50.06
Std Dev 3.783
Entry
ampl
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4
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6
7
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0
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60
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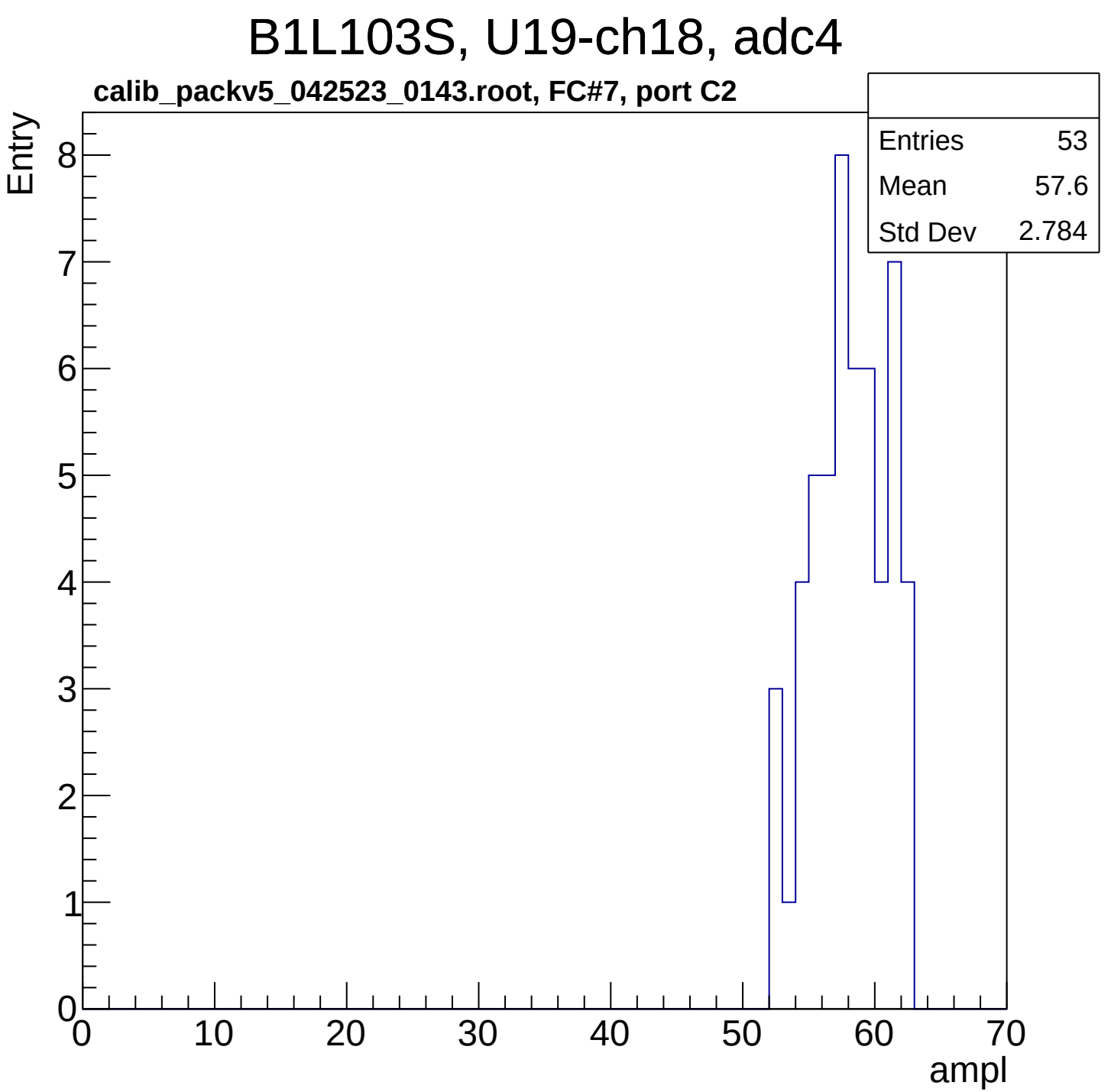
B1L103S, U19-ch18, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 57.6
Std Dev 2.784
Entry
ampl
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1
2
3
4
5
6
7
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60
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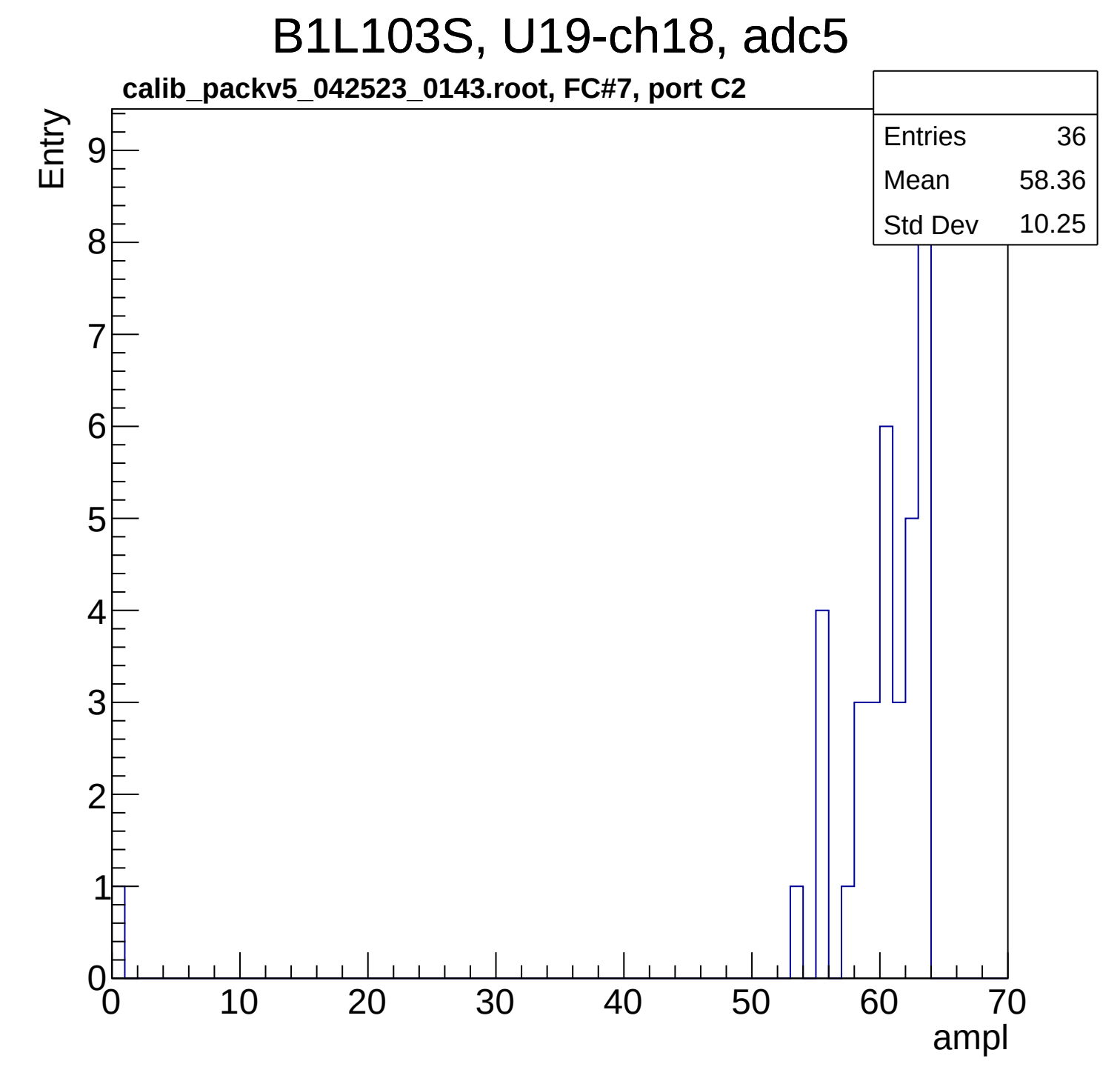
B1L103S, U19-ch18, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.36
Std Dev 10.25
Entry
ampl
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2
3
4
5
6
7
8
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0
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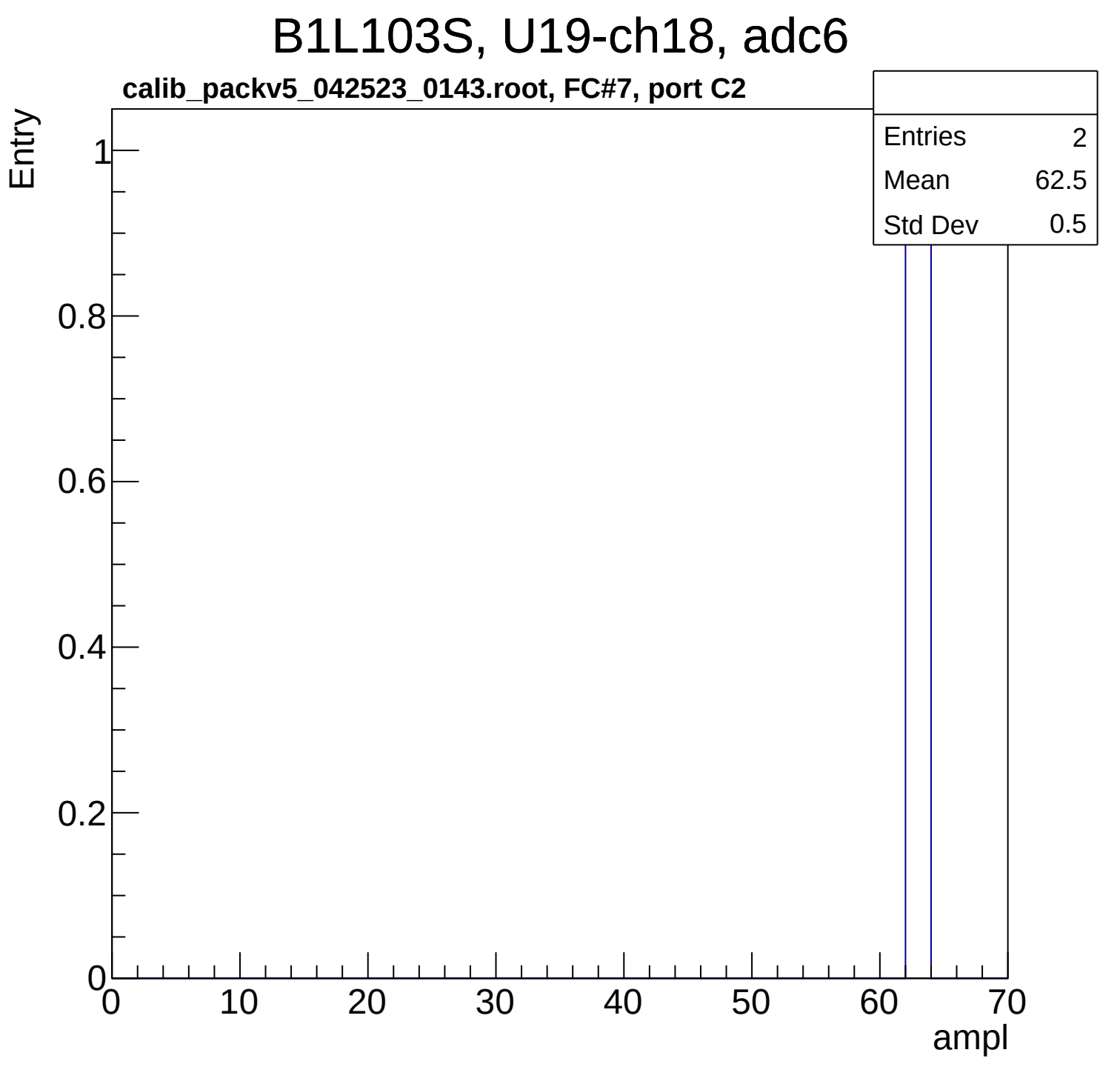
B1L103S, U19-ch18, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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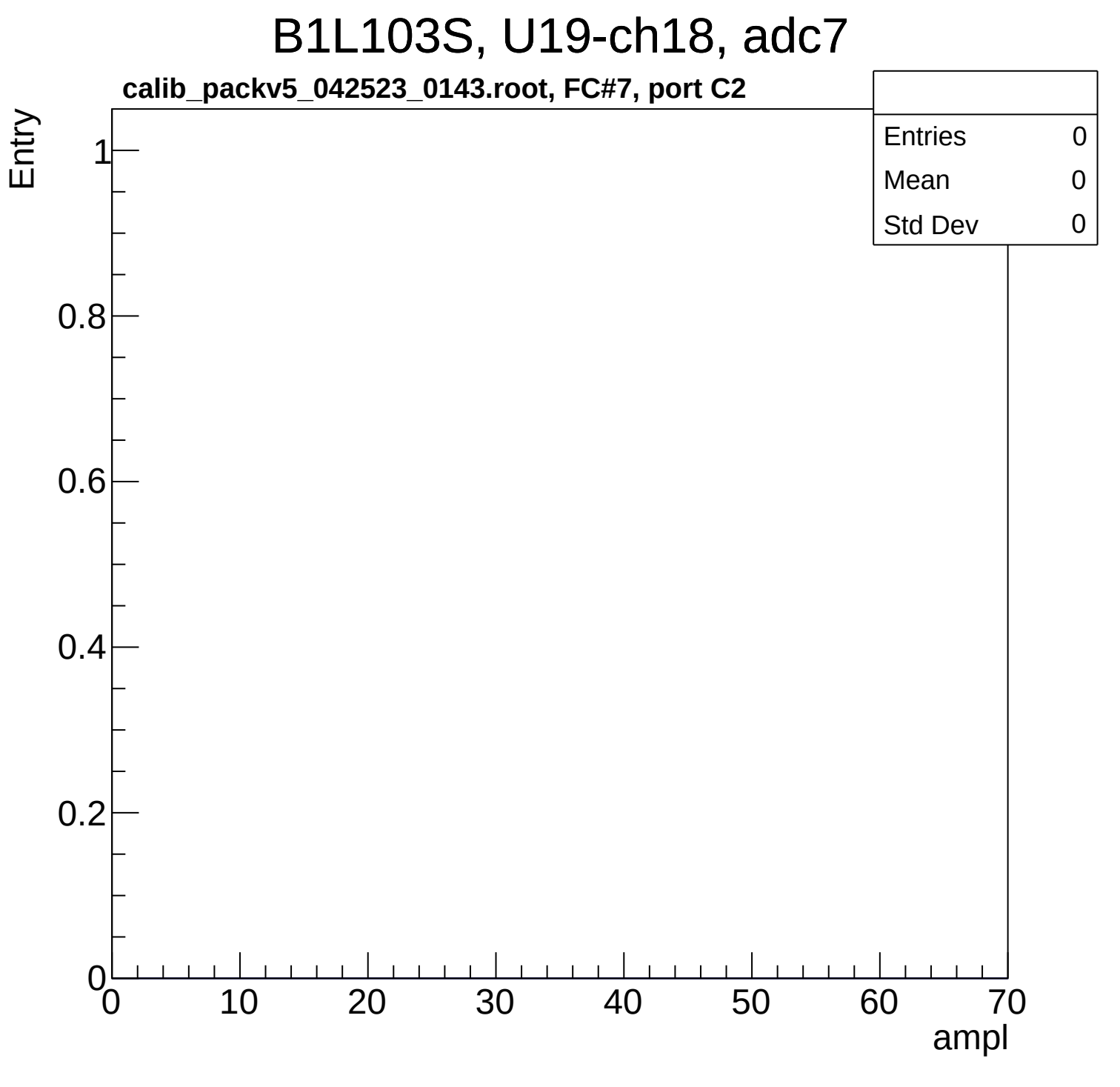
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch18, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
20
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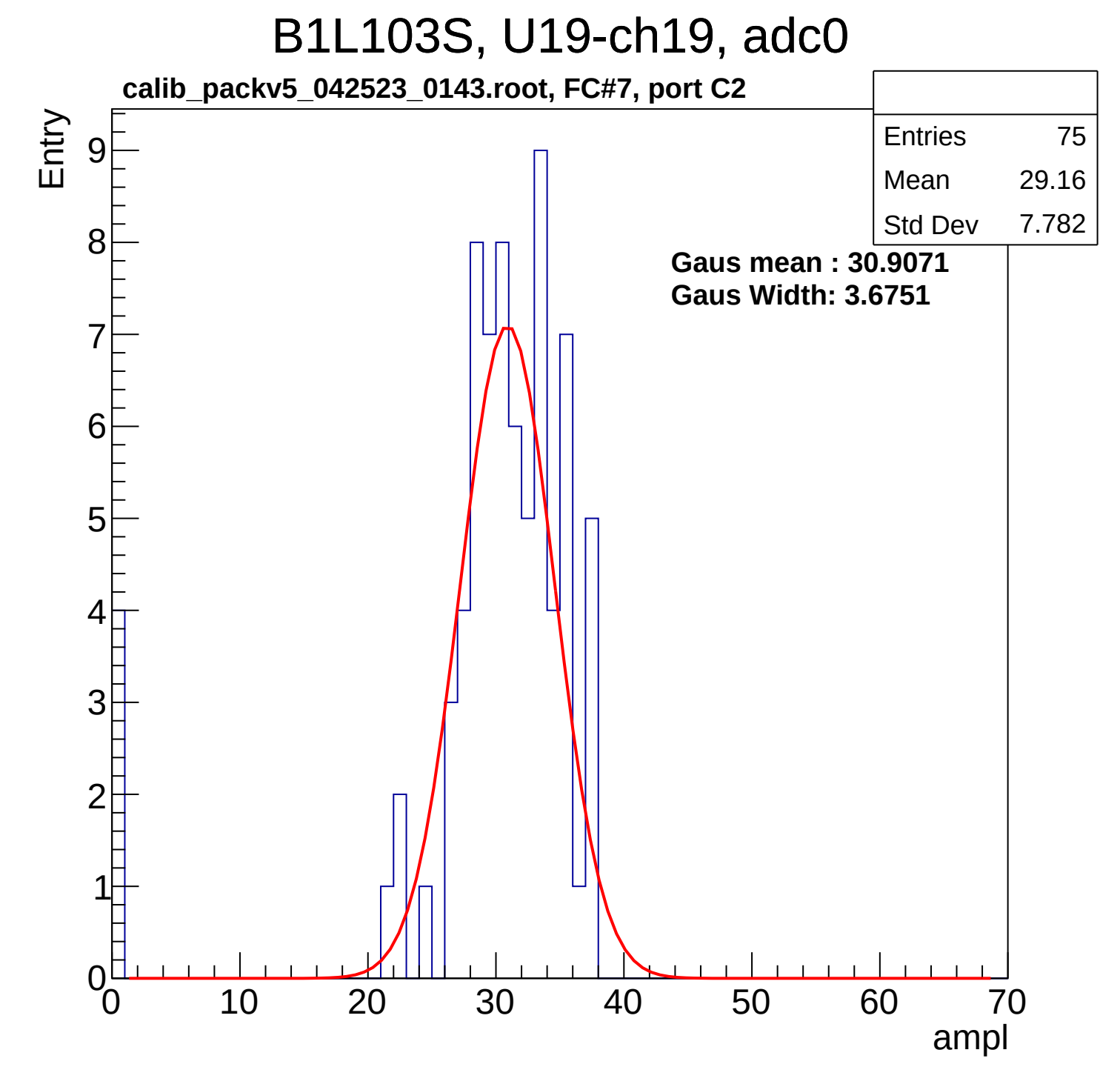
50
60
70
ampl


B1L103S, U19-ch19, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.16
Std Dev 7.782
Gaus mean : 30.9071
Gaus Width: 3.6751
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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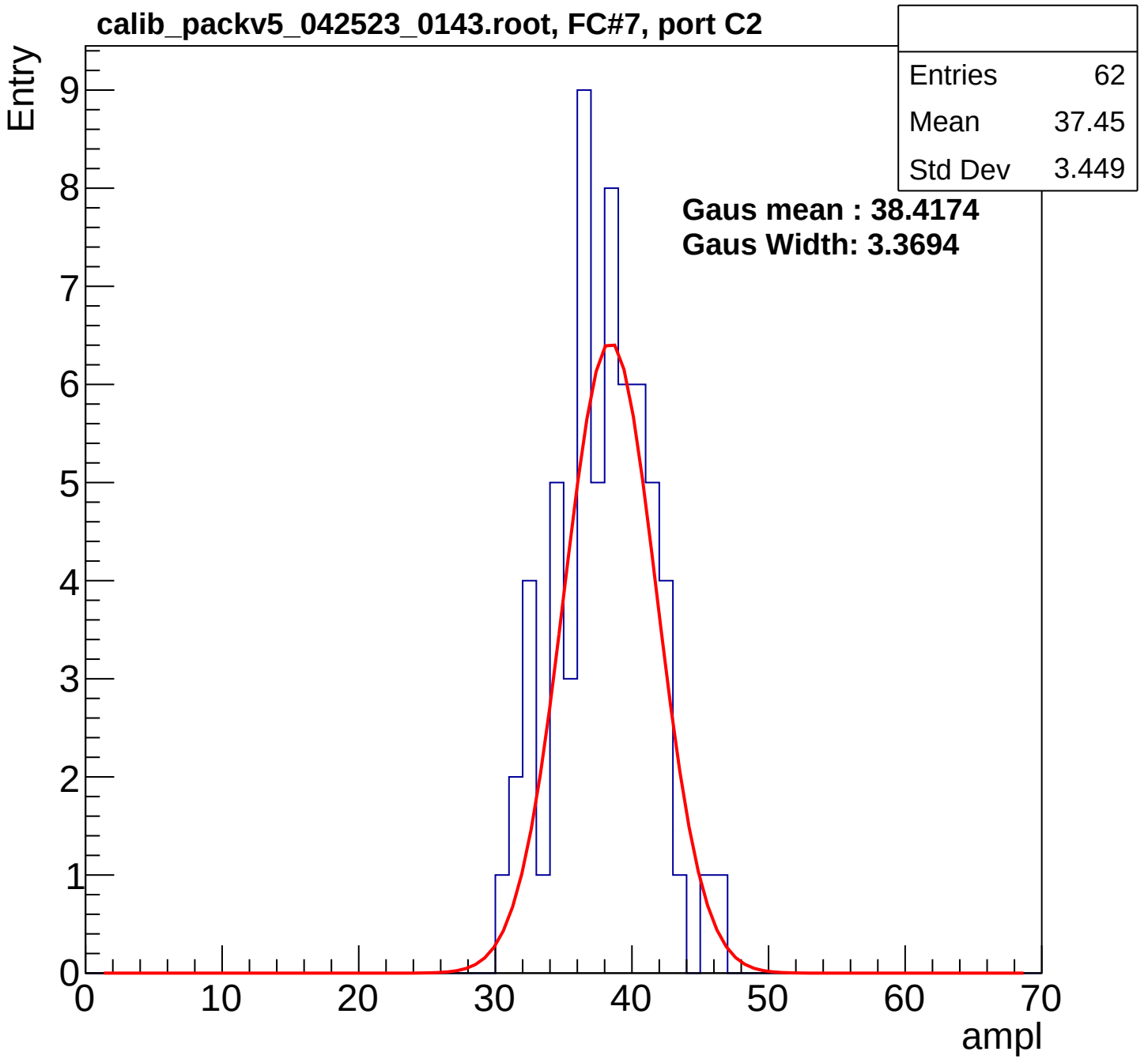
B1L103S, U19-ch19, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 37.45
Std Dev 3.449
Gaus mean : 38.4174
Gaus Width: 3.3694
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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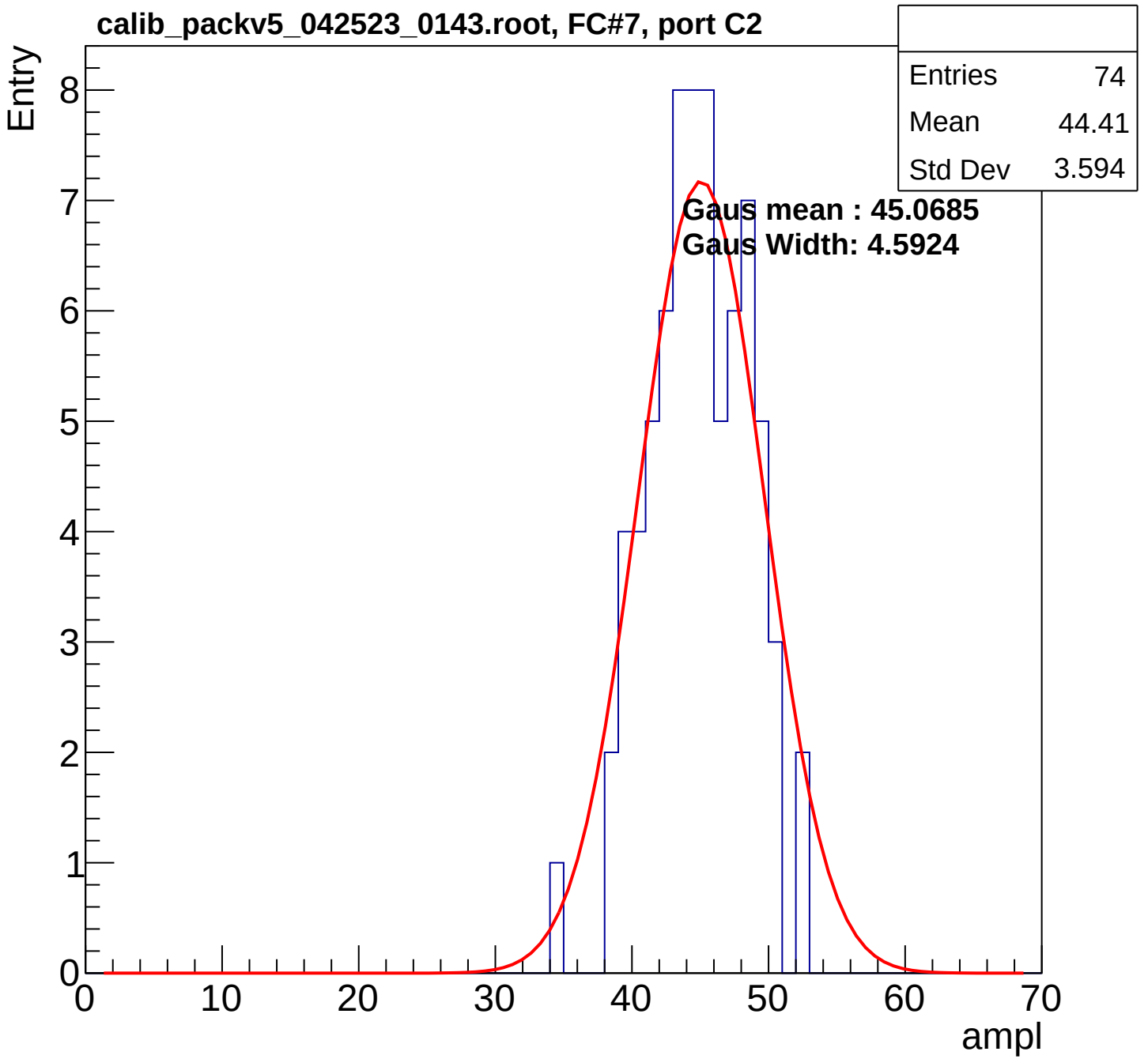
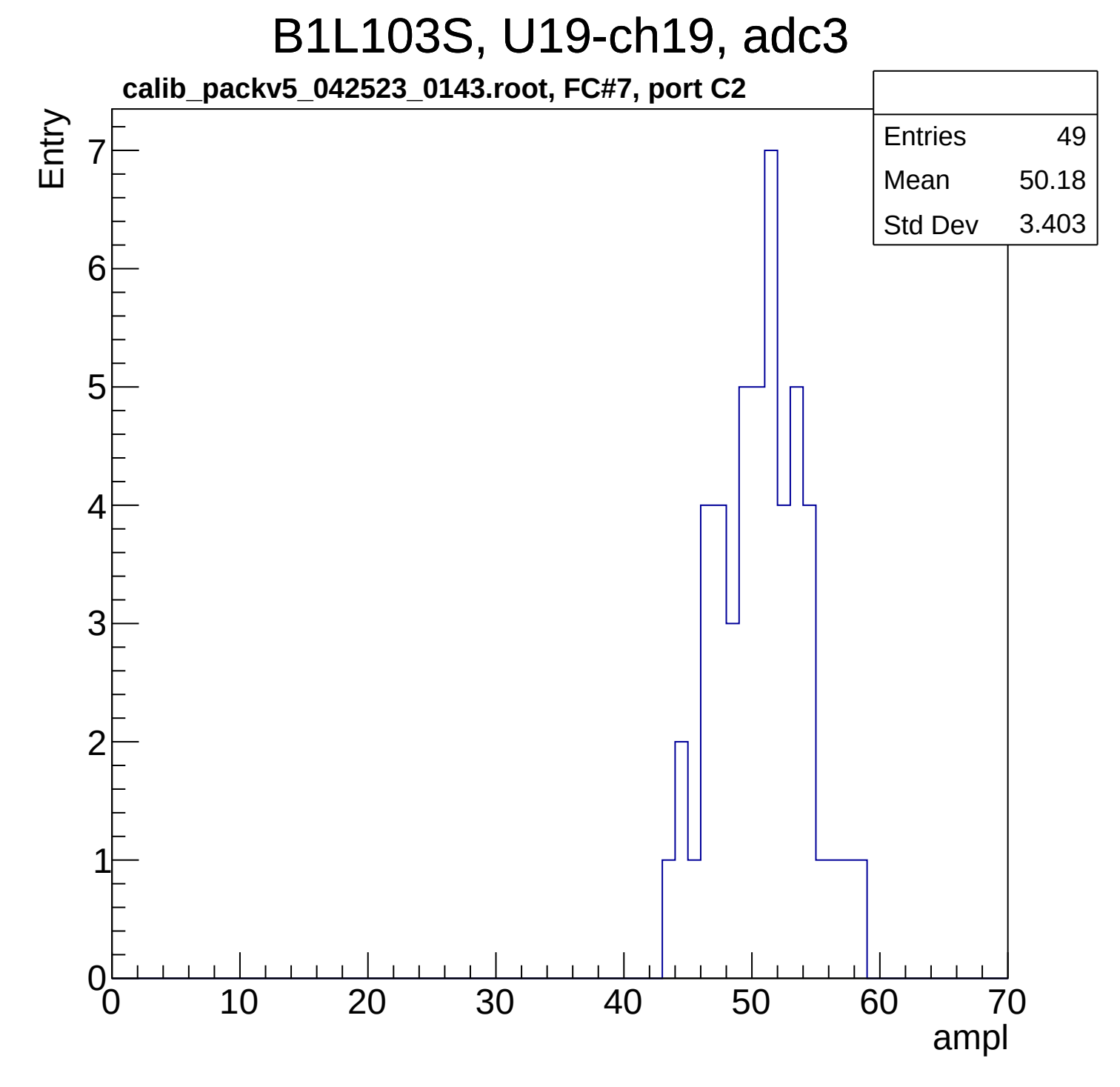

B1L103S, U19-ch19, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 44.41
Std Dev 3.594
Gaus mean : 45.0685
Gaus Width: 4.5924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch19, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 50.18
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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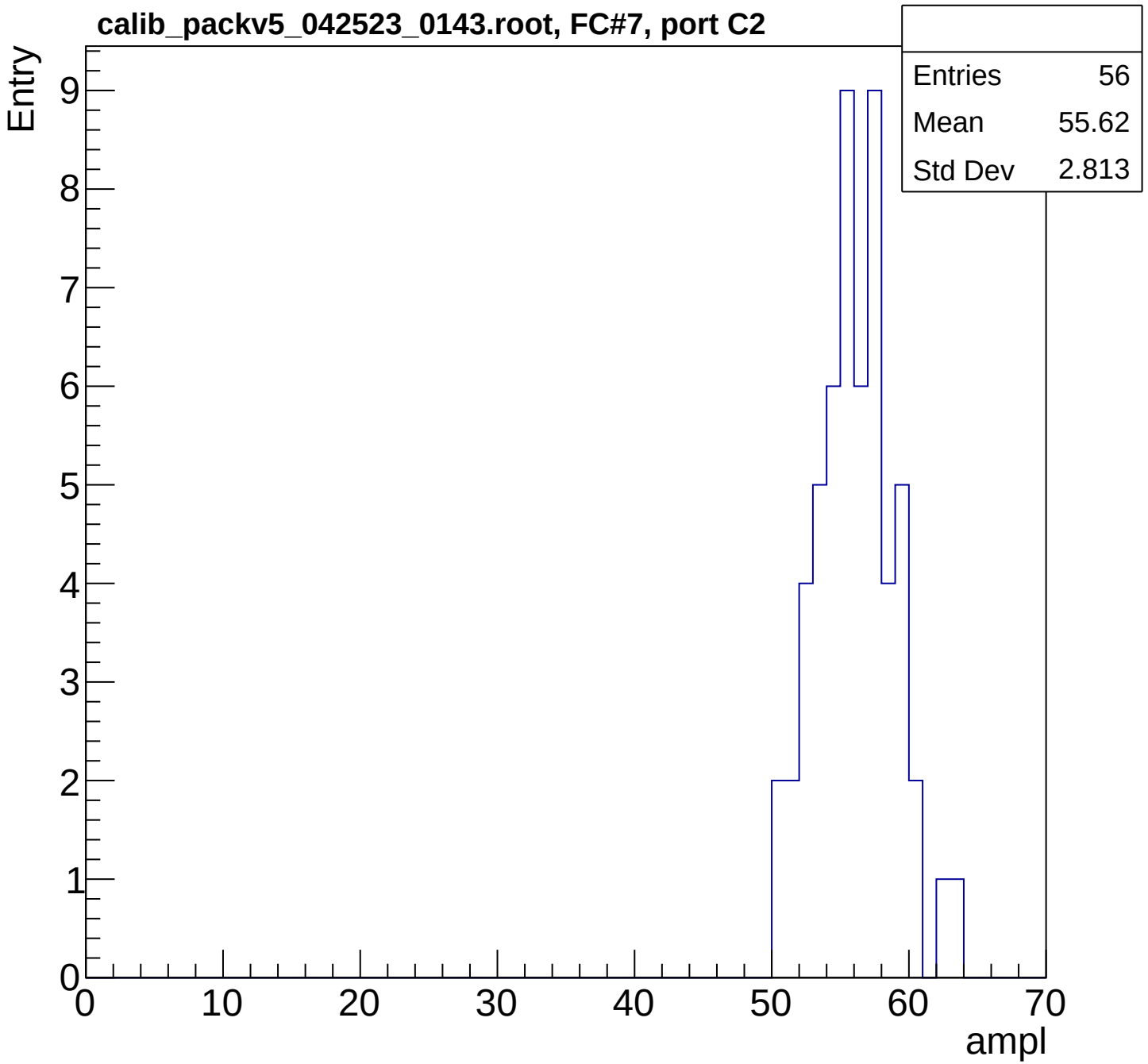
B1L103S, U19-ch19, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55.62
Std Dev 2.813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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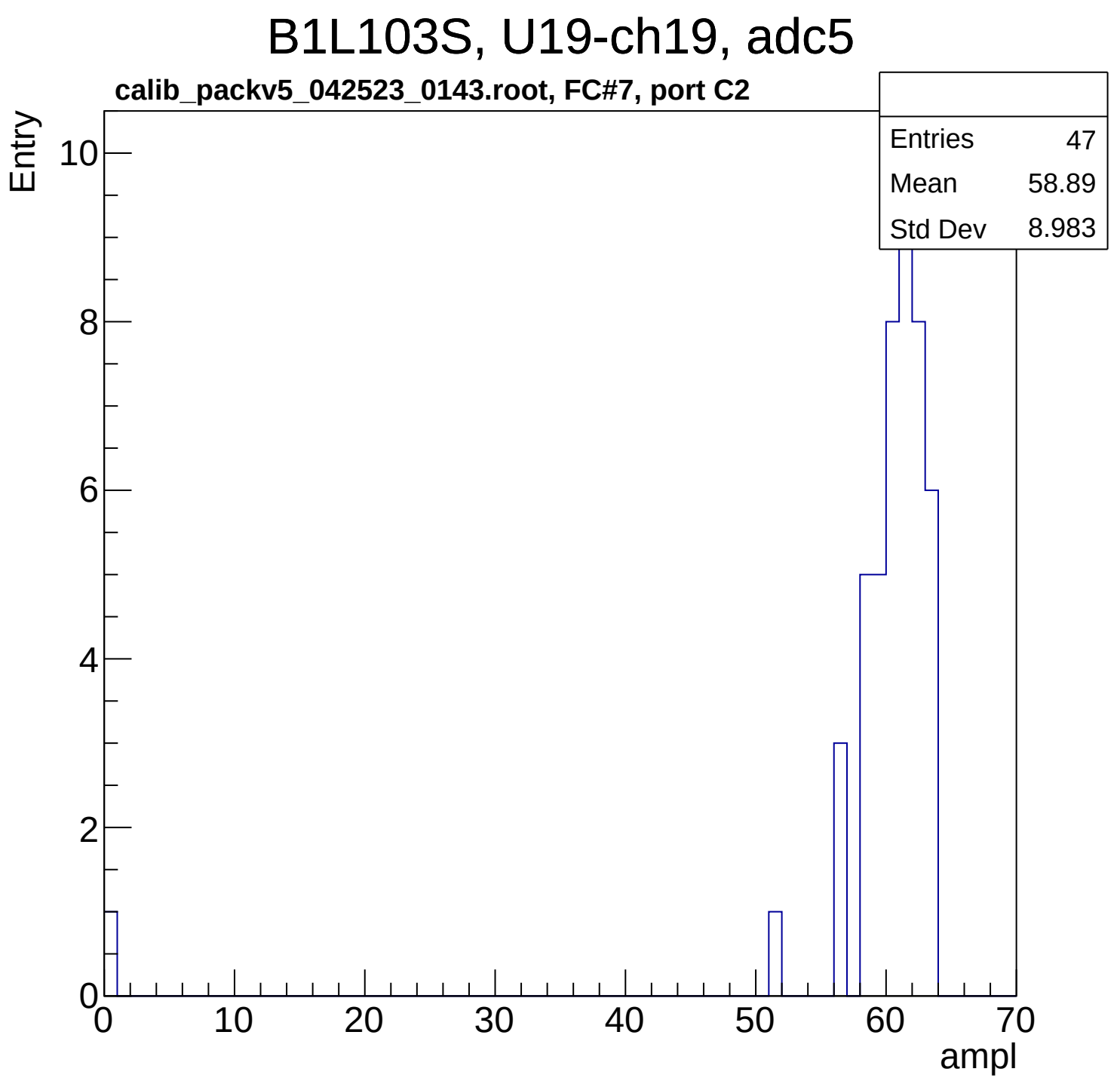
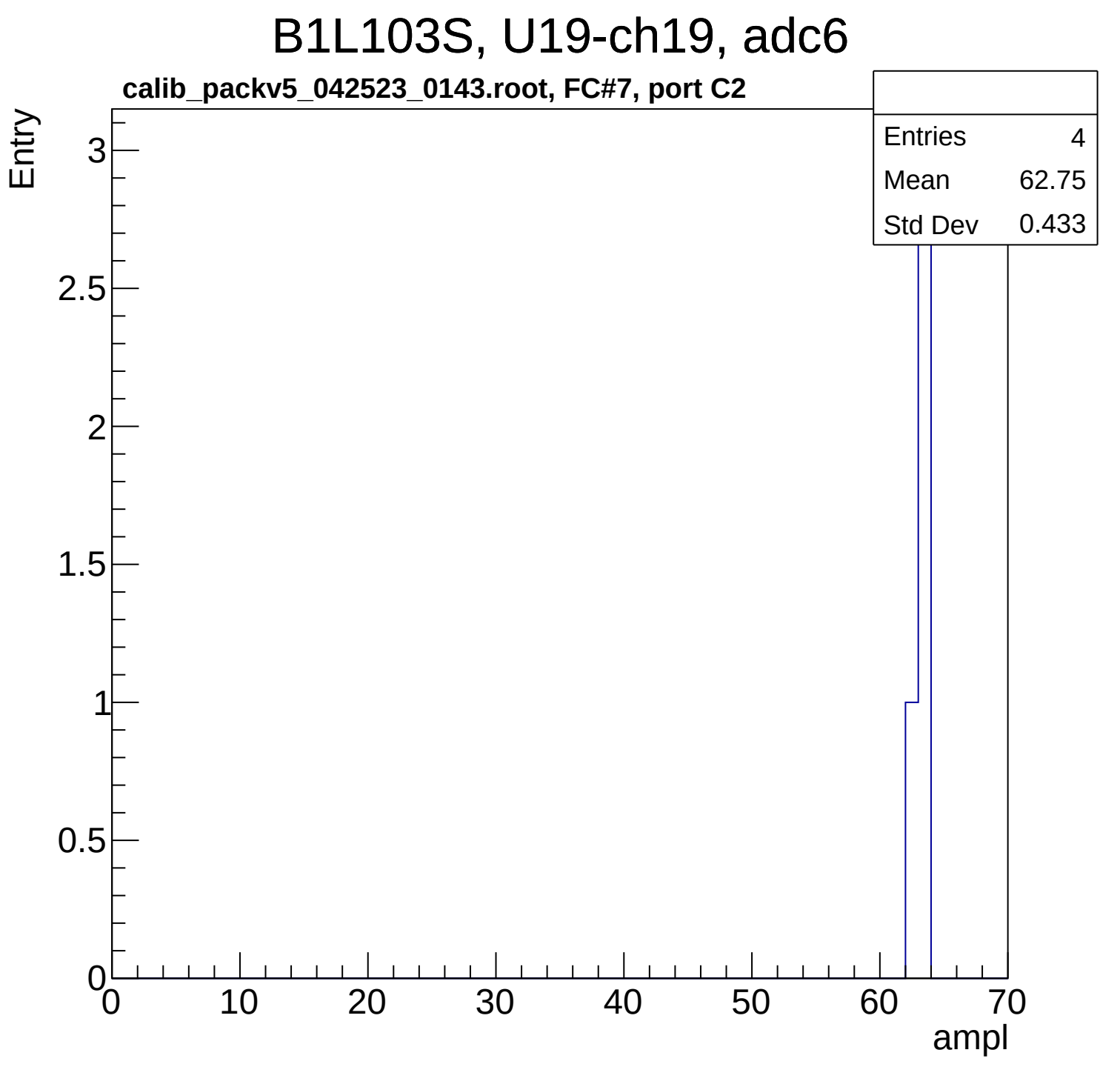

B1L103S, U19-ch19, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.89
Std Dev 8.983
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch19, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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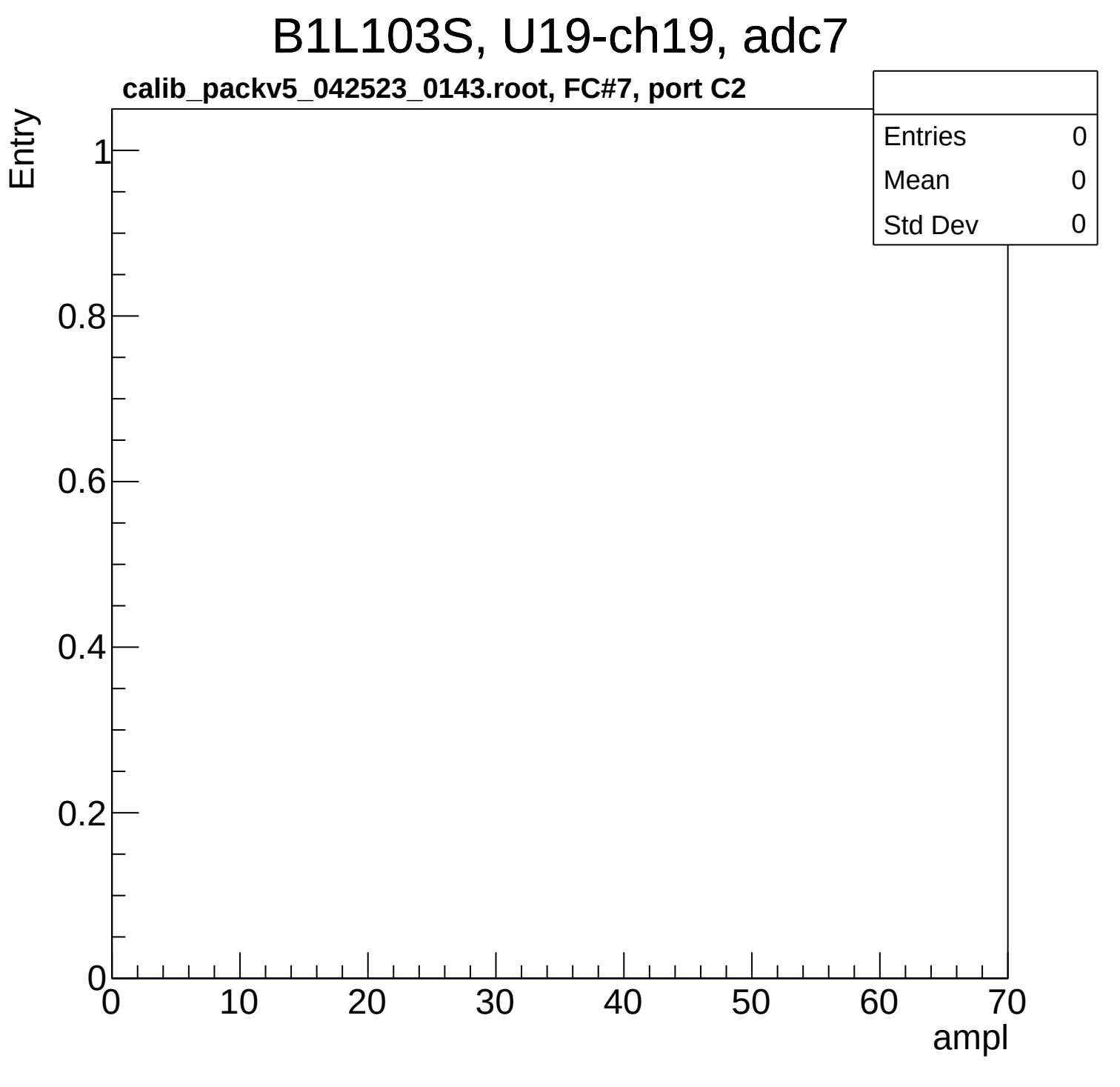
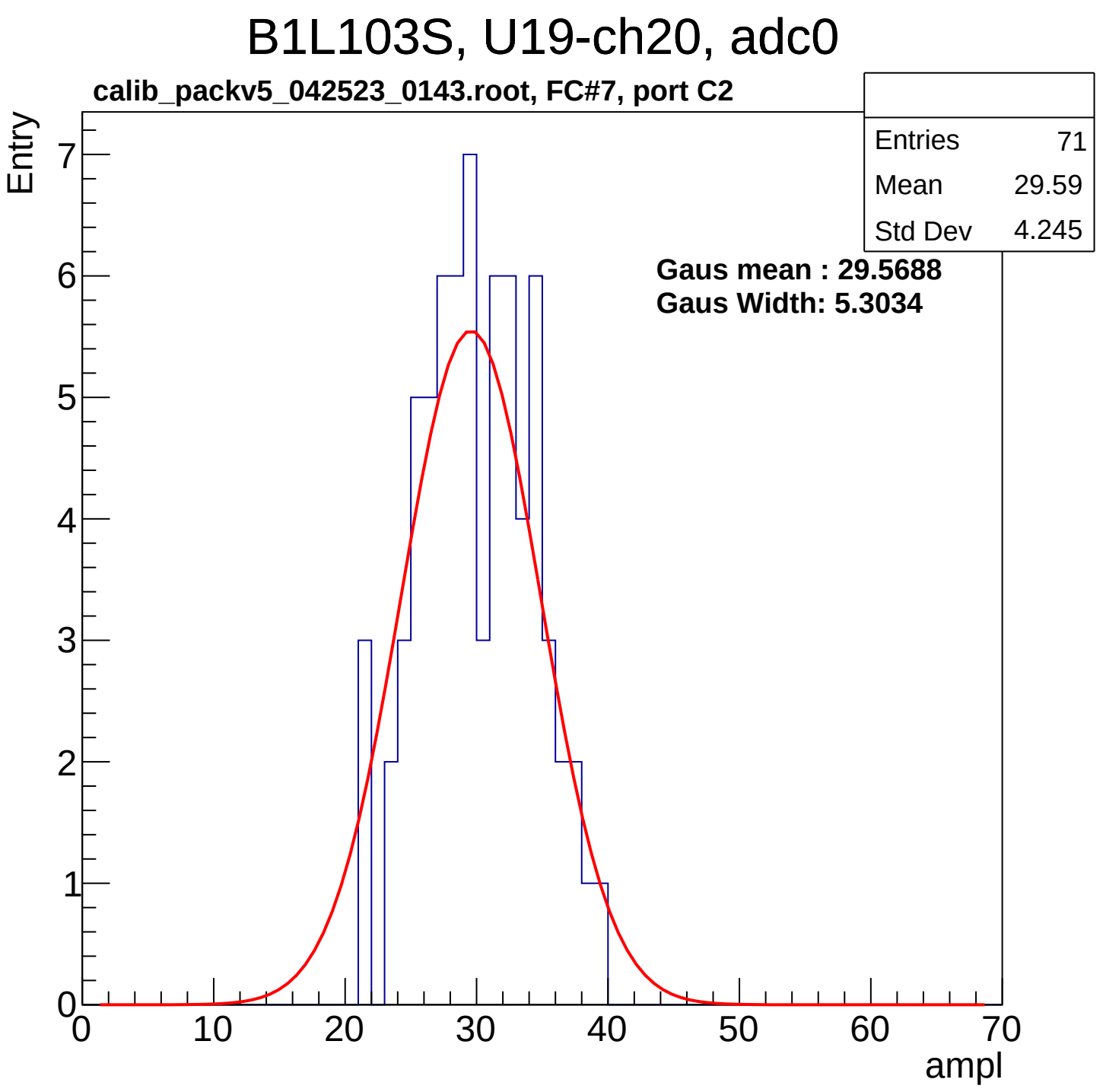
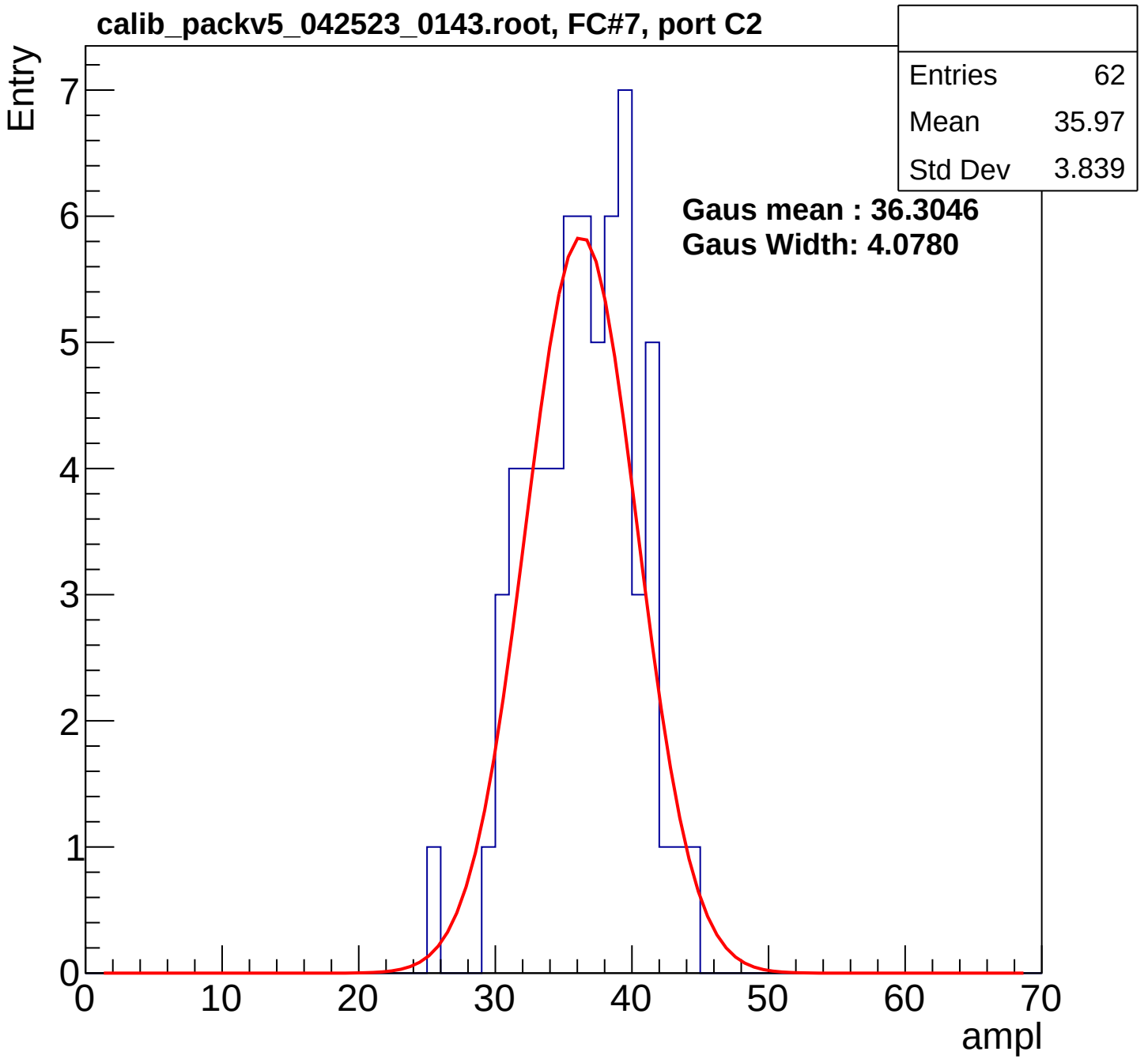
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch19, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch20, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 29.59
Std Dev 4.245
Gaus mean : 29.5688
Gaus Width: 5.3034
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch20, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.97
Std Dev 3.839
Gaus mean : 36.3046
Gaus Width: 4.0780
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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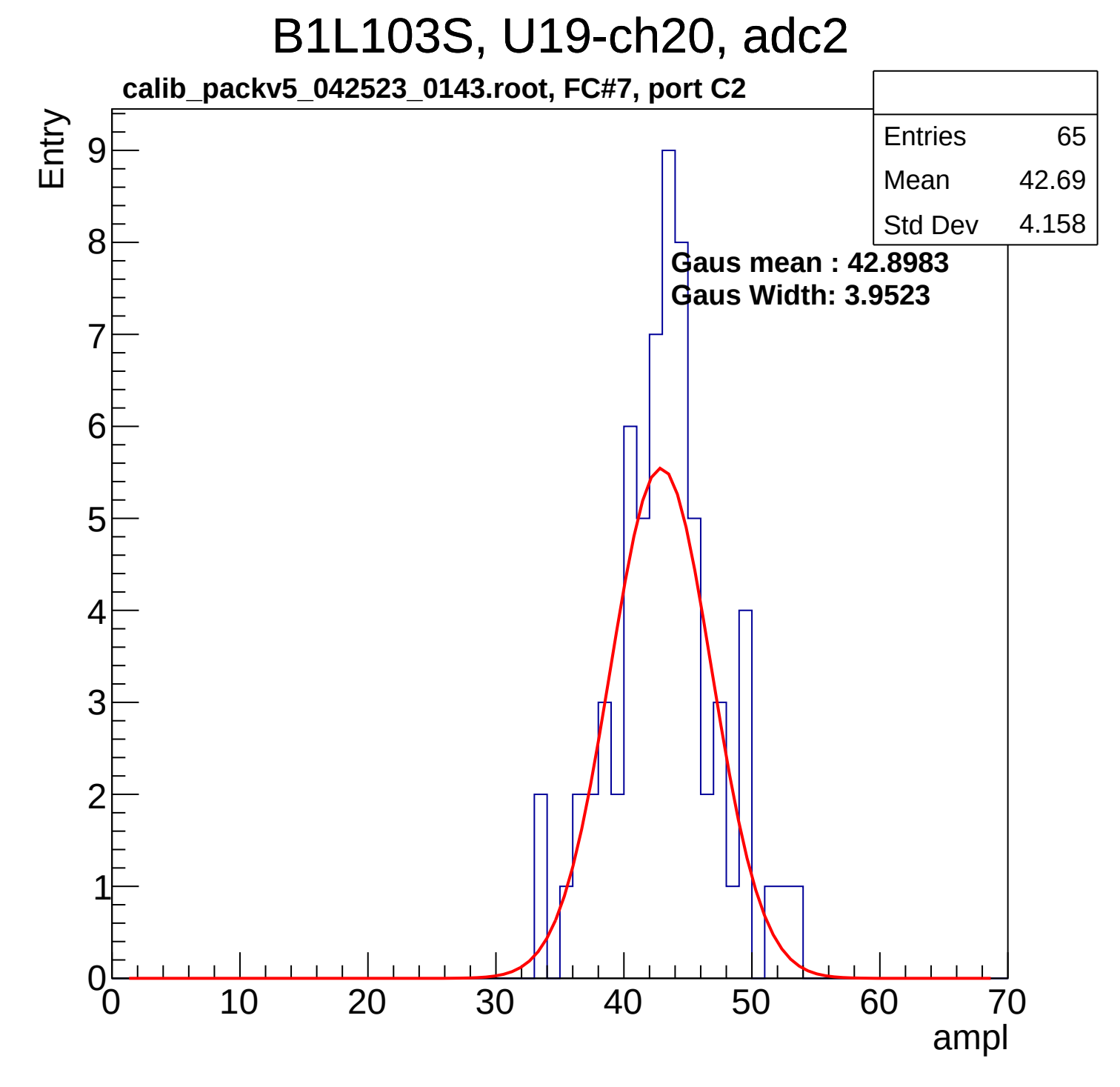
B1L103S, U19-ch20, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.69
Std Dev 4.158
Gaus mean : 42.8983
Gaus Width: 3.9523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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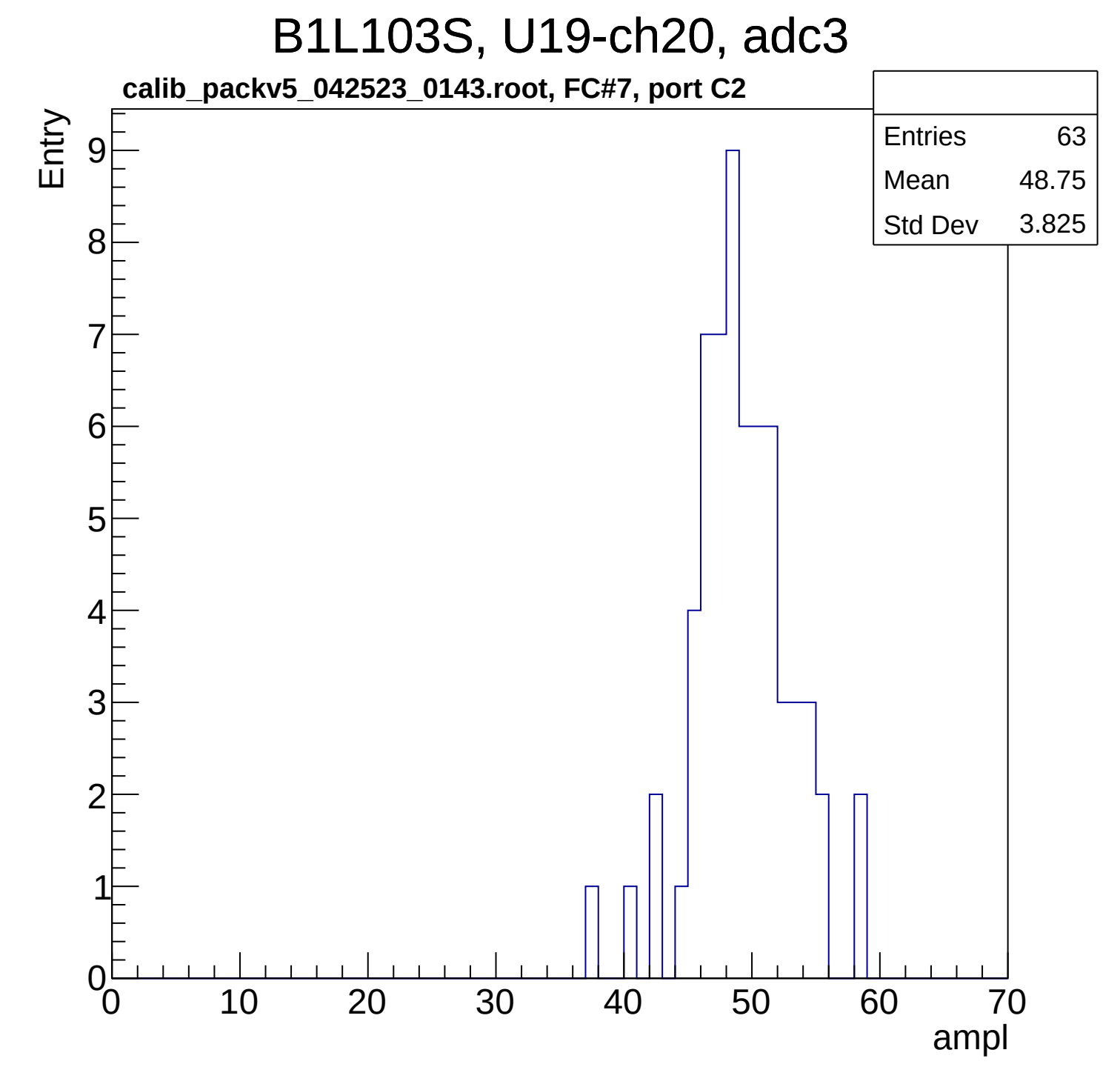
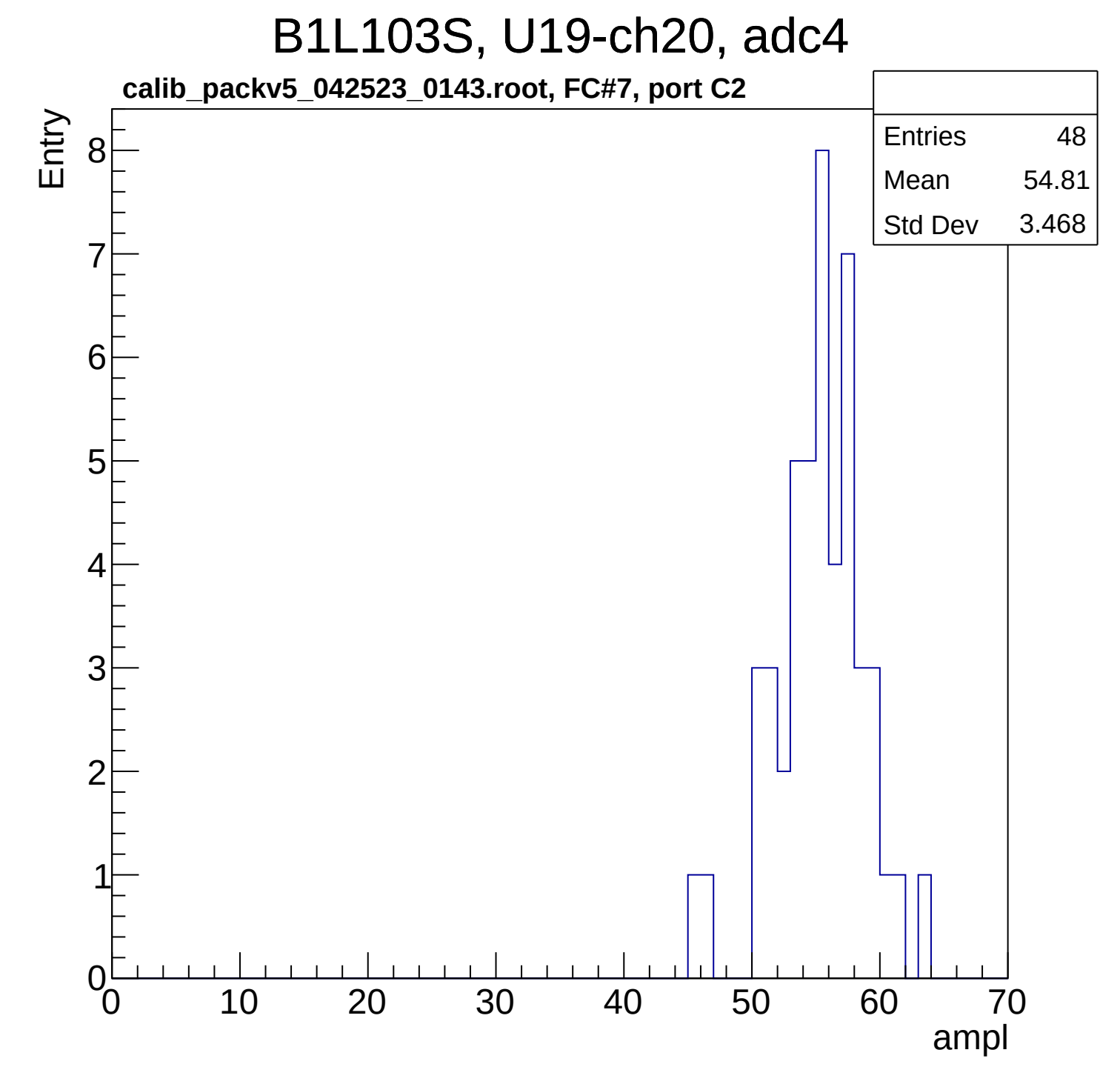

B1L103S, U19-ch20, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.75
Std Dev 3.825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch20, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.81
Std Dev 3.468
Entry
ampl
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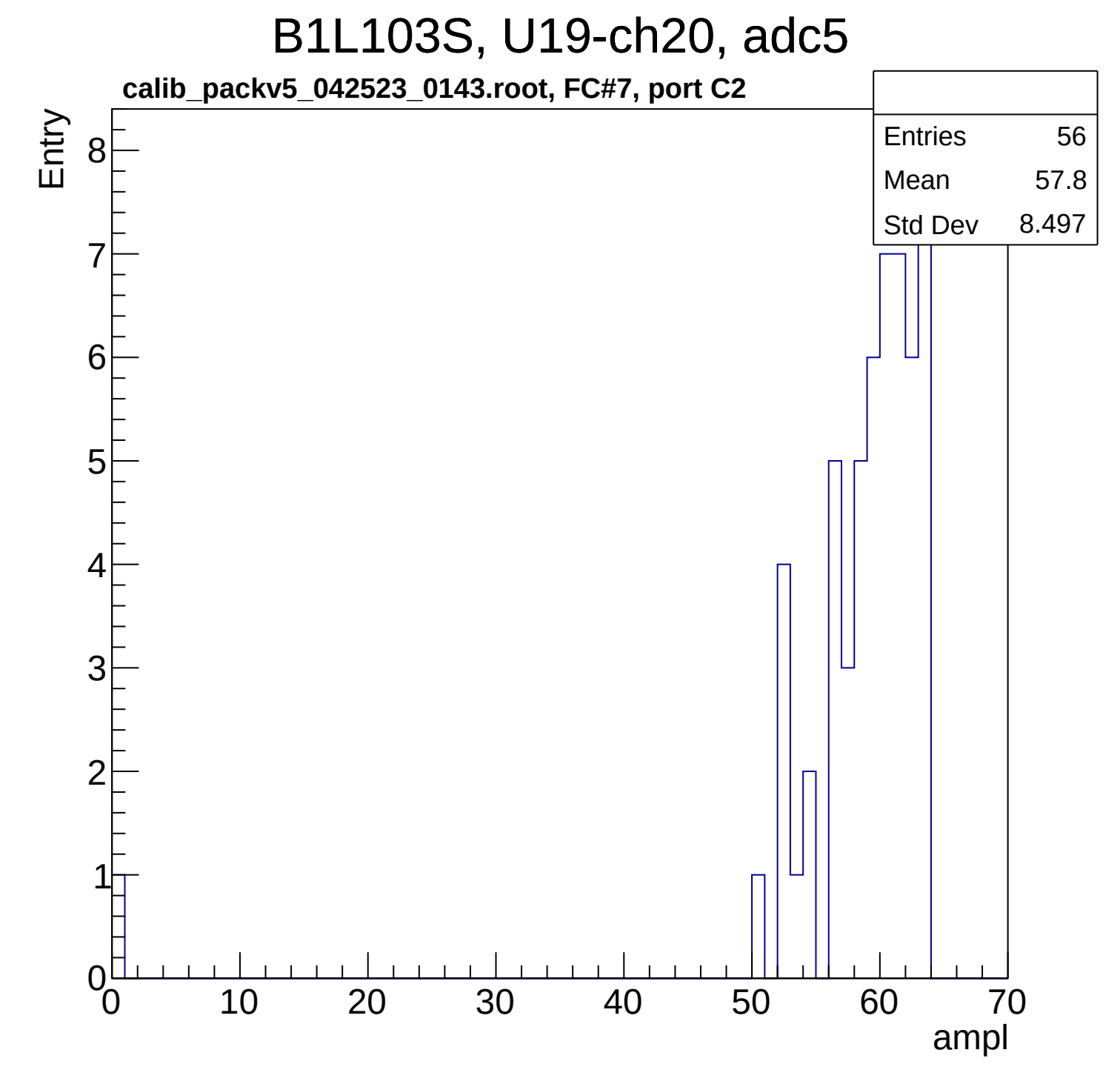
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch20, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.8
Std Dev 8.497
Entry
ampl
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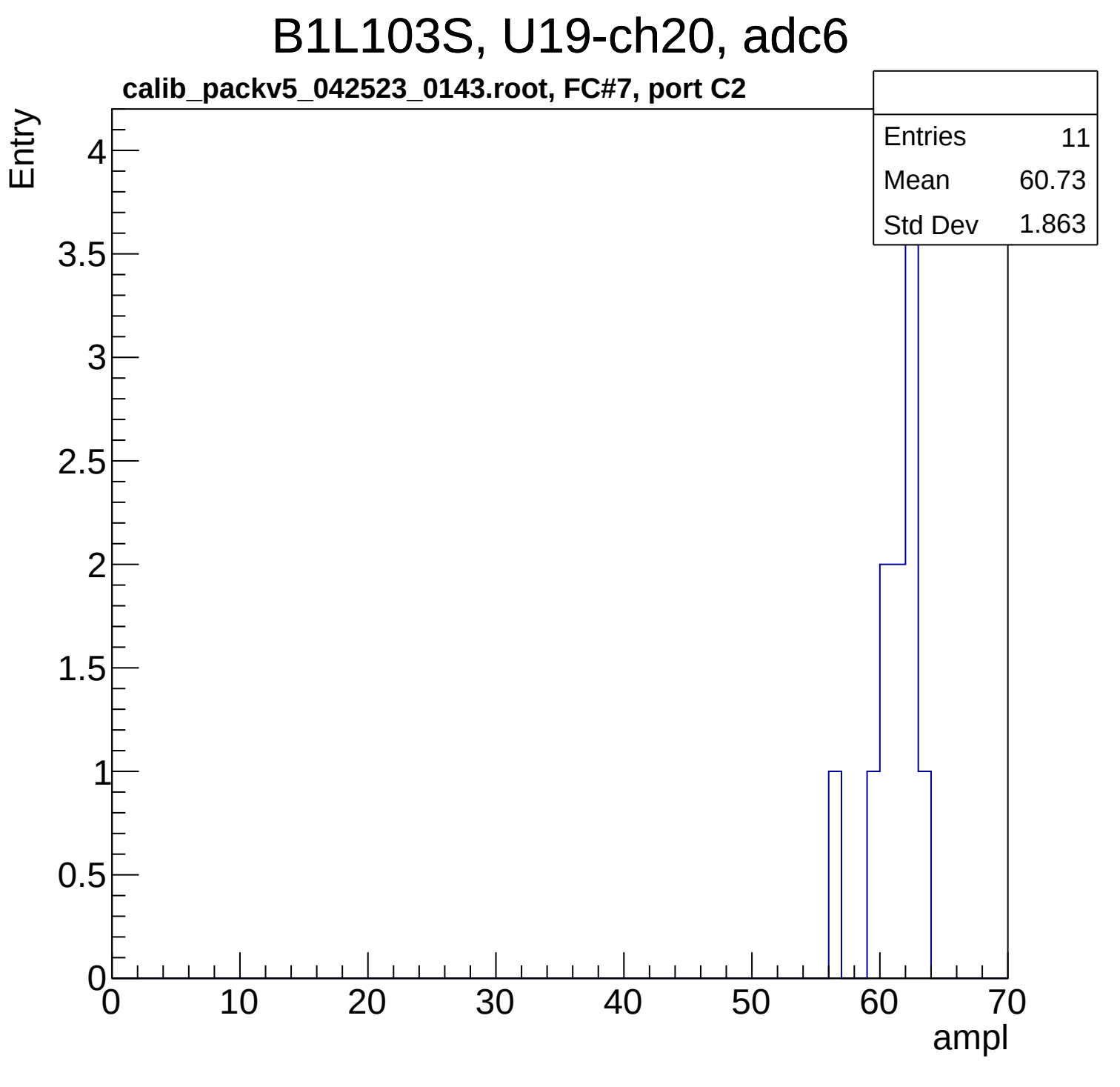
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch20, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 60.73
Std Dev 1.863
Entry
ampl
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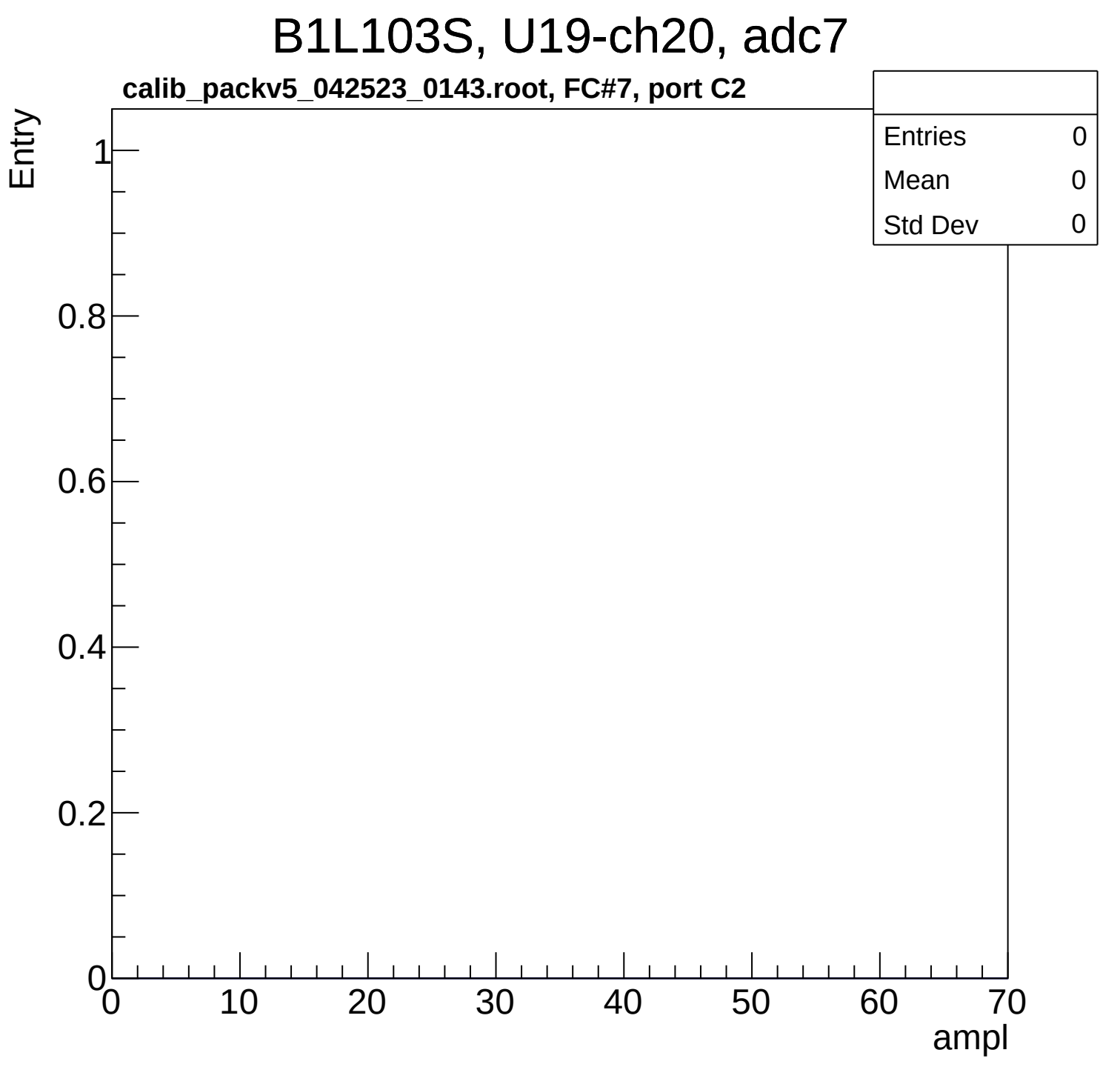
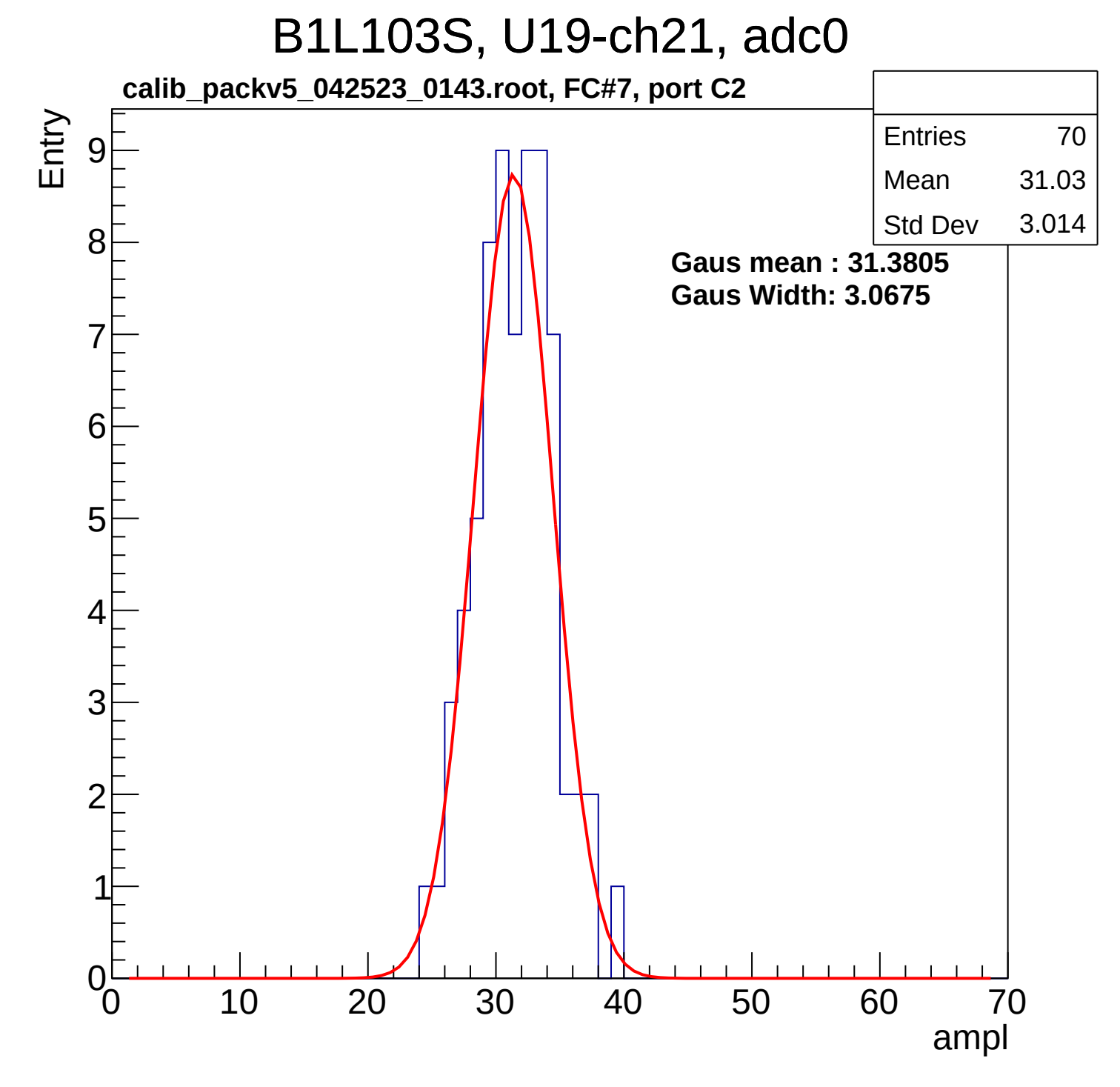
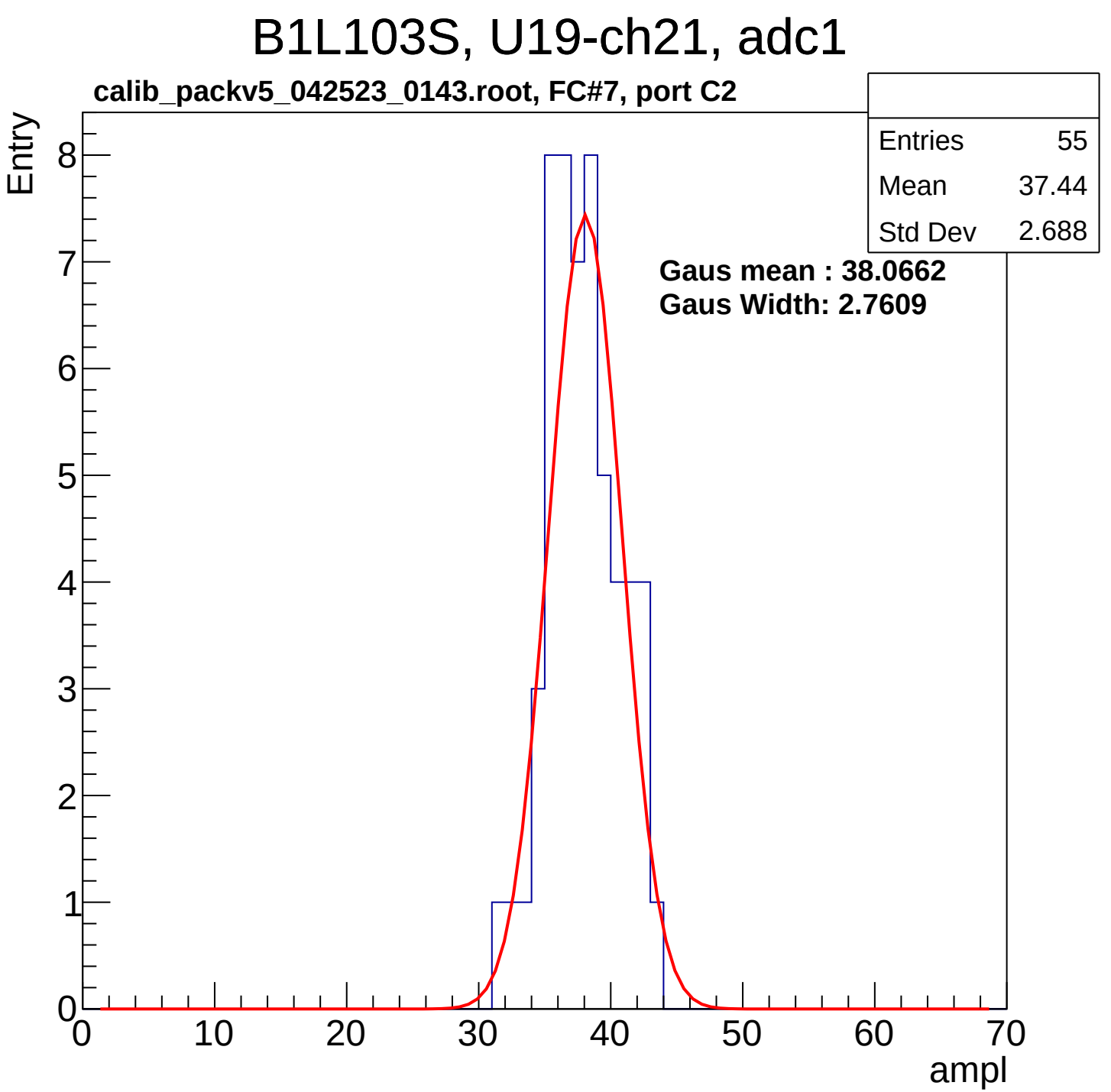
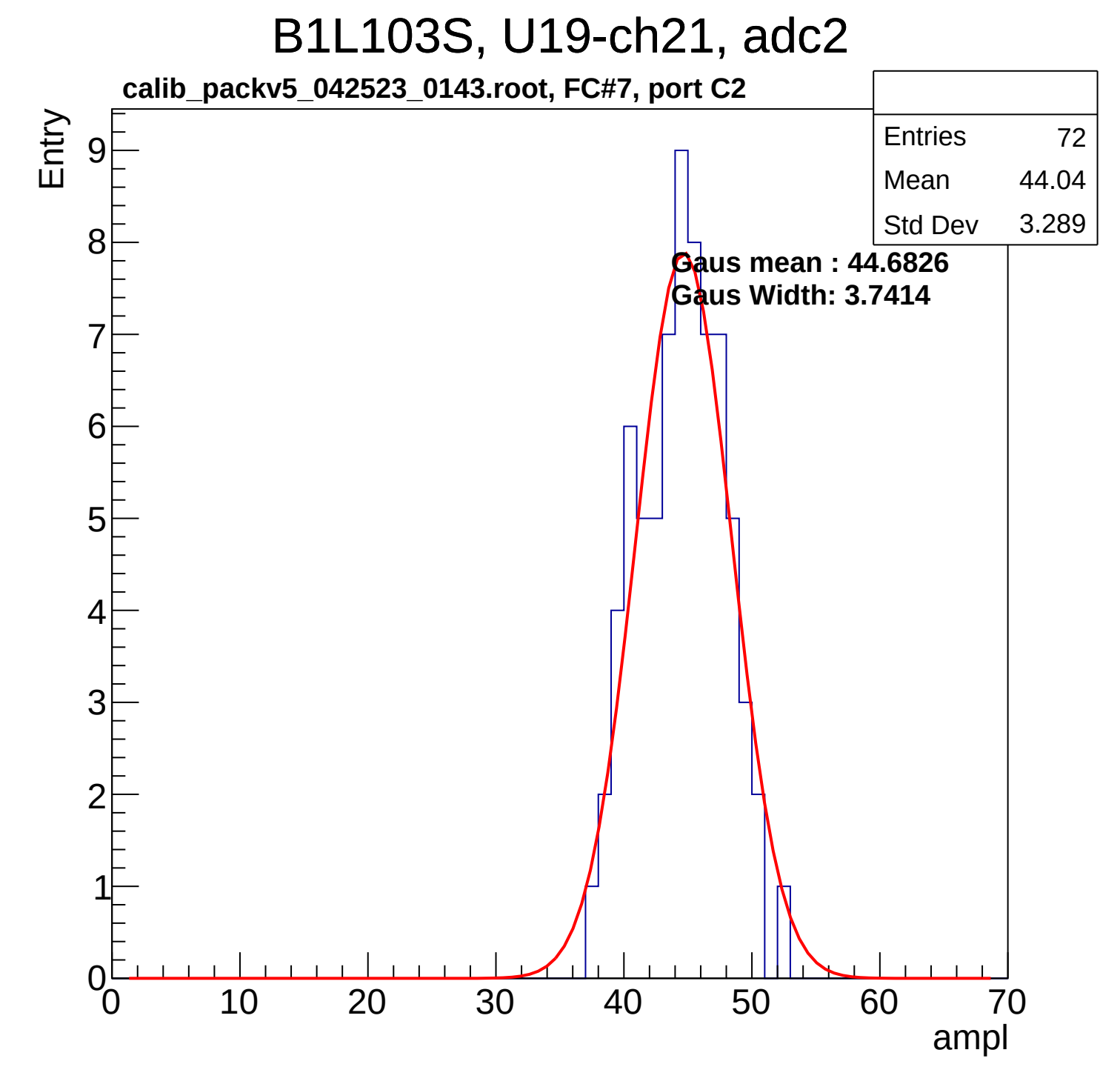
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U19-ch20, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch21, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 31.03
Std Dev 3.014
Gaus mean : 31.3805
Gaus Width: 3.0675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch21, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 37.44
Std Dev 2.688
Gaus mean : 38.0662
Gaus Width: 2.7609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch21, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 44.04
Std Dev 3.289
Gaus mean : 44.6826
Gaus Width: 3.7414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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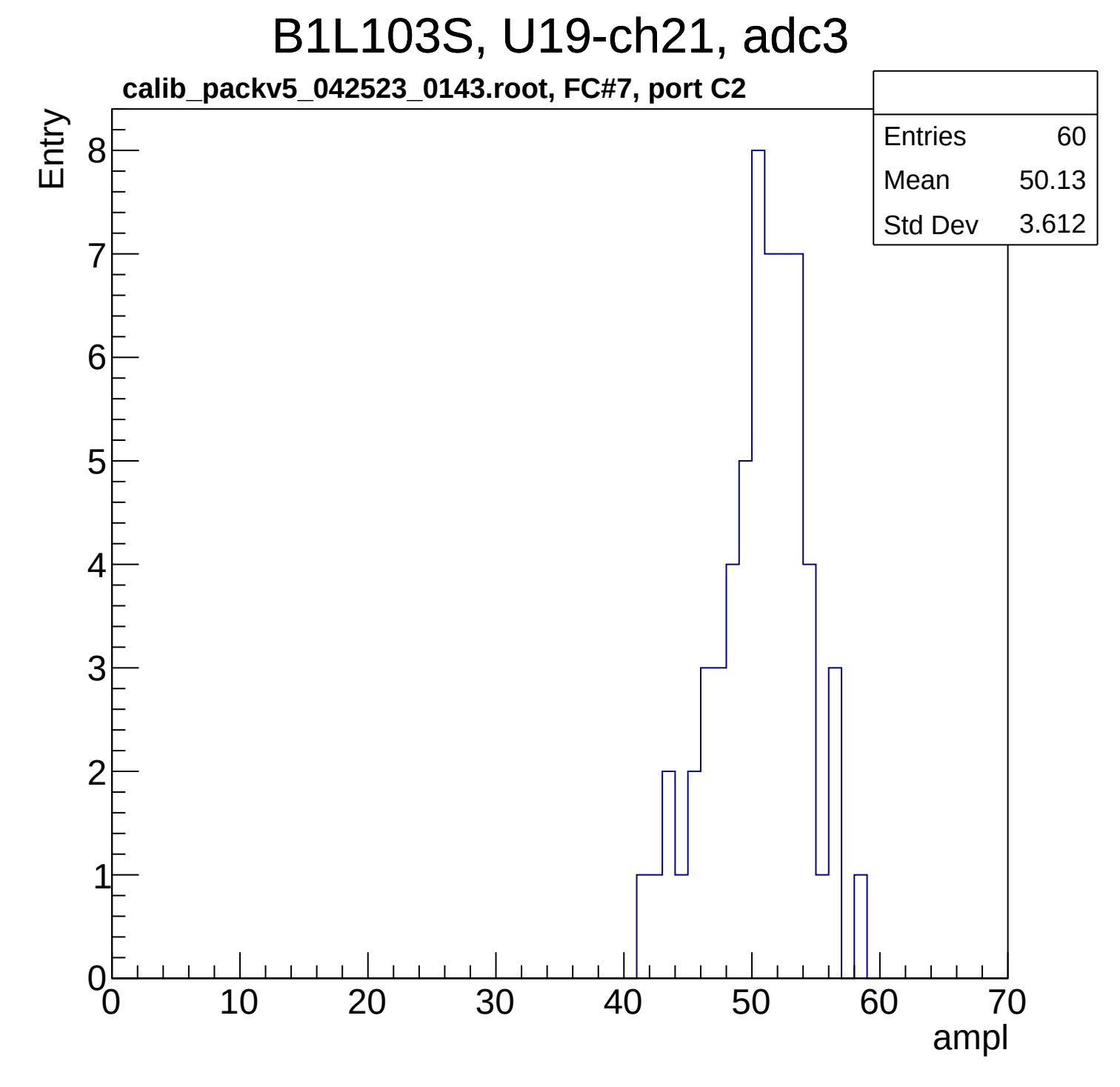
B1L103S, U19-ch21, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.13
Std Dev 3.612
Entry
ampl
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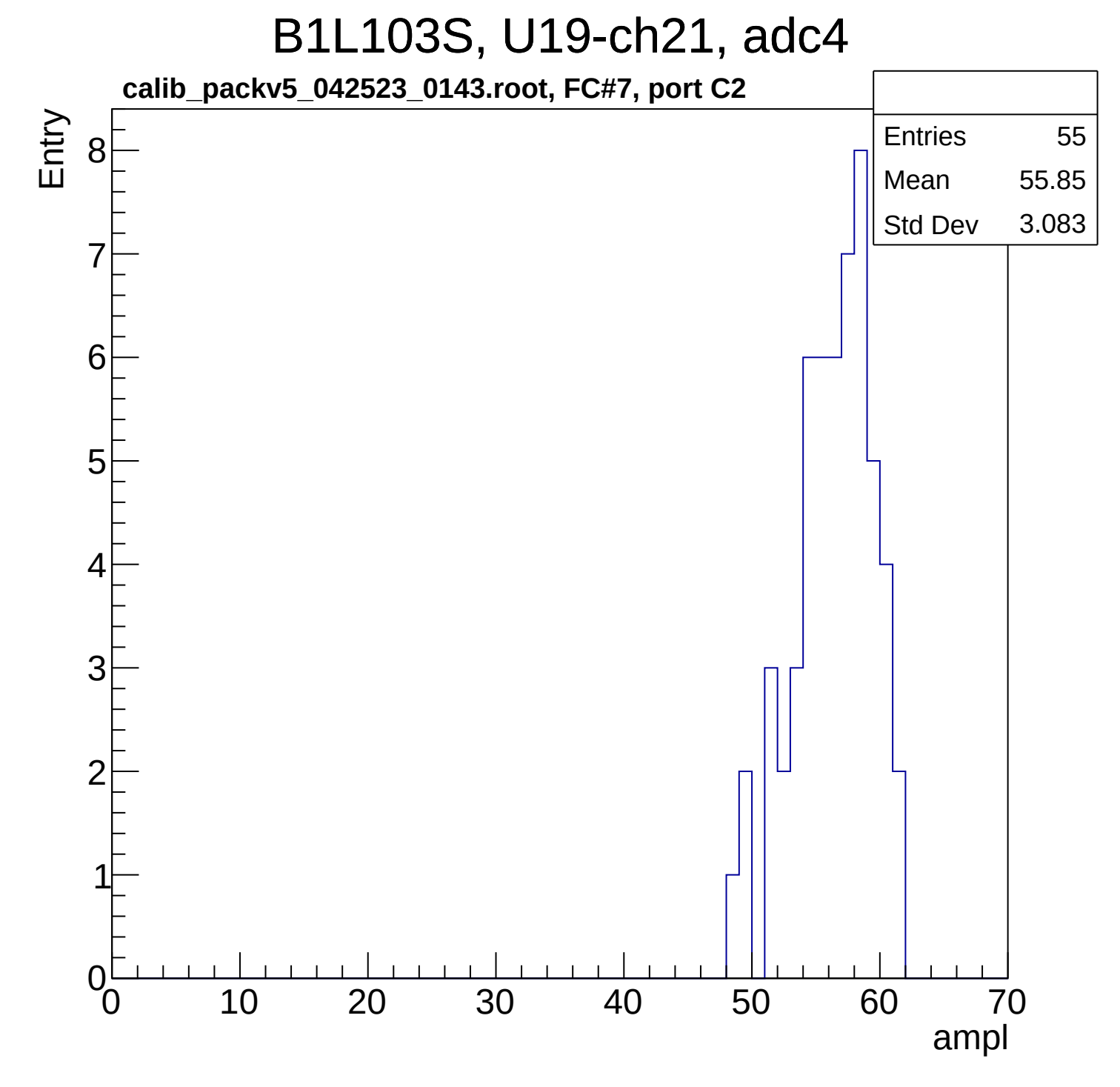
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch21, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.85
Std Dev 3.083
Entry
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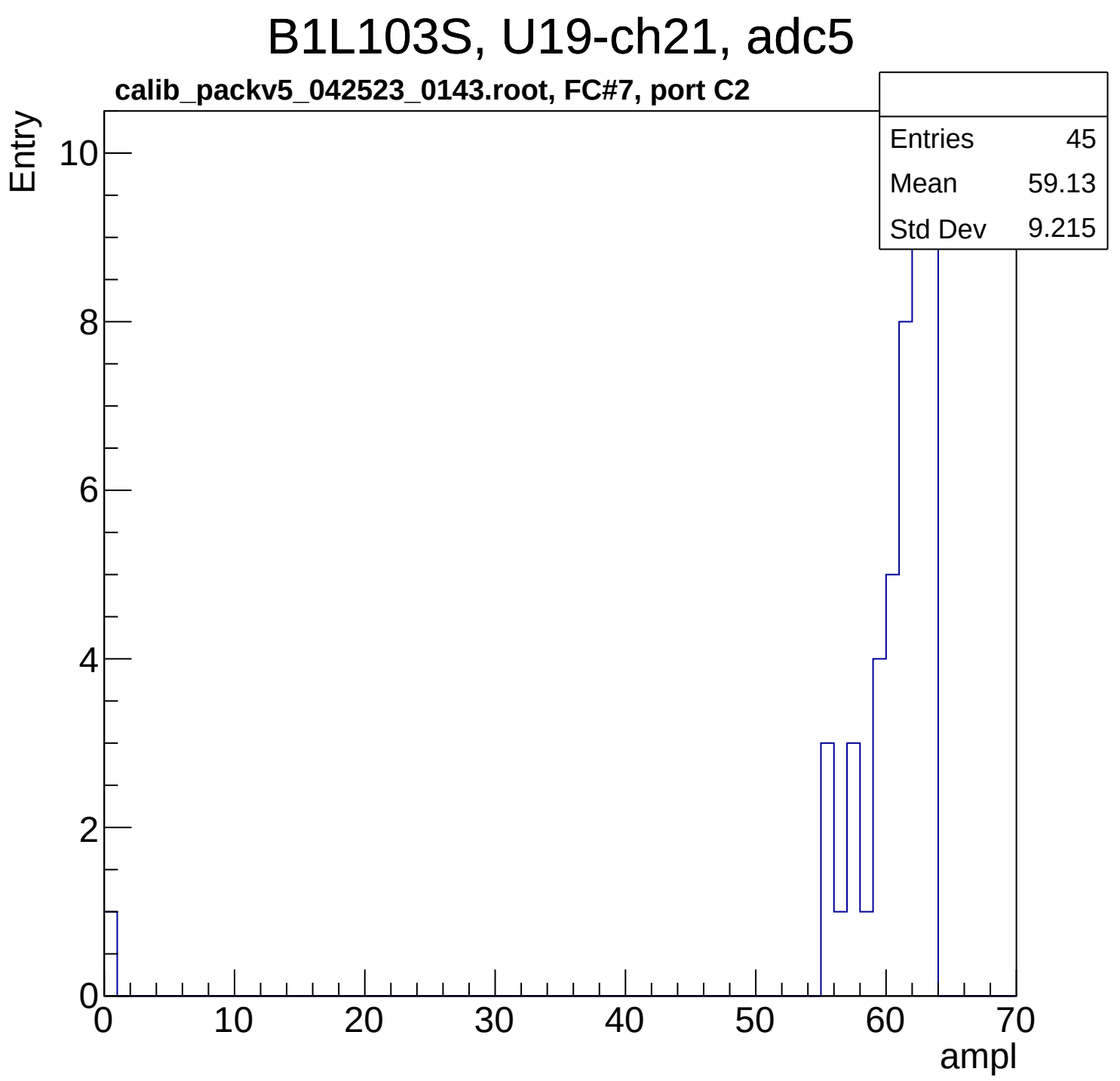
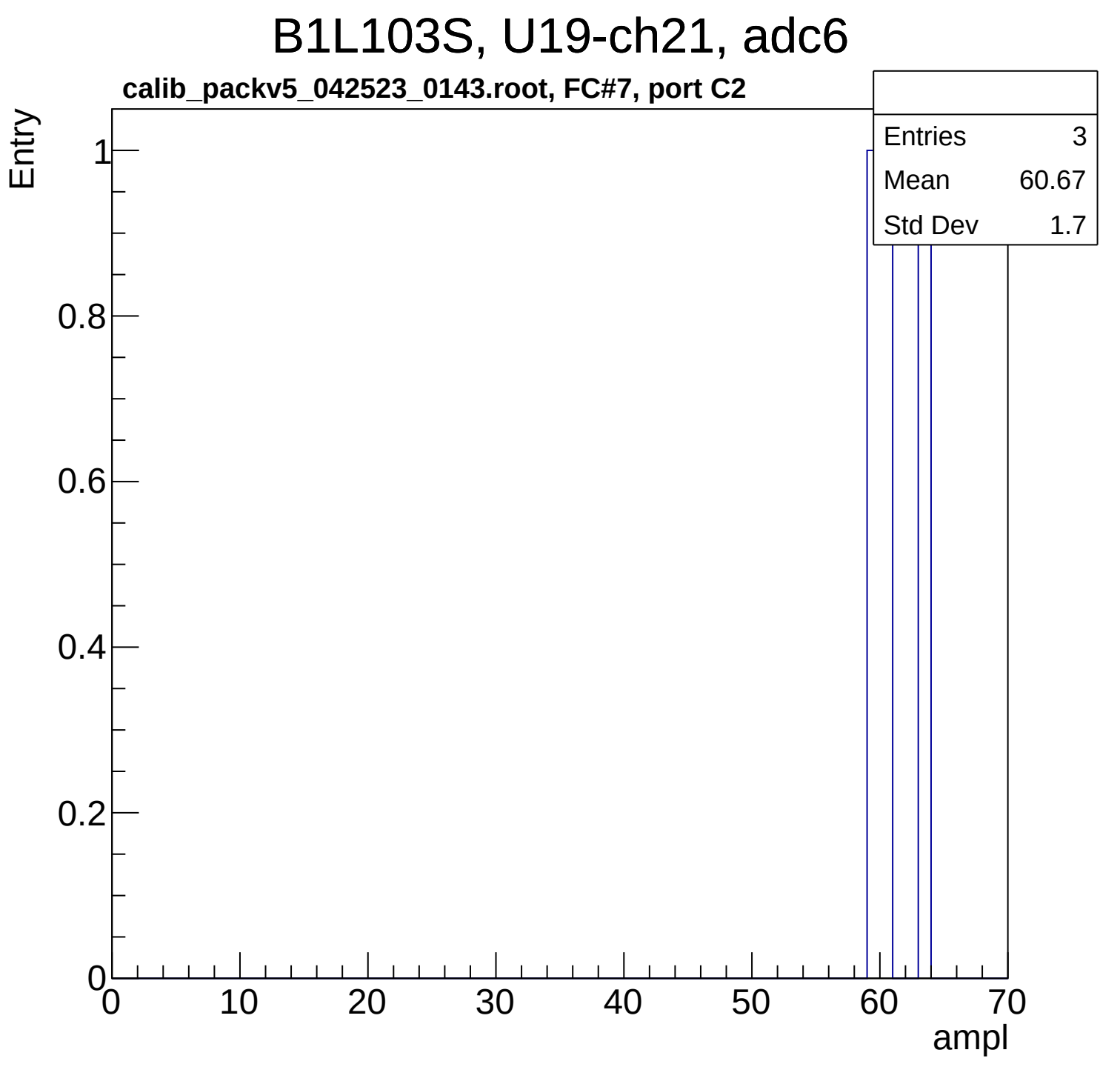
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch21, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.13
Std Dev 9.215
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch21, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 60.67
Std Dev 1.7
Entry
ampl
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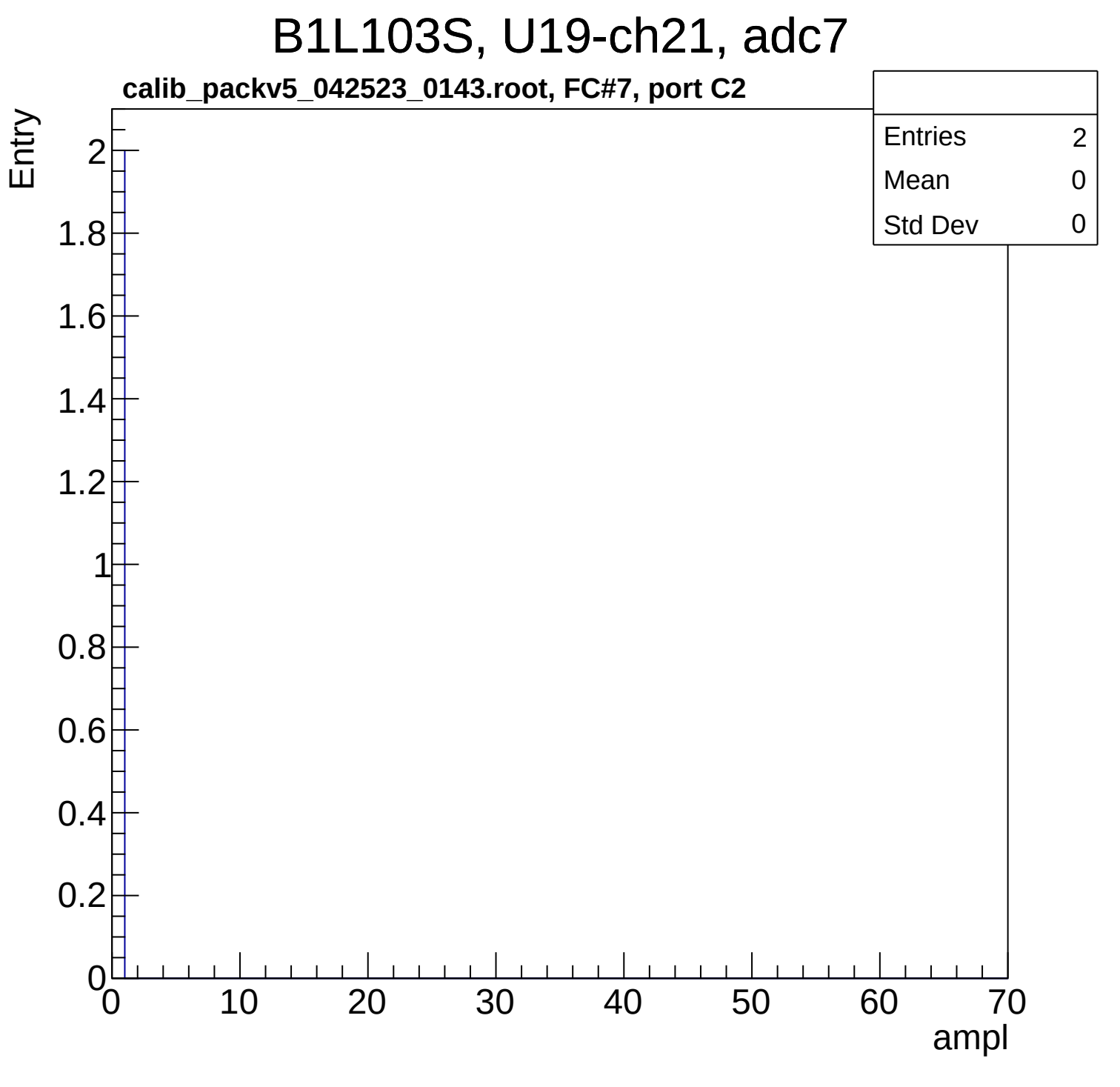
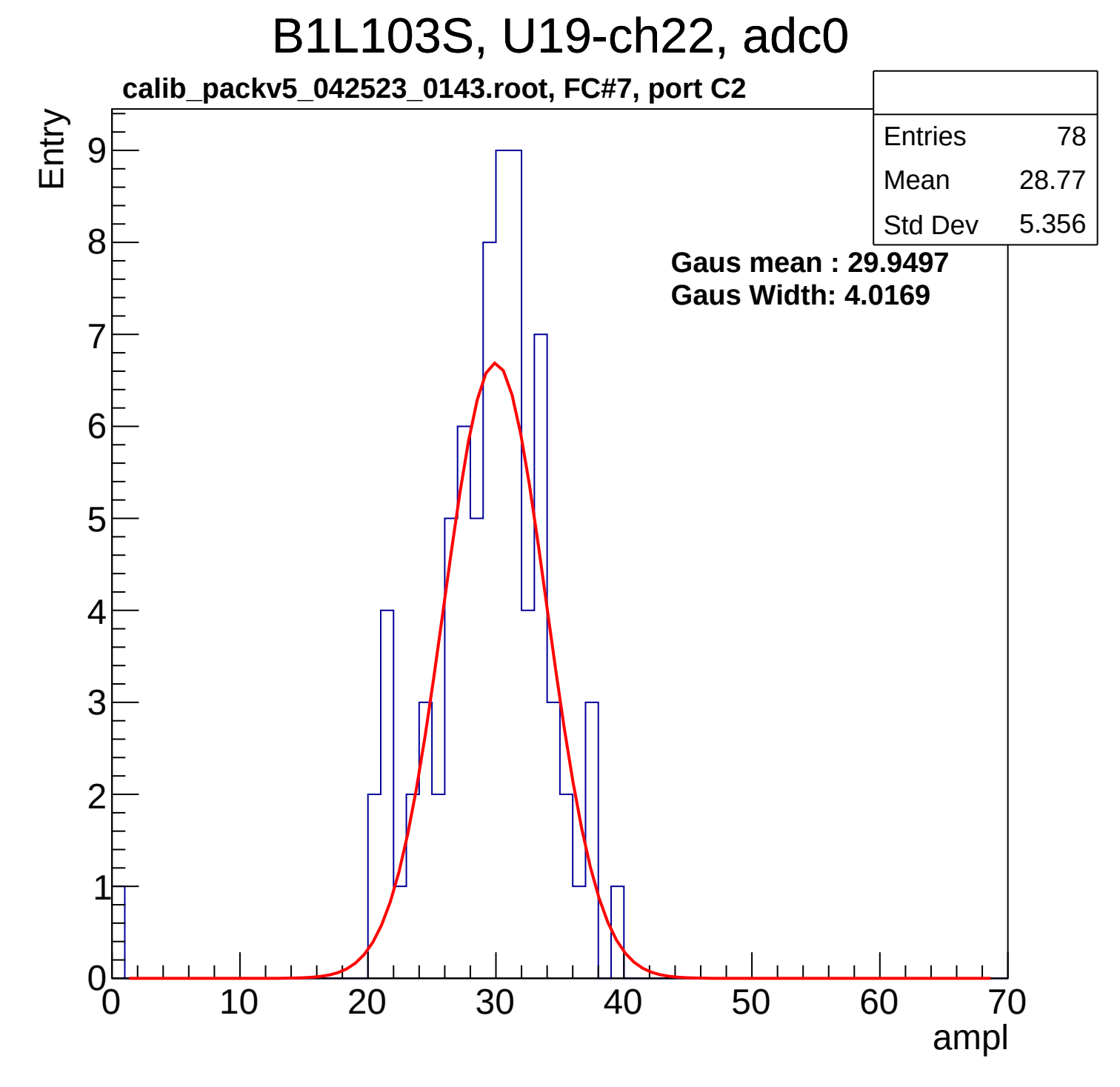
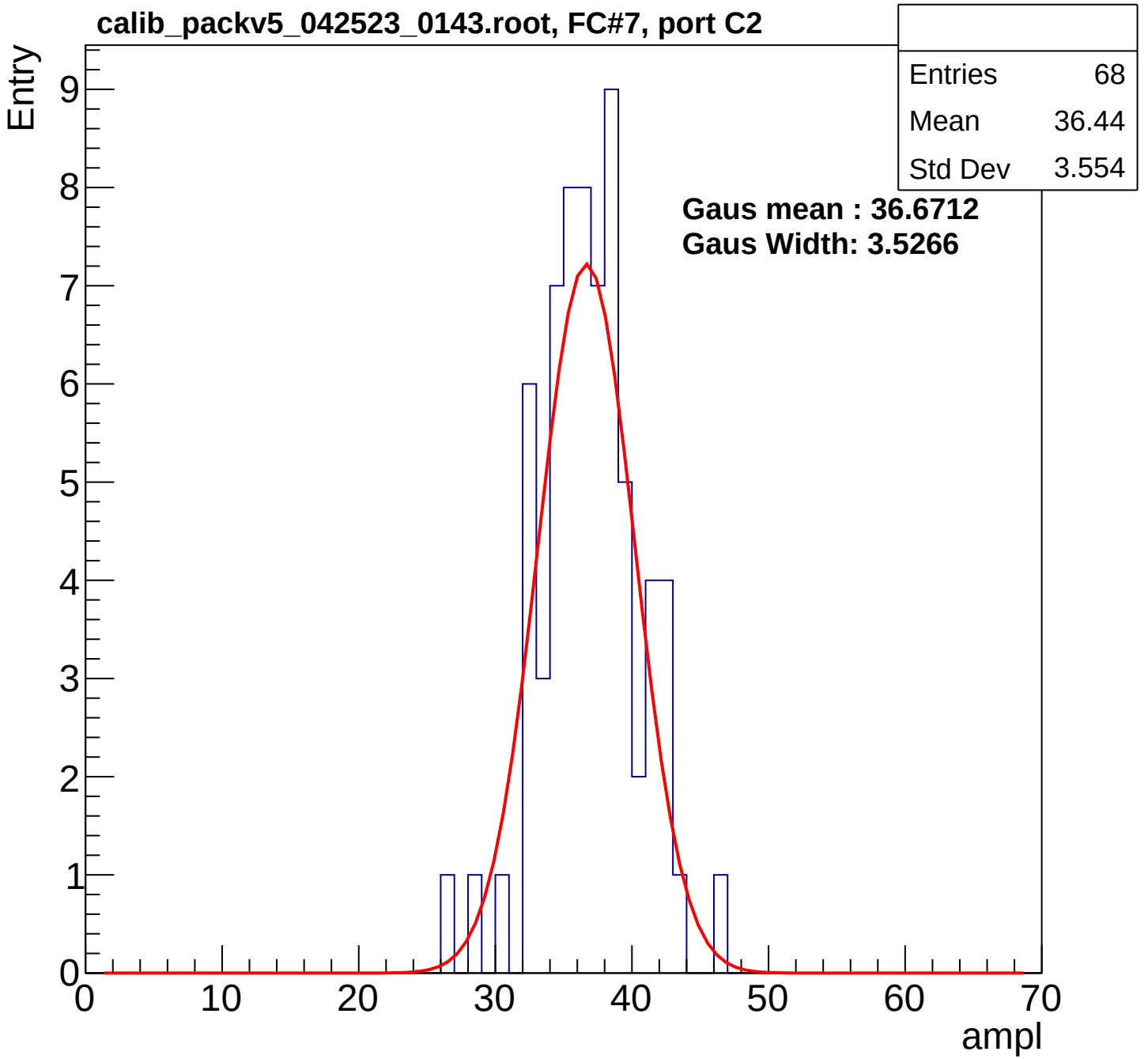
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U19-ch21, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch22, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 28.77
Std Dev 5.356
Gaus mean : 29.9497
Gaus Width: 4.0169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch22, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.44
Std Dev 3.554
Gaus mean : 36.6712
Gaus Width: 3.5266
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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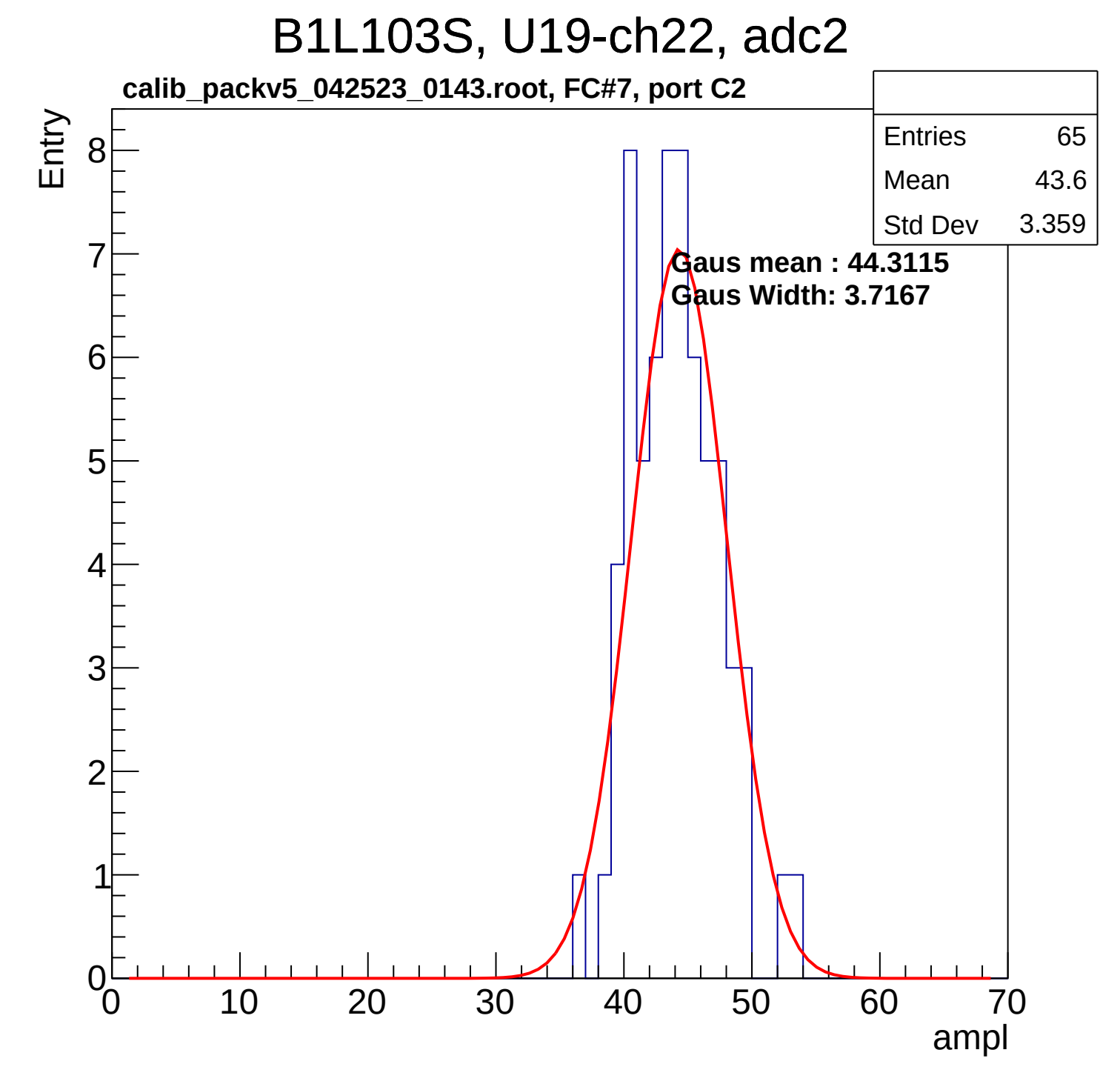
B1L103S, U19-ch22, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.6
Std Dev 3.359
Gaus mean : 44.3115
Gaus Width: 3.7167
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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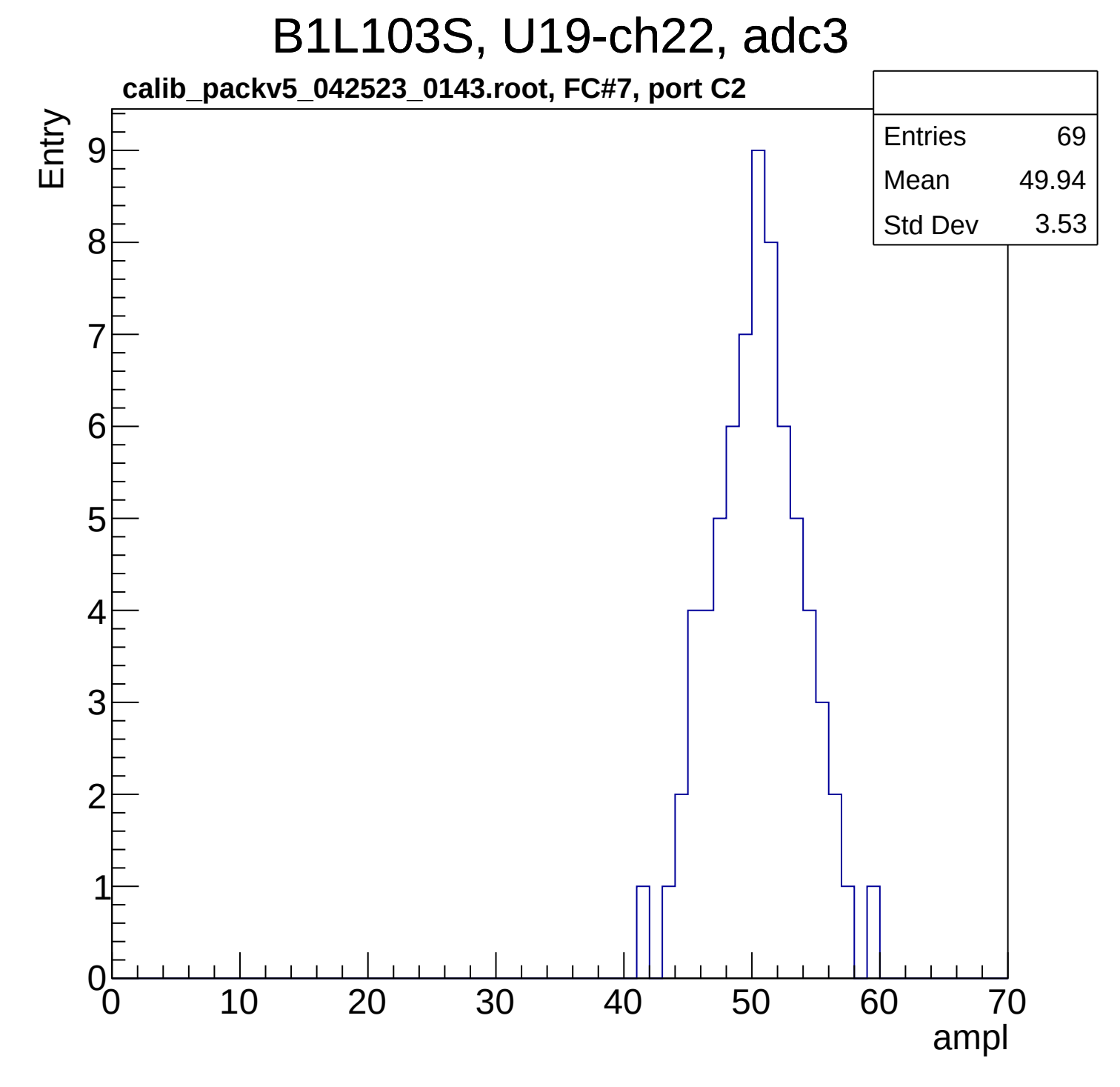
B1L103S, U19-ch22, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.94
Std Dev 3.53
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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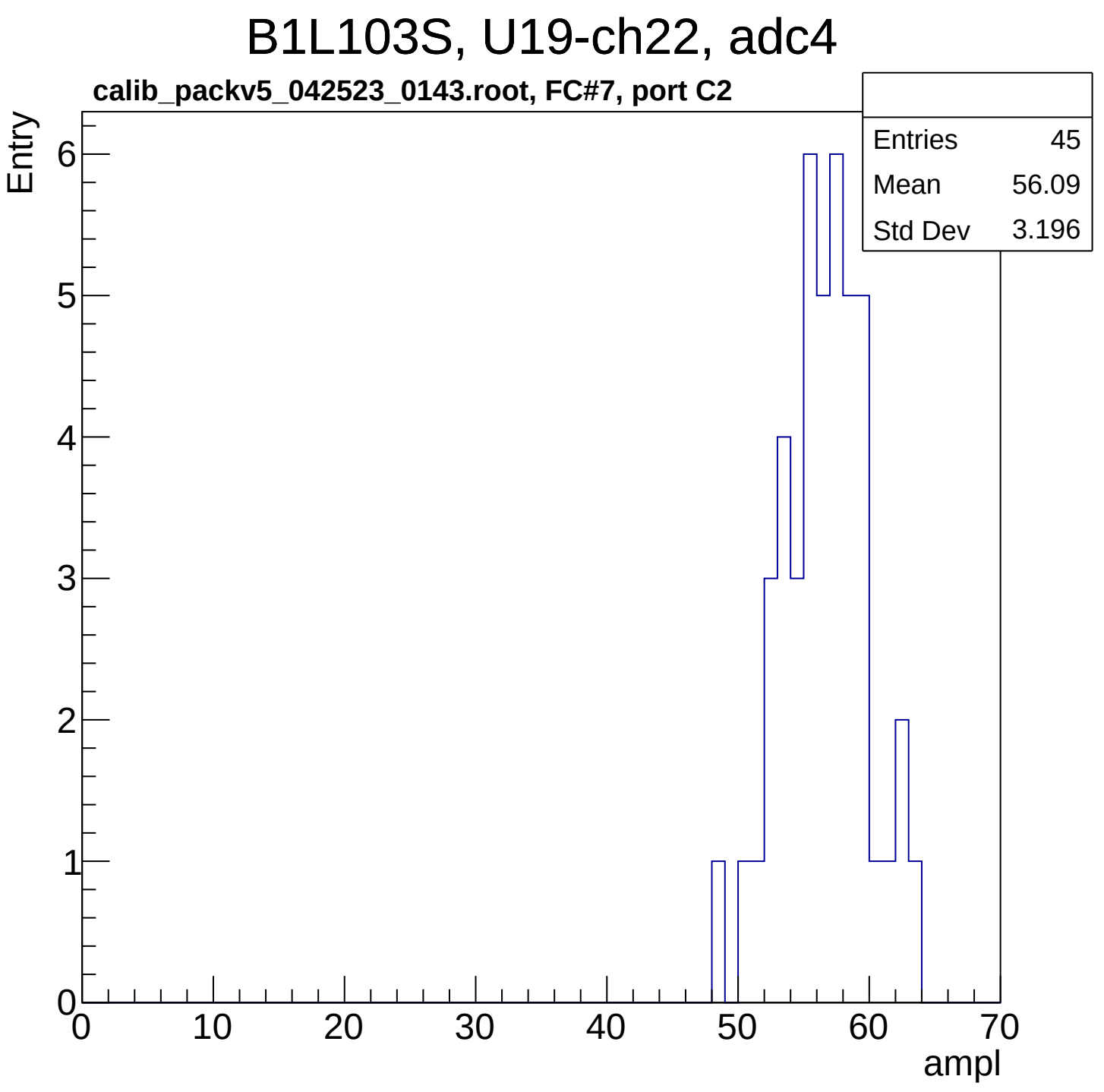
B1L103S, U19-ch22, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 56.09
Std Dev 3.196
Entry
0
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2
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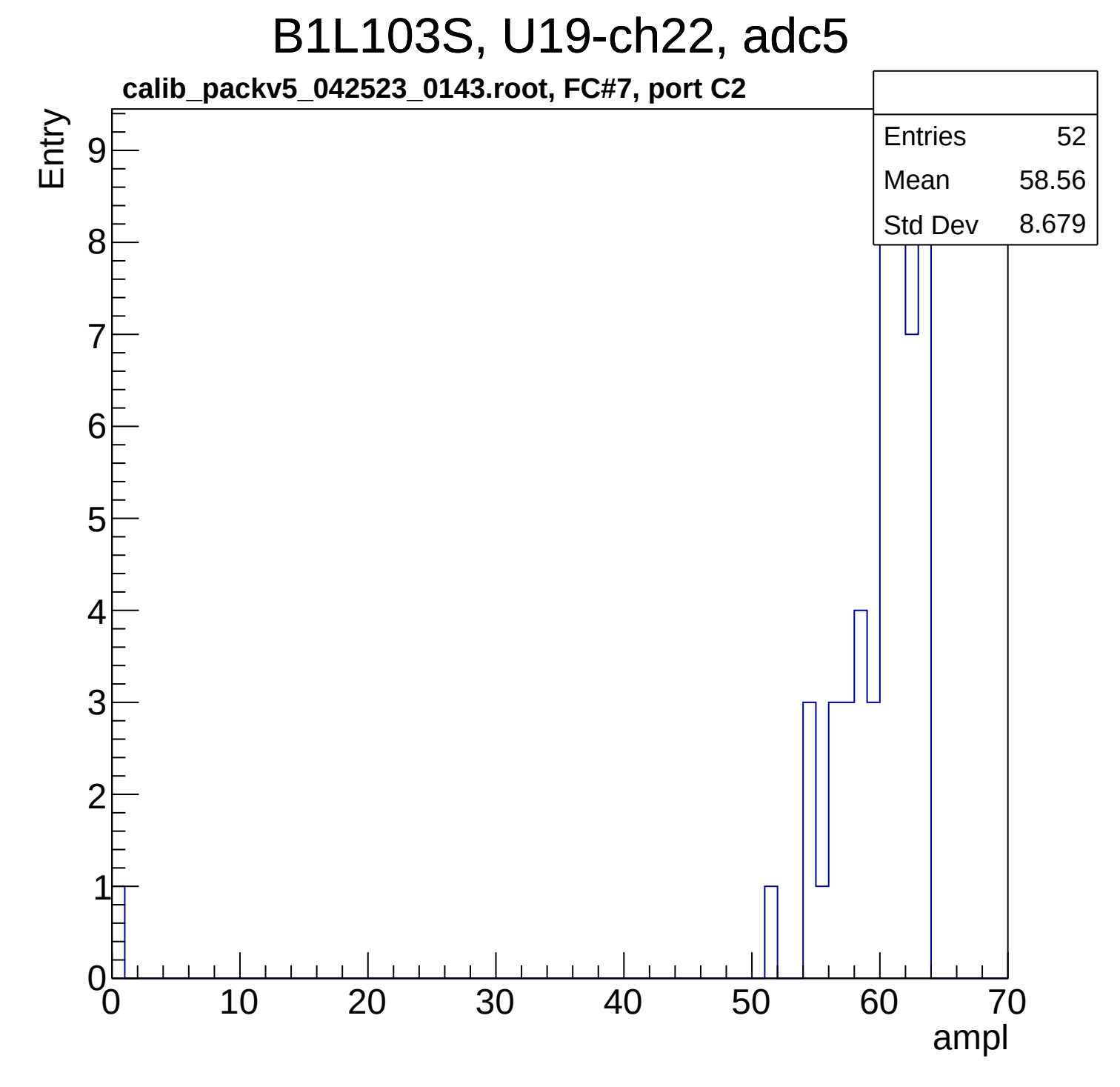
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch22, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.56
Std Dev 8.679
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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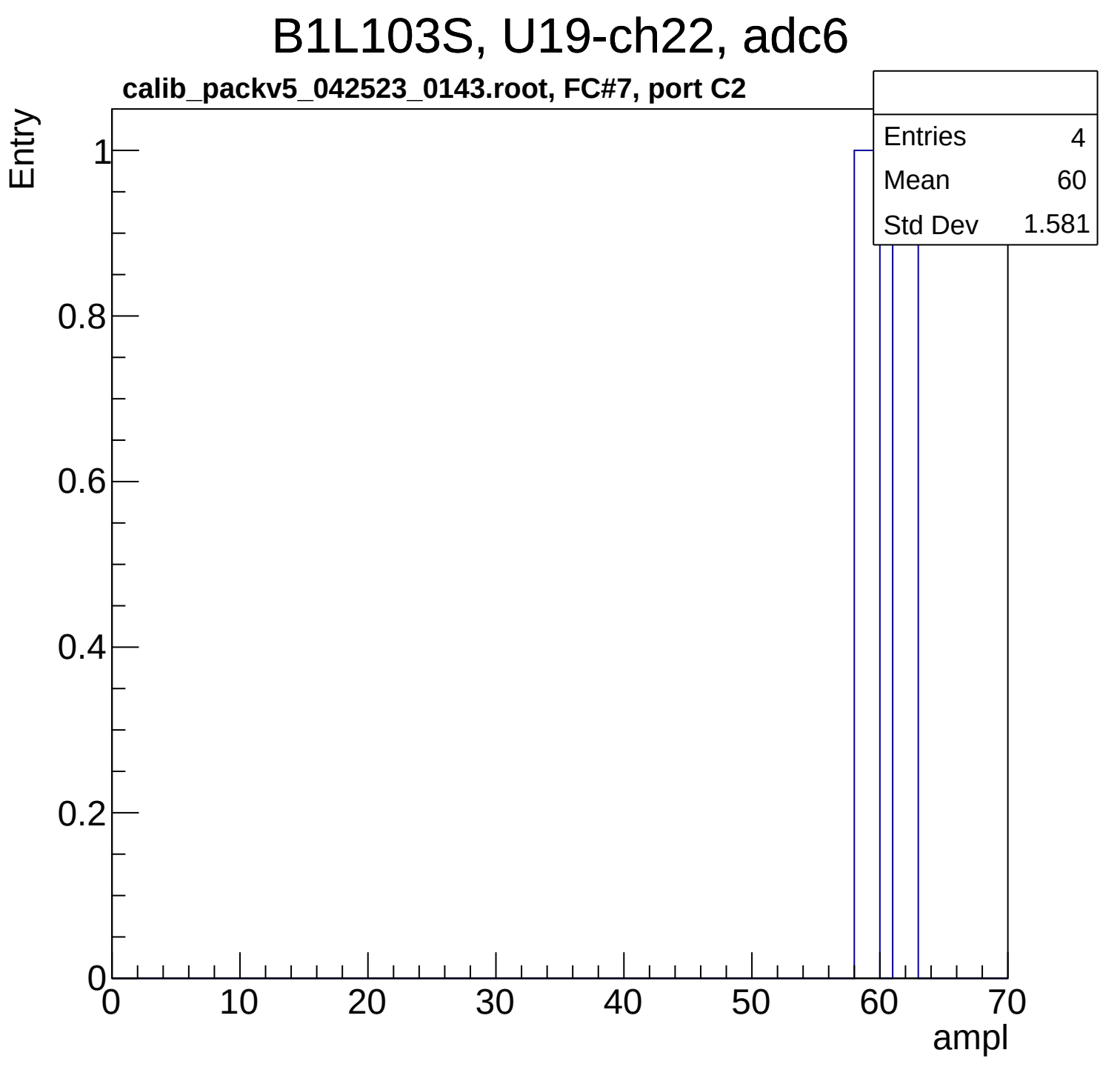
B1L103S, U19-ch22, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 60
Std Dev 1.581
Entry
ampl
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0.2
0.4
0.6
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10
20
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40
50
60
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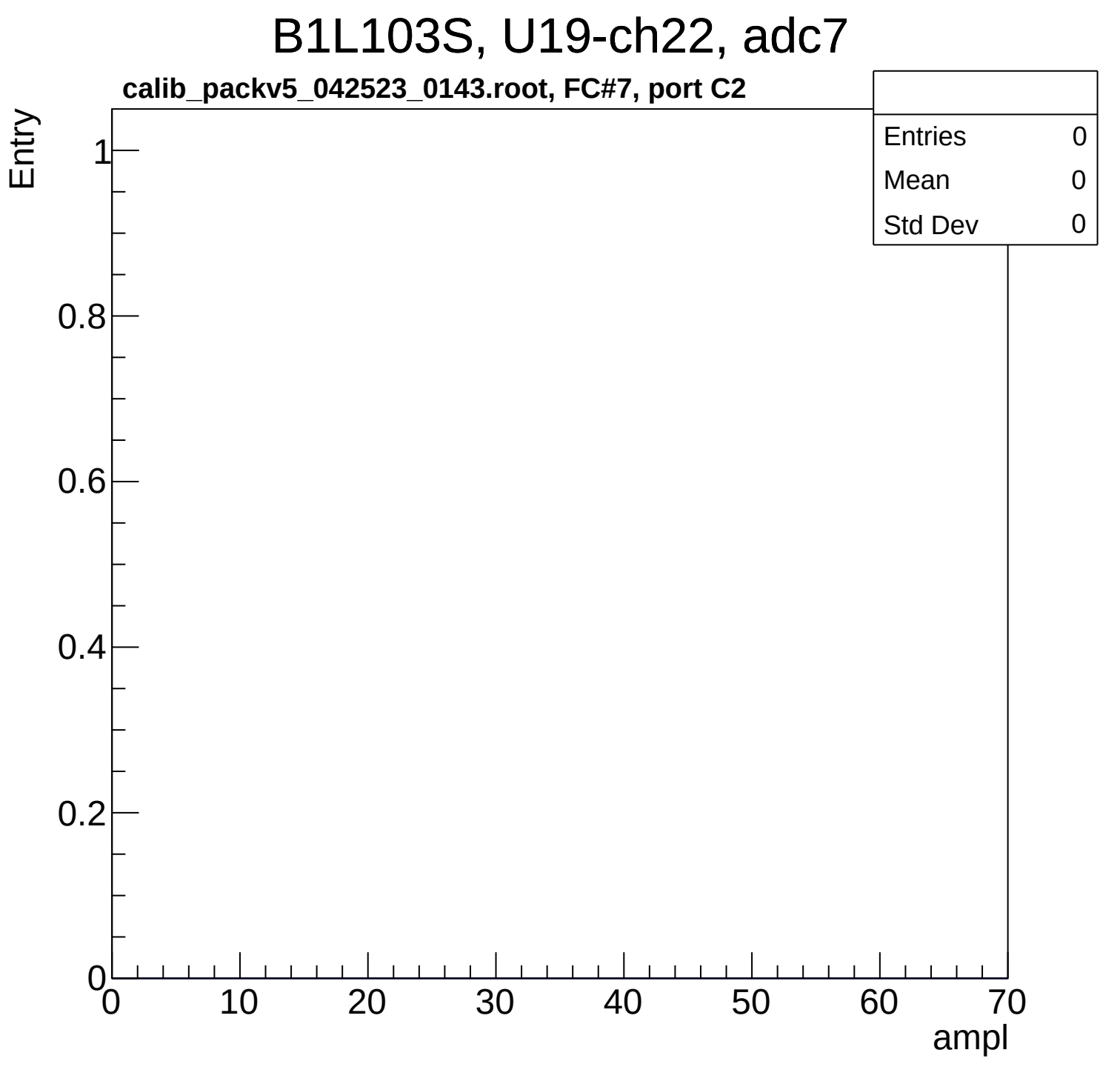
B1L103S, U19-ch22, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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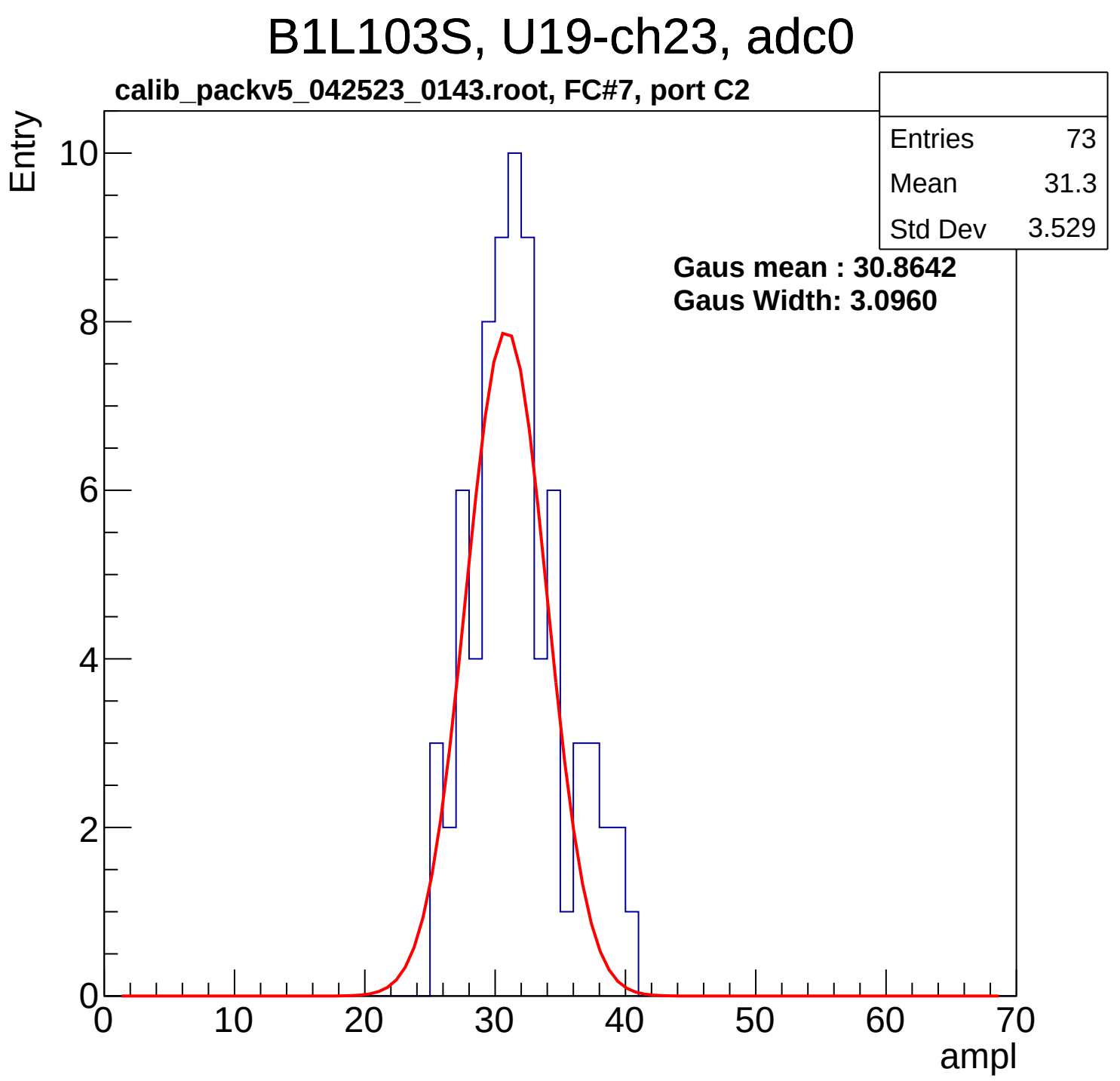
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch23, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 31.3
Std Dev 3.529
Gaus mean : 30.8642
Gaus Width: 3.0960
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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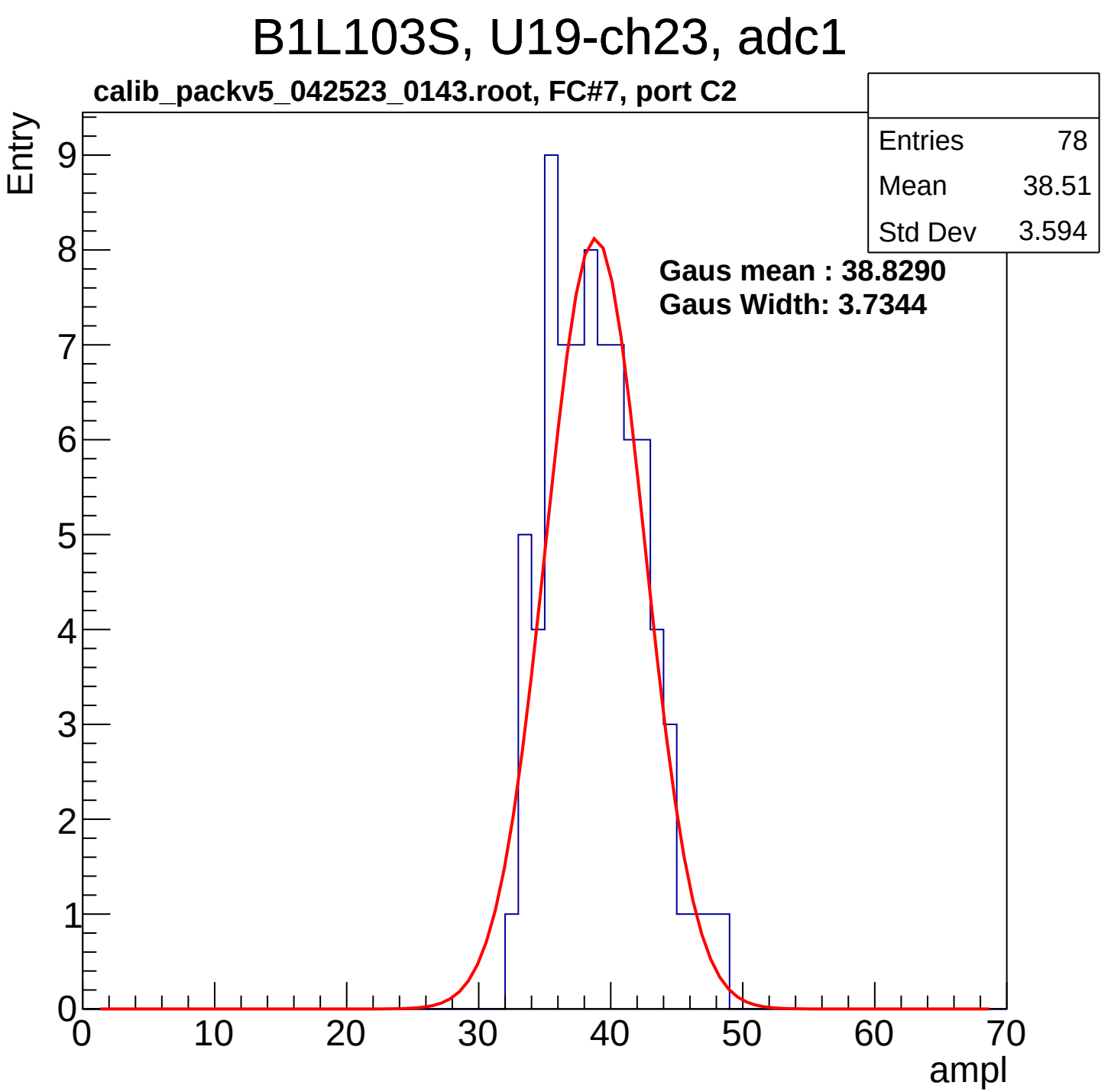
B1L103S, U19-ch23, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 38.51
Std Dev 3.594
Gaus mean : 38.8290
Gaus Width: 3.7344
Entry
ampl
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2
3
4
5
6
7
8
9
0
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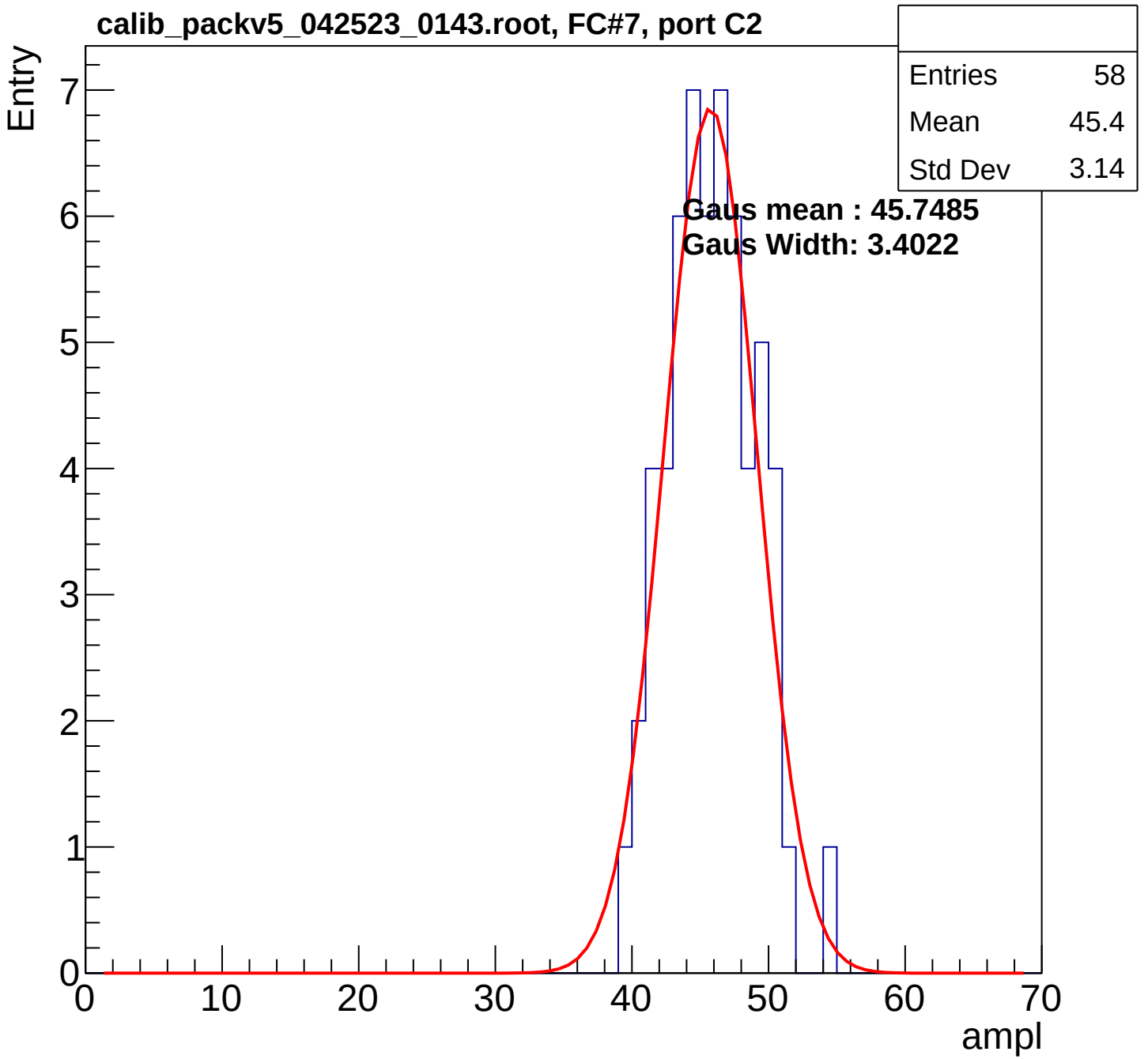
B1L103S, U19-ch23, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 45.4
Std Dev 3.14
Gaus mean : 45.7485
Gaus Width: 3.4022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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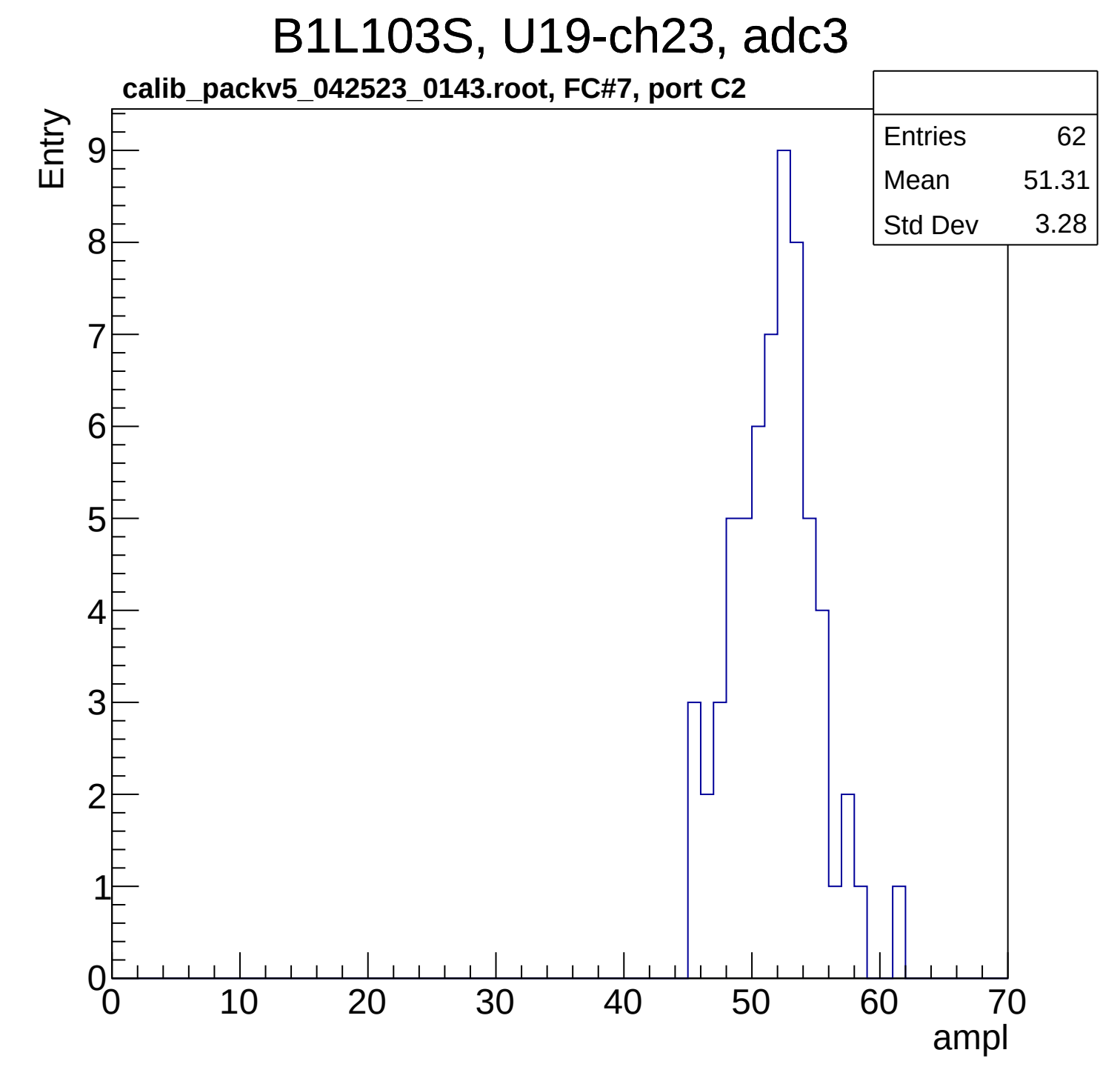
B1L103S, U19-ch23, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 51.31
Std Dev 3.28
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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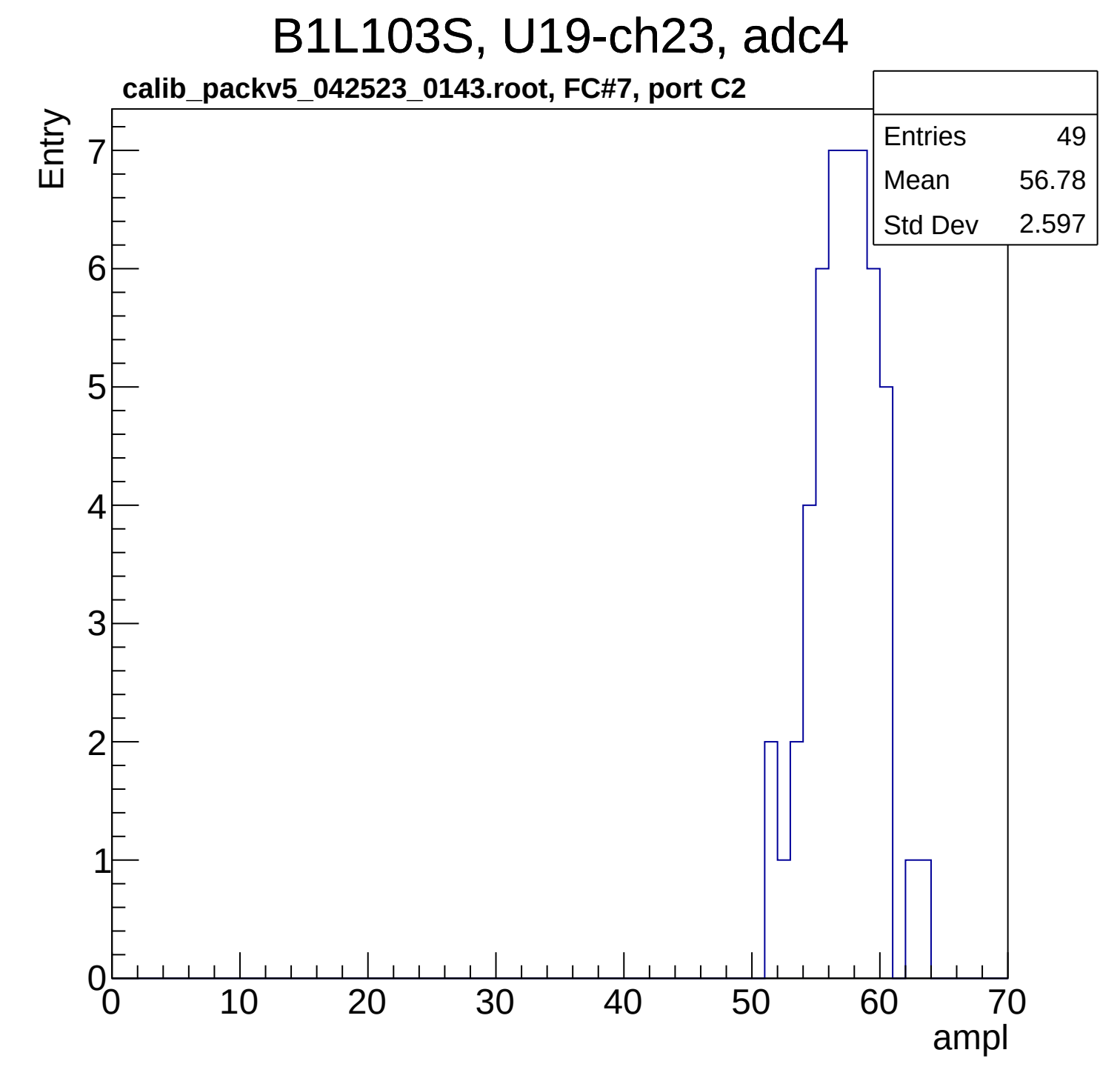
B1L103S, U19-ch23, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 56.78
Std Dev 2.597
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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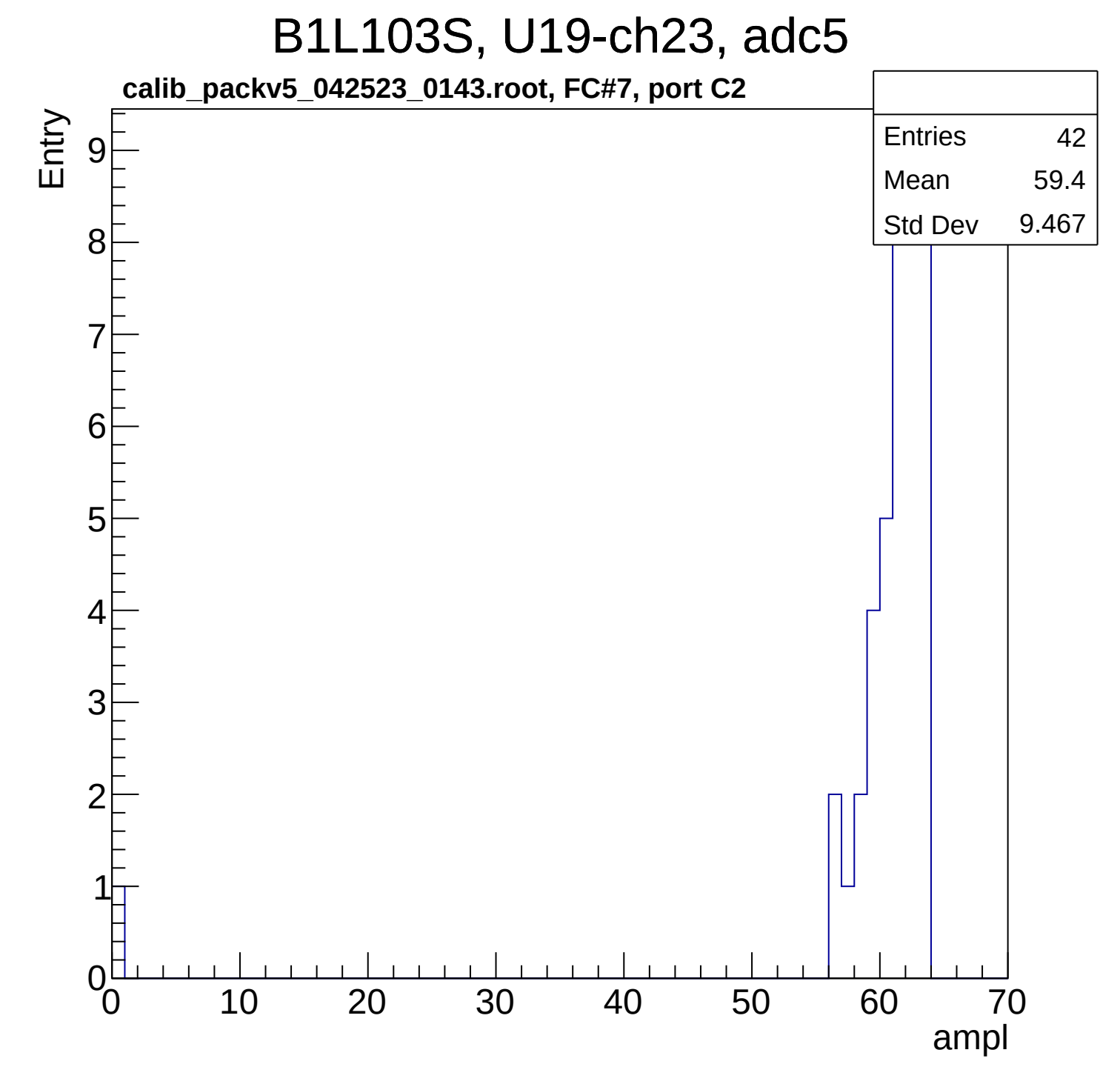
B1L103S, U19-ch23, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 59.4
Std Dev 9.467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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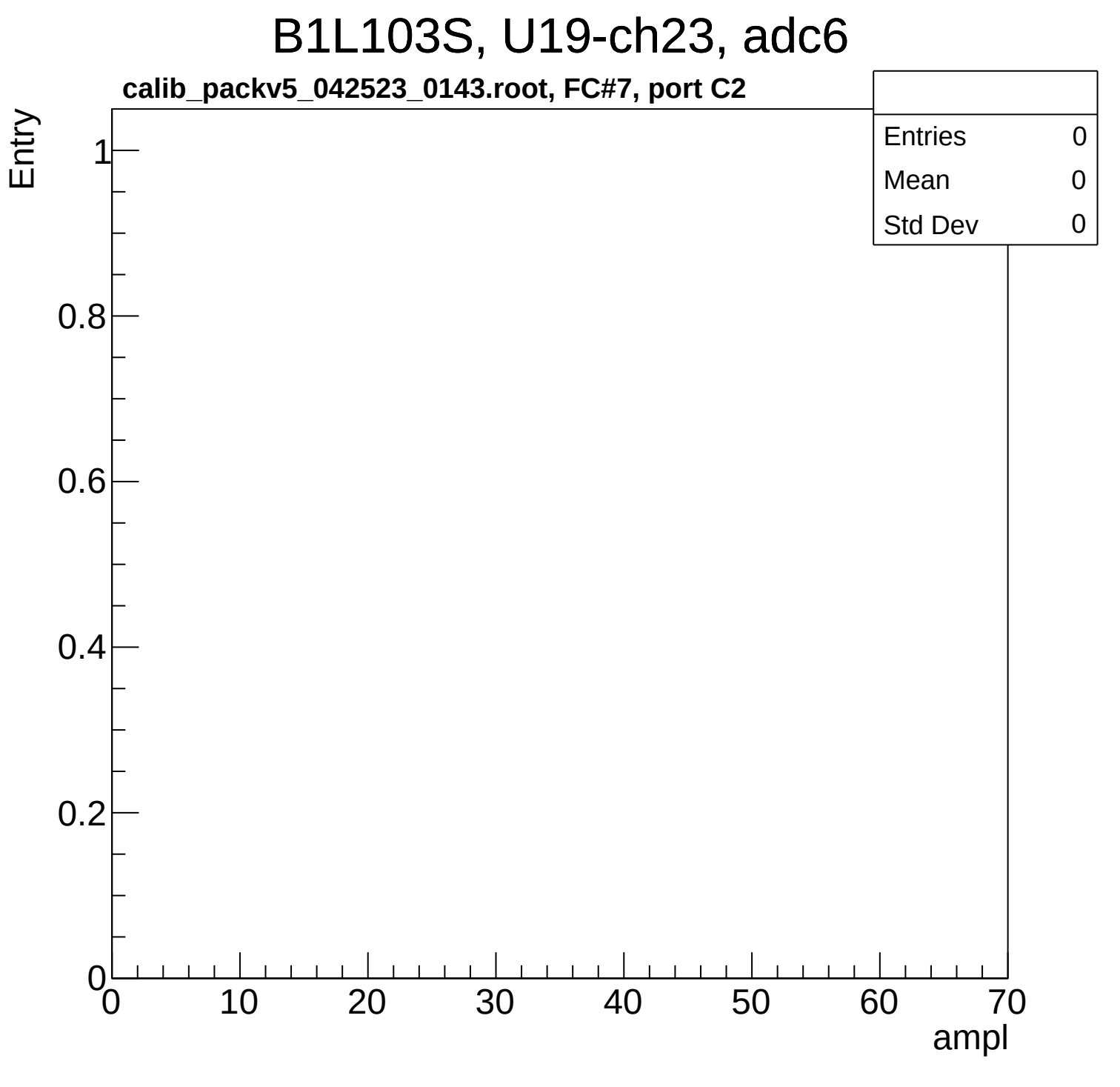
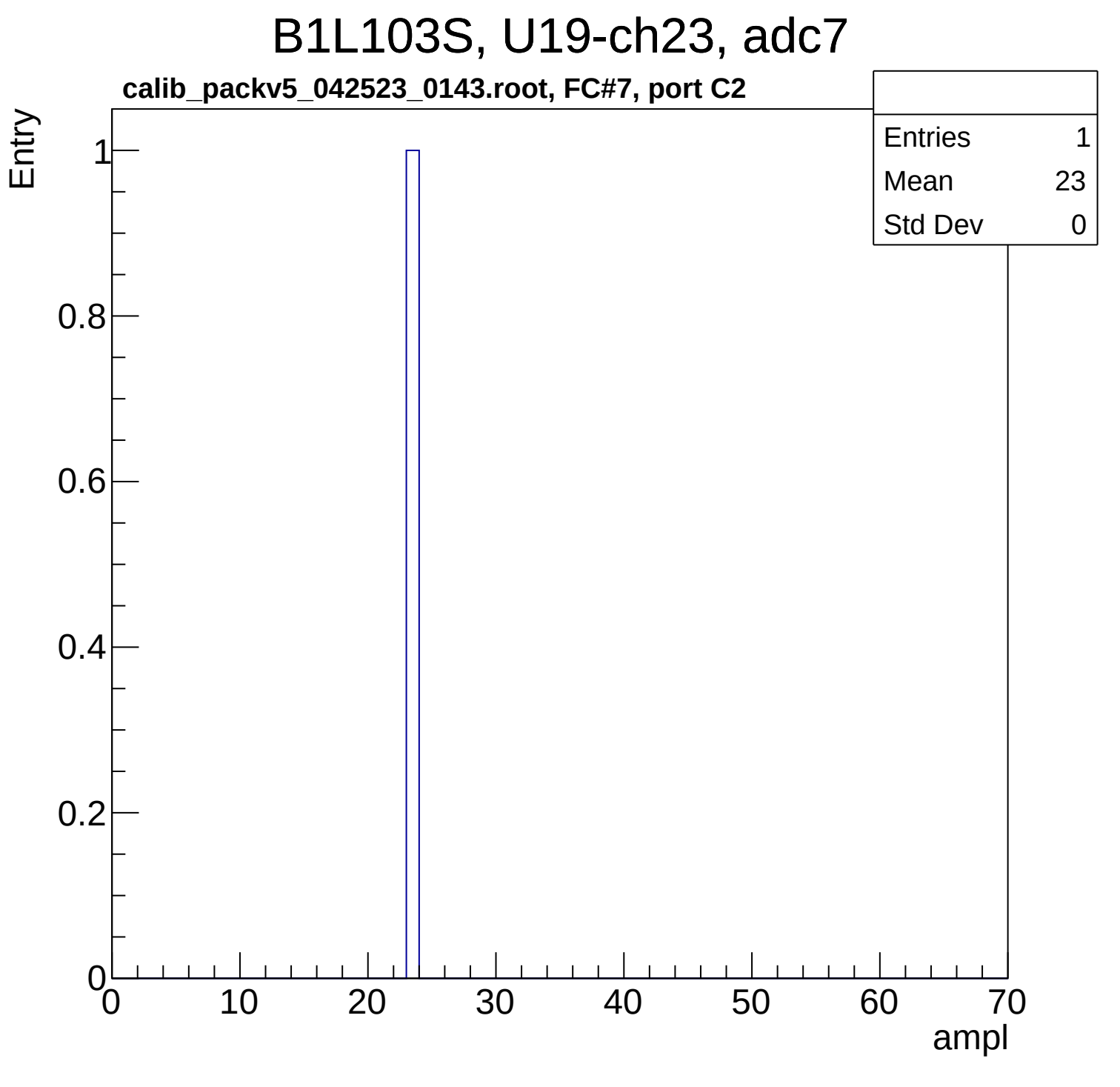

B1L103S, U19-ch23, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch23, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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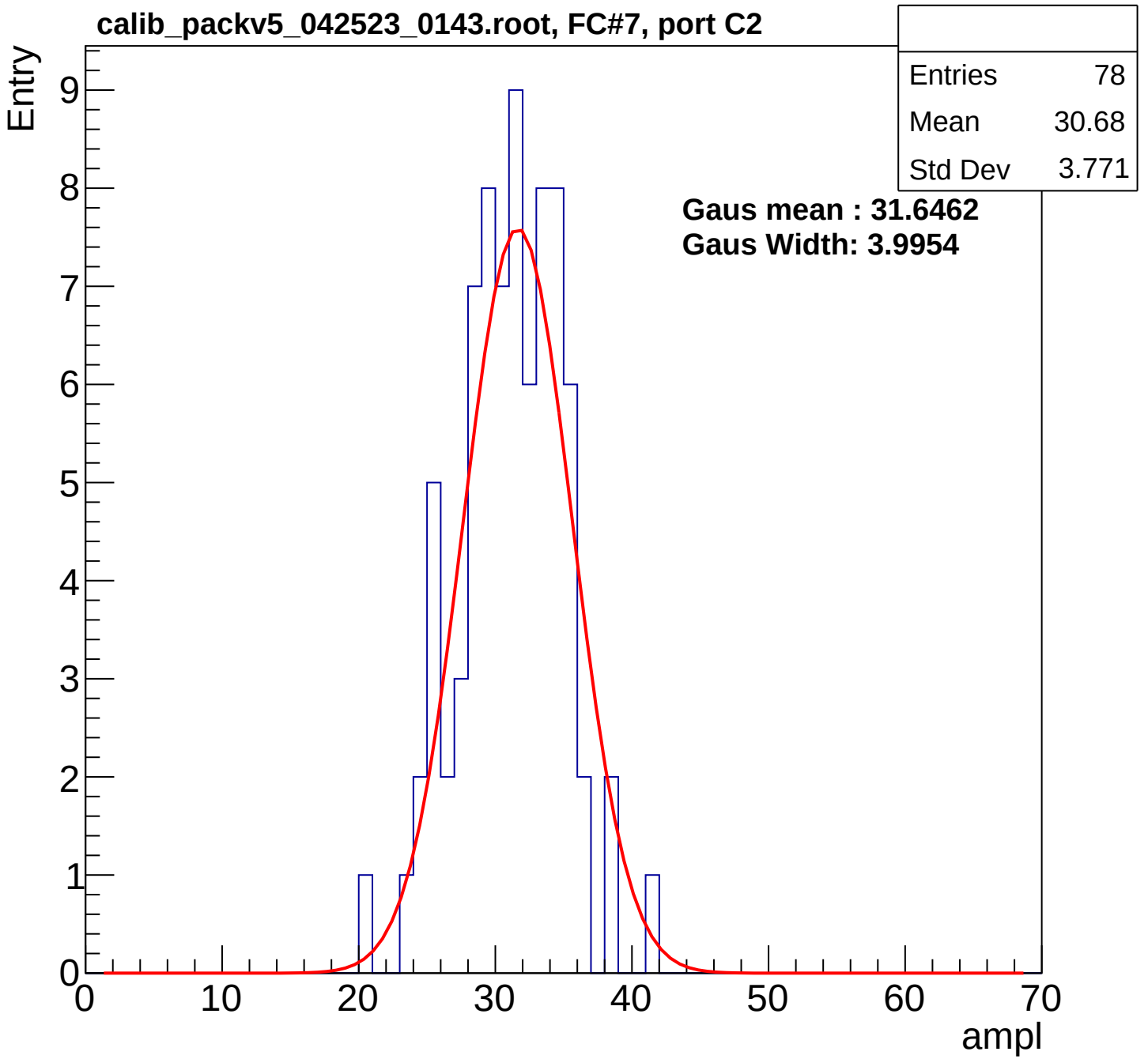
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch24, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 30.68
Std Dev 3.771
Gaus mean : 31.6462
Gaus Width: 3.9954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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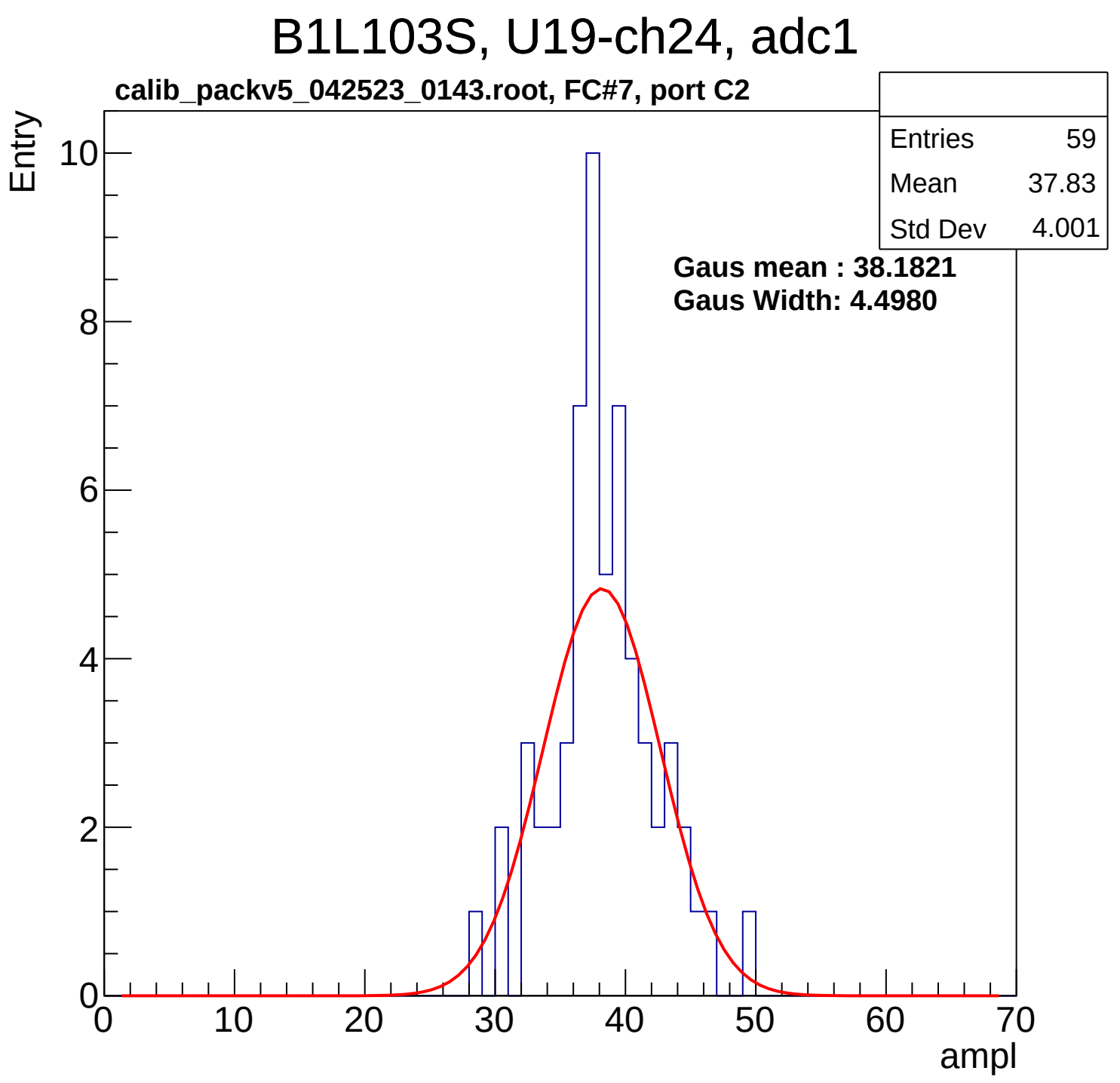
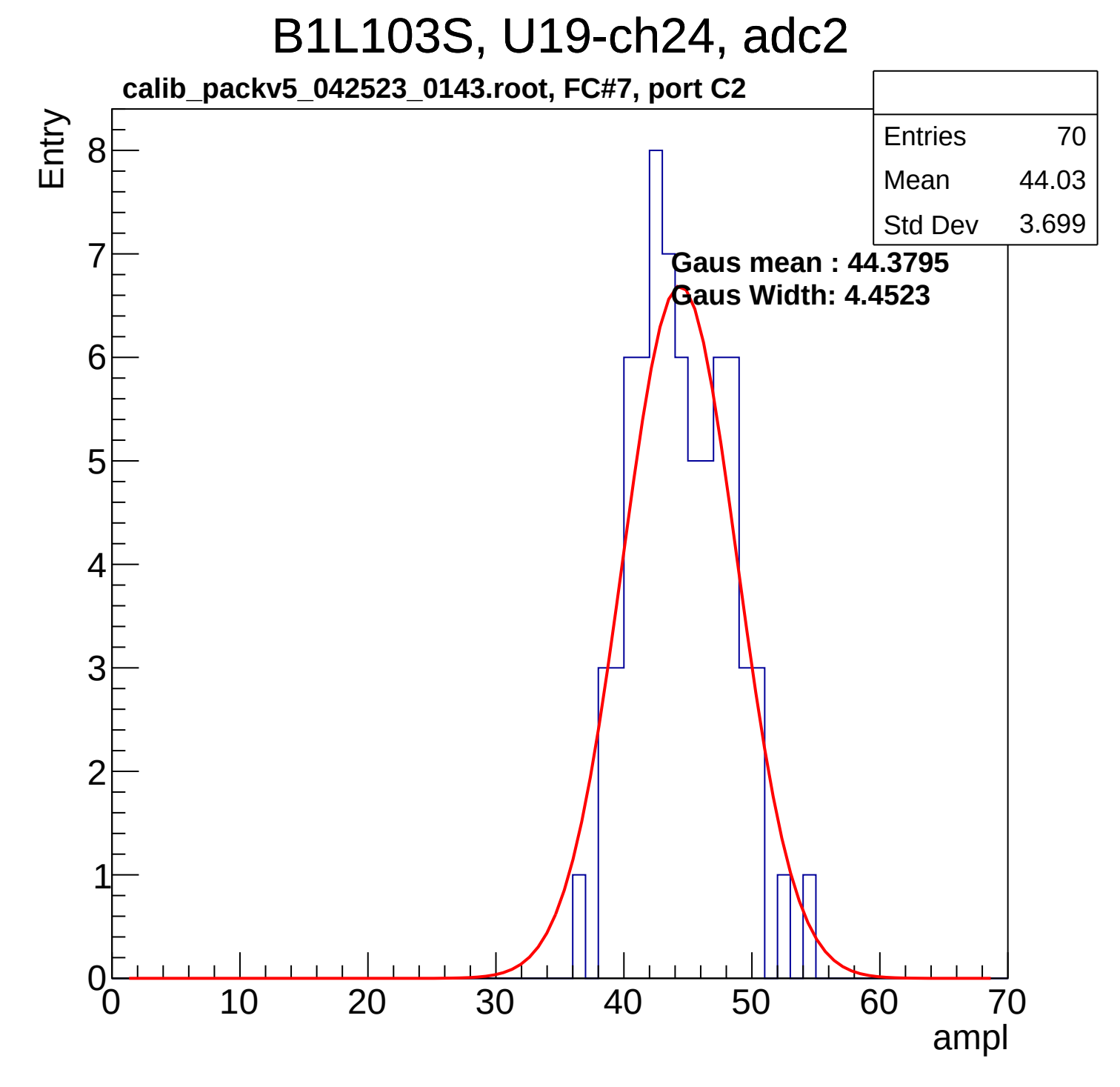
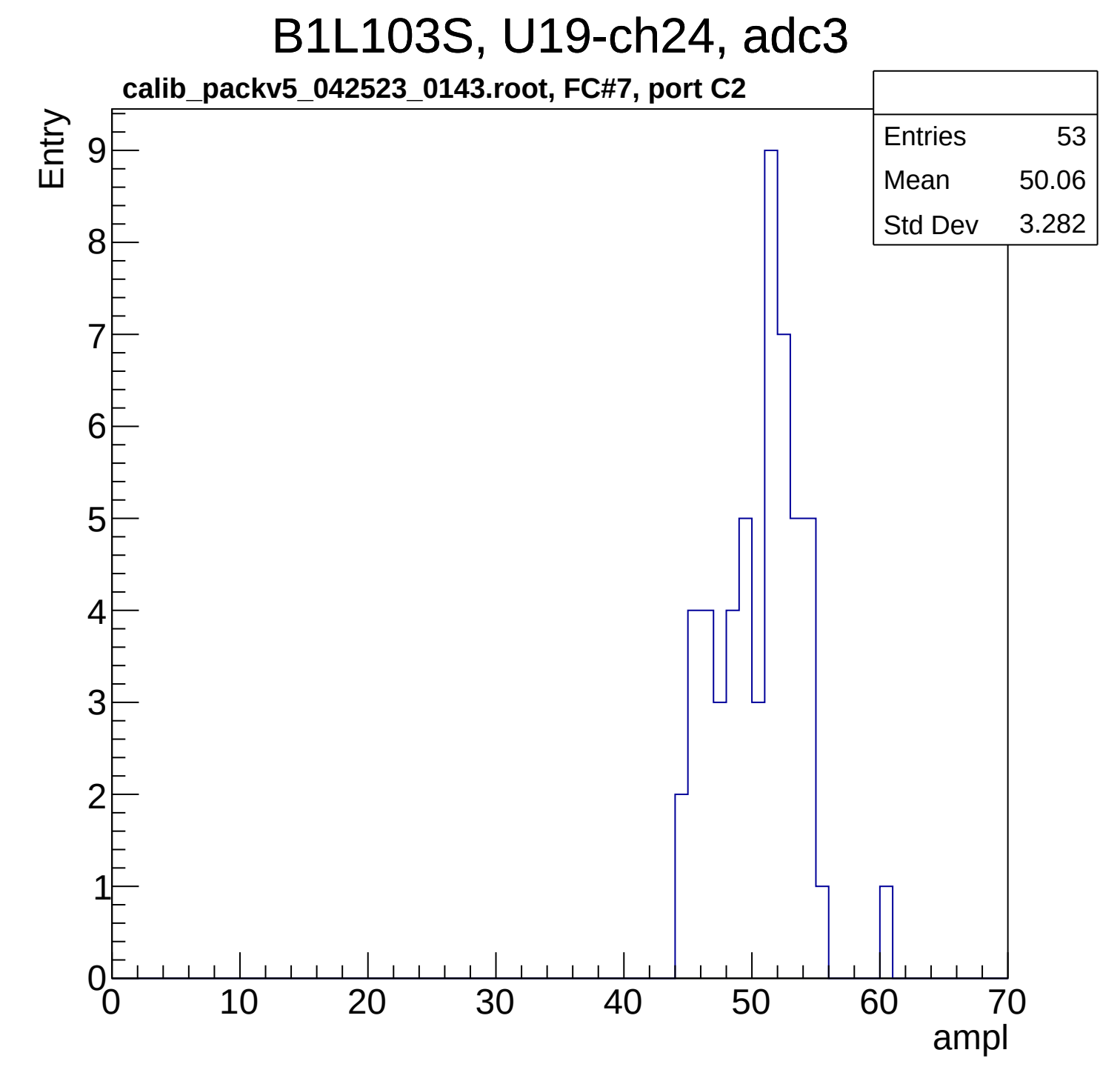

B1L103S, U19-ch24, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 37.83
Std Dev 4.001
Gaus mean : 38.1821
Gaus Width: 4.4980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch24, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 44.03
Std Dev 3.699
Gaus mean : 44.3795
Gaus Width: 4.4523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch24, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 50.06
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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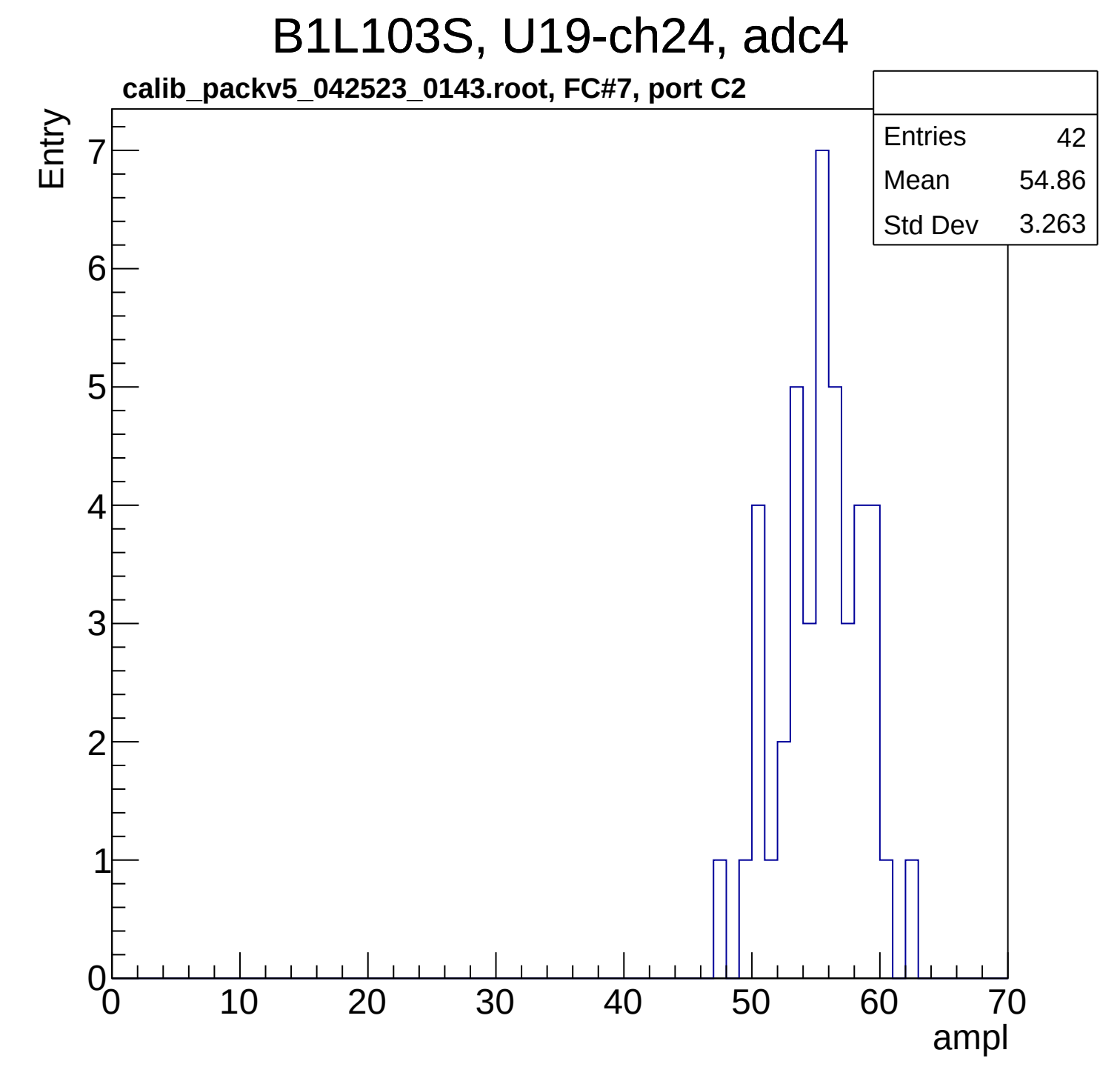
B1L103S, U19-ch24, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 54.86
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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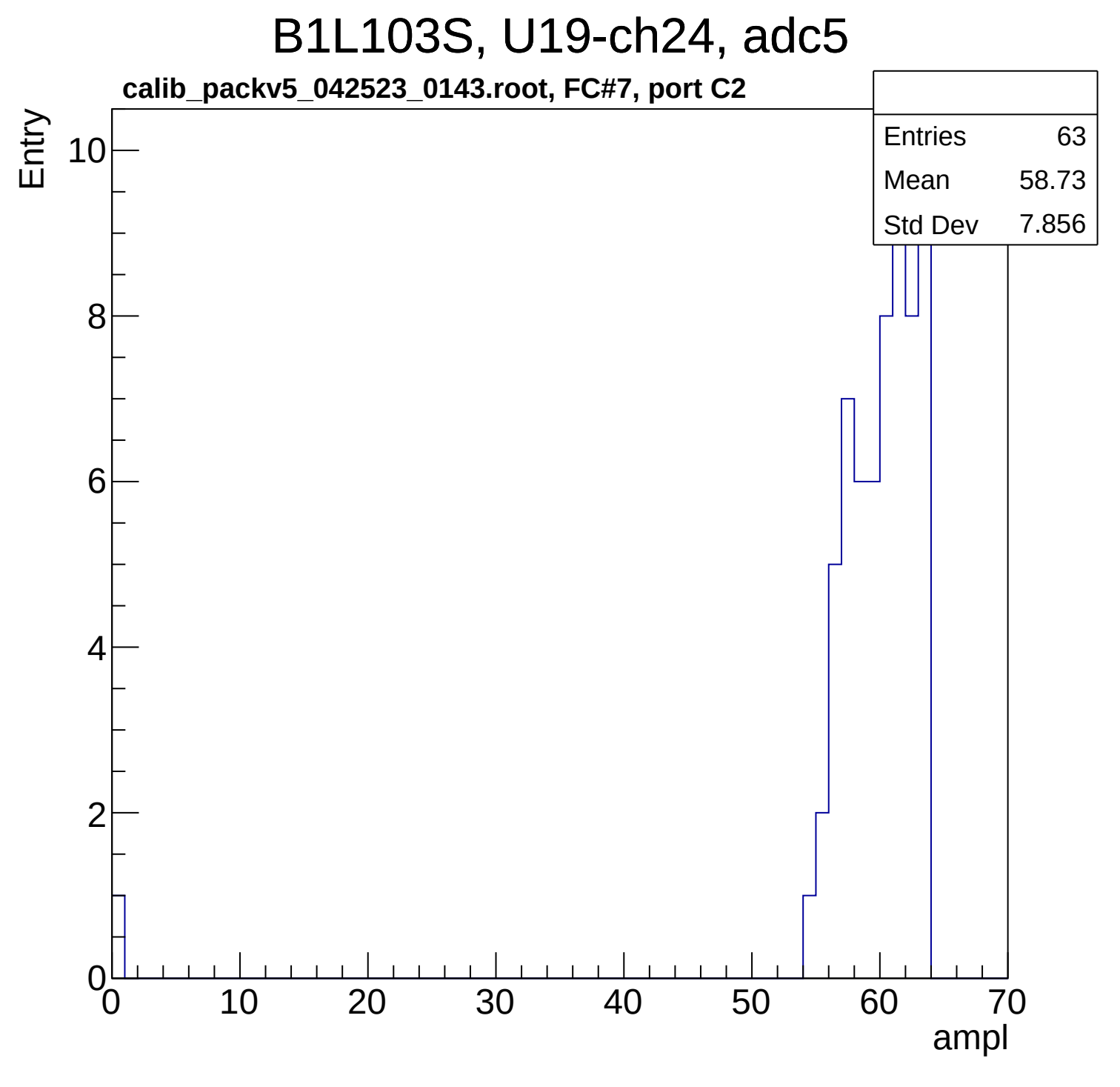
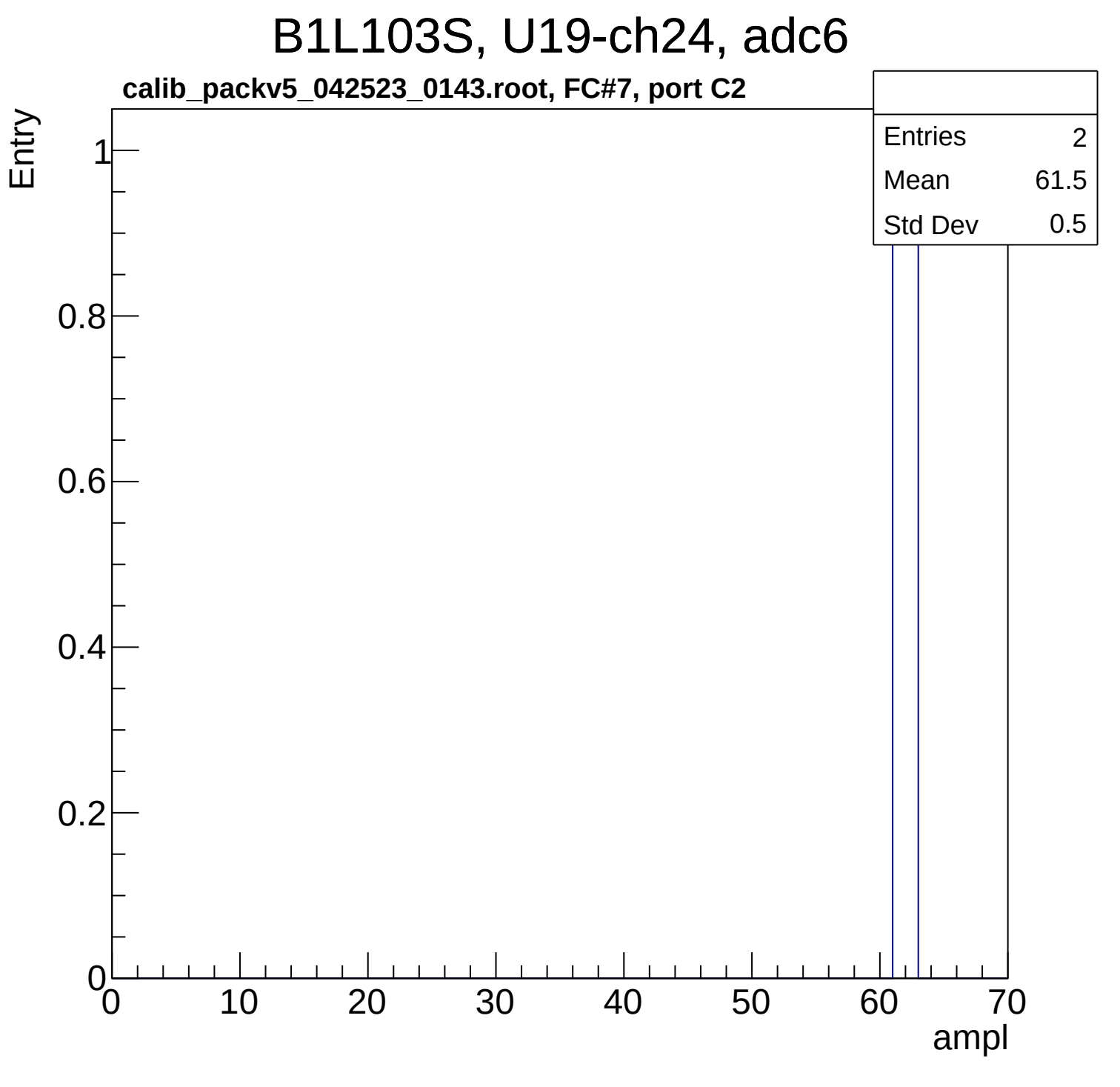
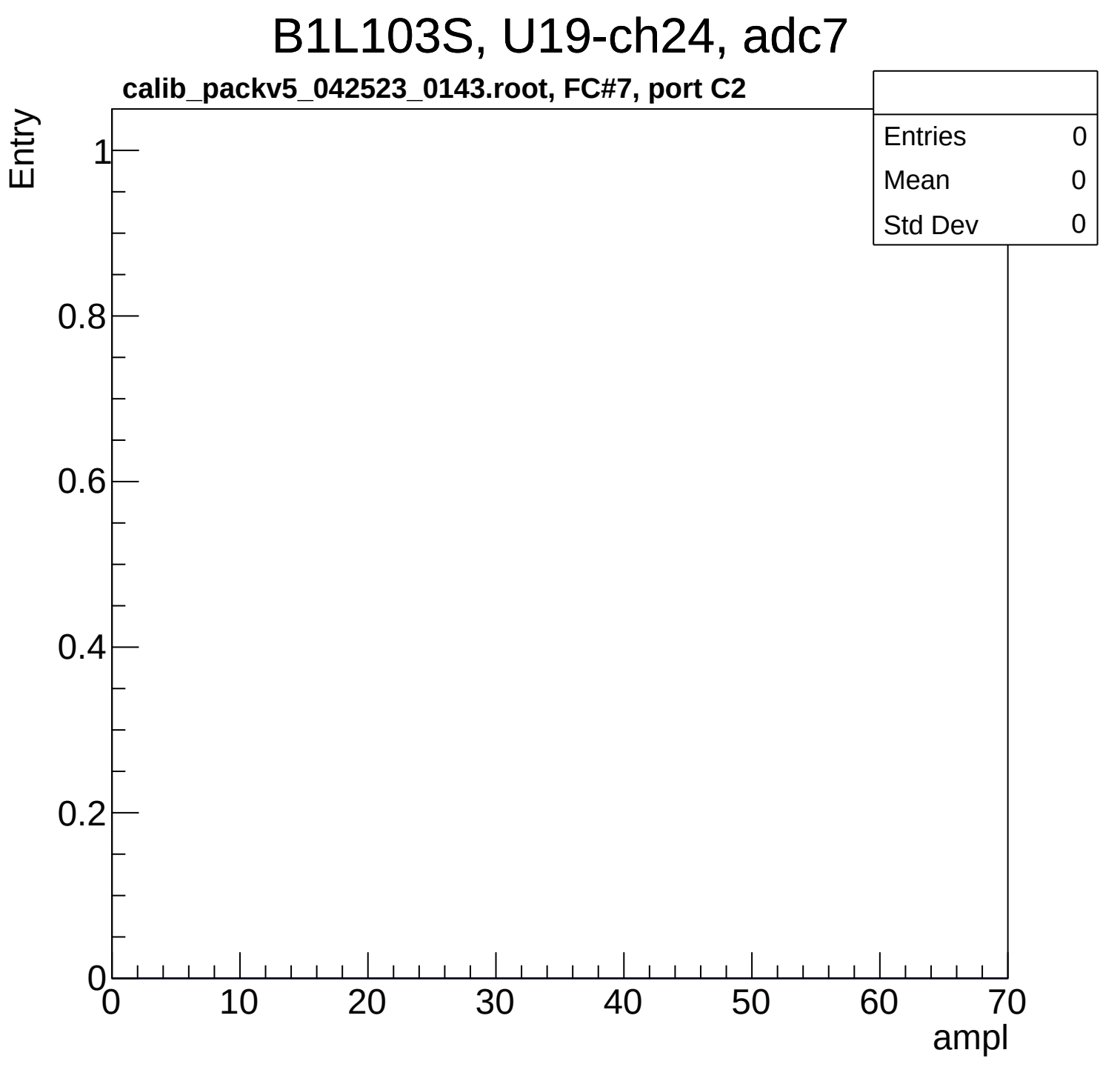

B1L103S, U19-ch24, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 58.73
Std Dev 7.856
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch24, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch24, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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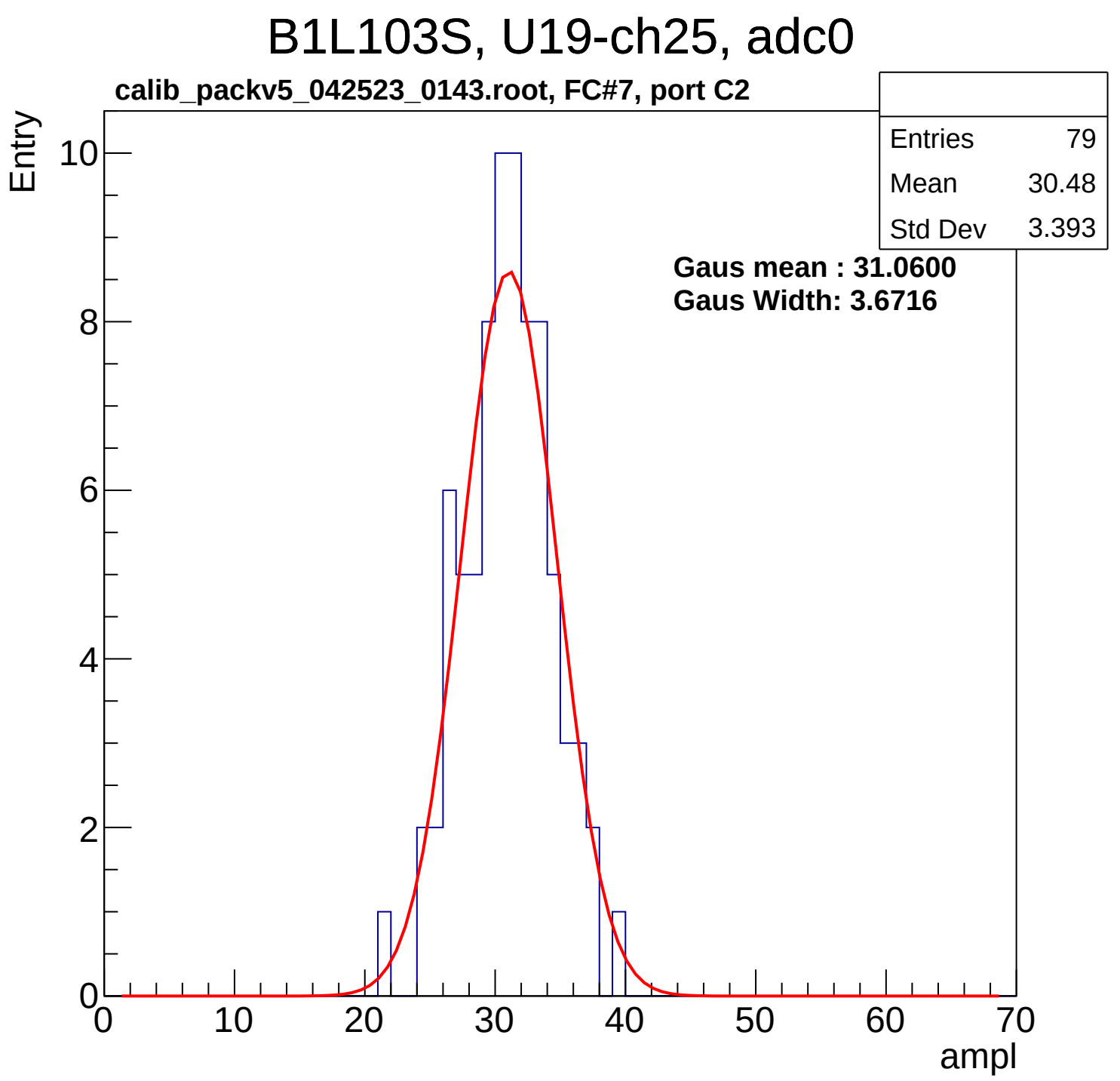
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch25, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 30.48
Std Dev 3.393
Gaus mean : 31.0600
Gaus Width: 3.6716
Entry
ampl
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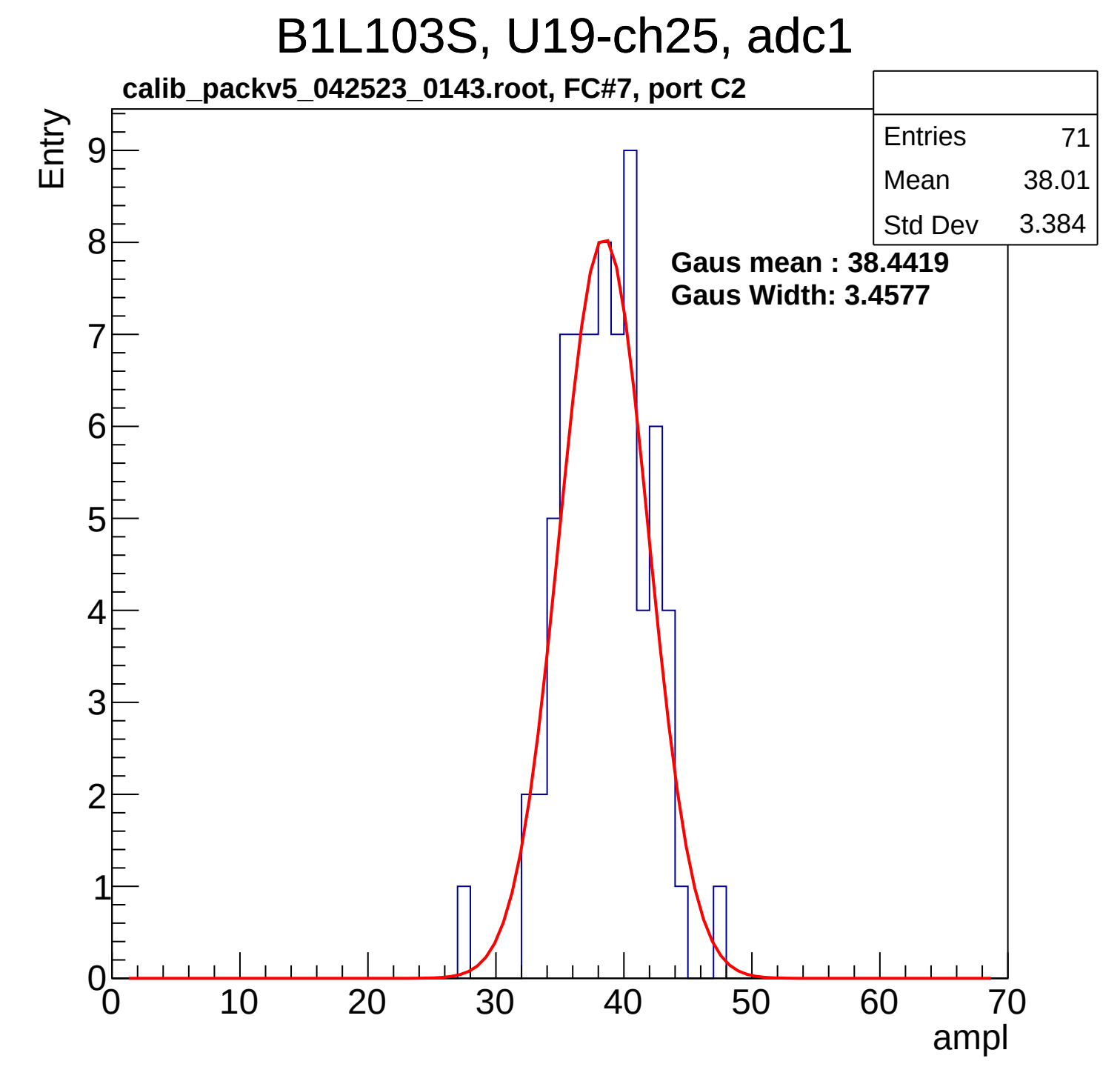
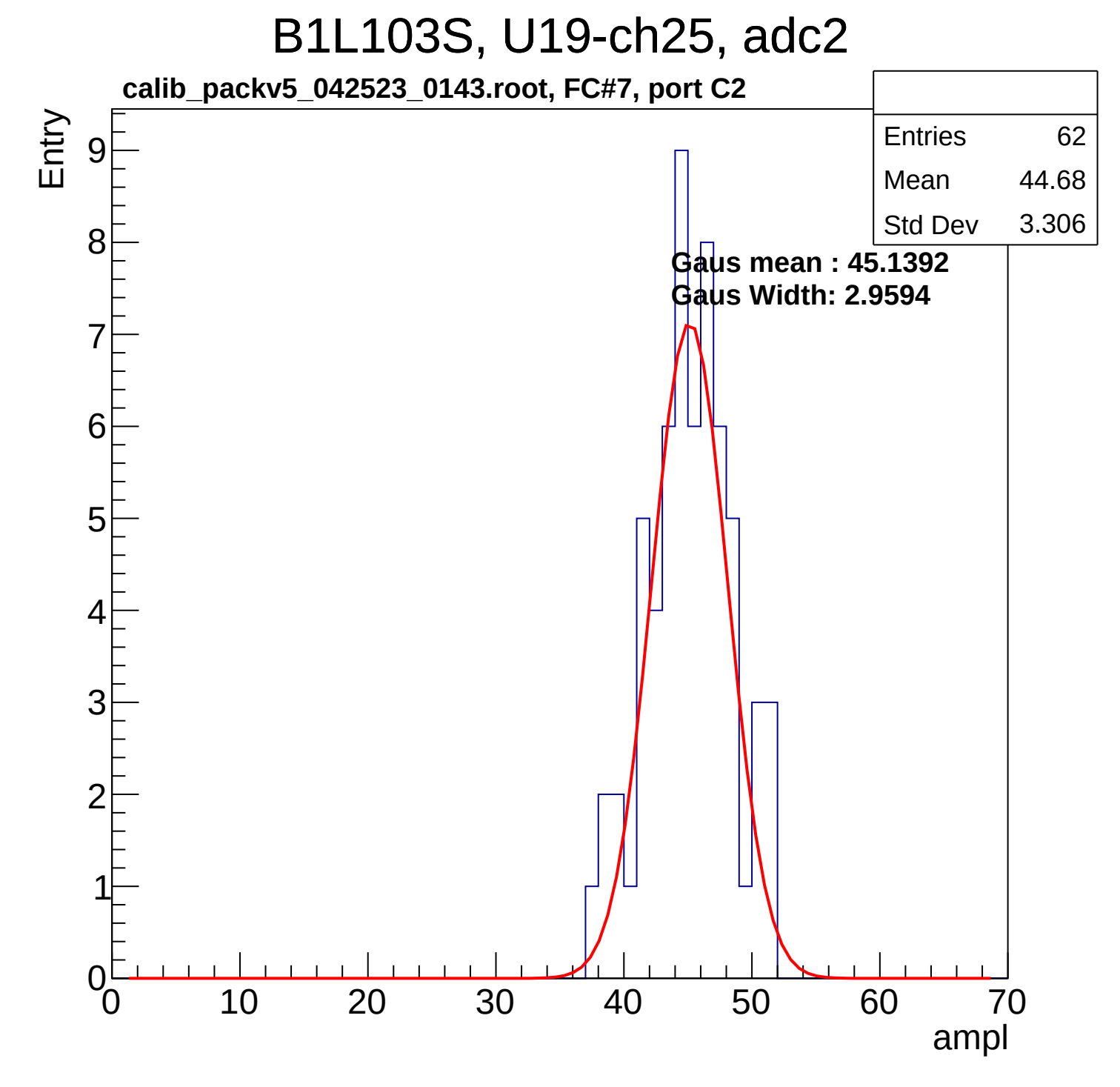
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch25, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 38.01
Std Dev 3.384
Gaus mean : 38.4419
Gaus Width: 3.4577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch25, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 44.68
Std Dev 3.306
Gaus mean : 45.1392
Gaus Width: 2.9594
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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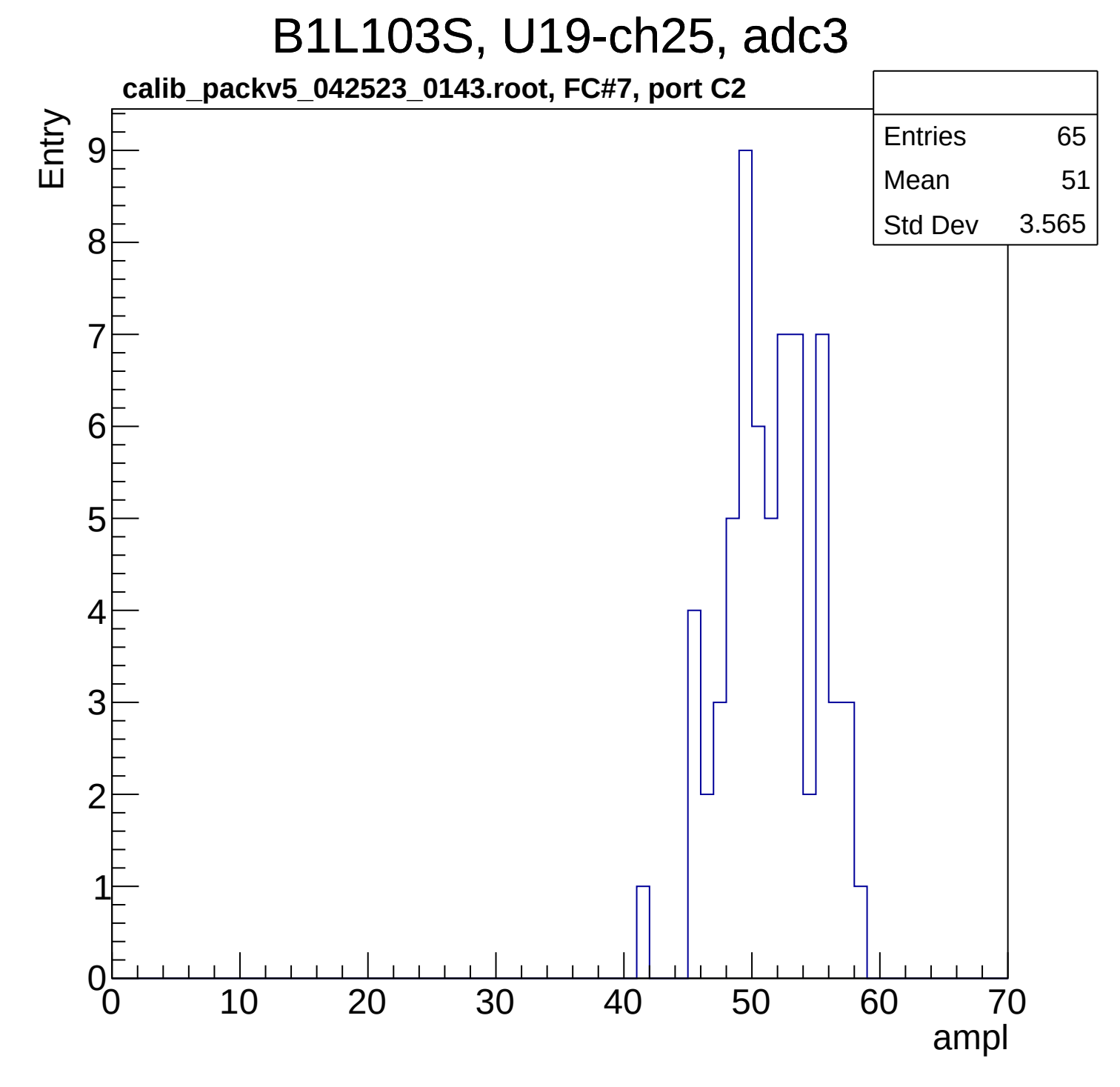
B1L103S, U19-ch25, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 51
Std Dev 3.565
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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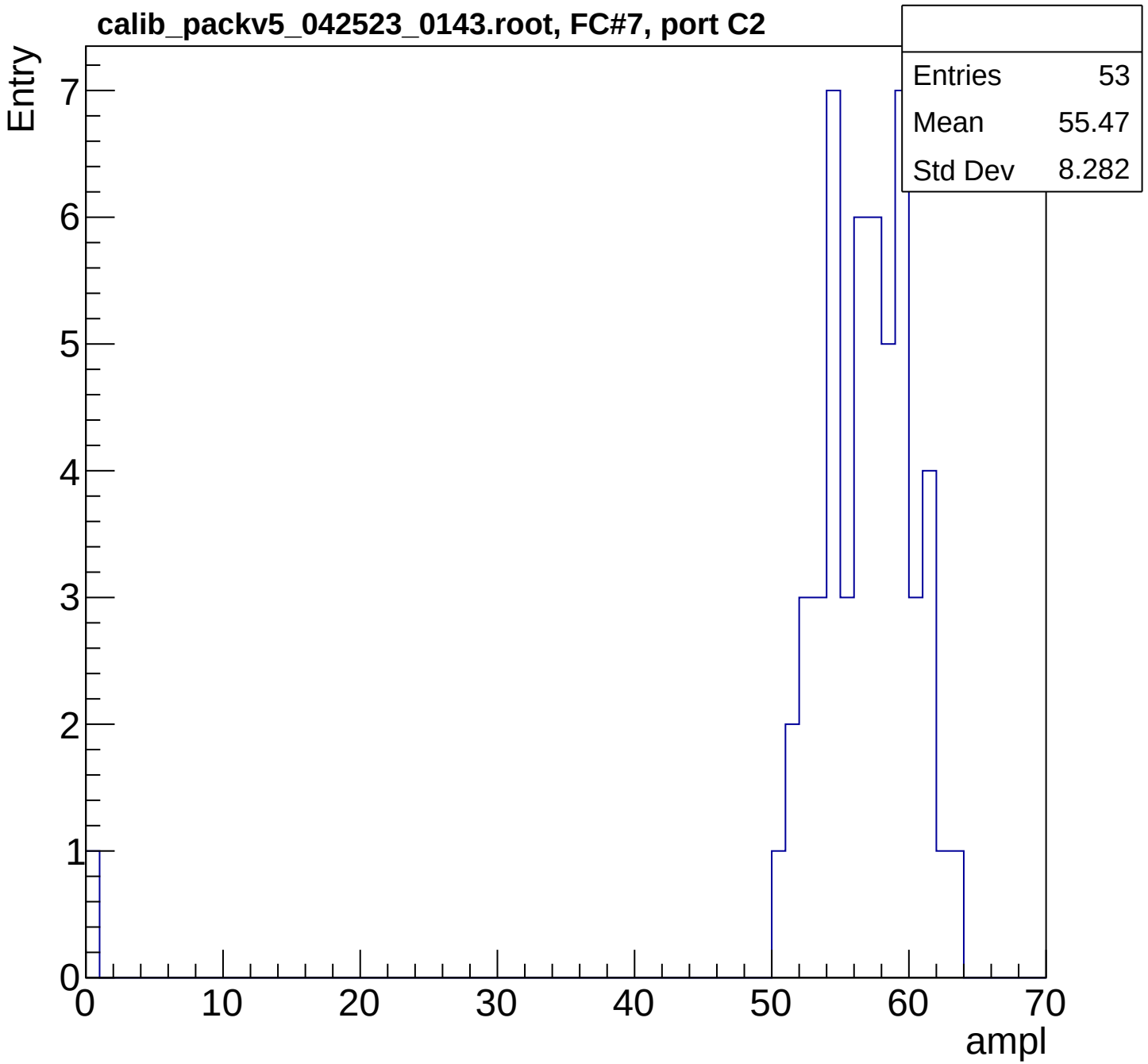
B1L103S, U19-ch25, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.47
Std Dev 8.282
Entry
ampl
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0
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20
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40
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60
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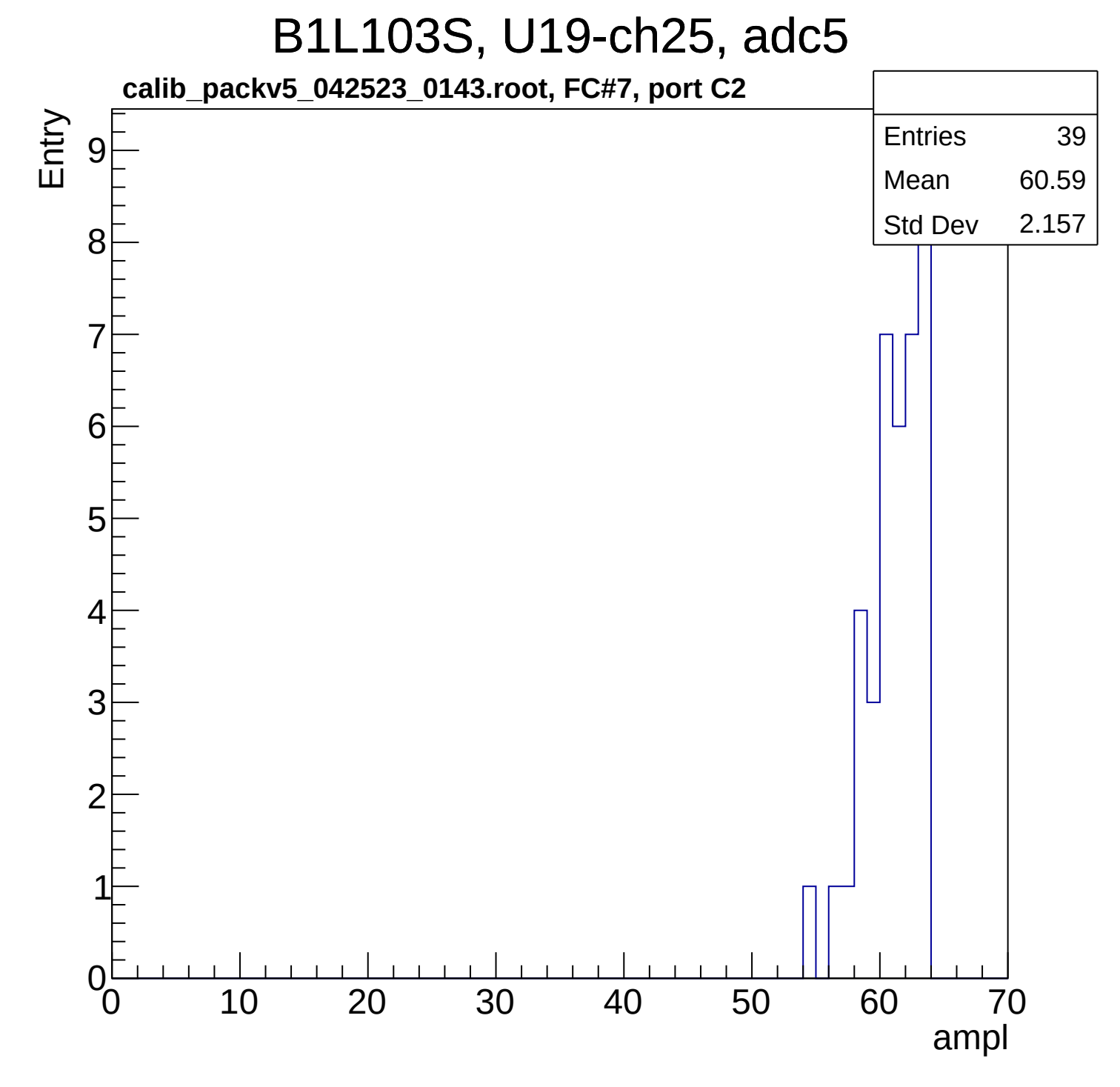
B1L103S, U19-ch25, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 60.59
Std Dev 2.157
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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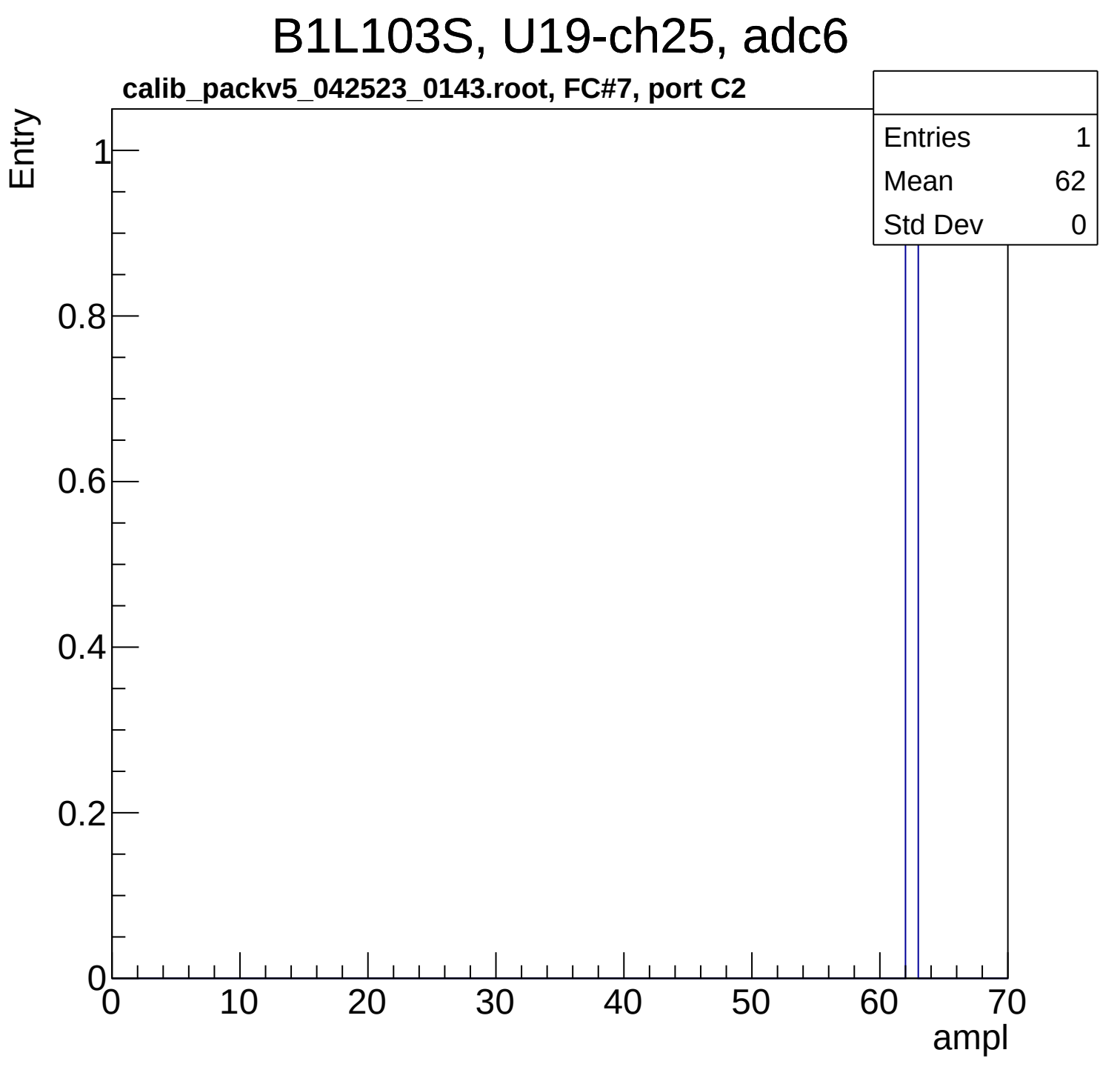
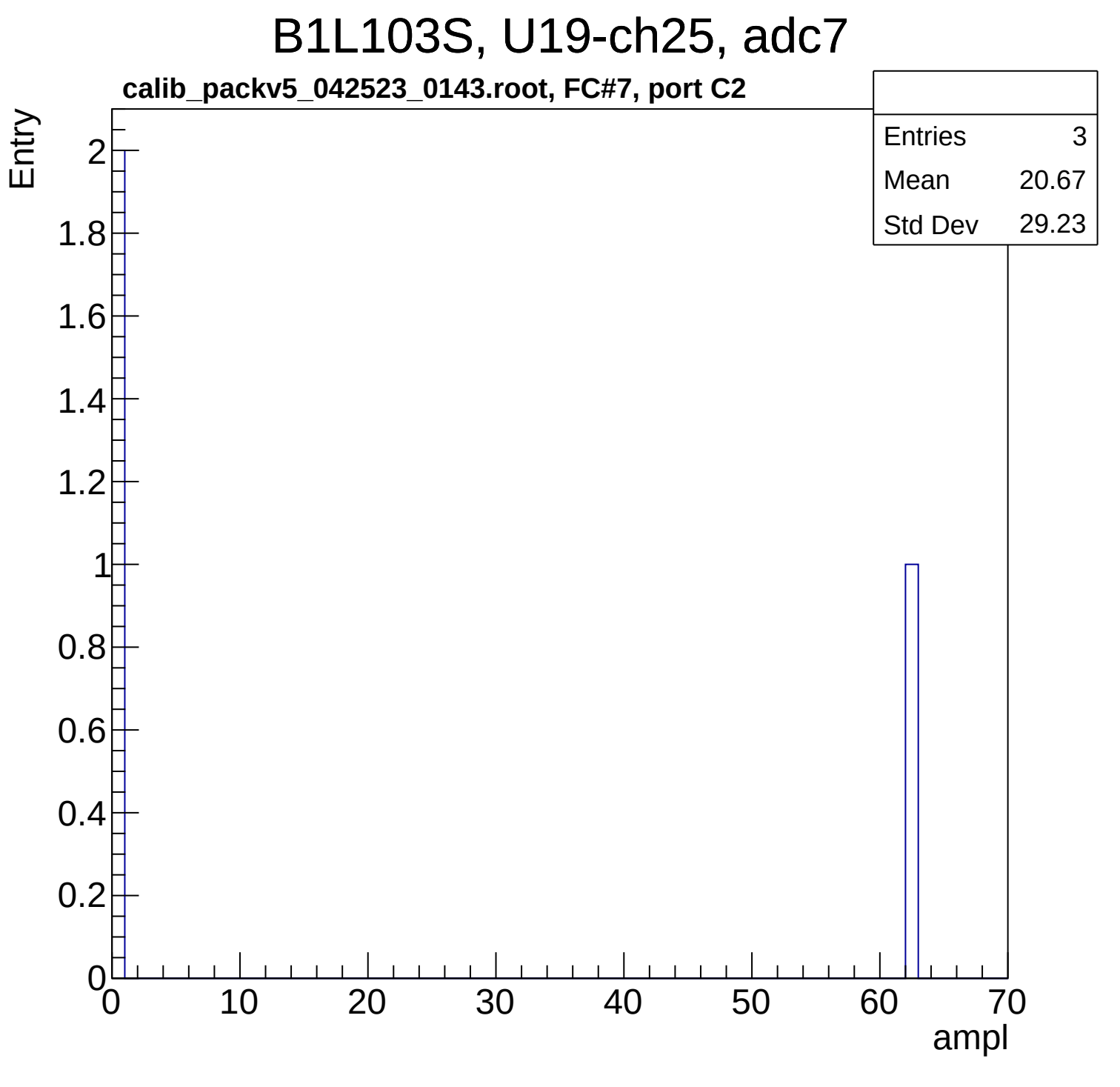

B1L103S, U19-ch25, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch25, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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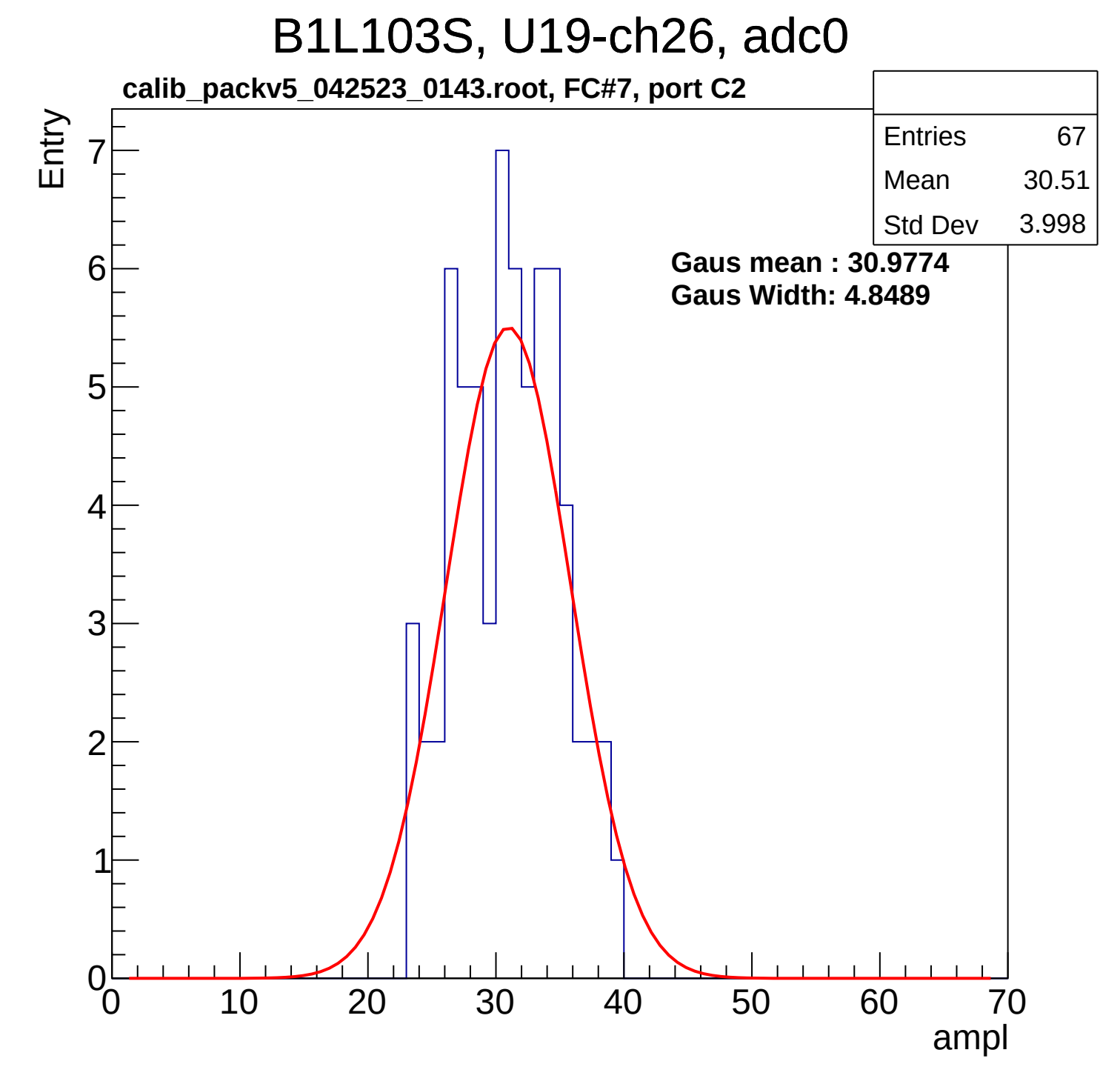
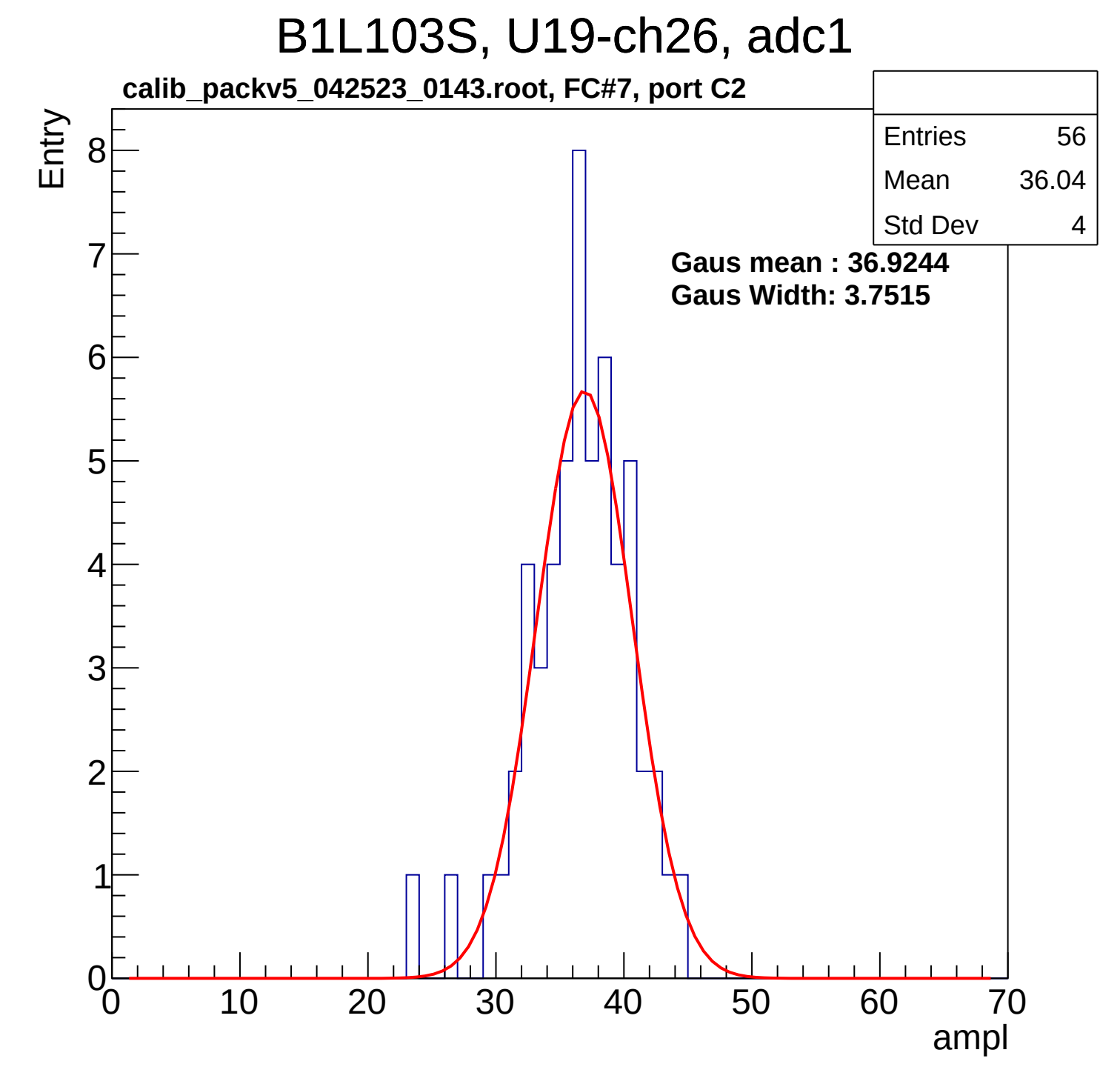

B1L103S, U19-ch26, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 30.51
Std Dev 3.998
Gaus mean : 30.9774
Gaus Width: 4.8489
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch26, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 36.04
Std Dev 4
Gaus mean : 36.9244
Gaus Width: 3.7515
Entry
ampl
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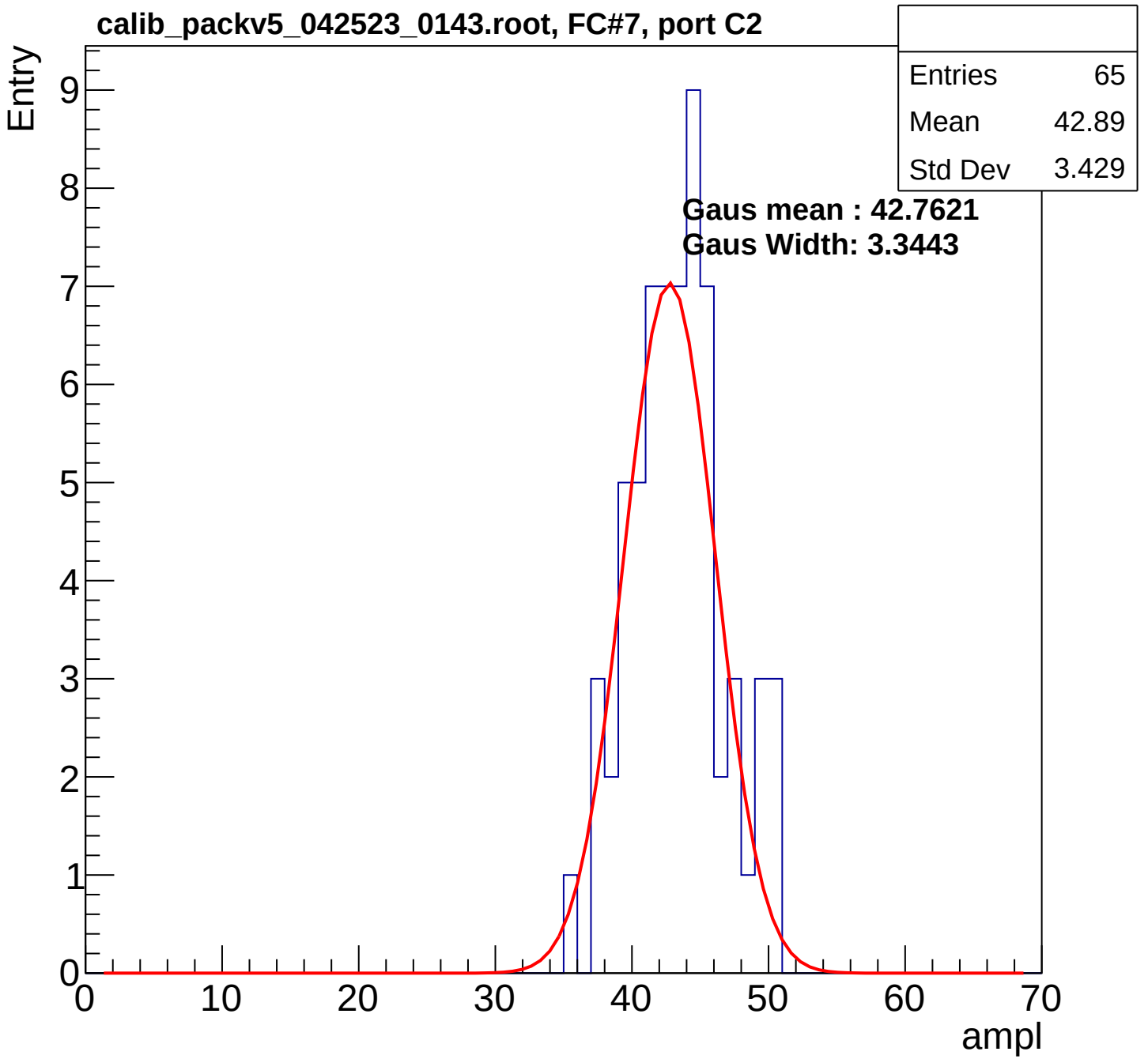
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch26, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 42.89
Std Dev 3.429
Gaus mean : 42.7621
Gaus Width: 3.3443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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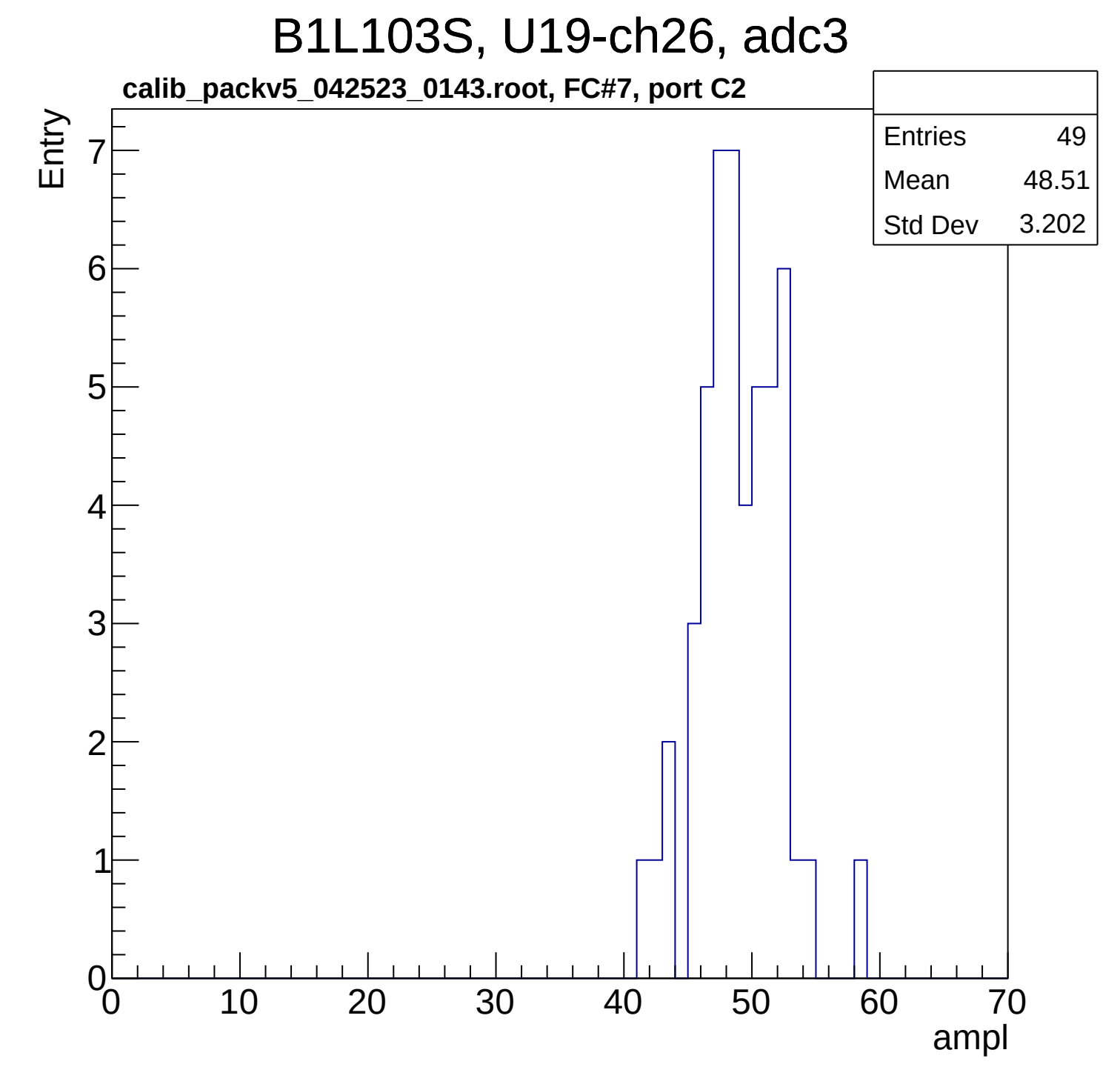
B1L103S, U19-ch26, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 48.51
Std Dev 3.202
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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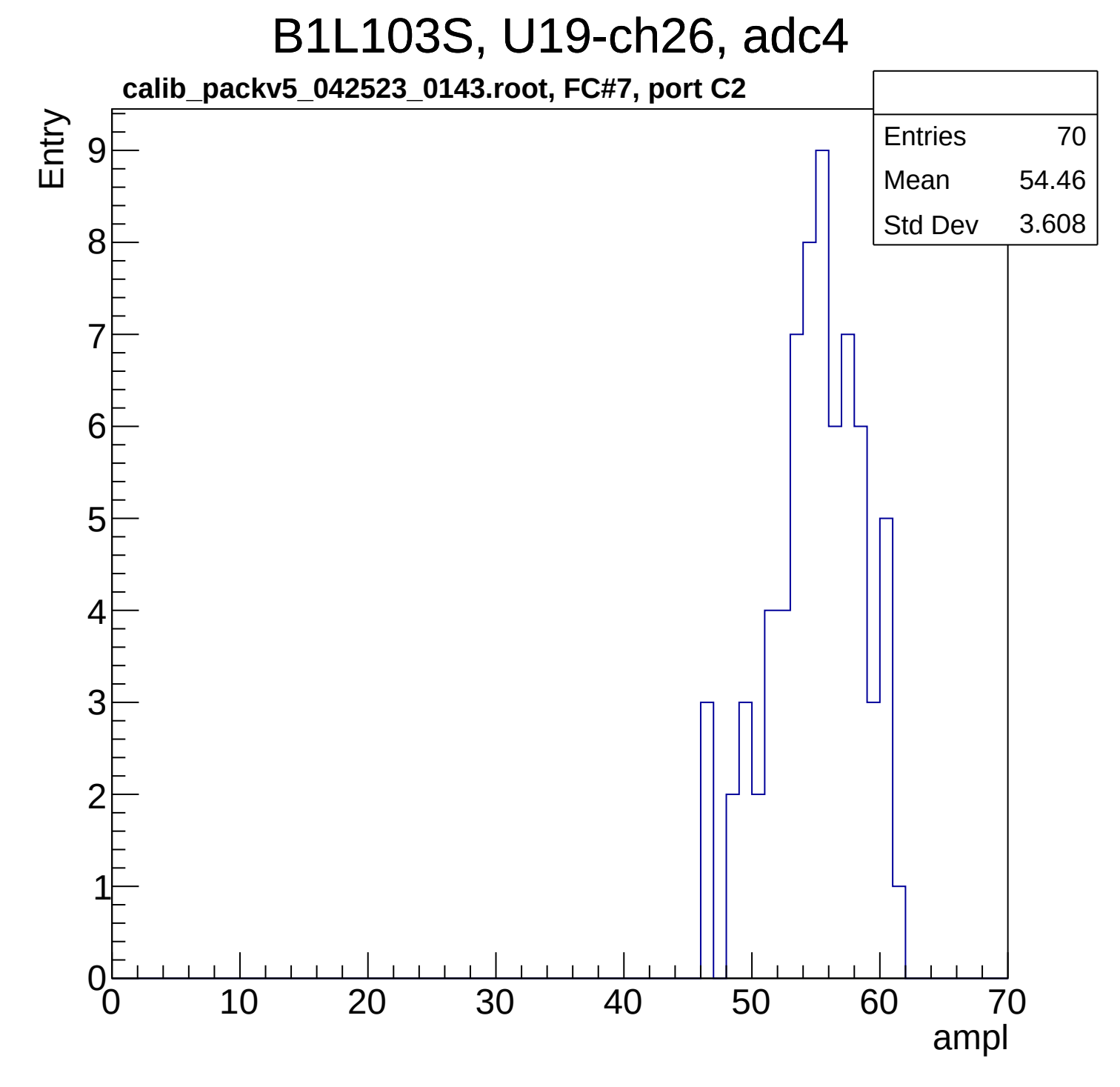
B1L103S, U19-ch26, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 54.46
Std Dev 3.608
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
30
40
50
60
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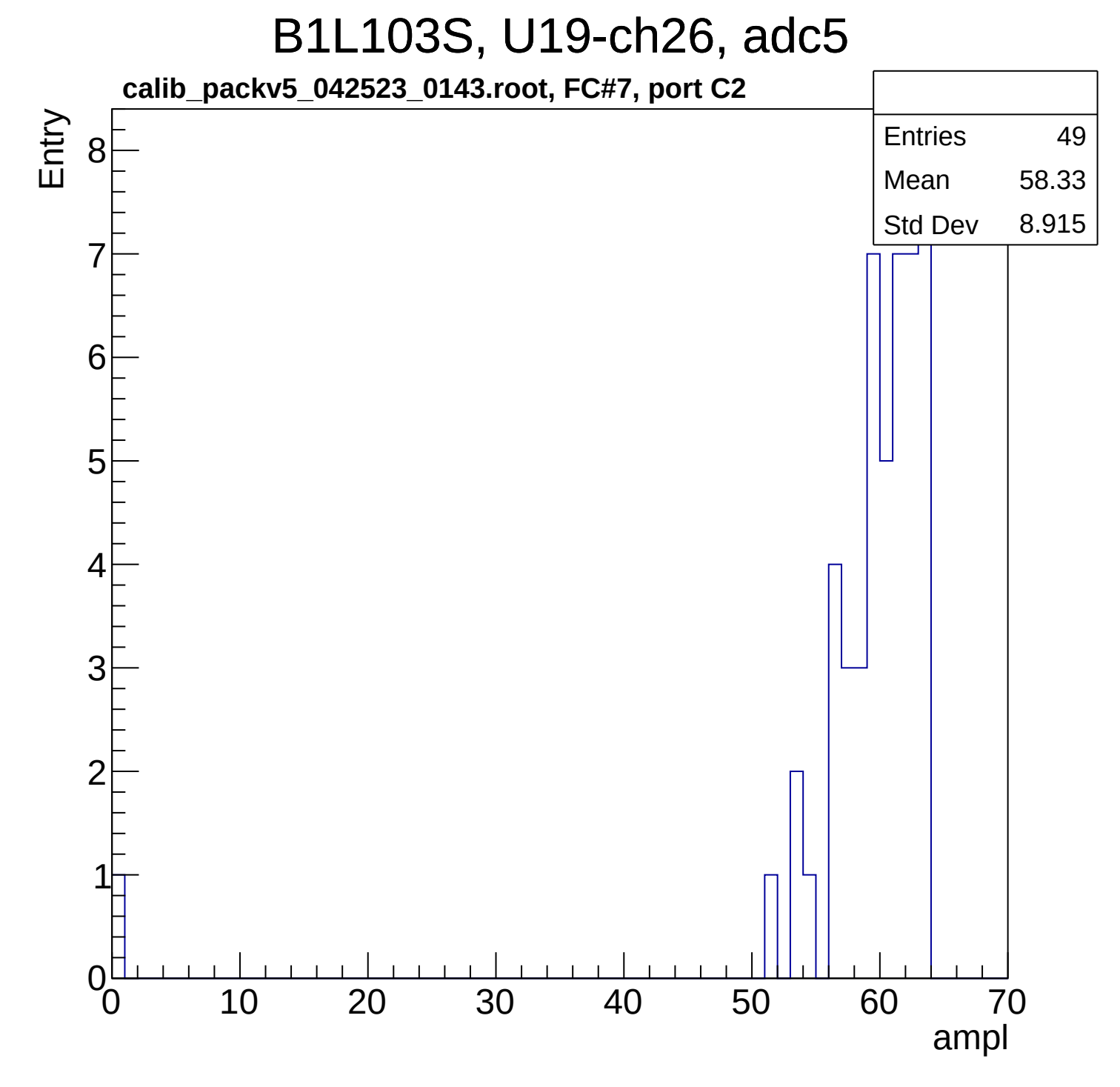
B1L103S, U19-ch26, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.33
Std Dev 8.915
Entry
ampl
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60
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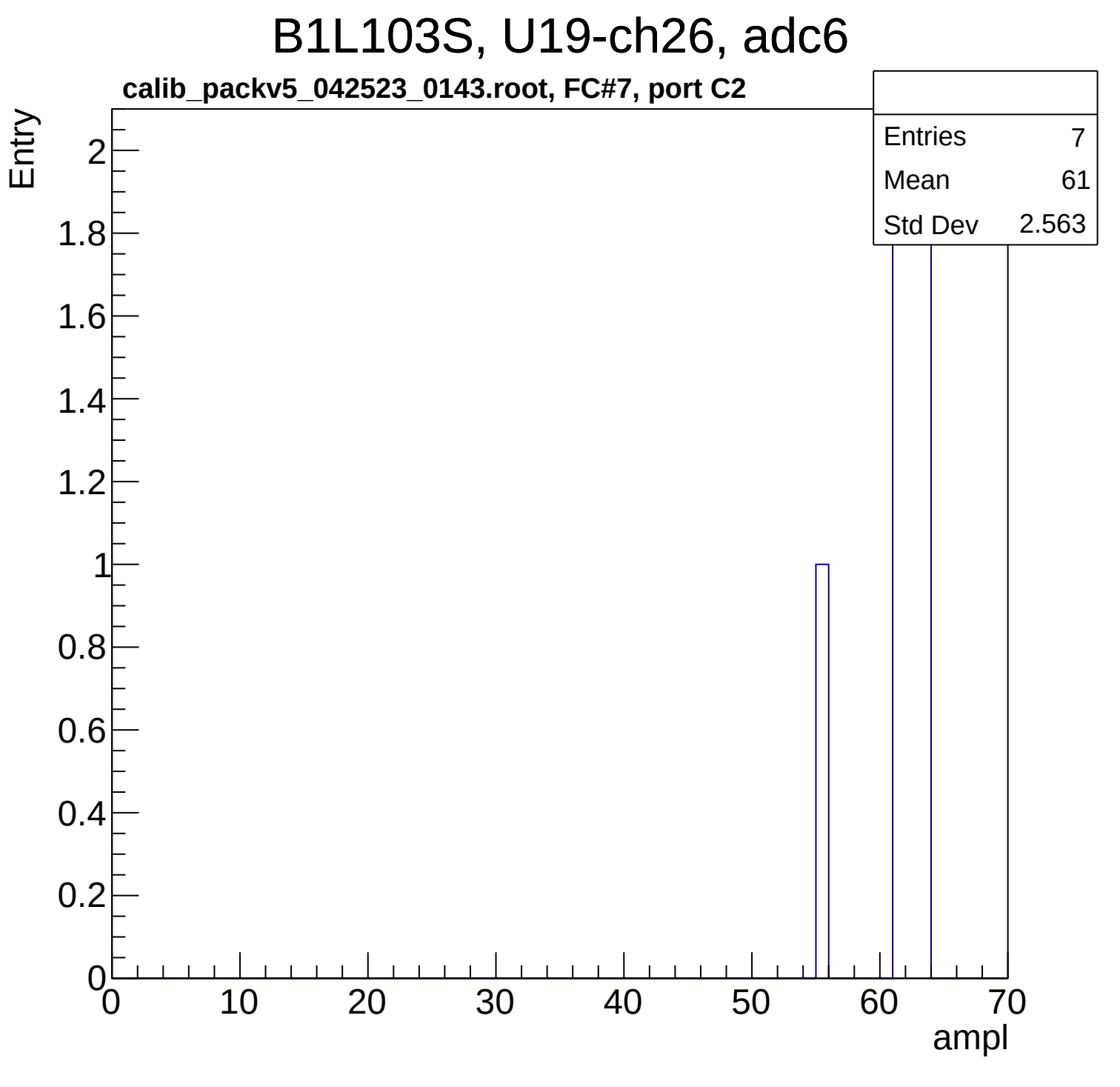
B1L103S, U19-ch26, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61
Std Dev 2.563
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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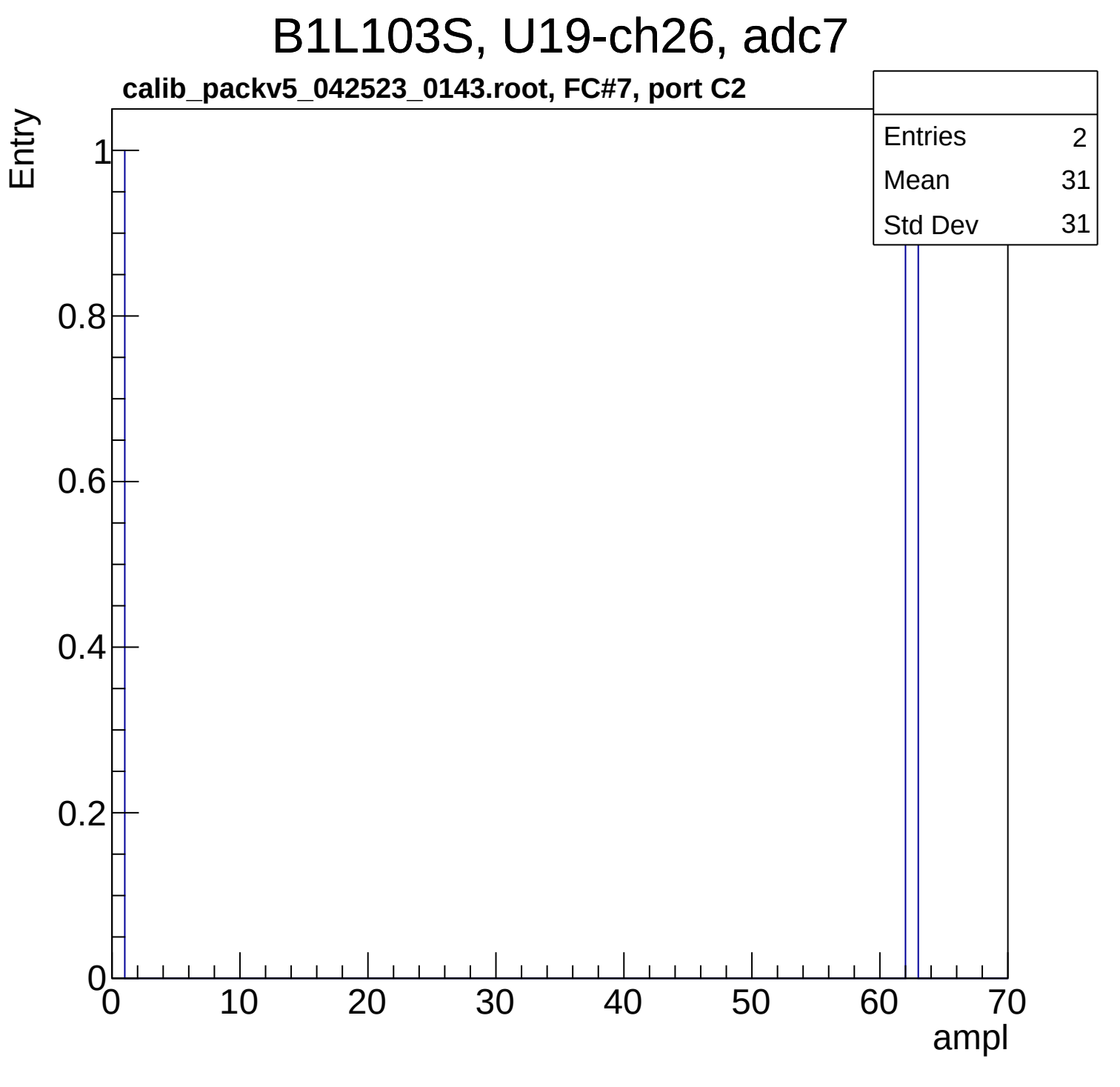
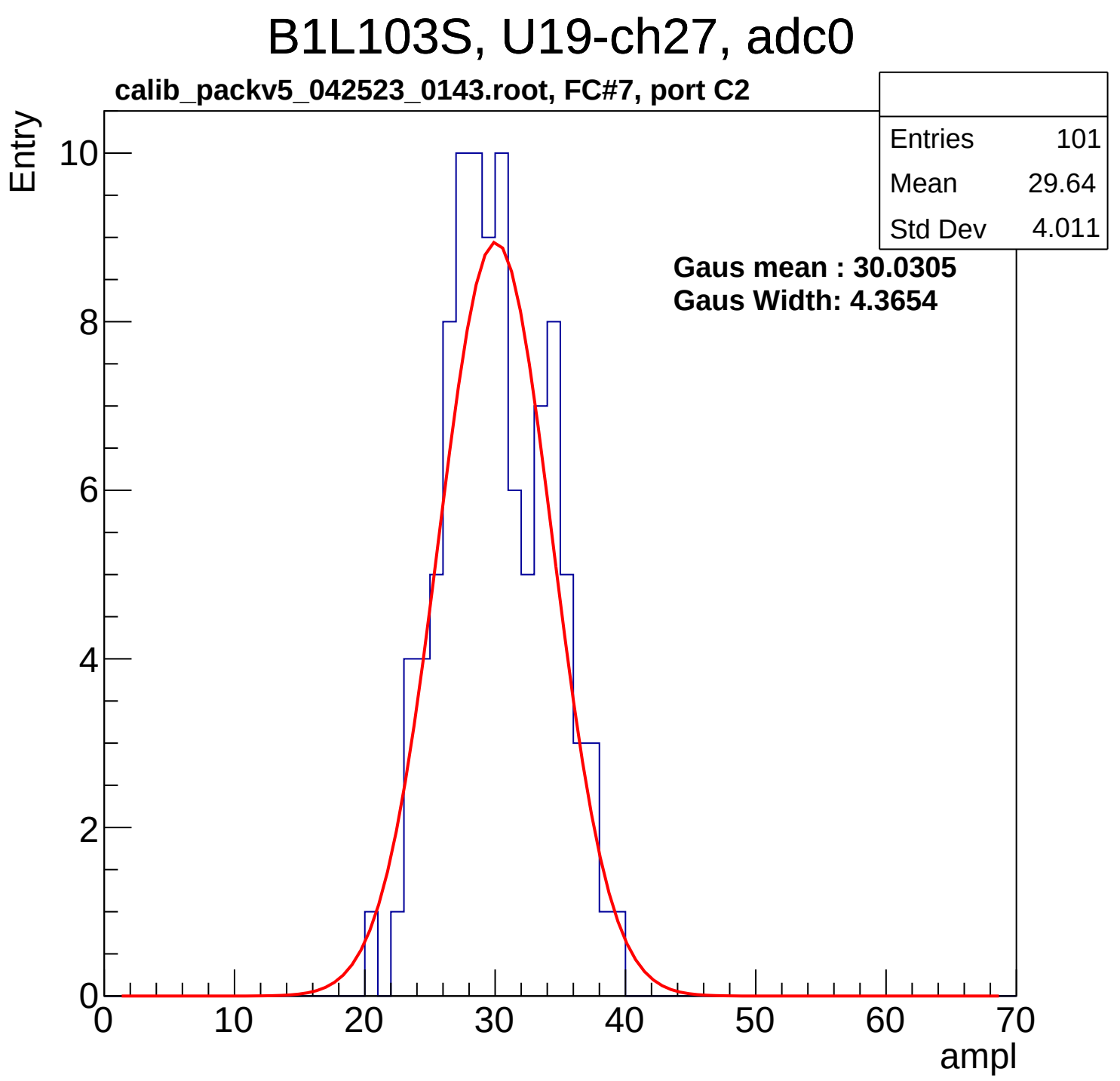

B1L103S, U19-ch26, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch27, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 101
Mean 29.64
Std Dev 4.011
Gaus mean : 30.0305
Gaus Width: 4.3654
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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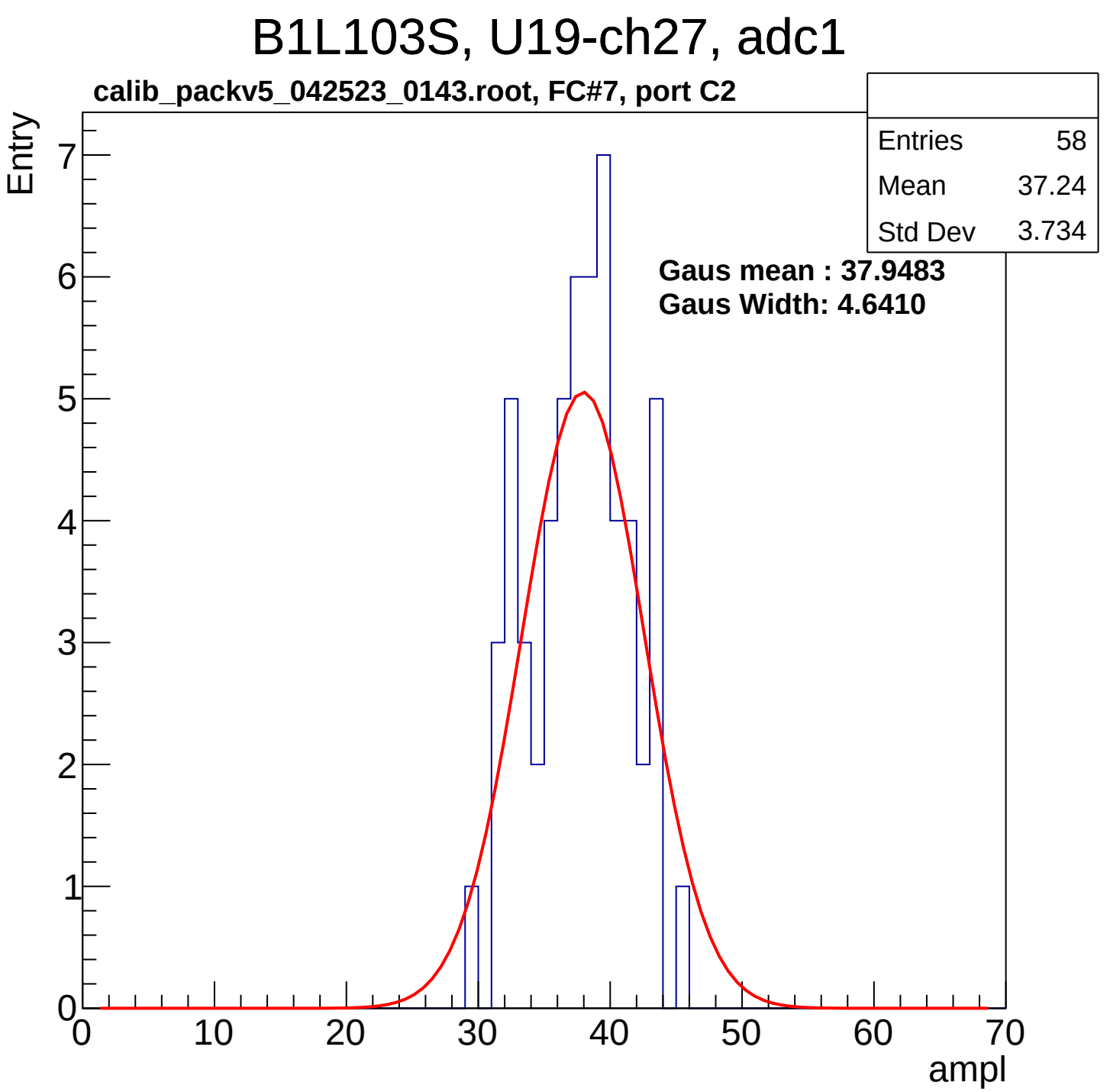
B1L103S, U19-ch27, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 37.24
Std Dev 3.734
Gaus mean : 37.9483
Gaus Width: 4.6410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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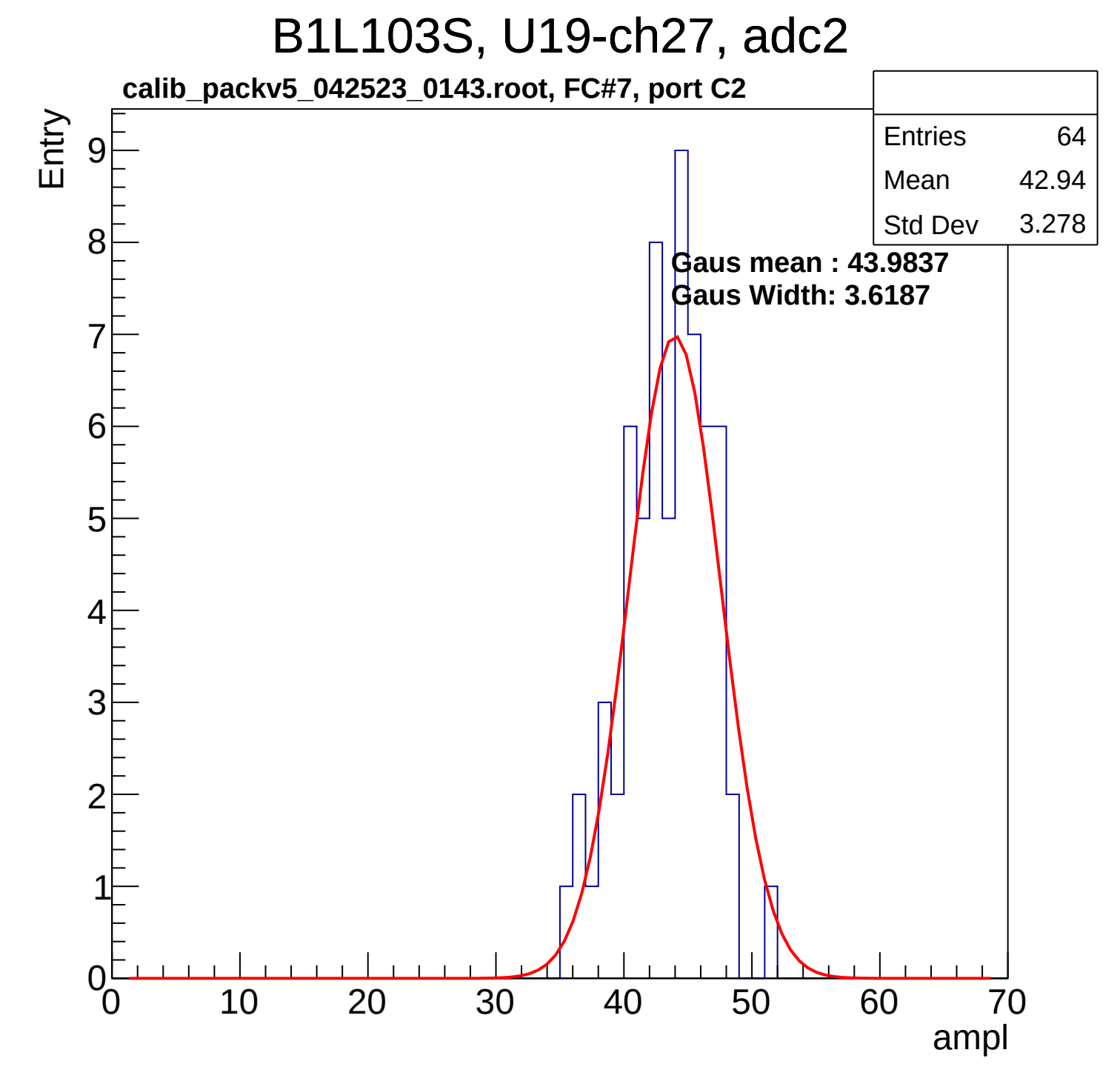
B1L103S, U19-ch27, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 42.94
Std Dev 3.278
Gaus mean : 43.9837
Gaus Width: 3.6187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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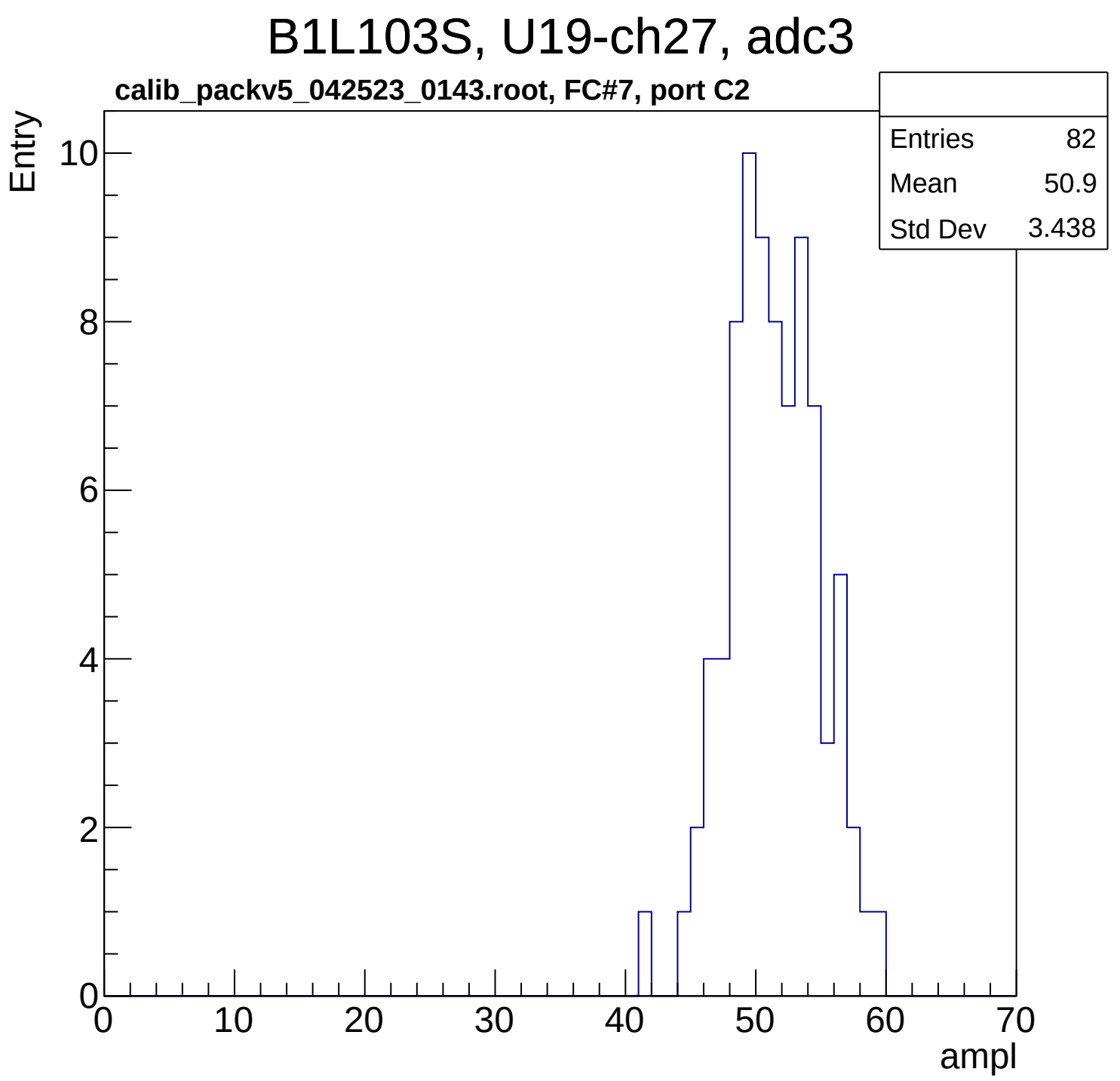
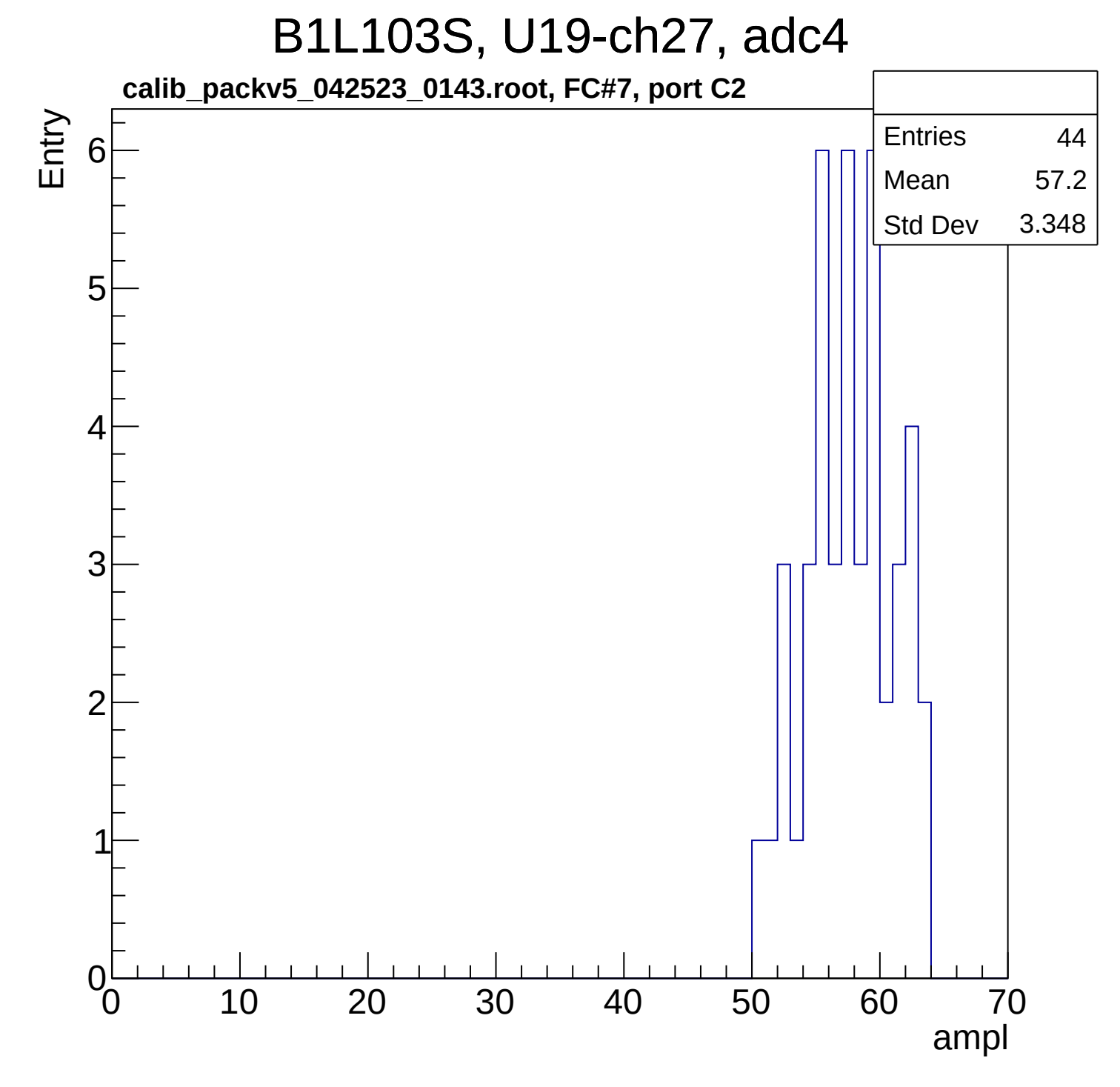
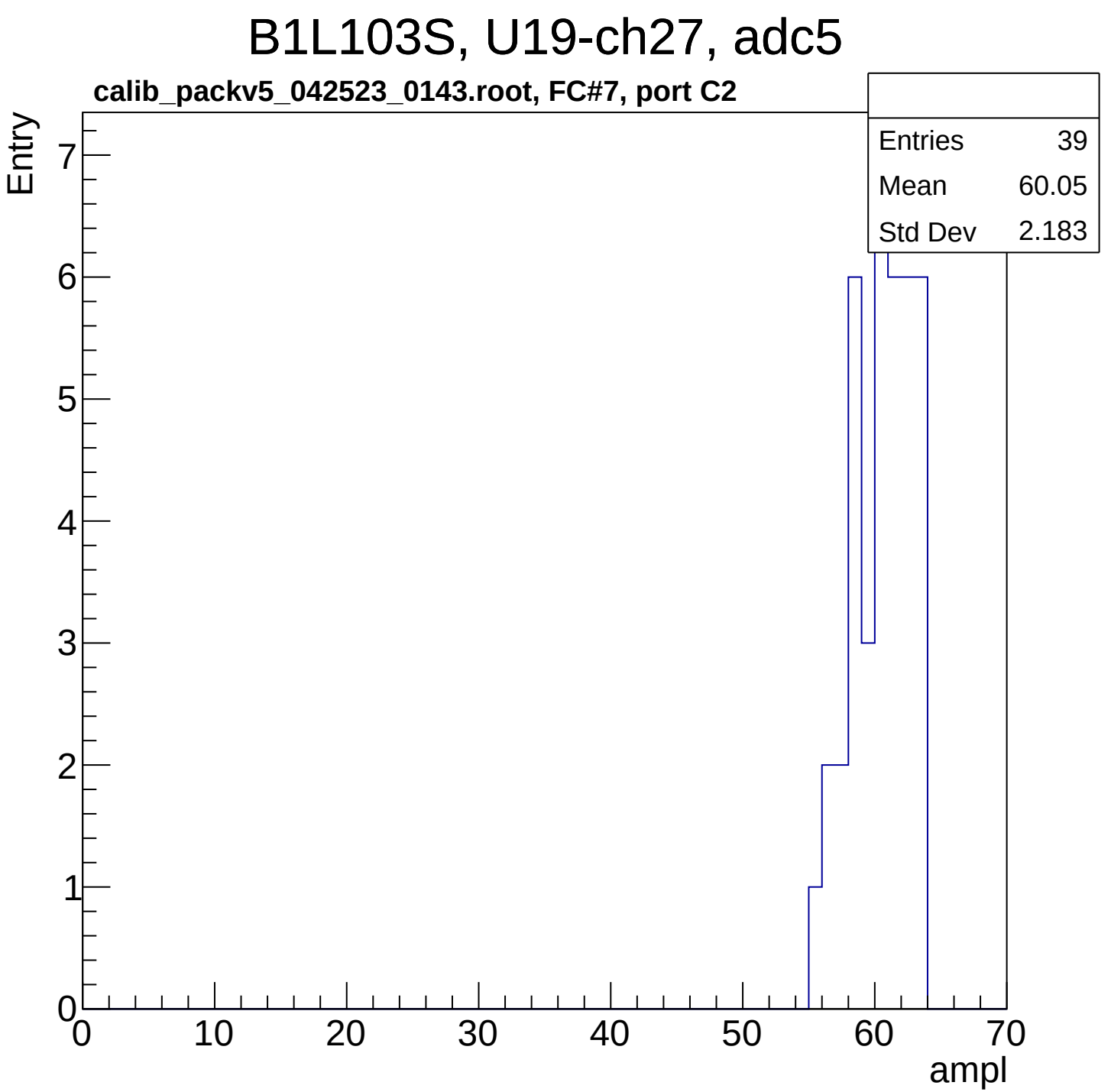
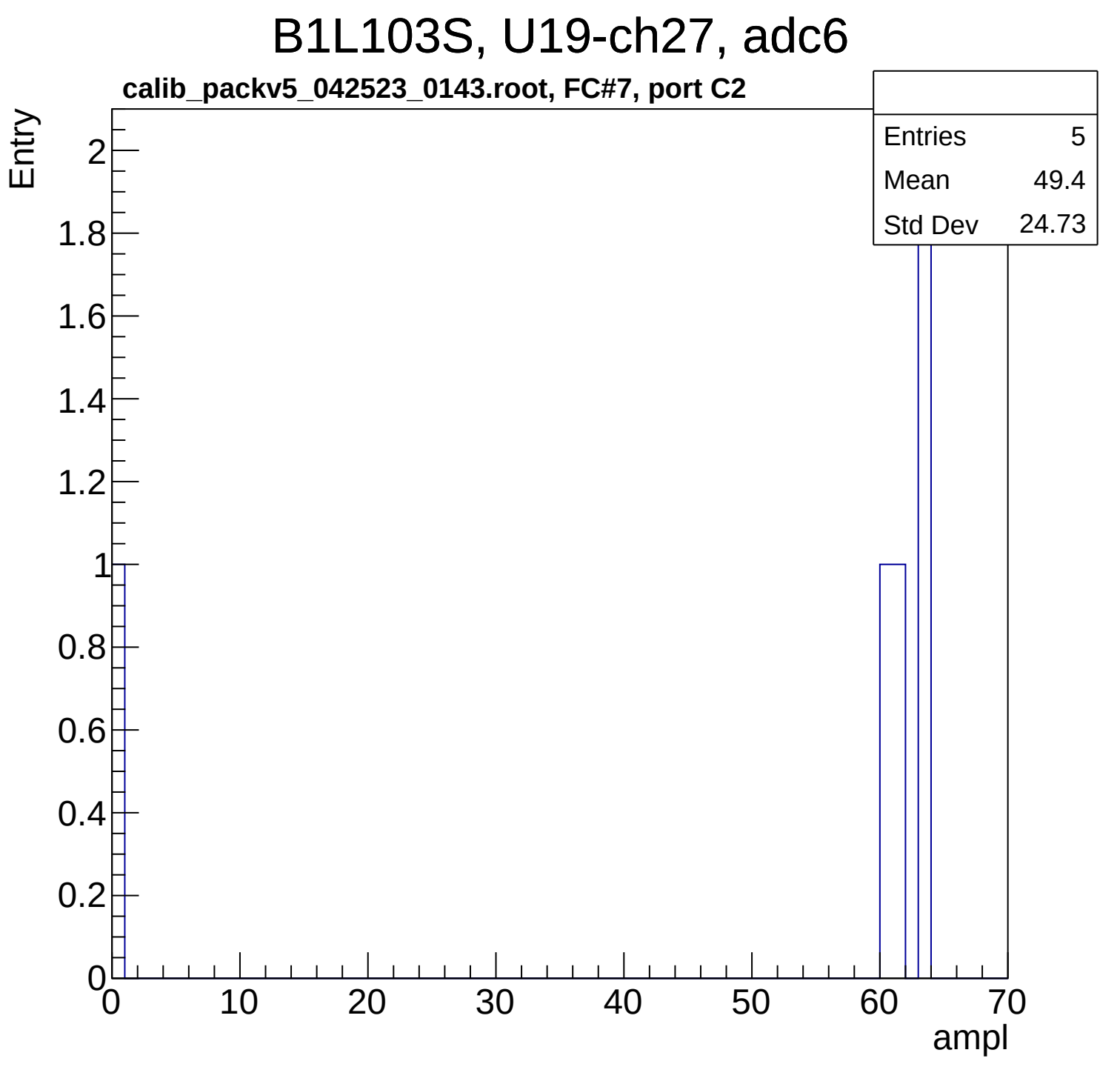

B1L103S, U19-ch27, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 50.9
Std Dev 3.438
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch27, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 57.2
Std Dev 3.348
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch27, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 60.05
Std Dev 2.183
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch27, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.4
Std Dev 24.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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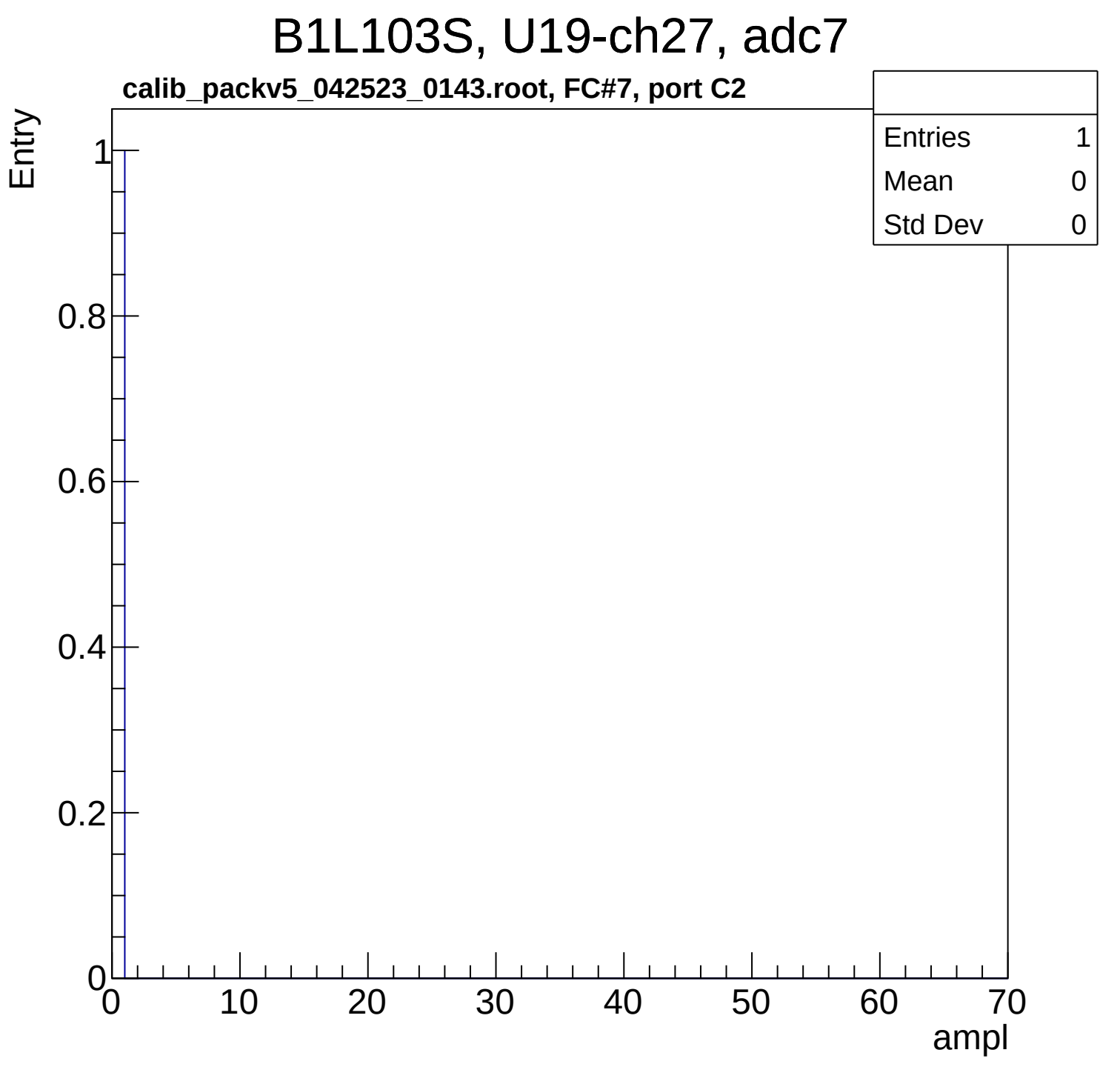
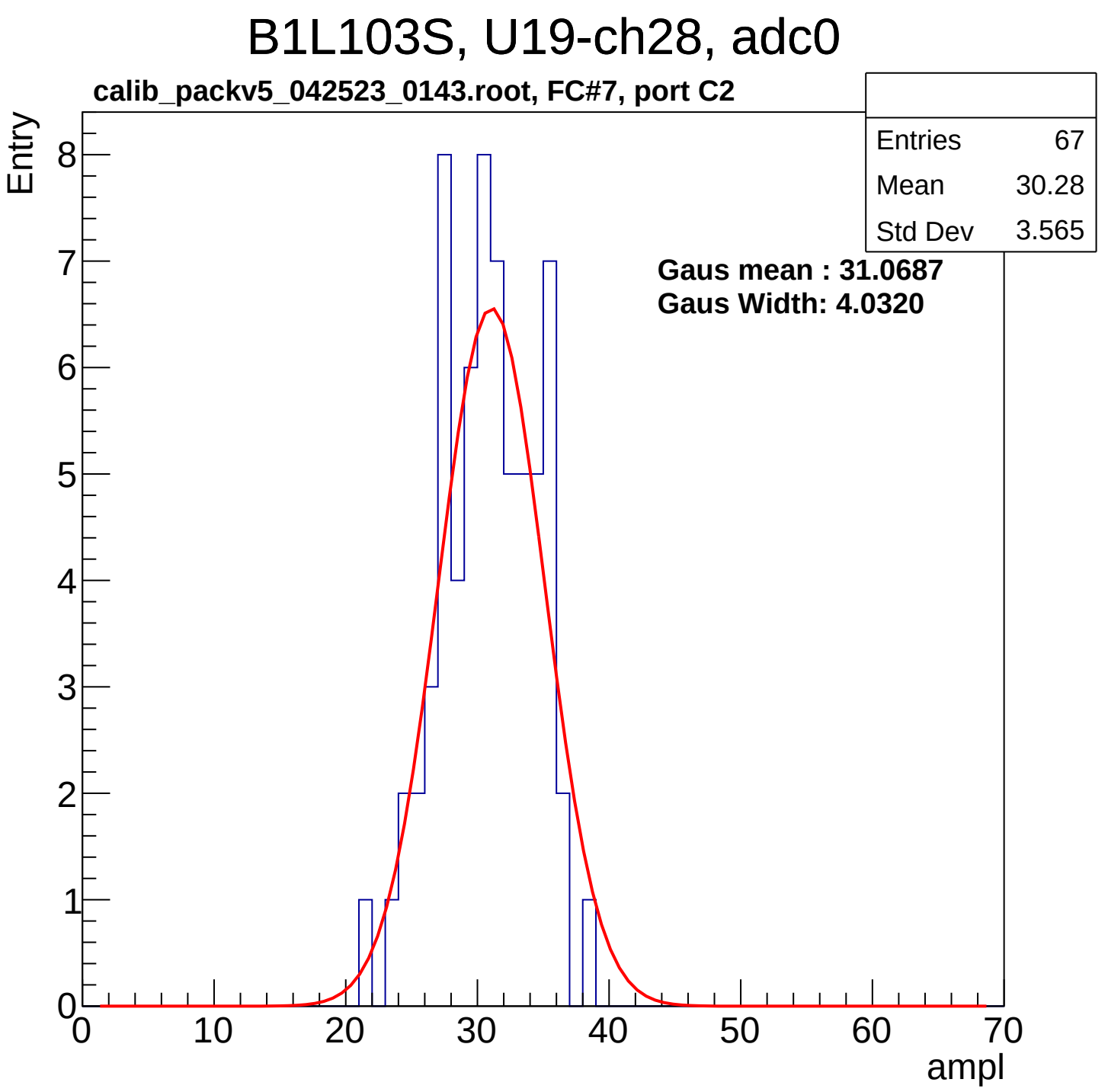
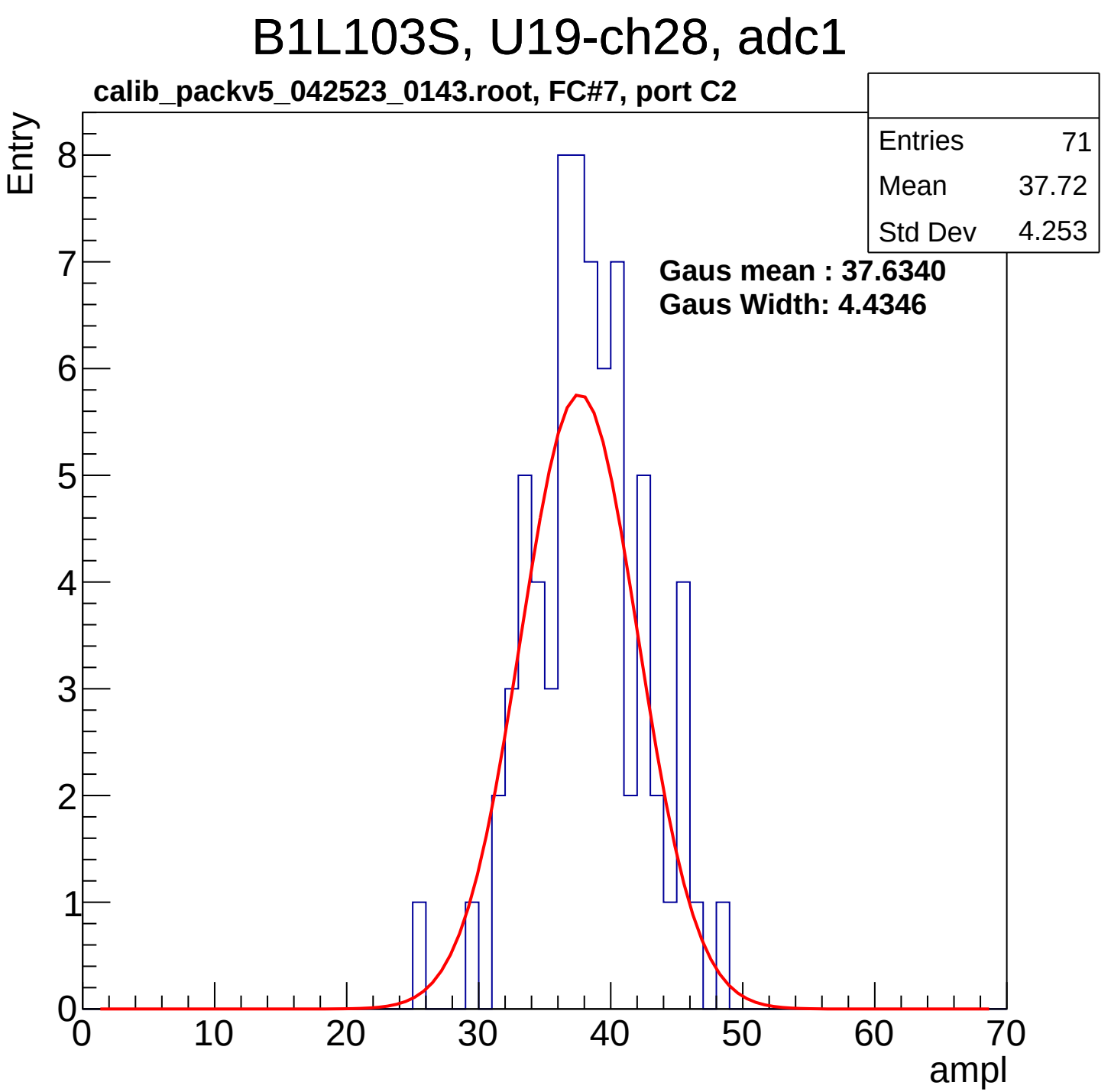
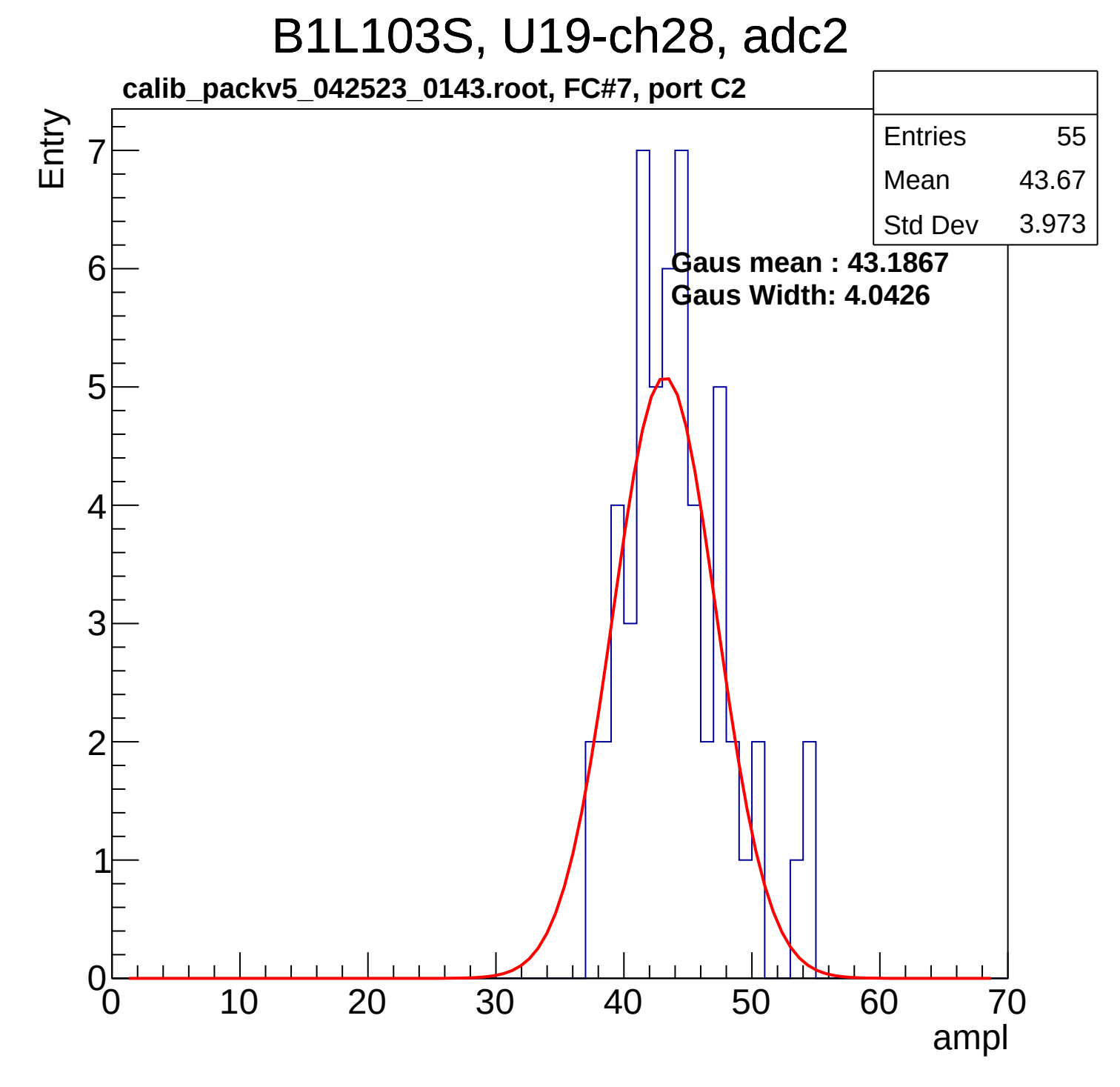

B1L103S, U19-ch27, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch28, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 30.28
Std Dev 3.565
Gaus mean : 31.0687
Gaus Width: 4.0320
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch28, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 37.72
Std Dev 4.253
Gaus mean : 37.6340
Gaus Width: 4.4346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch28, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 43.67
Std Dev 3.973
Gaus mean : 43.1867
Gaus Width: 4.0426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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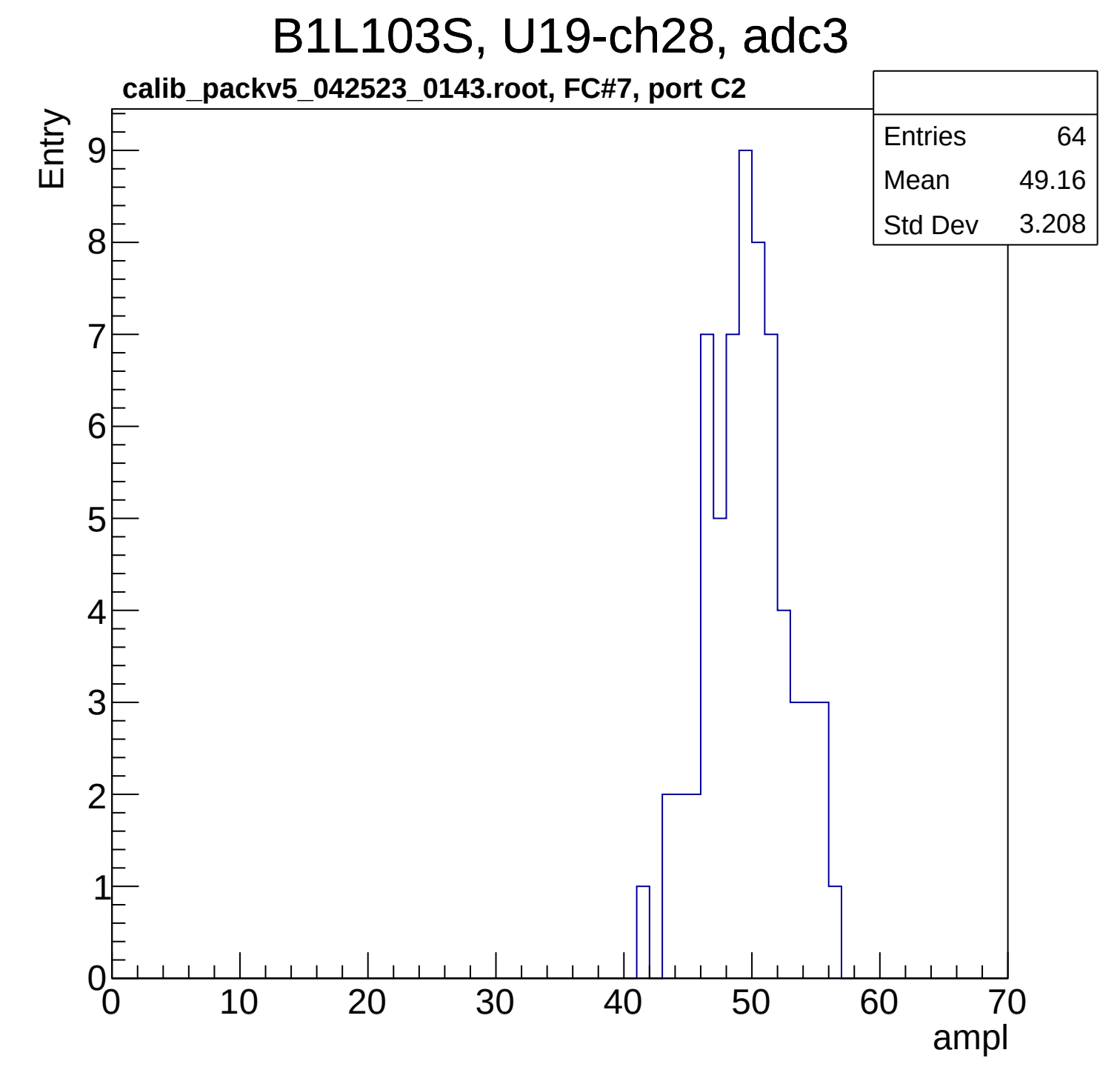
B1L103S, U19-ch28, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.16
Std Dev 3.208
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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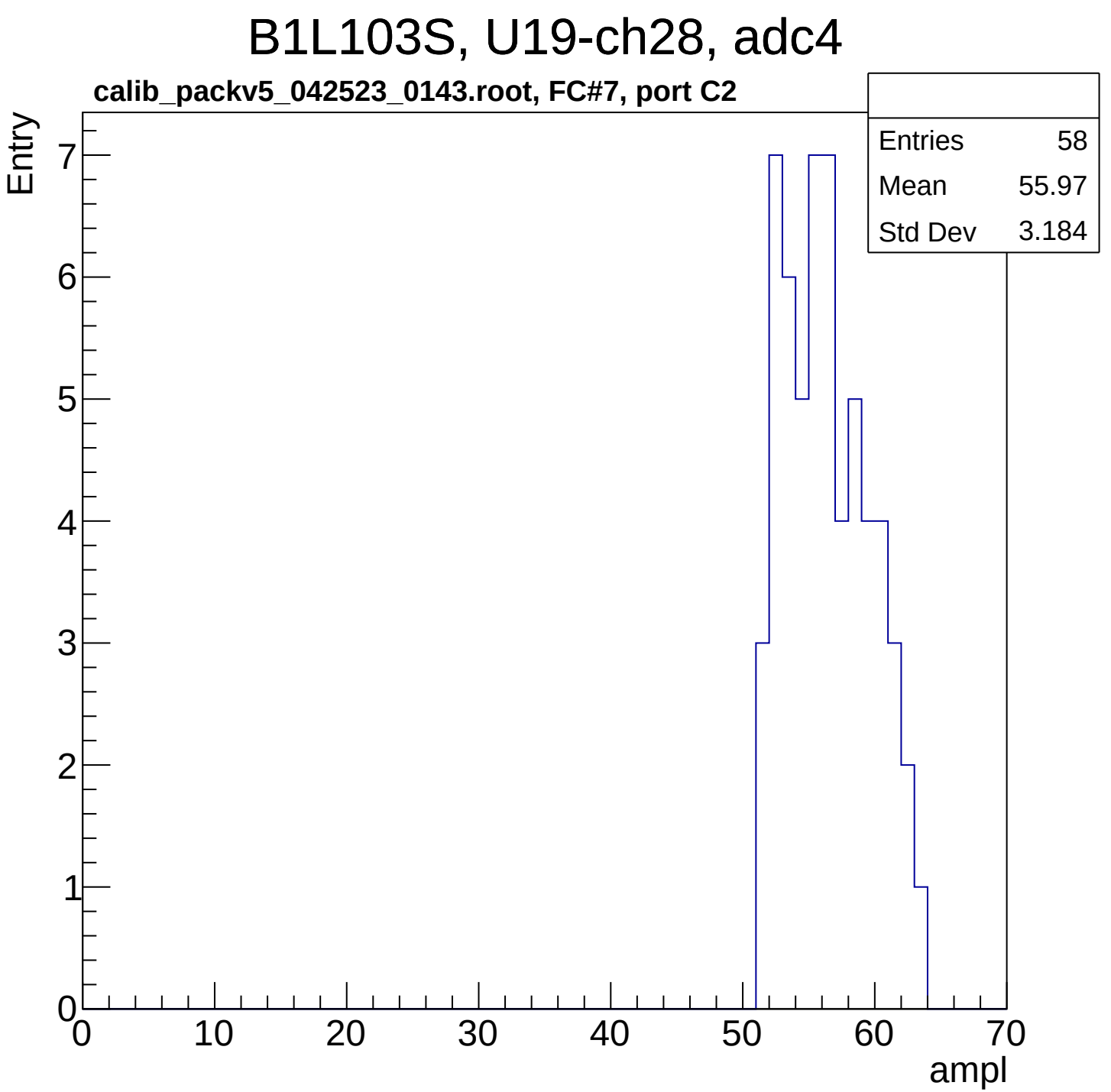
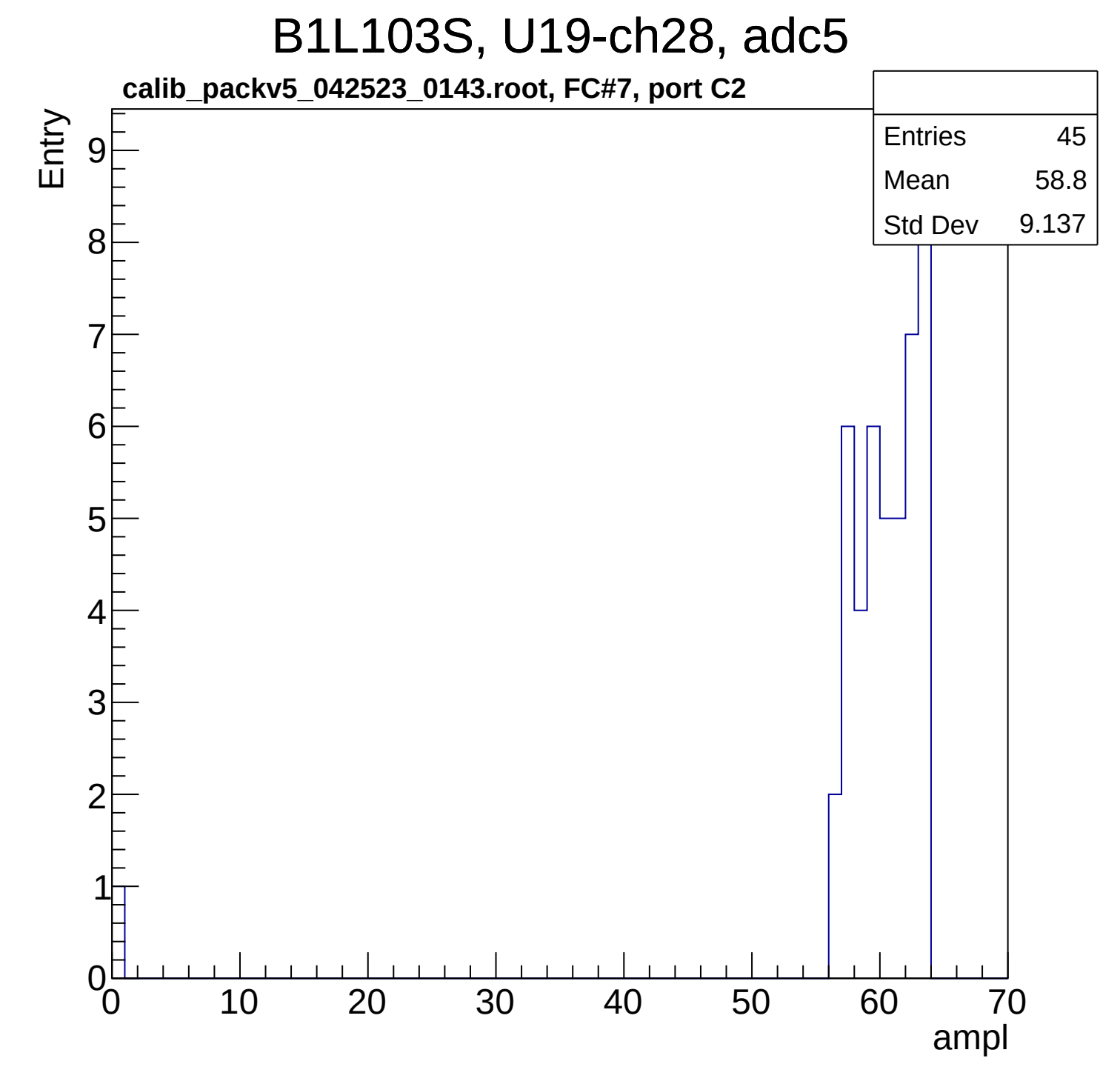
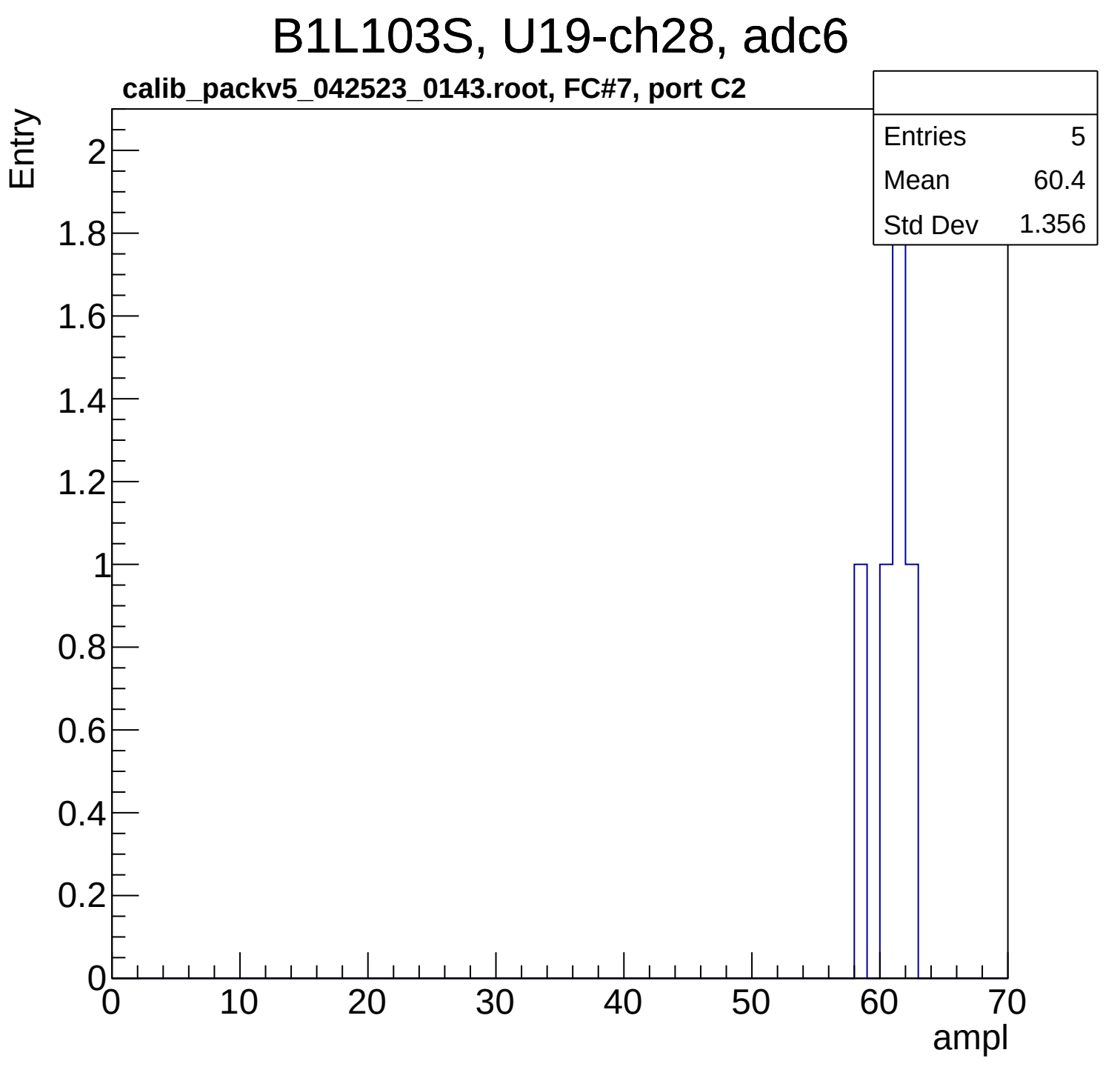
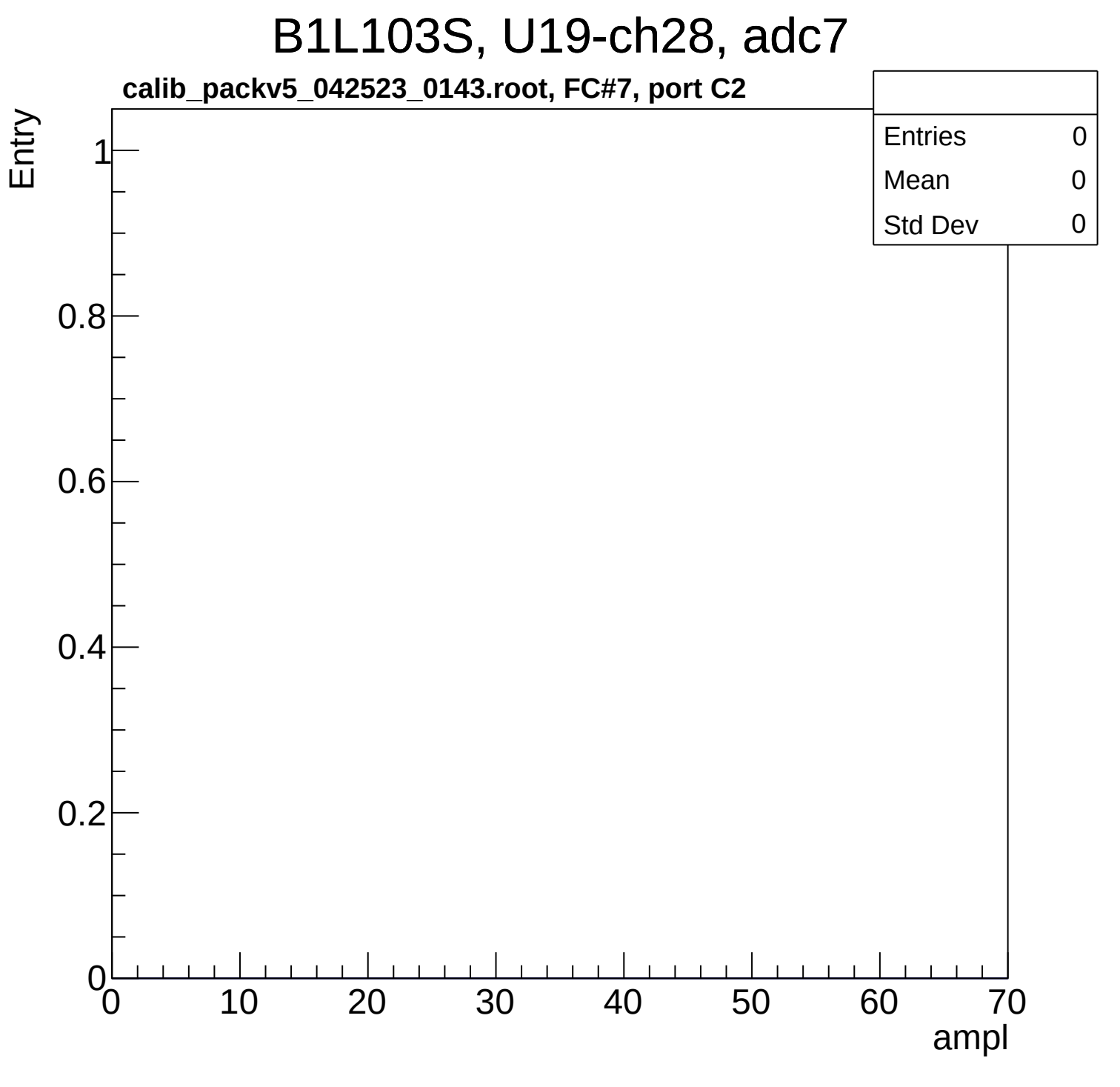
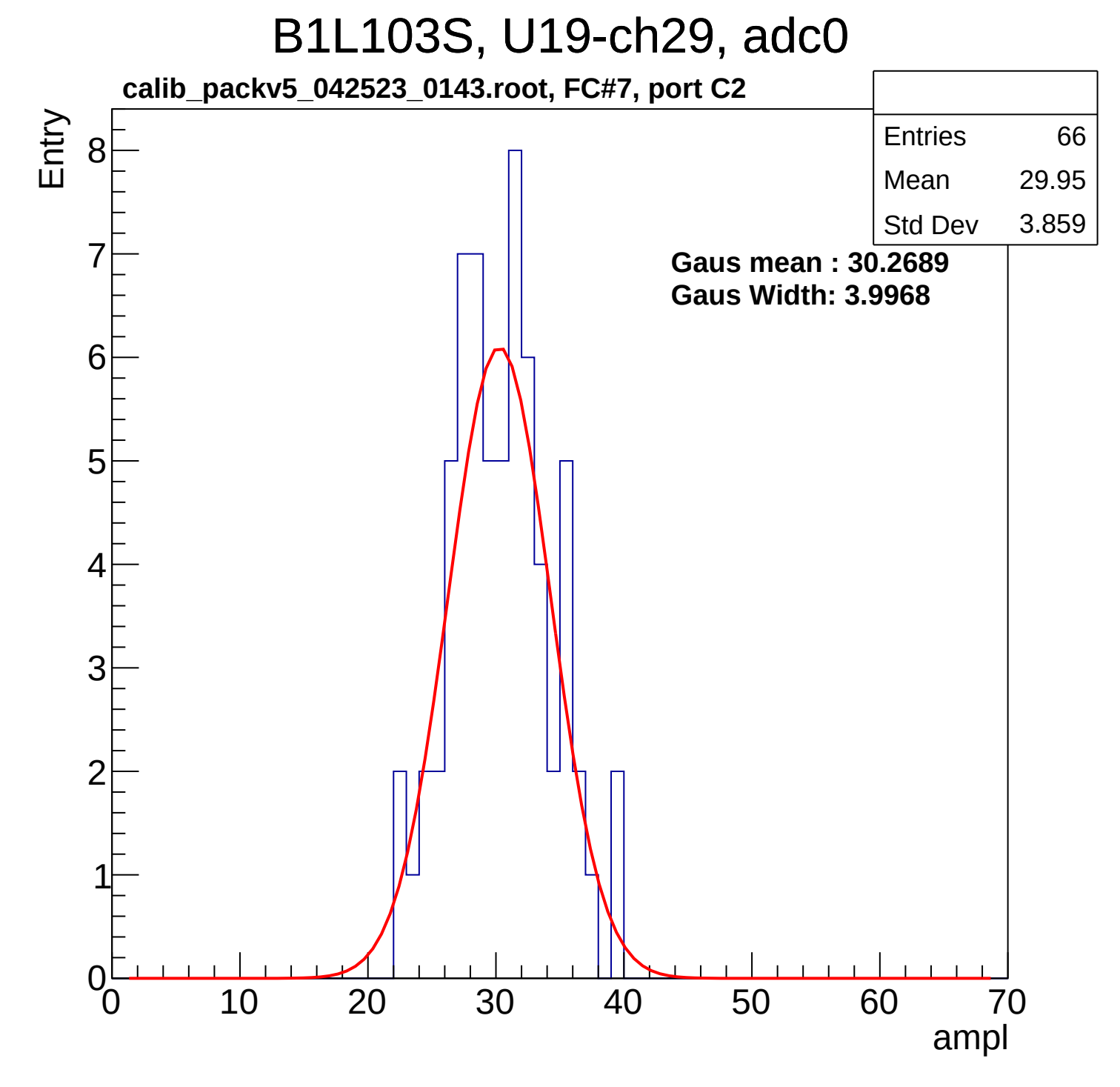

B1L103S, U19-ch28, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 55.97
Std Dev 3.184
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch28, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.8
Std Dev 9.137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch28, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 60.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch28, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch29, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.95
Std Dev 3.859
Gaus mean : 30.2689
Gaus Width: 3.9968
Entry
ampl
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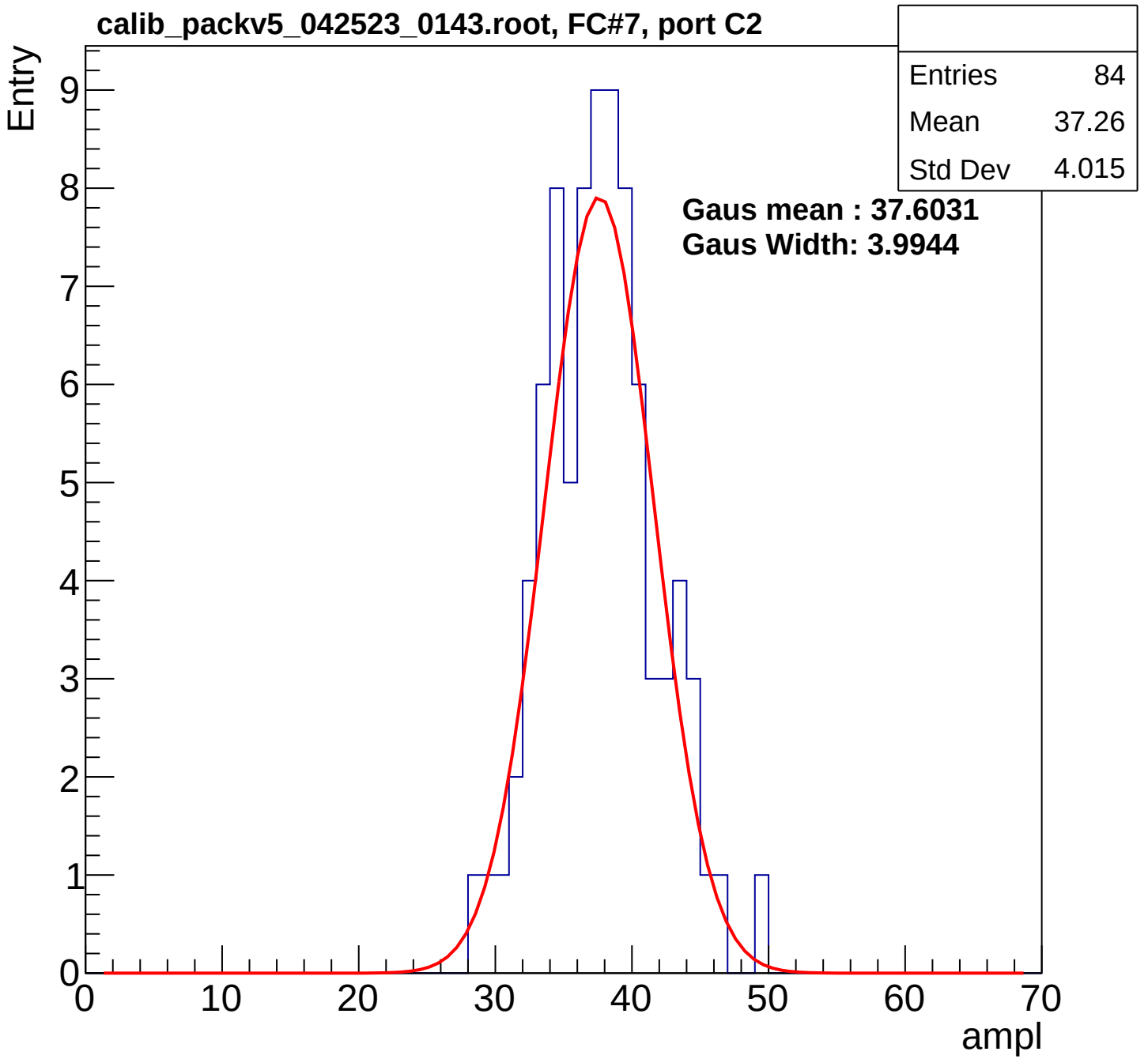
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch29, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 37.26
Std Dev 4.015
Gaus mean : 37.6031
Gaus Width: 3.9944
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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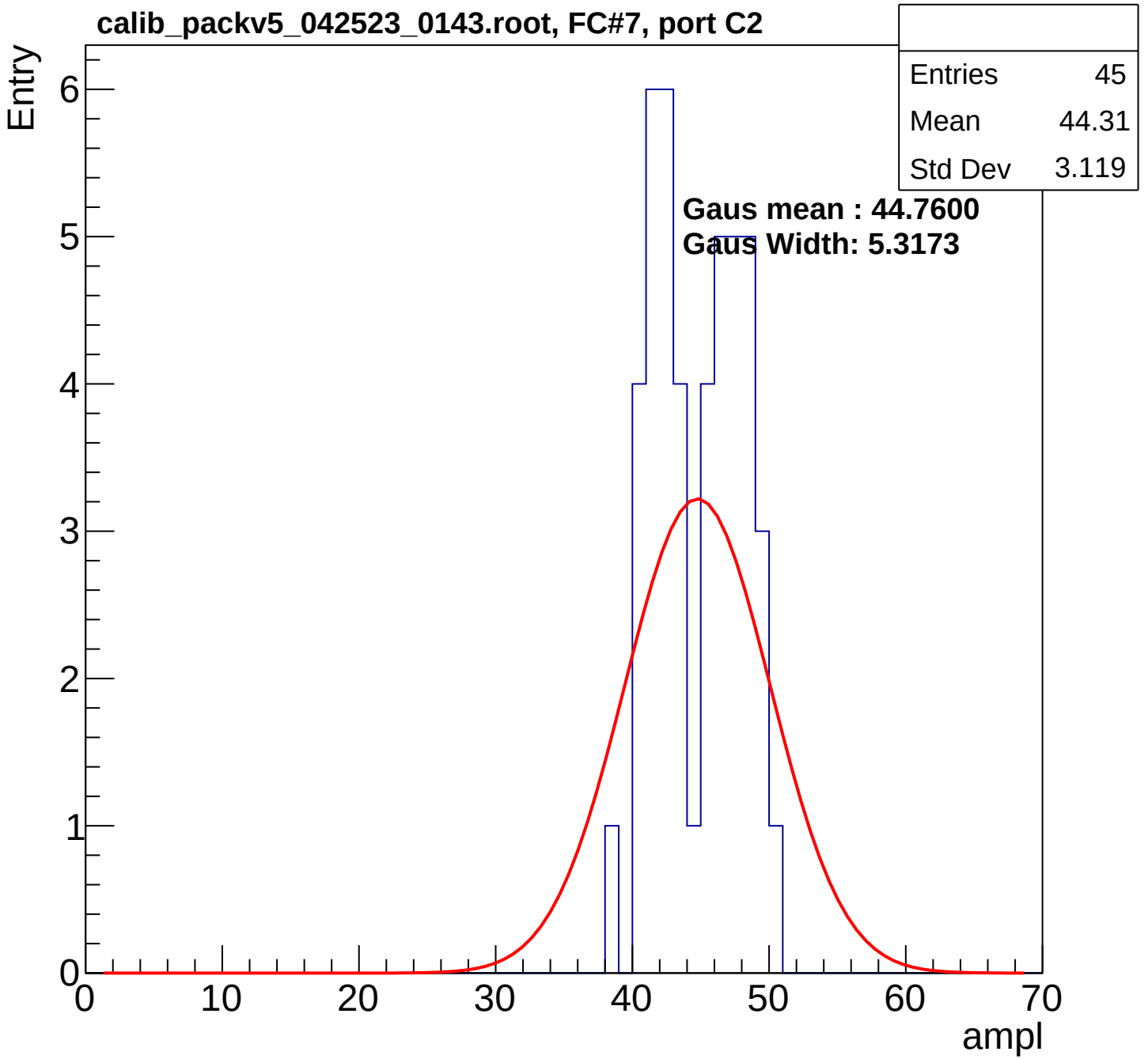
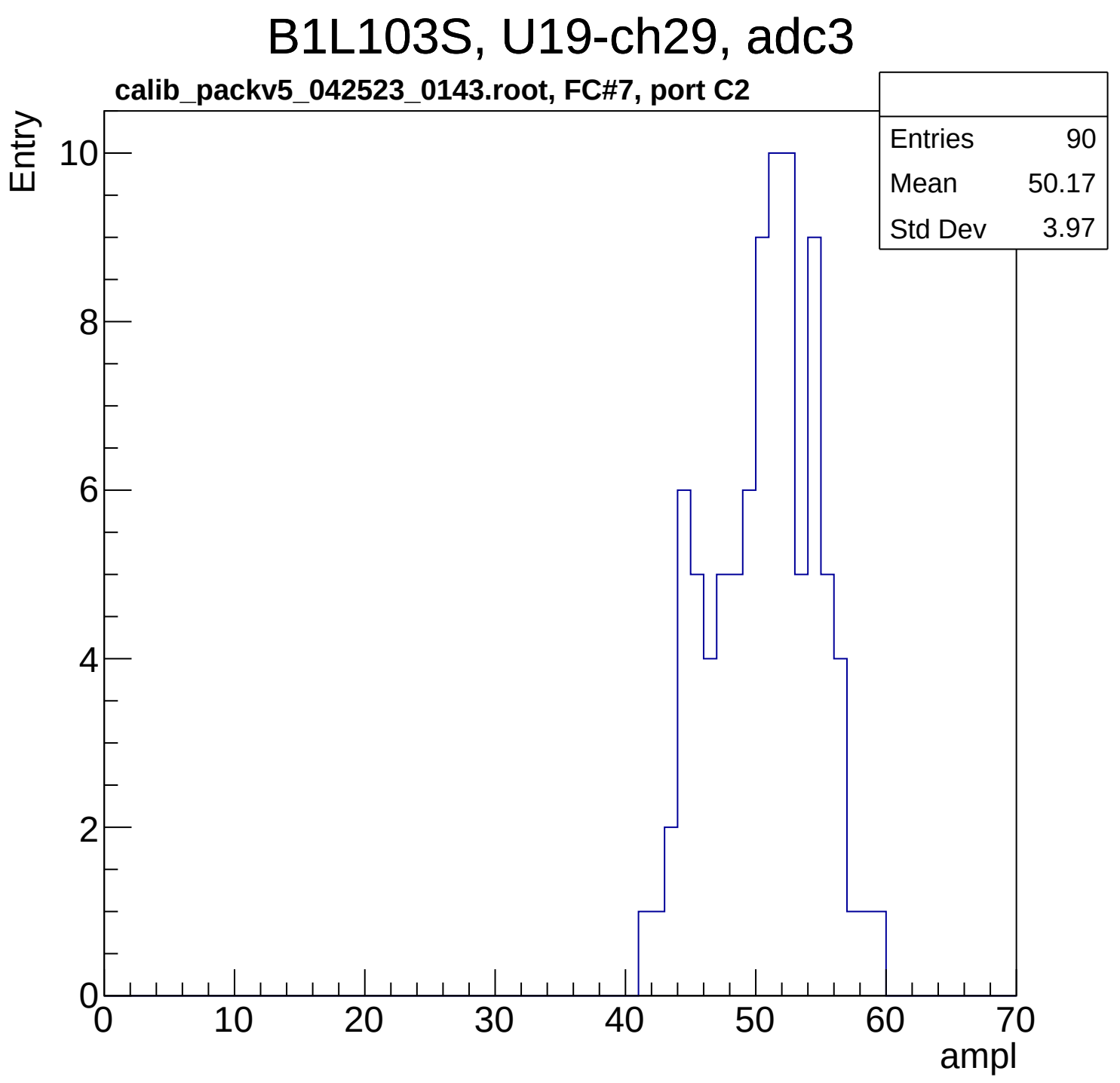

B1L103S, U19-ch29, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 44.31
Std Dev 3.119
Gaus mean : 44.7600
Gaus Width: 5.3173
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch29, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 50.17
Std Dev 3.97
Entry
0
2
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6
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10
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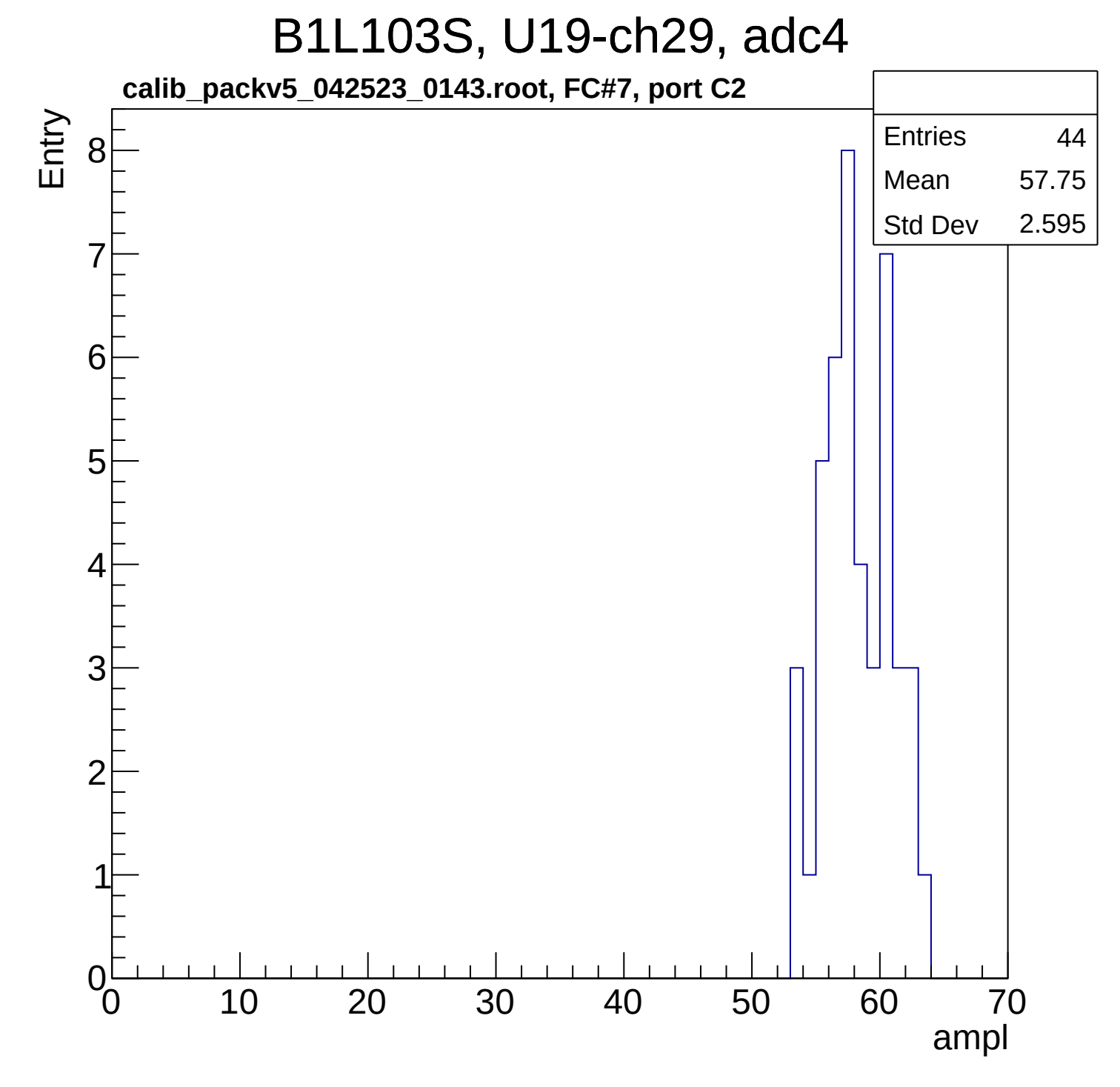
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch29, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 57.75
Std Dev 2.595
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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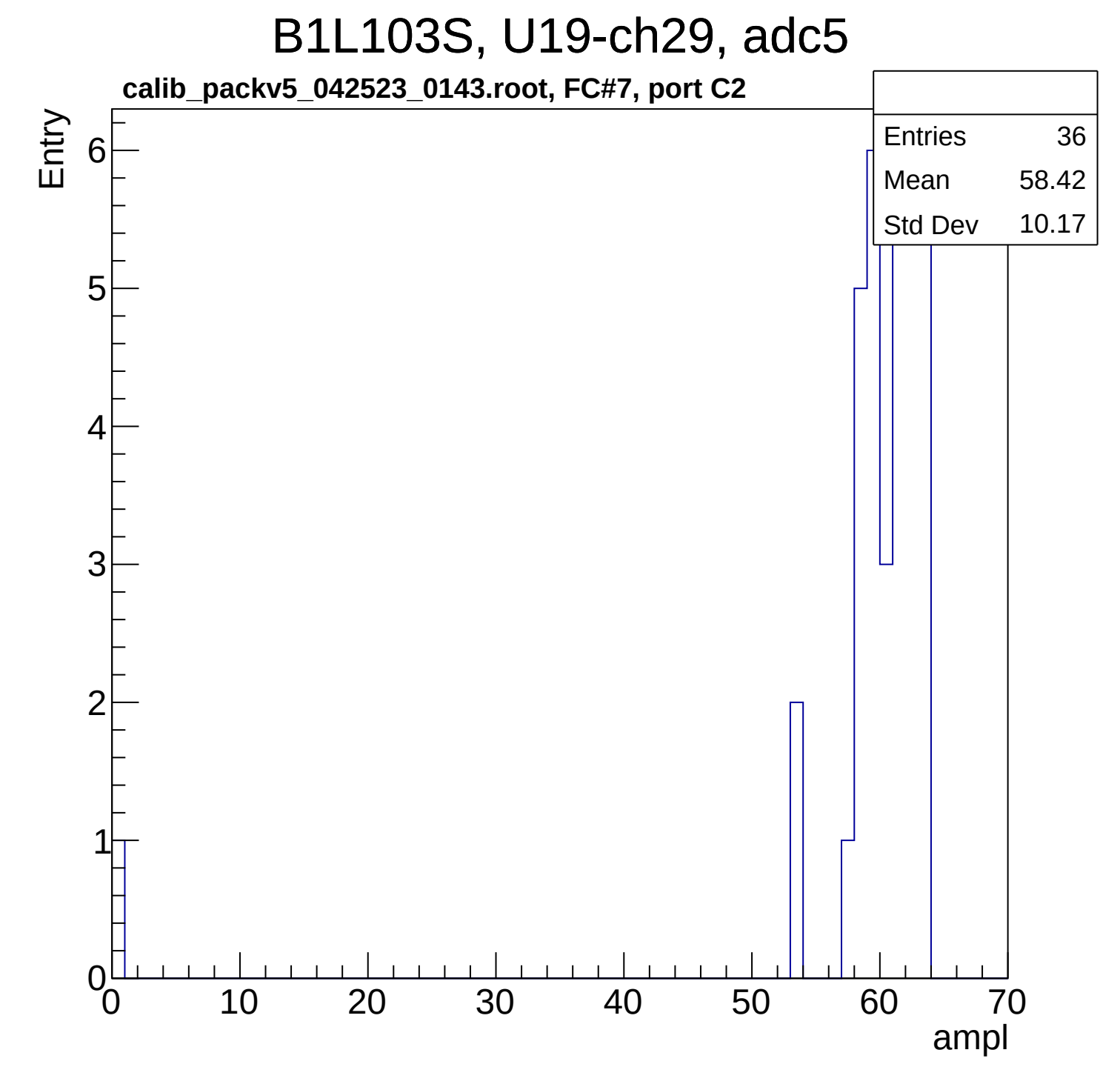
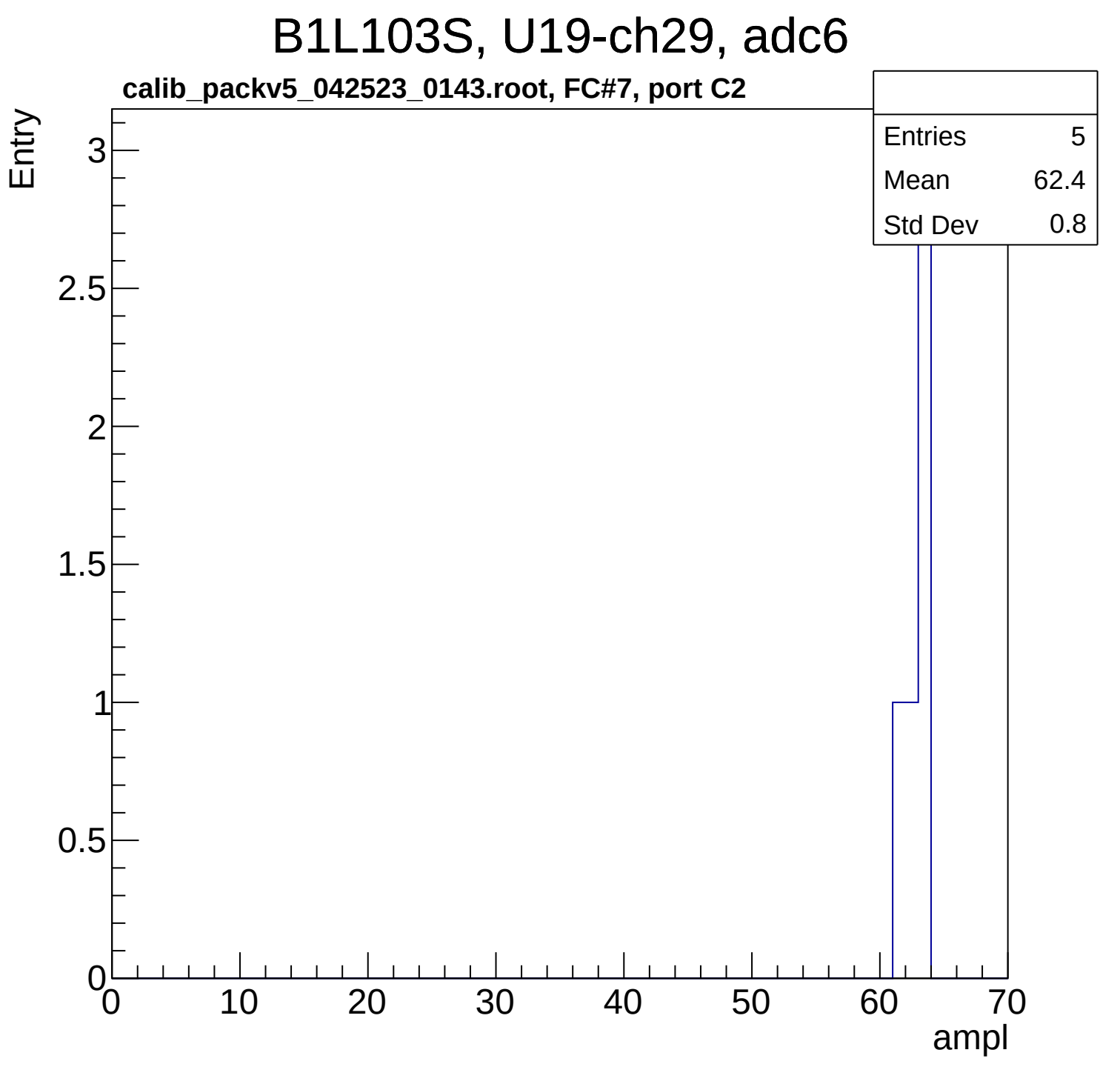
40
50
60
70


B1L103S, U19-ch29, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.42
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch29, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
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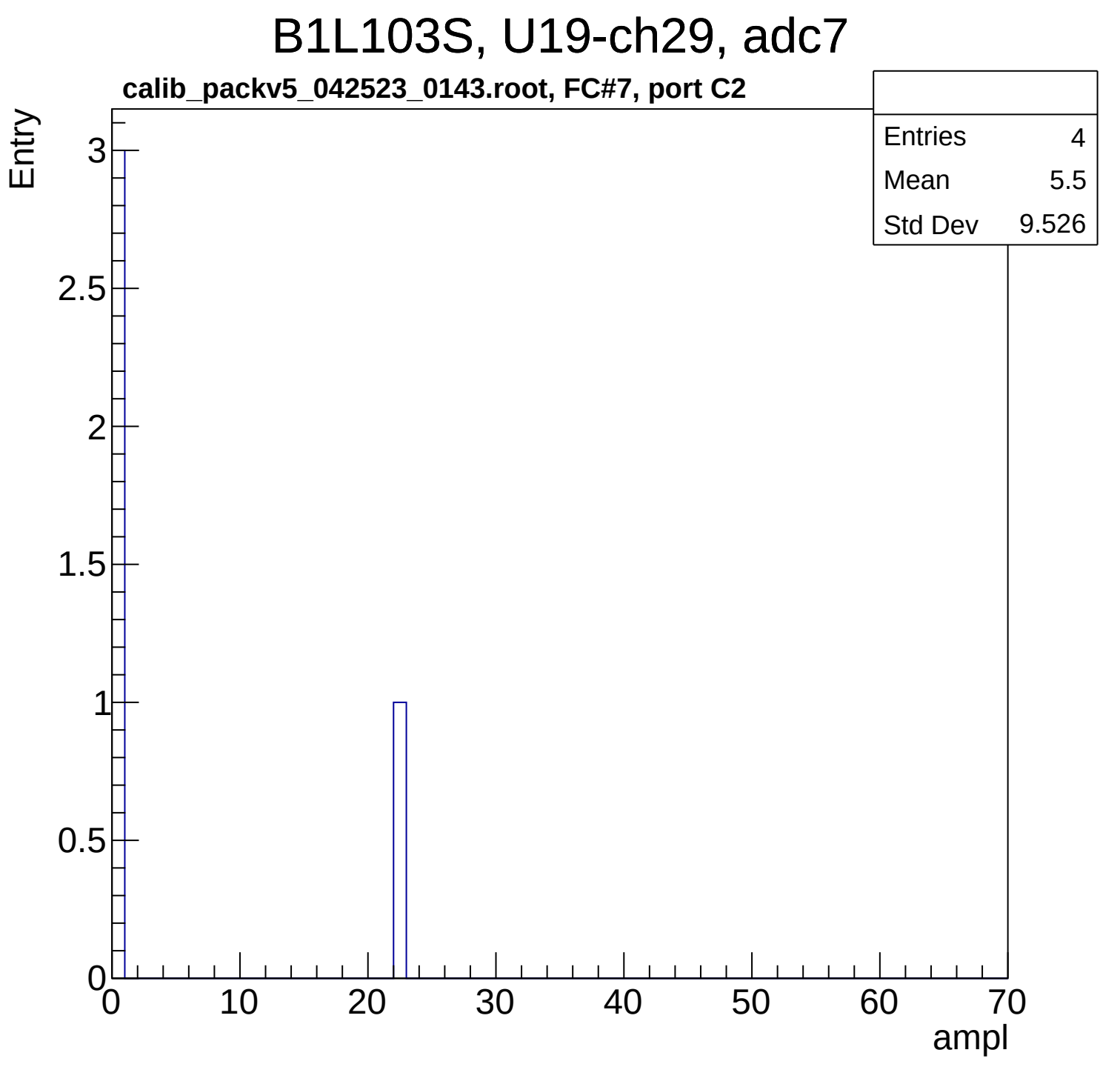
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch29, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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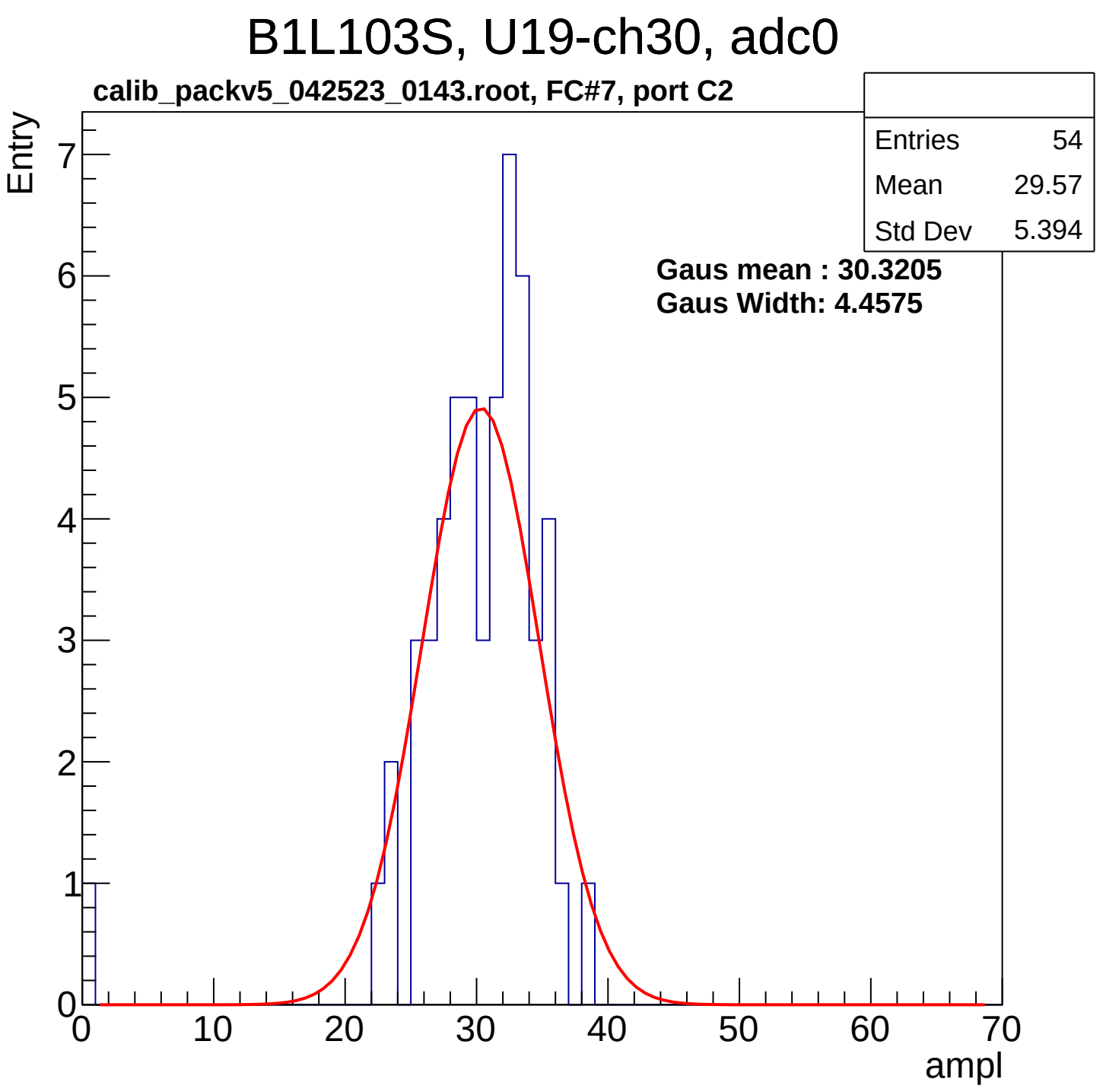
20
30
40
50
60
70


B1L103S, U19-ch30, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 29.57
Std Dev 5.394
Gaus mean : 30.3205
Gaus Width: 4.4575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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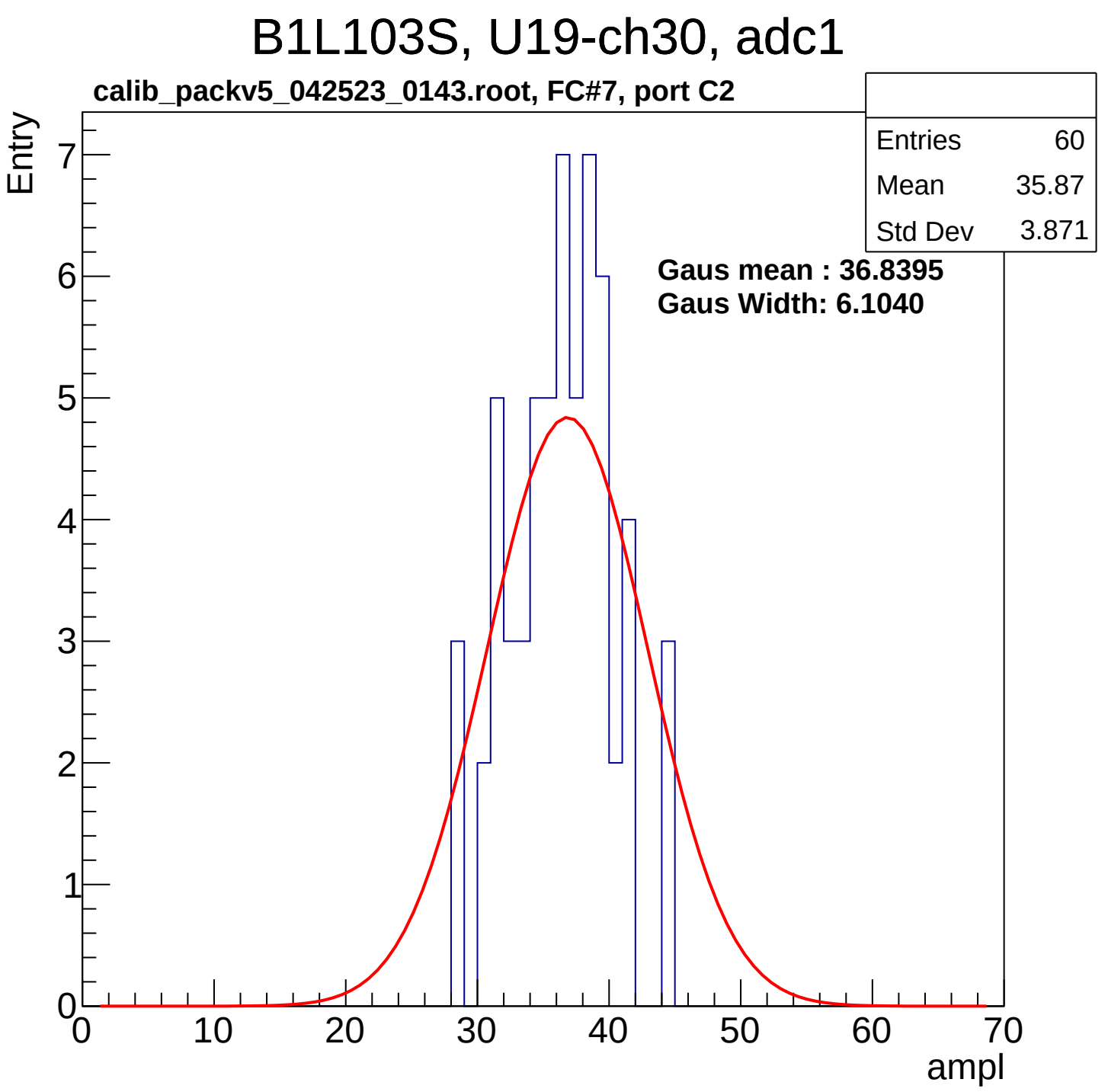
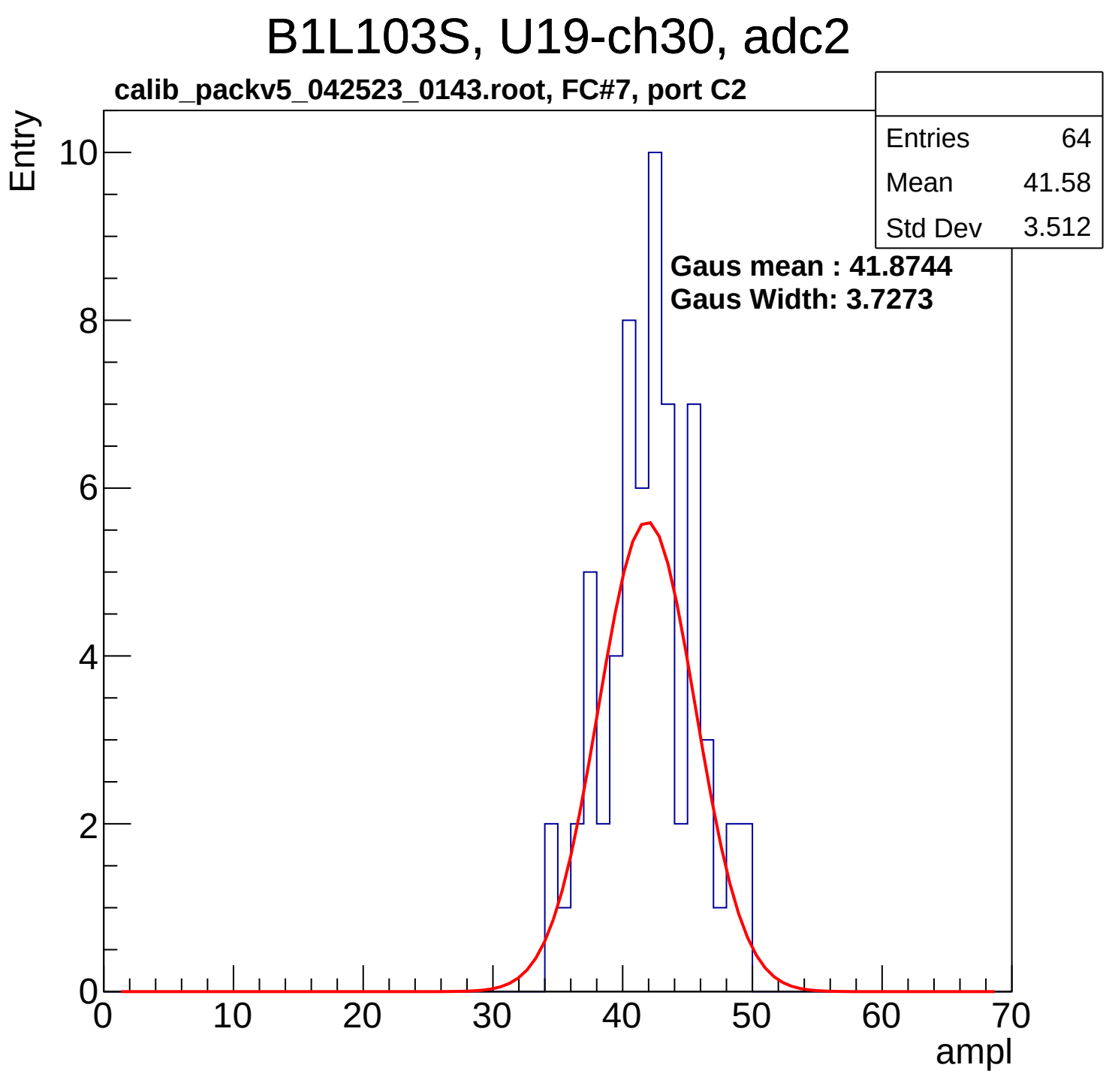
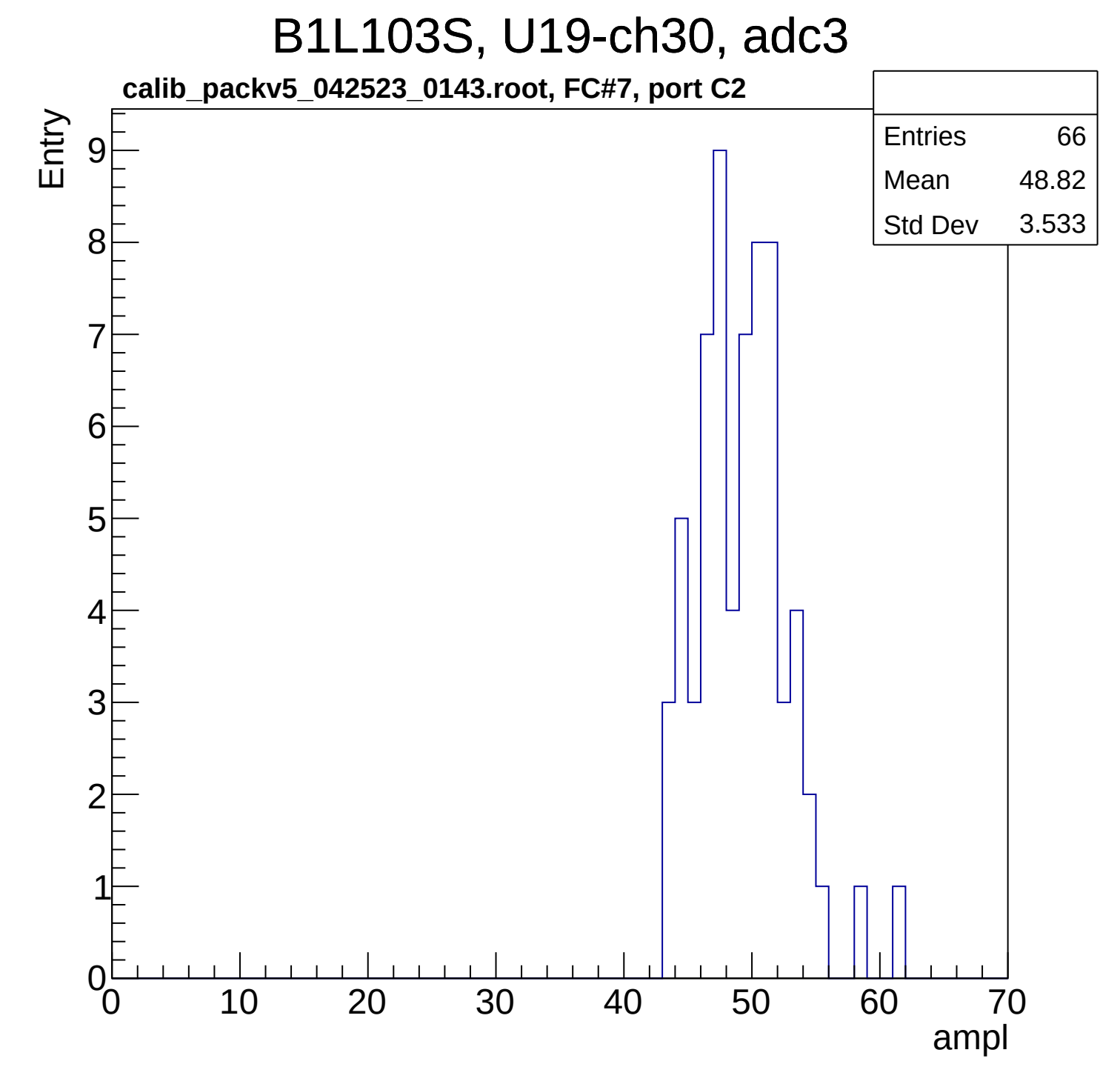

B1L103S, U19-ch30, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 35.87
Std Dev 3.871
Gaus mean : 36.8395
Gaus Width: 6.1040
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch30, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 41.58
Std Dev 3.512
Gaus mean : 41.8744
Gaus Width: 3.7273
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch30, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 48.82
Std Dev 3.533
Entry
ampl
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2
3
4
5
6
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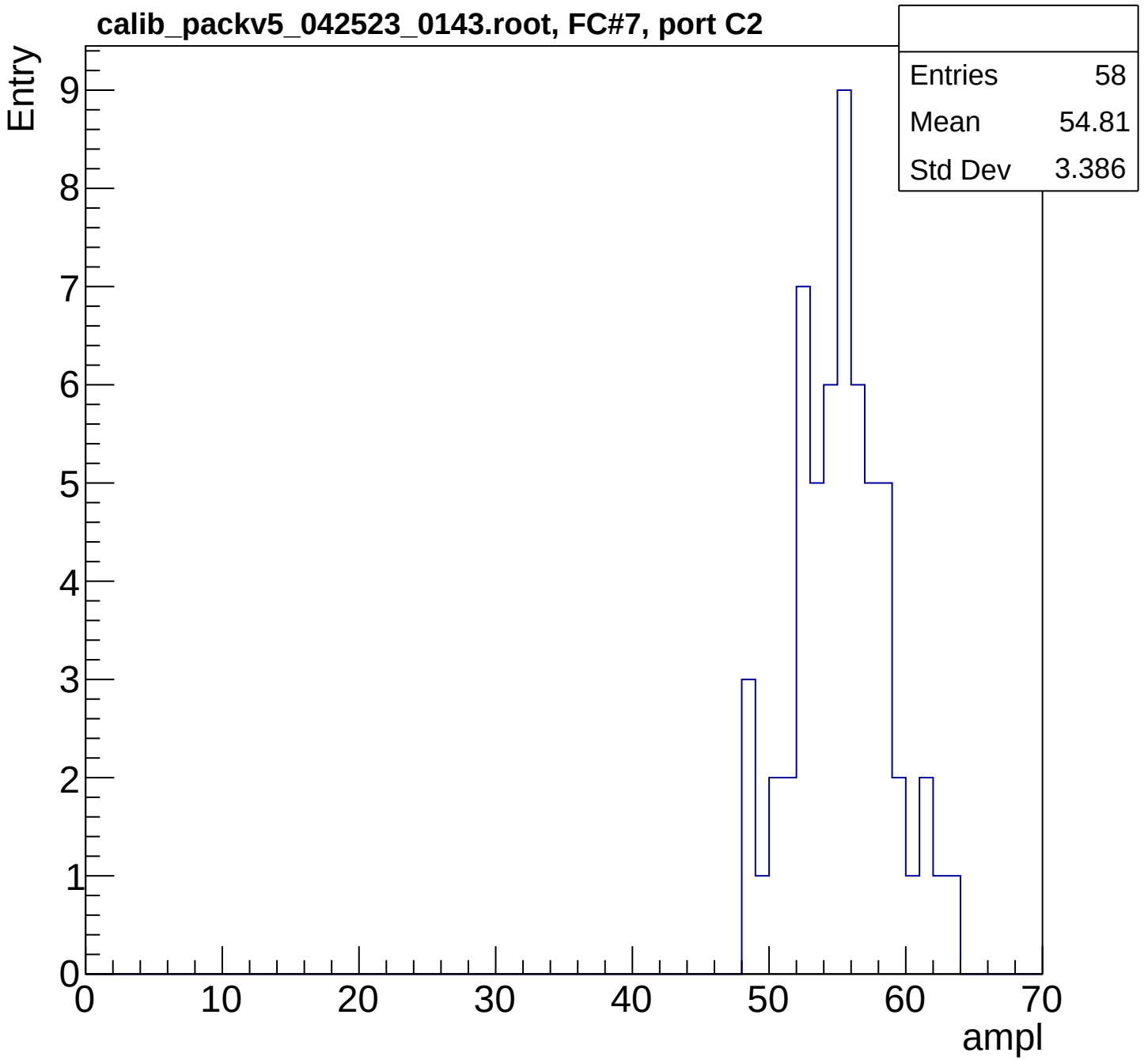
B1L103S, U19-ch30, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 54.81
Std Dev 3.386
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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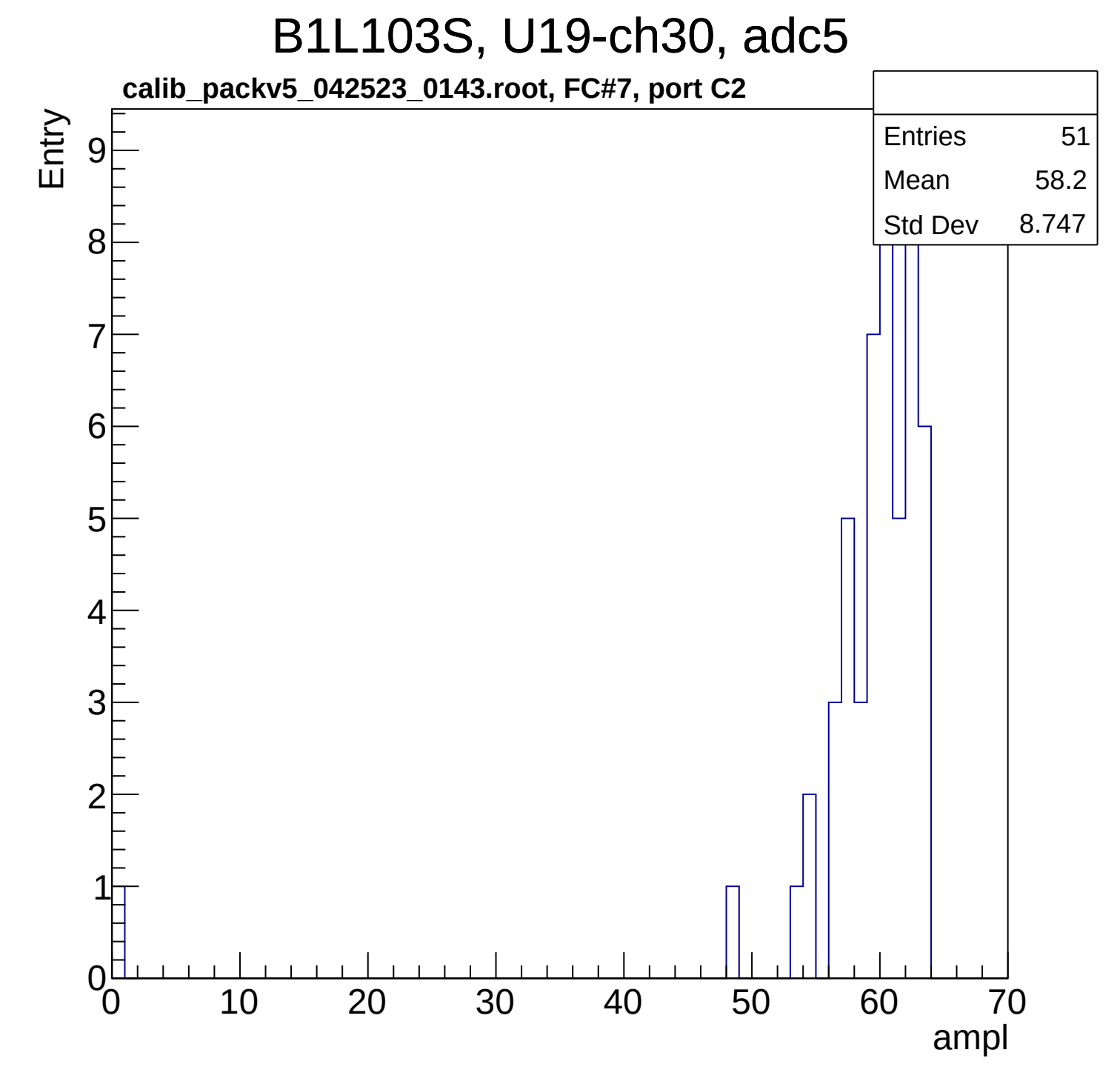
B1L103S, U19-ch30, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.2
Std Dev 8.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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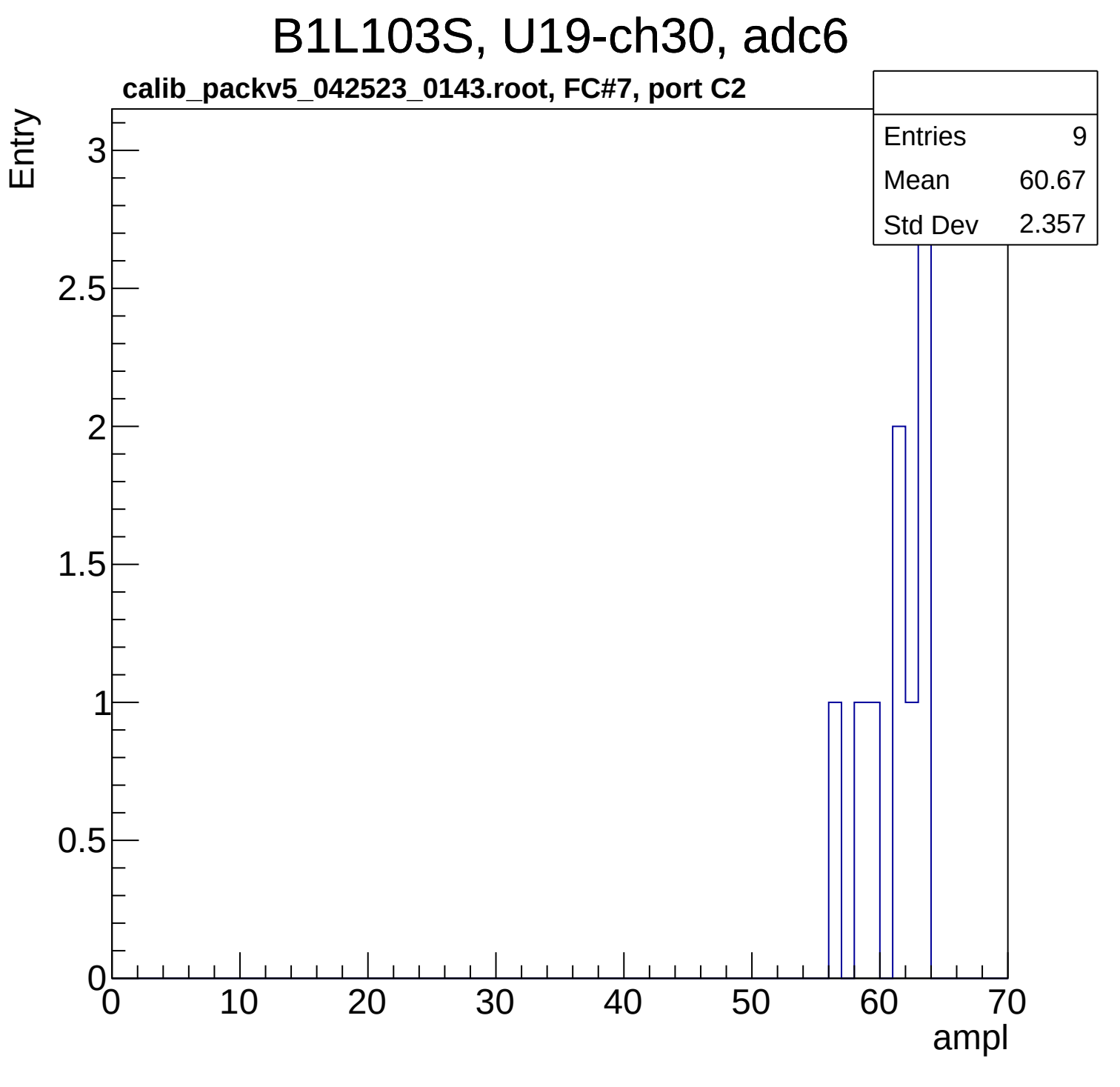
60
70


B1L103S, U19-ch30, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 60.67
Std Dev 2.357
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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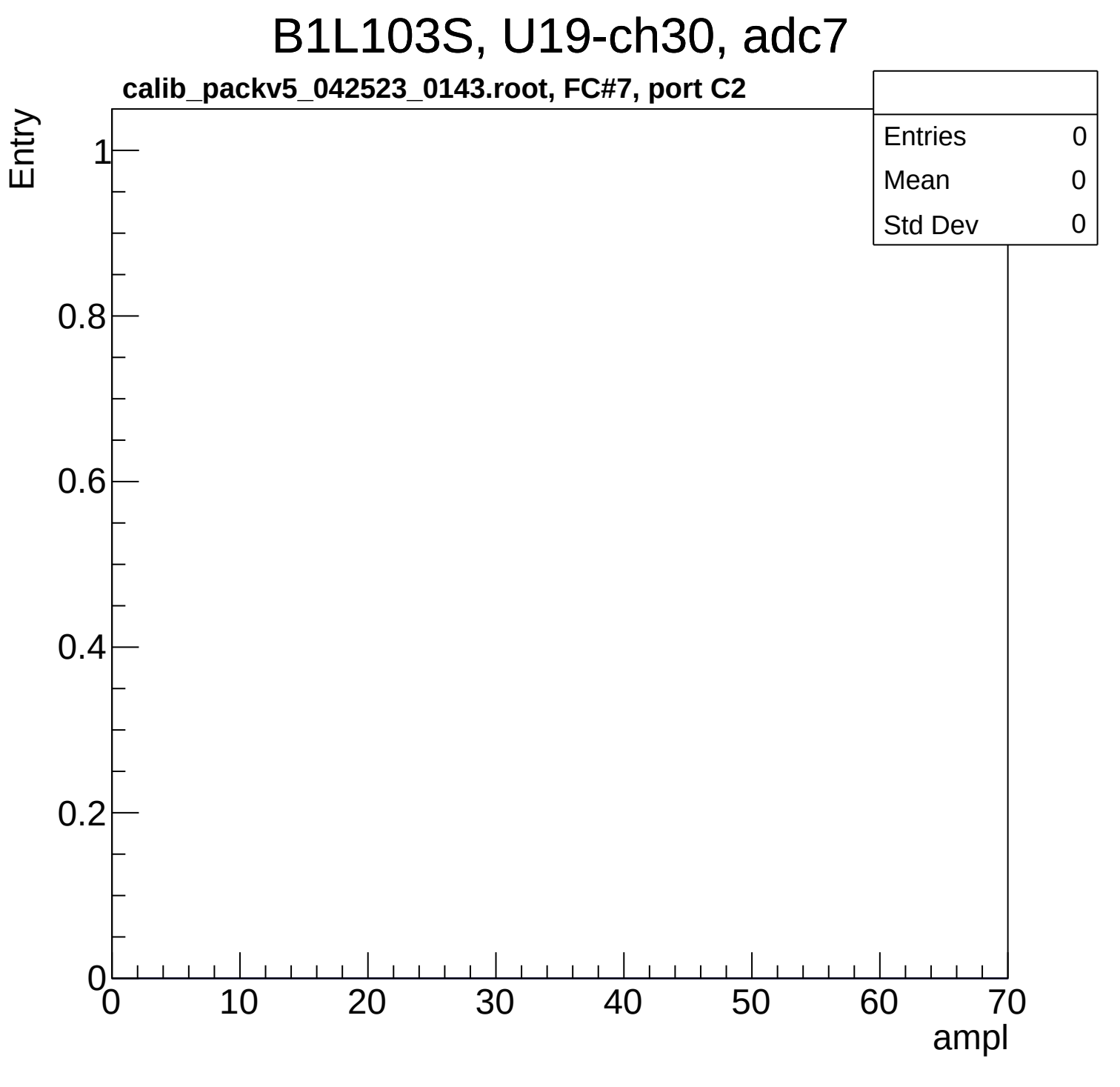
20
30
40
50
60
70


B1L103S, U19-ch30, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
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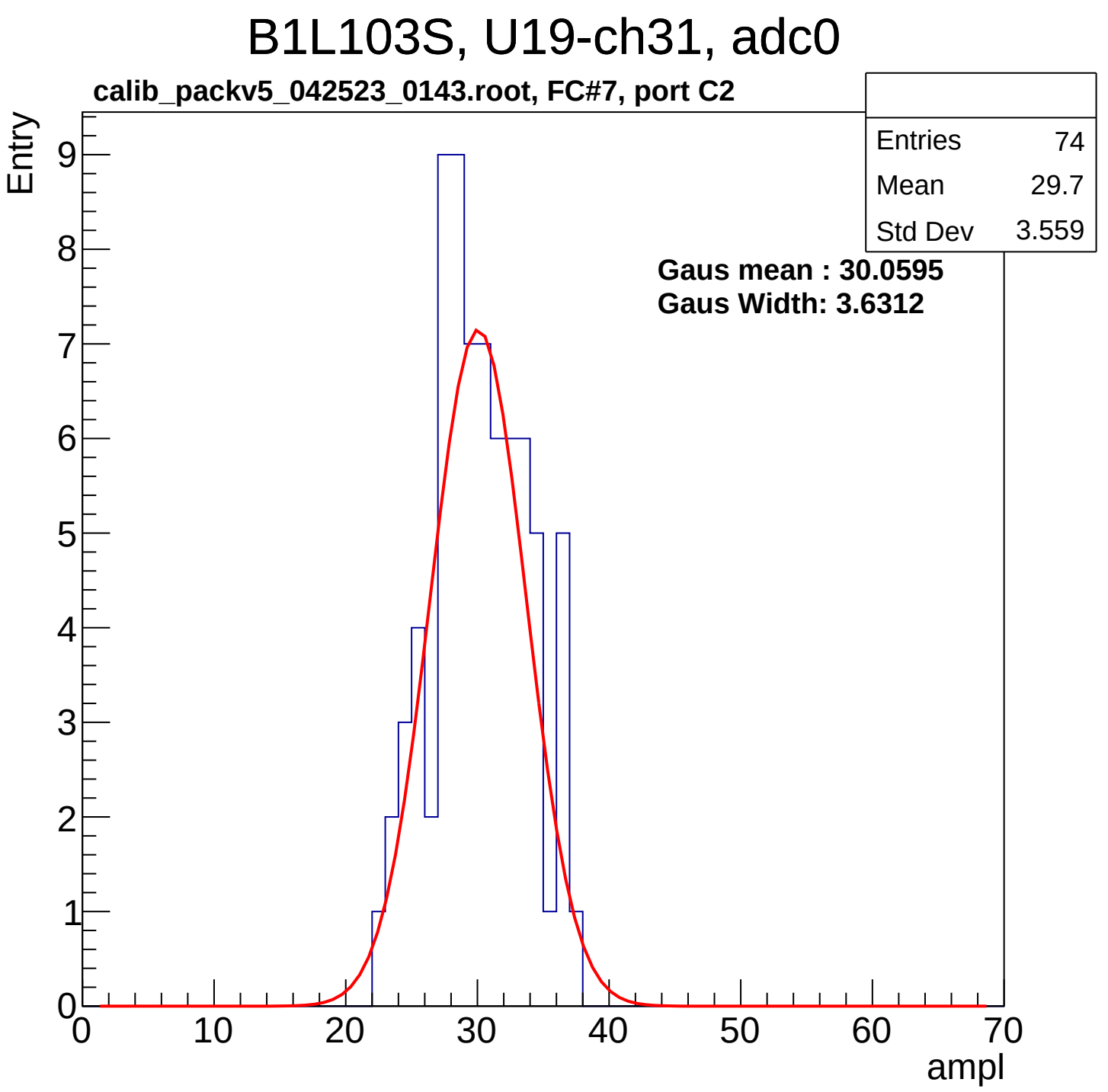
40
50
60
70
ampl


B1L103S, U19-ch31, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.7
Std Dev 3.559
Gaus mean : 30.0595
Gaus Width: 3.6312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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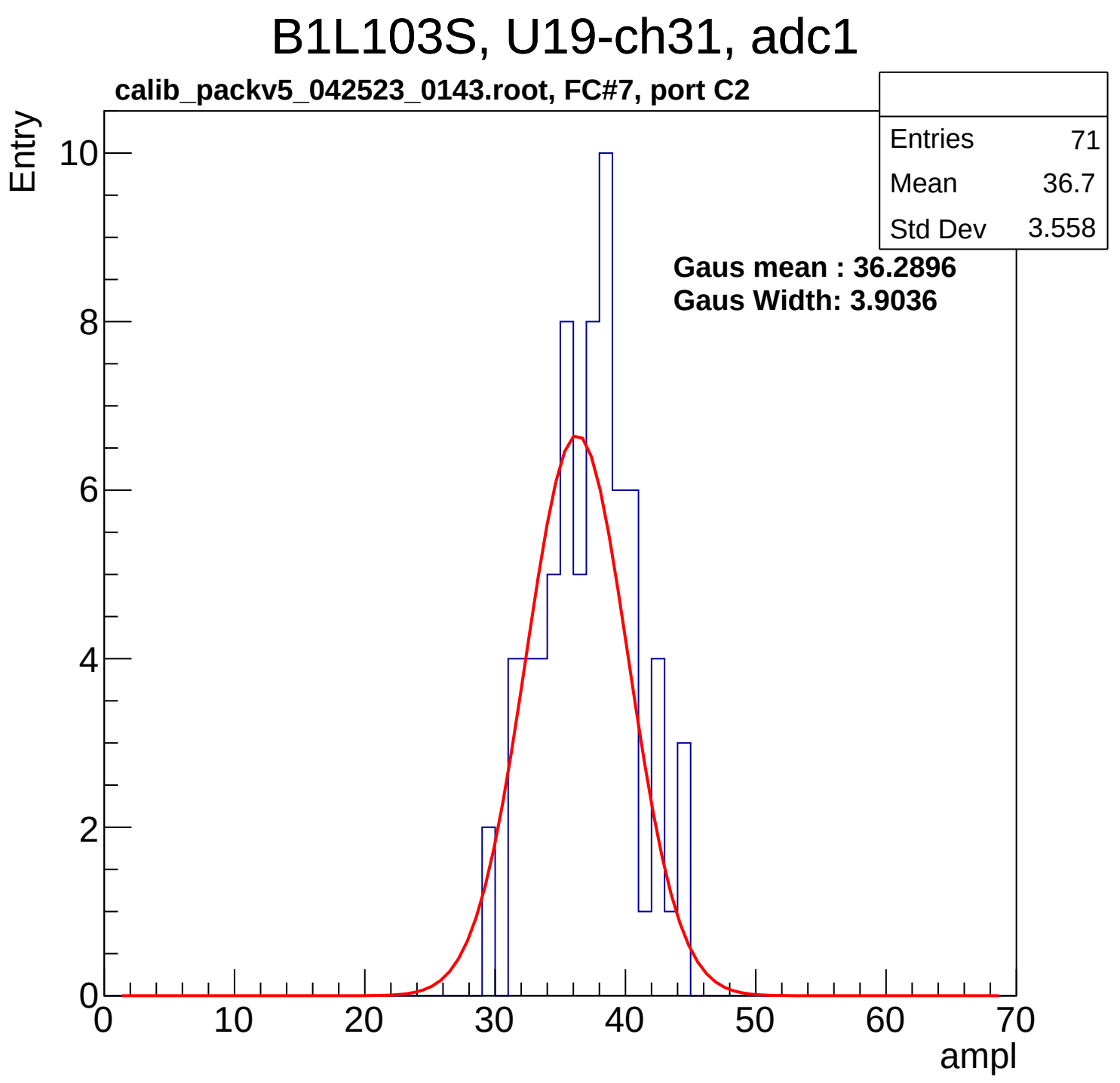
B1L103S, U19-ch31, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.7
Std Dev 3.558
Gaus mean : 36.2896
Gaus Width: 3.9036
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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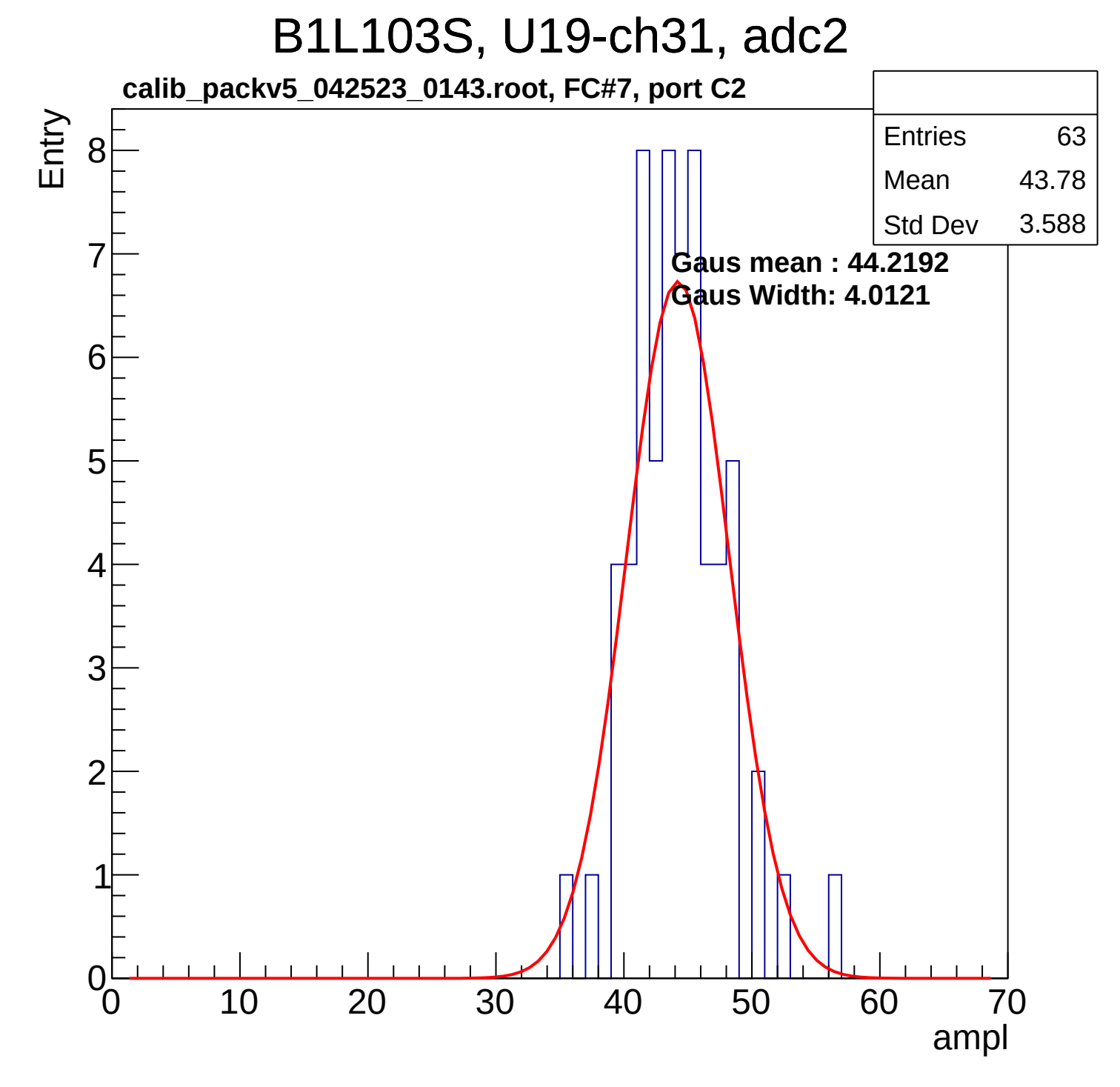
B1L103S, U19-ch31, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.78
Std Dev 3.588
Gaus mean : 44.2192
Gaus Width: 4.0121
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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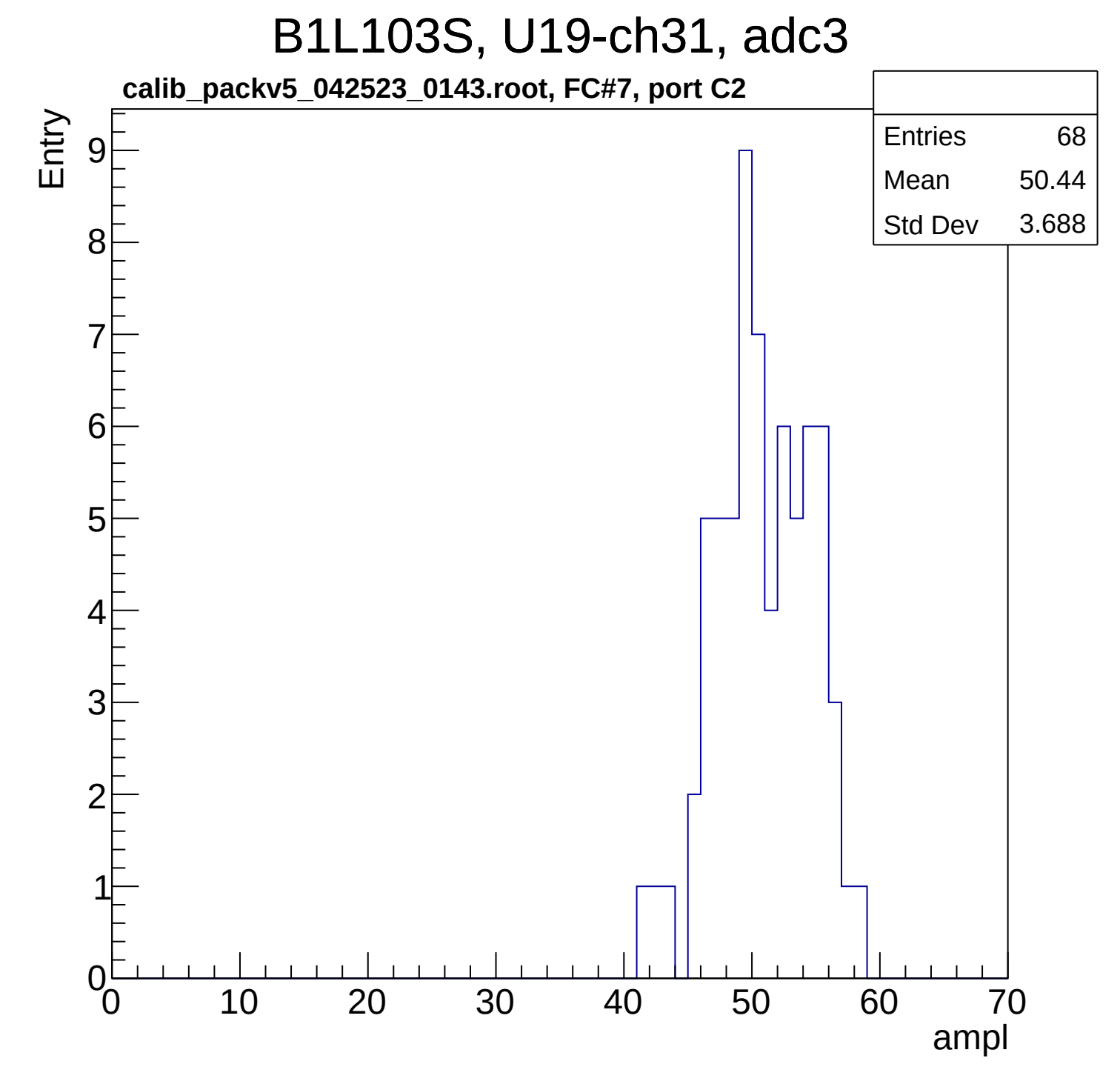
B1L103S, U19-ch31, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 50.44
Std Dev 3.688
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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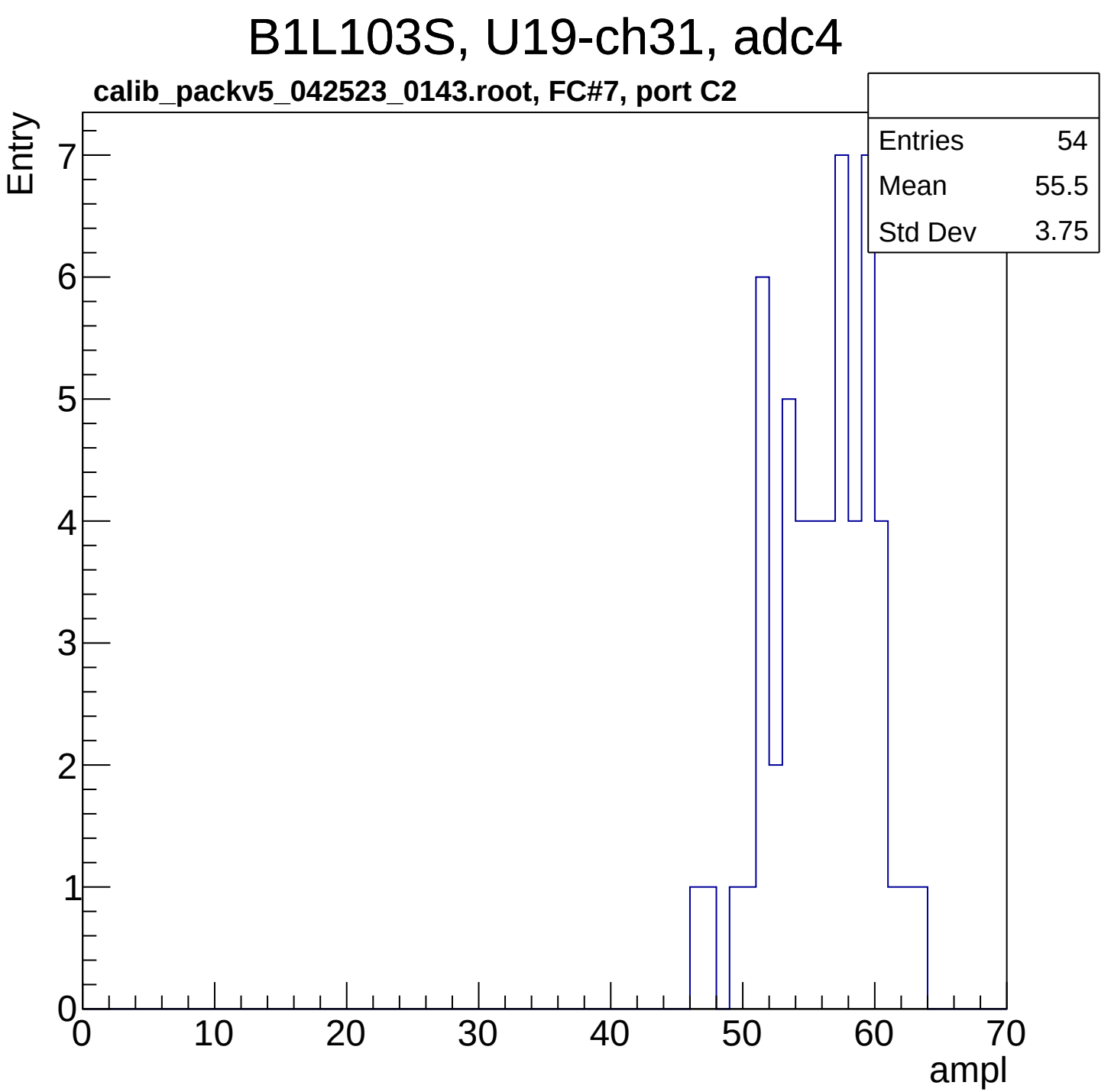
B1L103S, U19-ch31, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.5
Std Dev 3.75
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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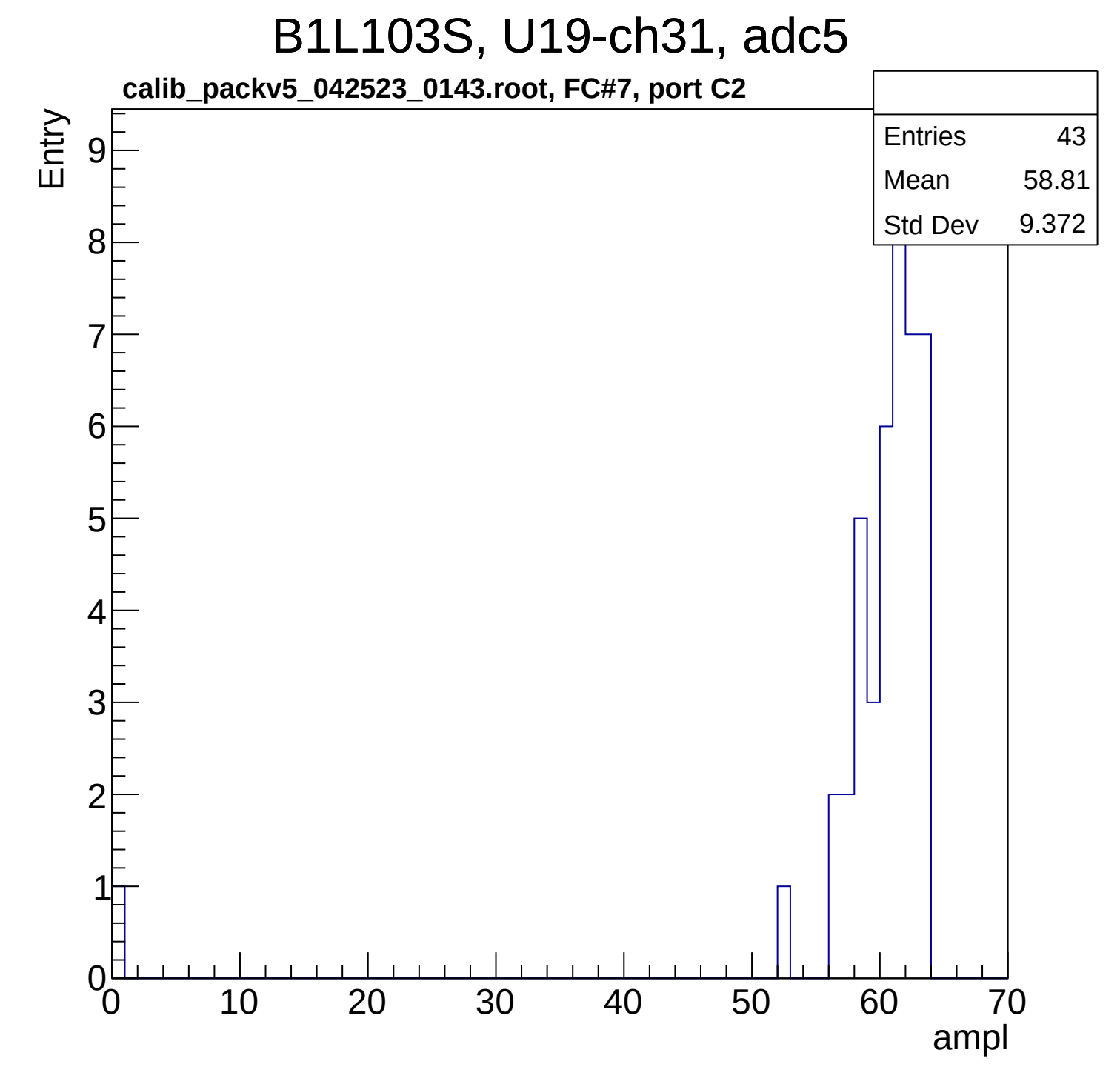
B1L103S, U19-ch31, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 58.81
Std Dev 9.372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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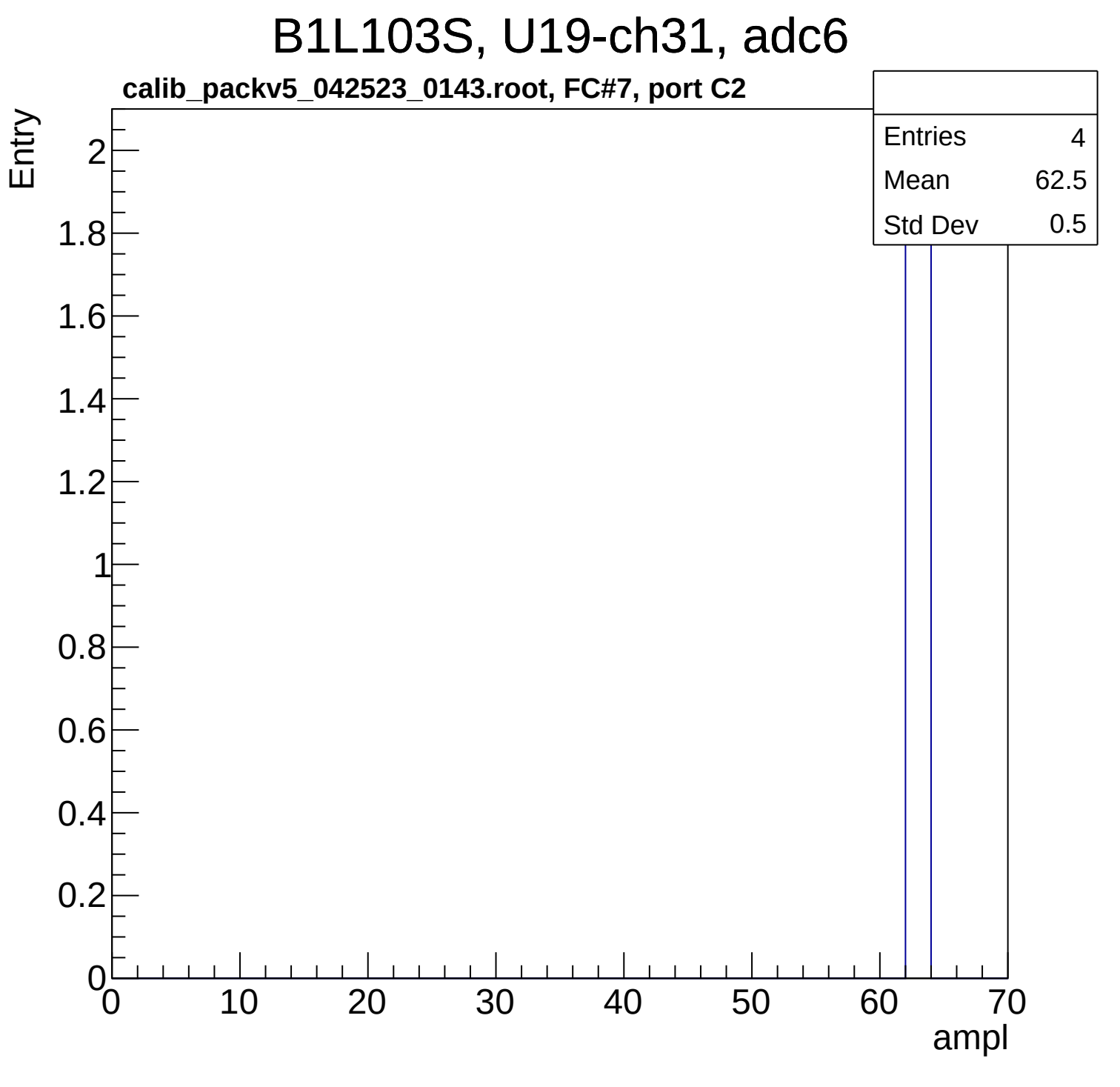
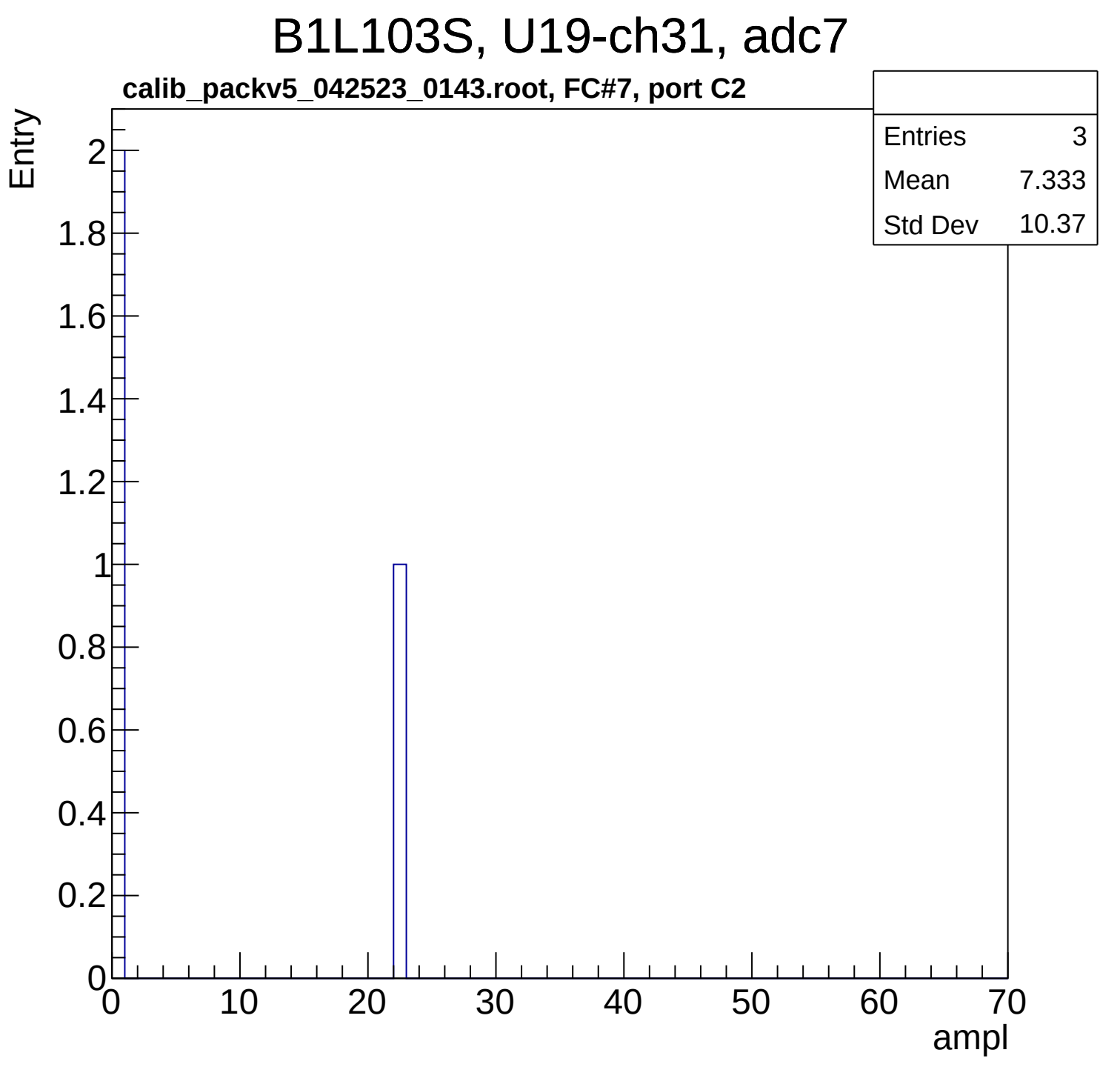

B1L103S, U19-ch31, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch31, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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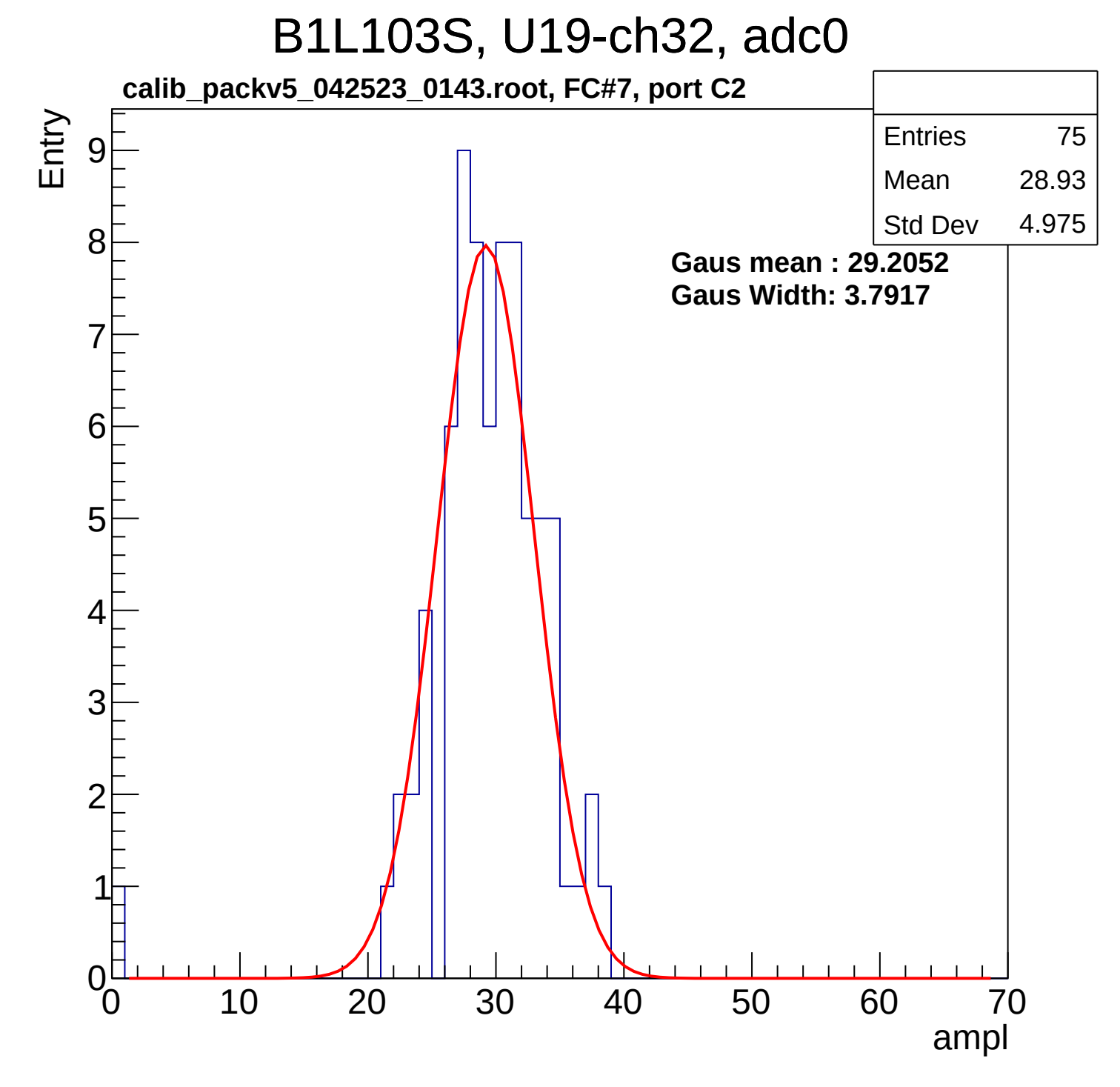
B1L103S, U19-ch32, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 28.93
Std Dev 4.975
Gaus mean : 29.2052
Gaus Width: 3.7917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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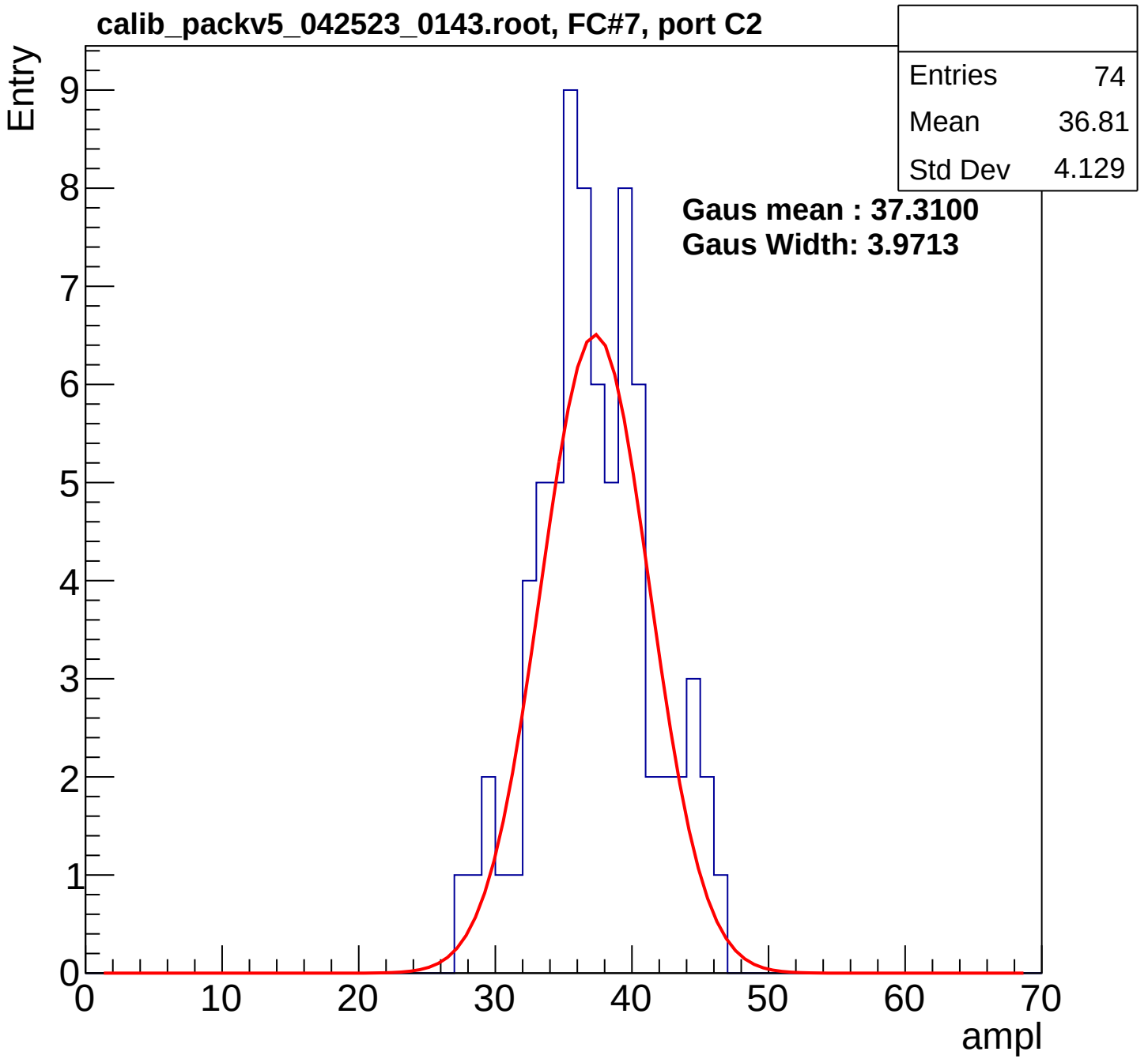
B1L103S, U19-ch32, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 36.81
Std Dev 4.129
Gaus mean : 37.3100
Gaus Width: 3.9713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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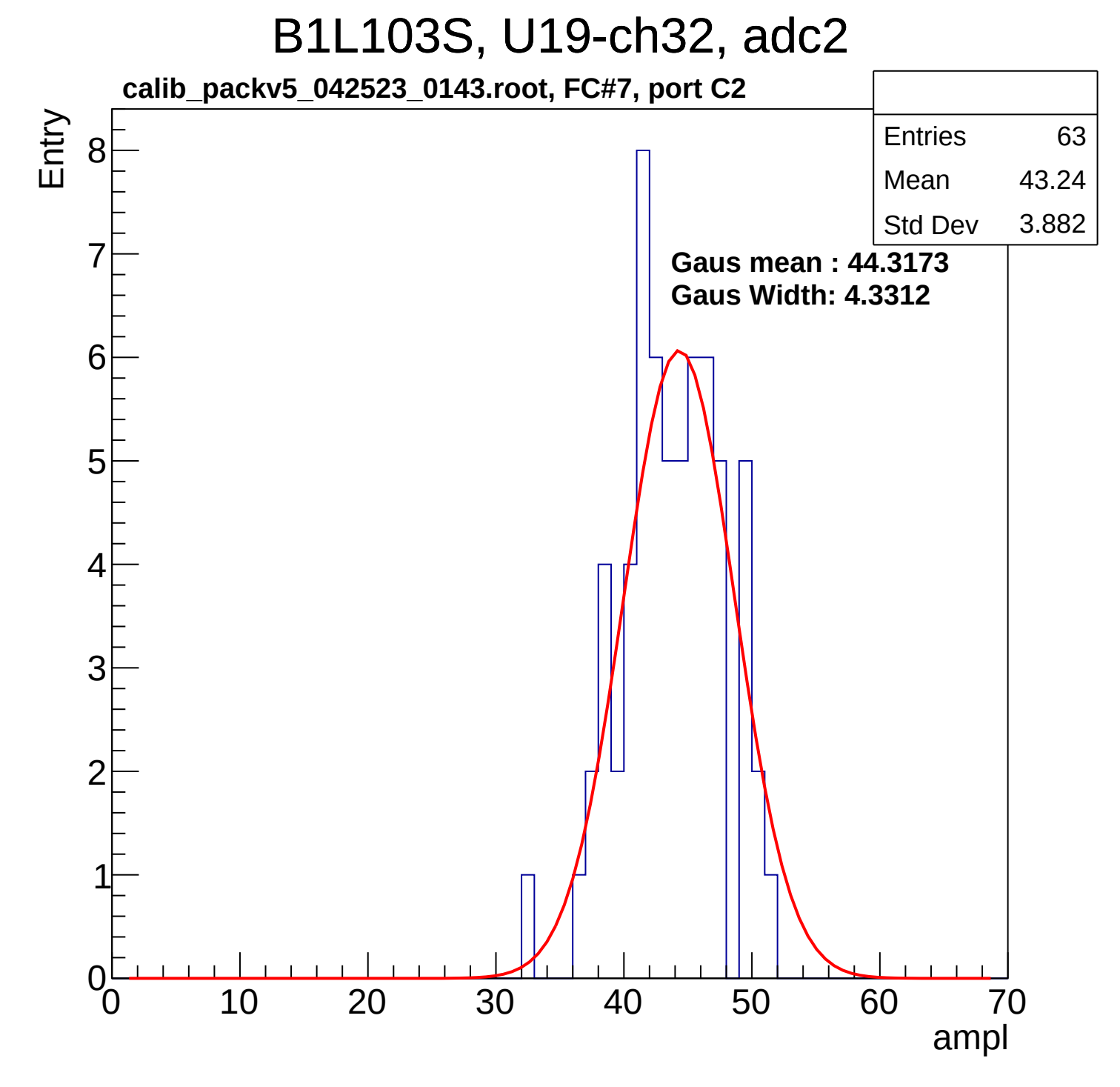
B1L103S, U19-ch32, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.24
Std Dev 3.882
Gaus mean : 44.3173
Gaus Width: 4.3312
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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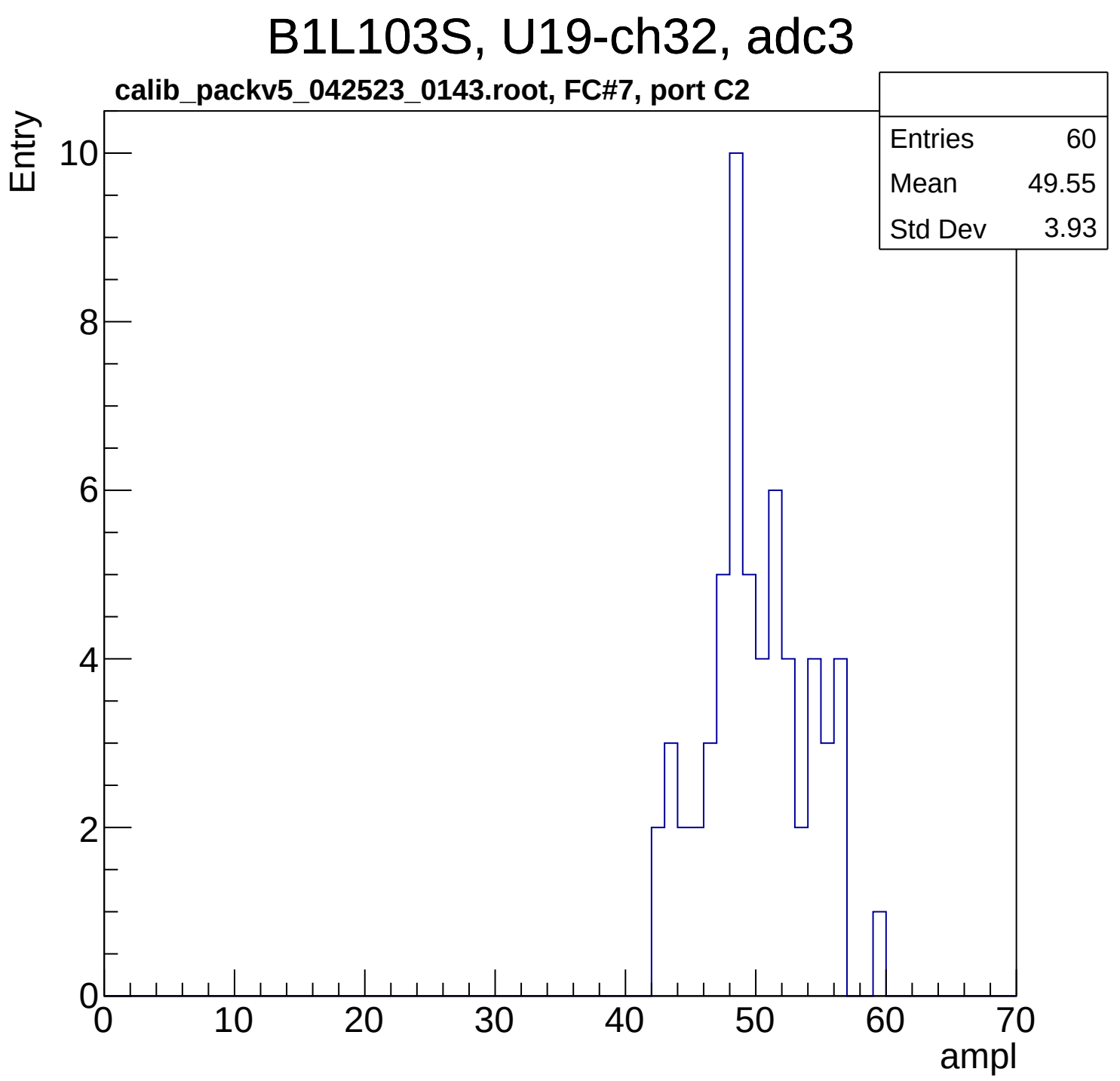
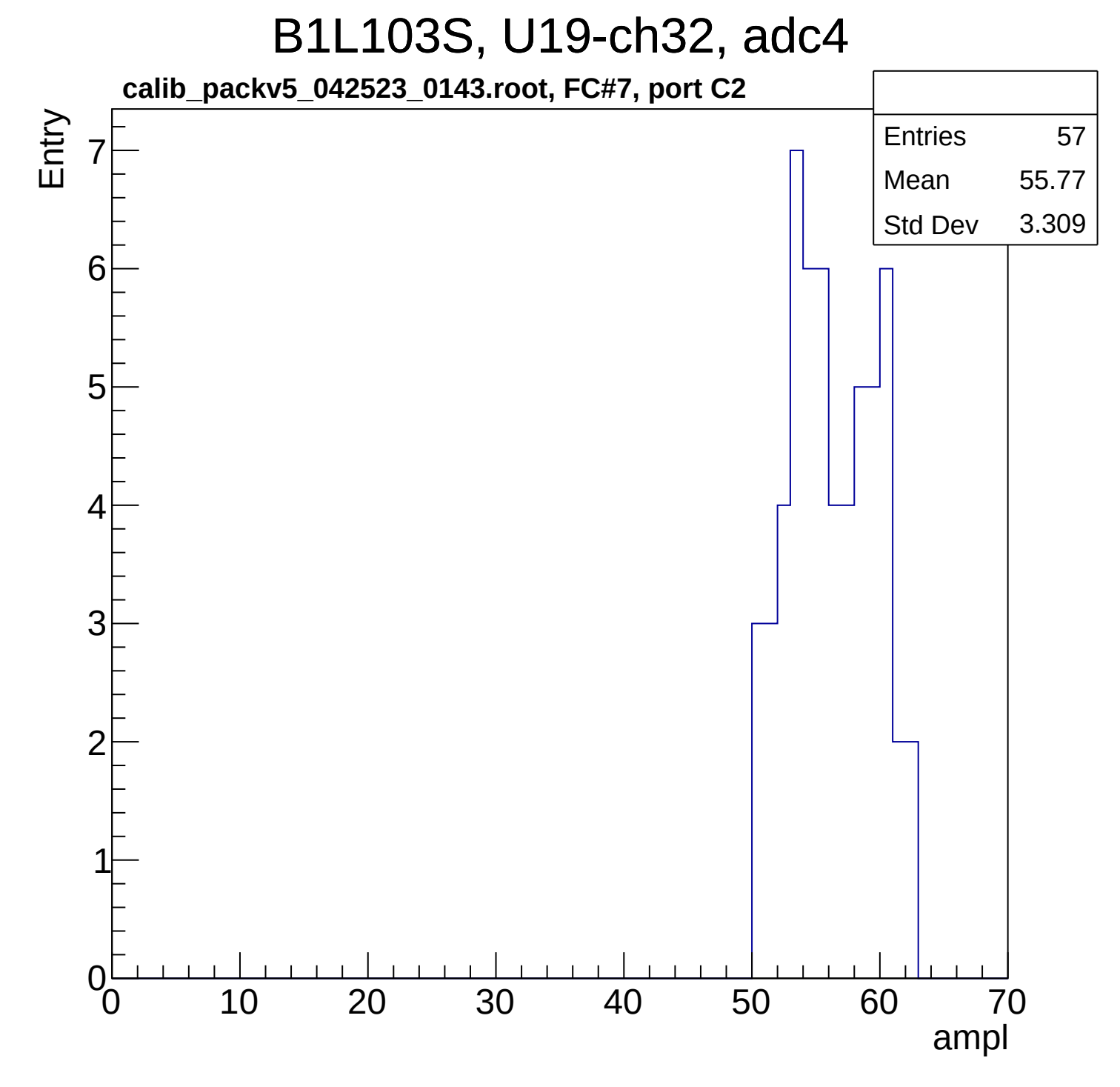

B1L103S, U19-ch32, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.55
Std Dev 3.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch32, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.77
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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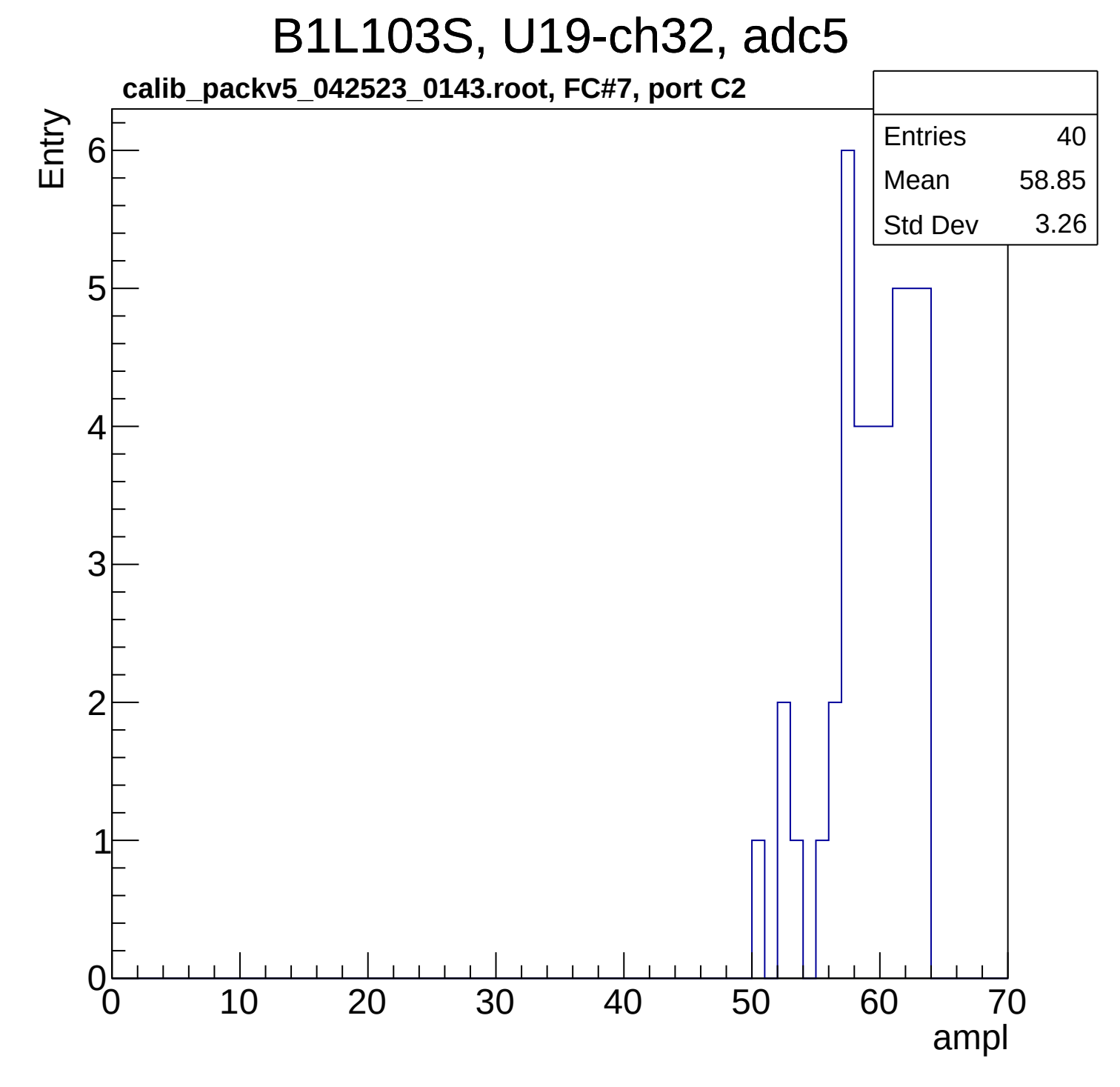
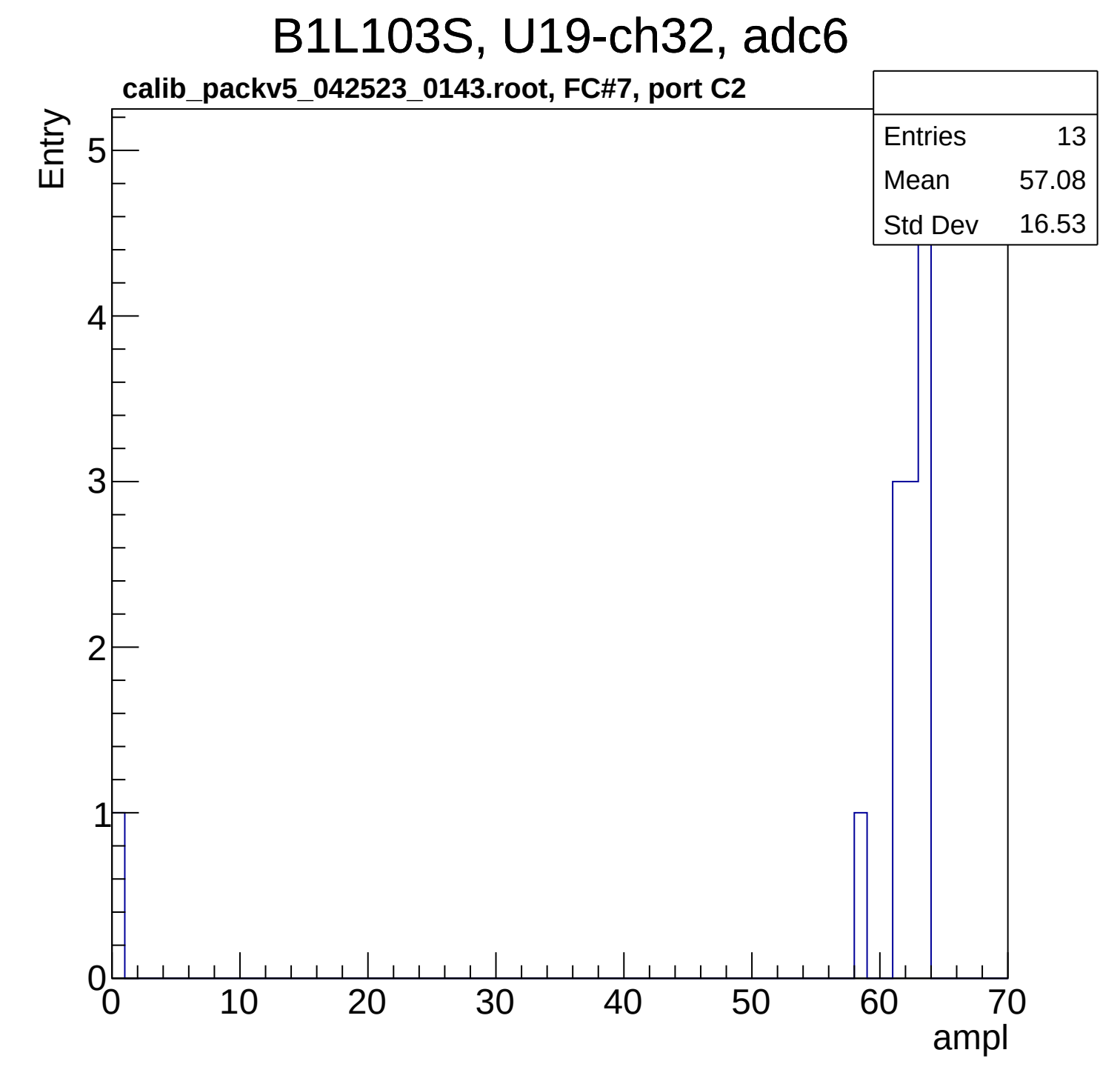
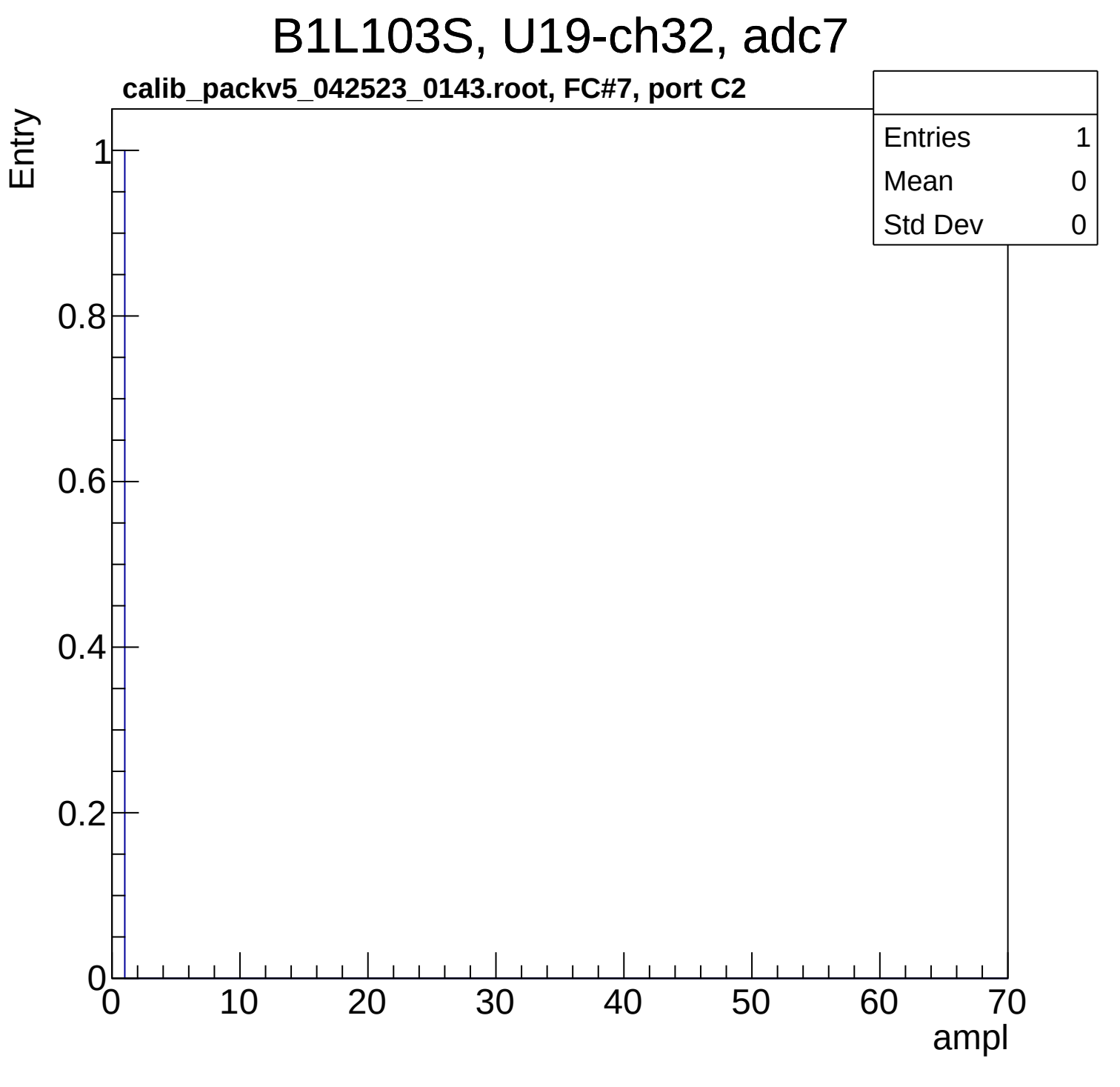

B1L103S, U19-ch32, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.85
Std Dev 3.26
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch32, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 57.08
Std Dev 16.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch32, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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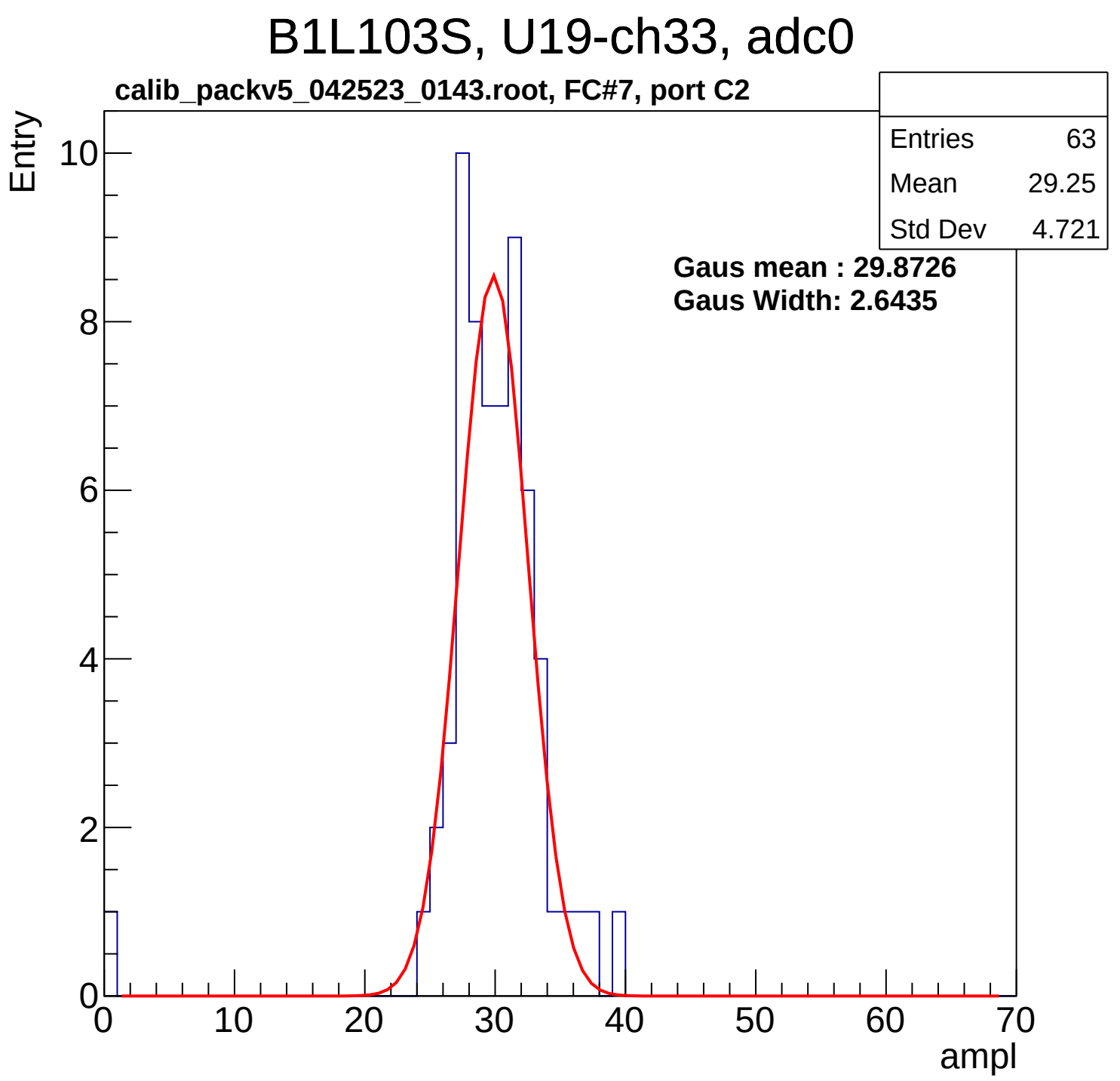
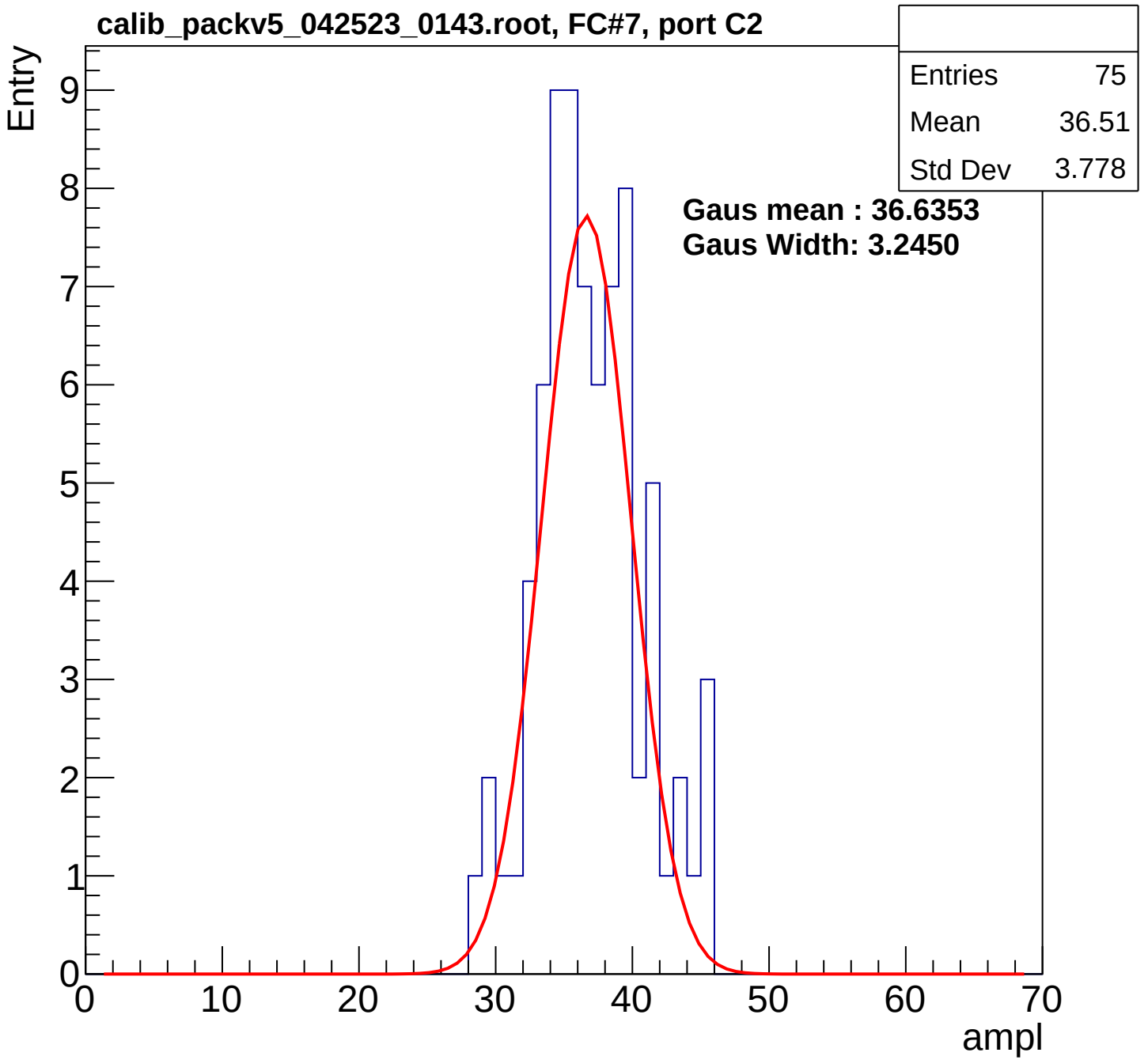
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch33, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.25
Std Dev 4.721
Gaus mean : 29.8726
Gaus Width: 2.6435
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch33, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 36.51
Std Dev 3.778
Gaus mean : 36.6353
Gaus Width: 3.2450
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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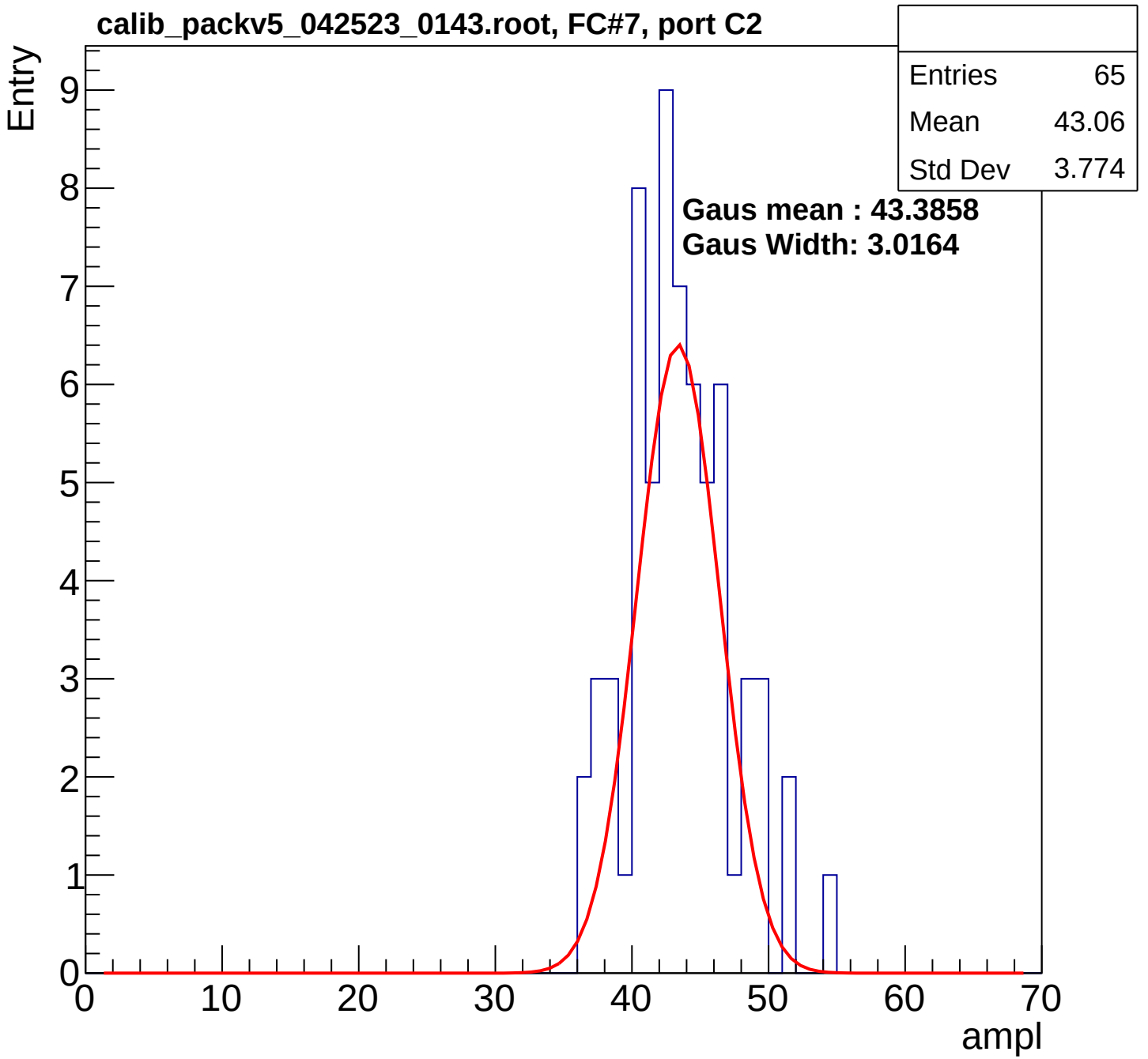
B1L103S, U19-ch33, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.06
Std Dev 3.774
Gaus mean : 43.3858
Gaus Width: 3.0164
Entry
ampl
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2
3
4
5
6
7
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0
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60
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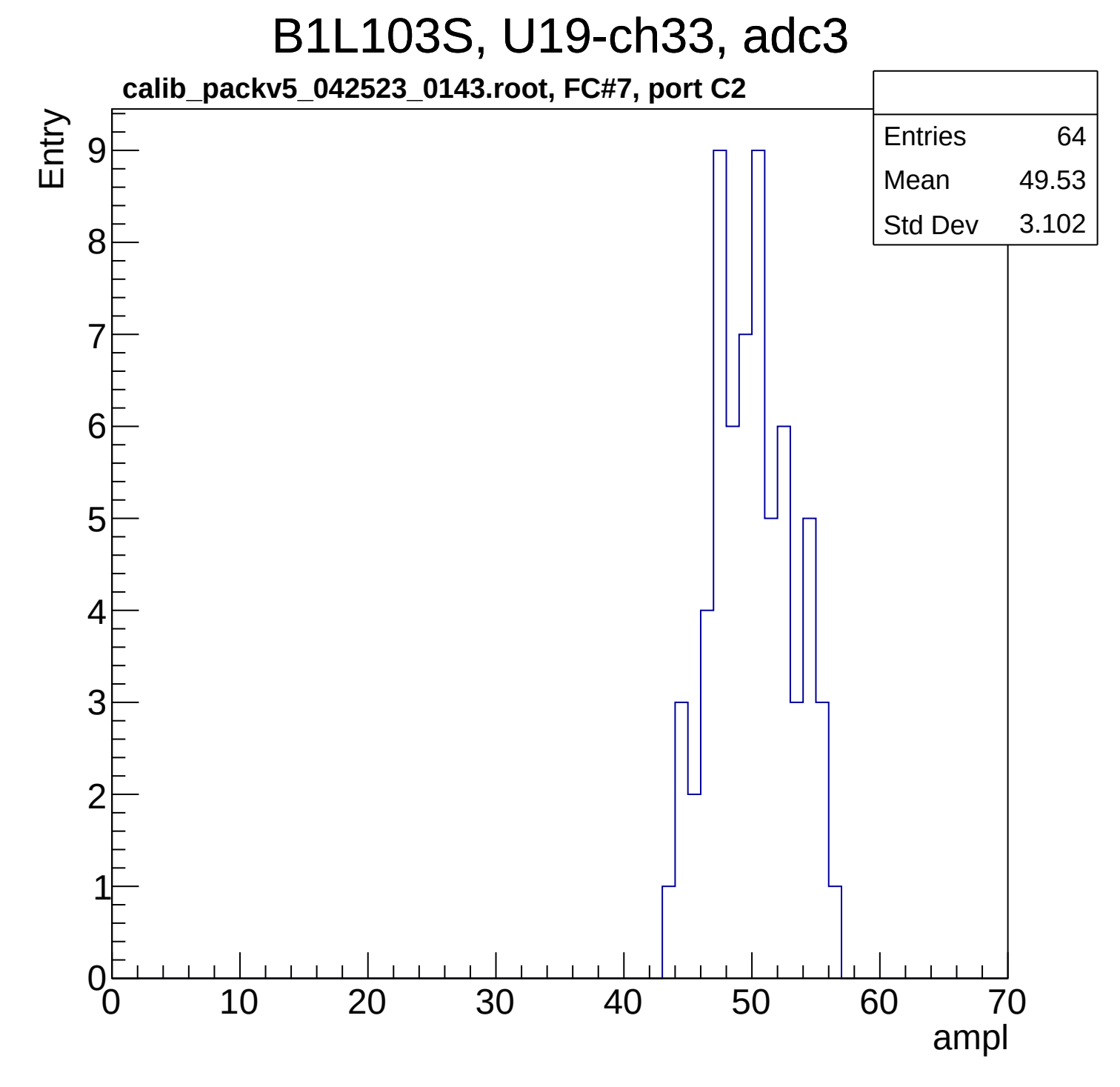
B1L103S, U19-ch33, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.53
Std Dev 3.102
Entry
ampl
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60
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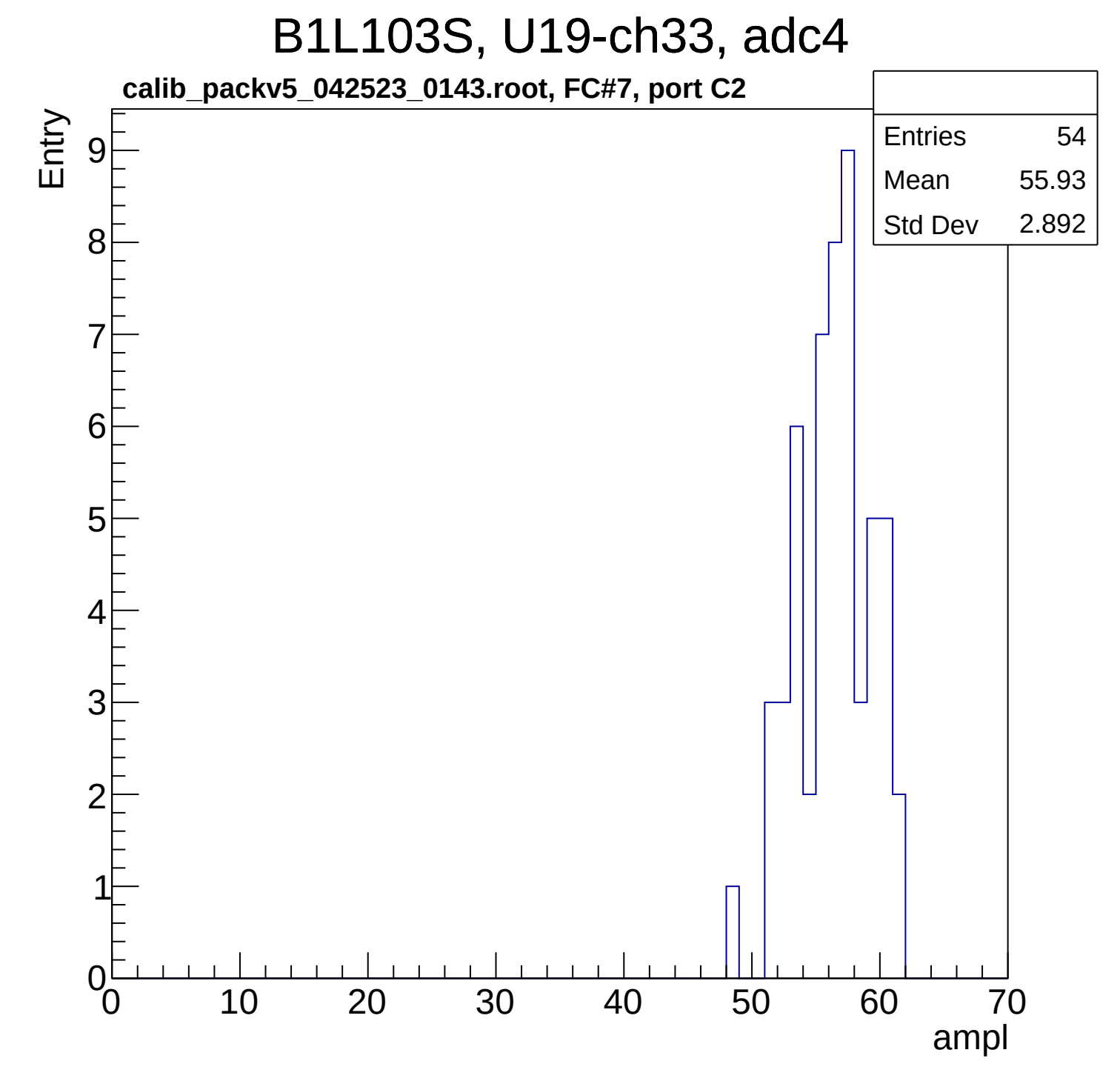
B1L103S, U19-ch33, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.93
Std Dev 2.892
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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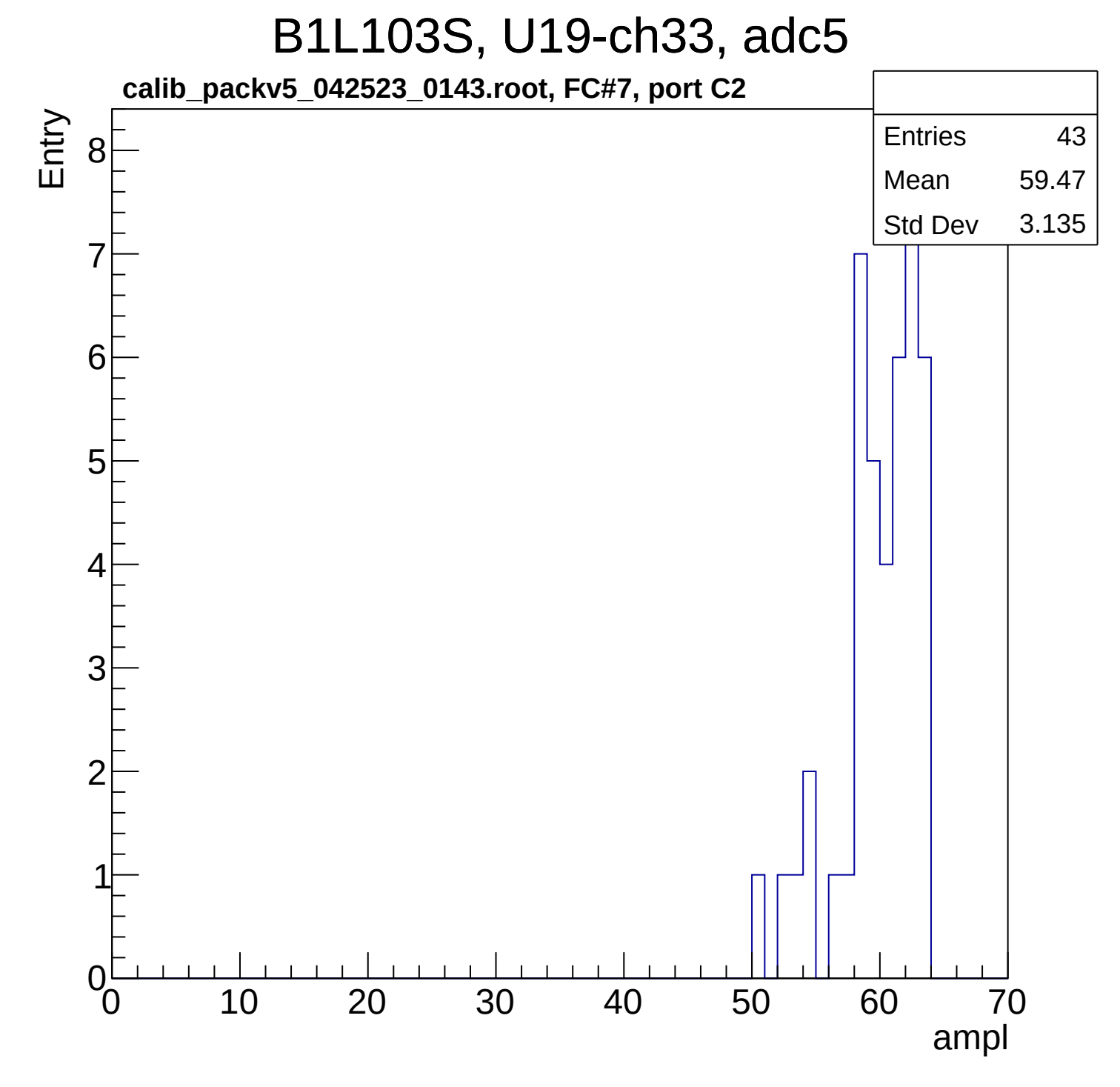
B1L103S, U19-ch33, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.47
Std Dev 3.135
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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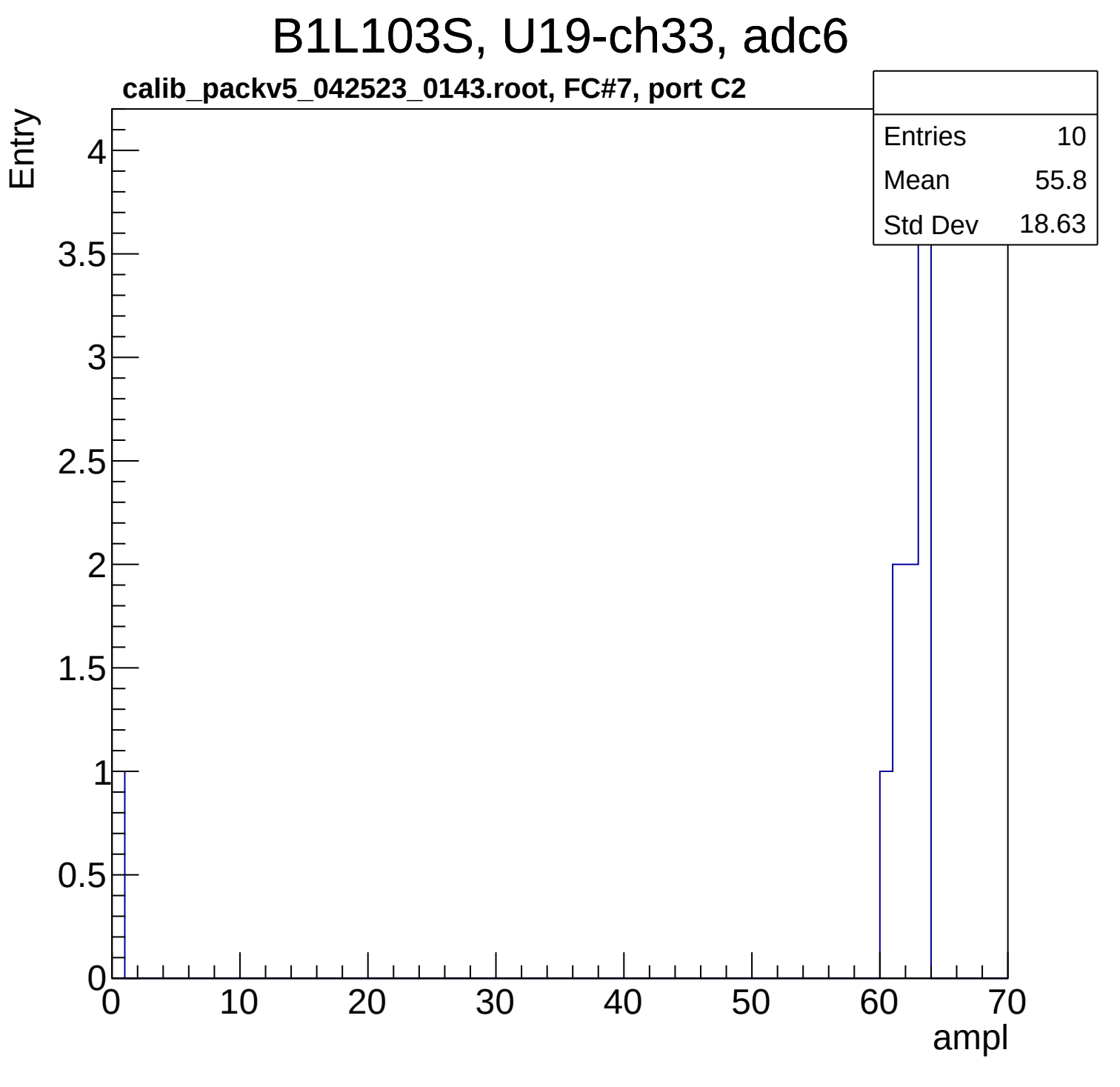
40
50
60
70


B1L103S, U19-ch33, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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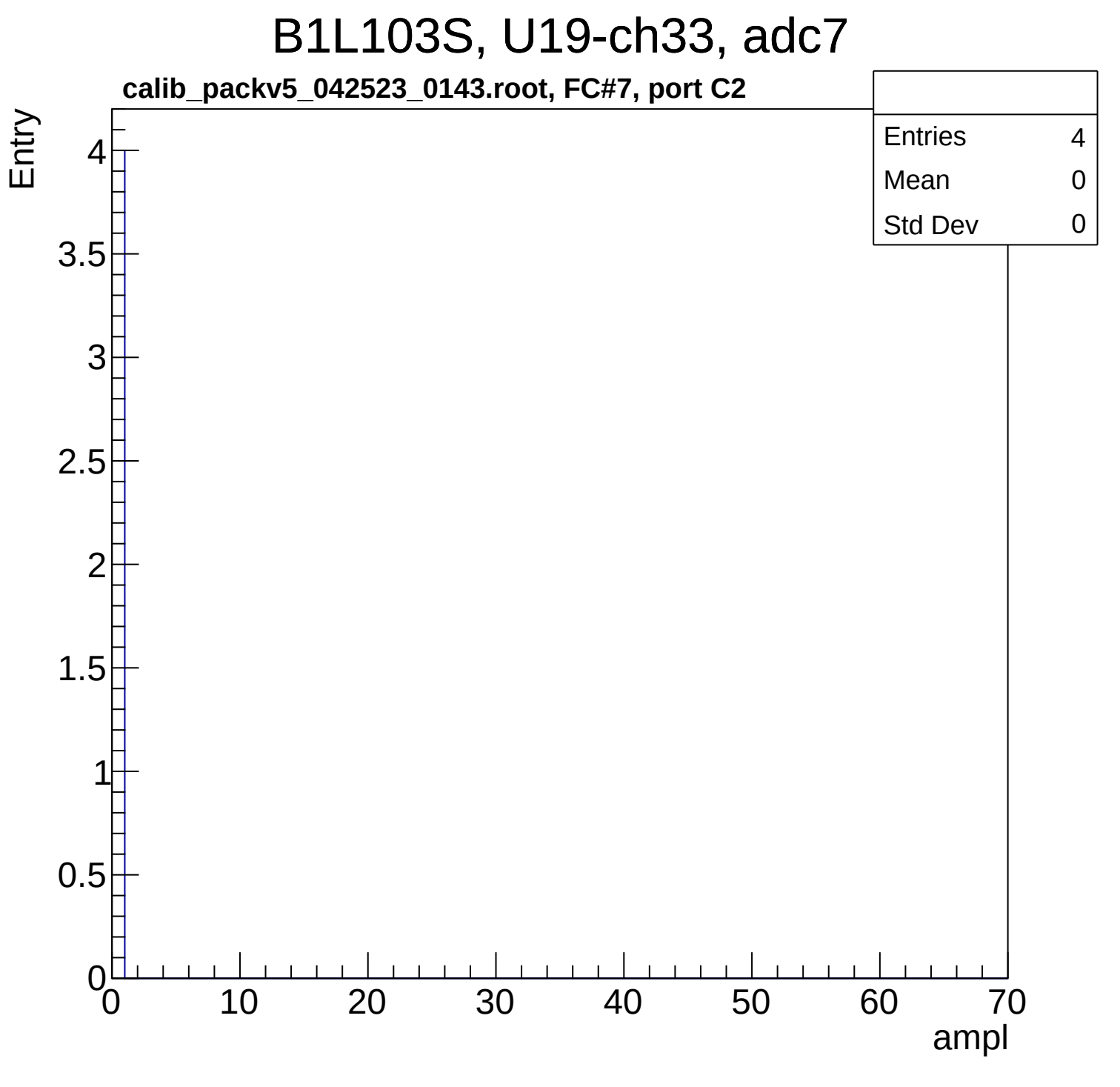
40
50
60
70


B1L103S, U19-ch33, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 0
Std Dev 0
Entry
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0.5
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2
2.5
3
3.5
4
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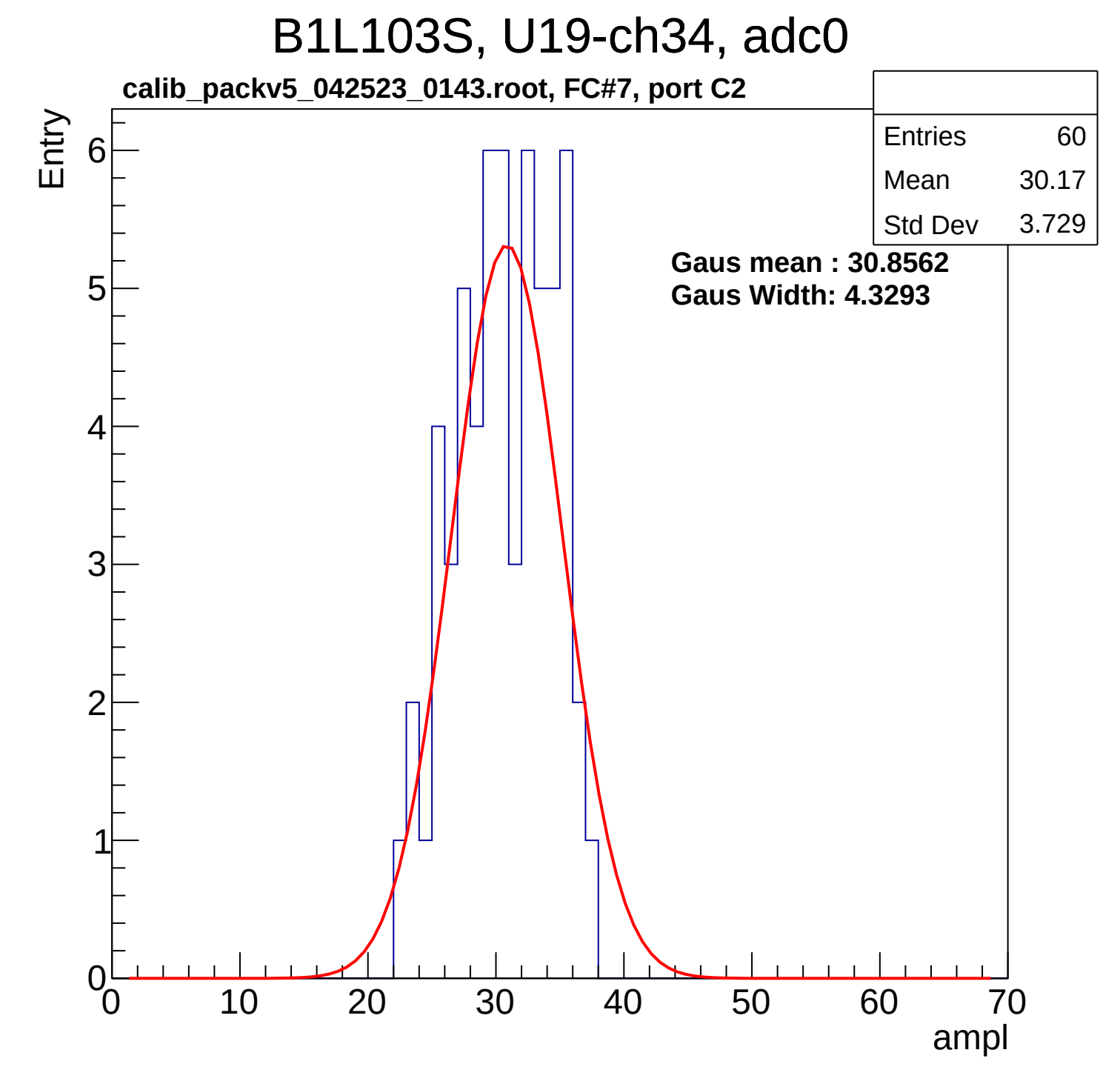
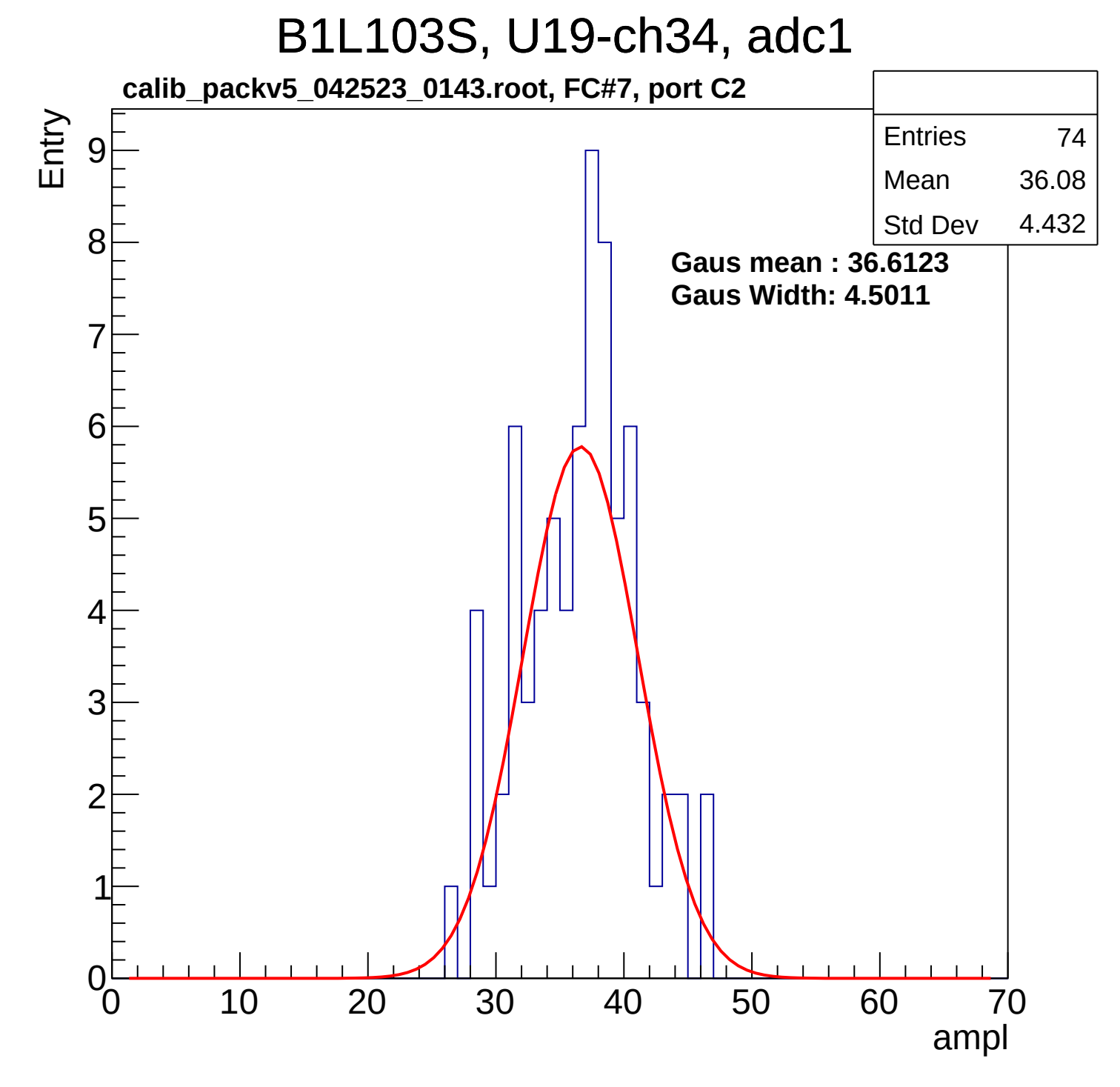
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch34, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 30.17
Std Dev 3.729
Gaus mean : 30.8562
Gaus Width: 4.3293
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch34, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 36.08
Std Dev 4.432
Gaus mean : 36.6123
Gaus Width: 4.5011
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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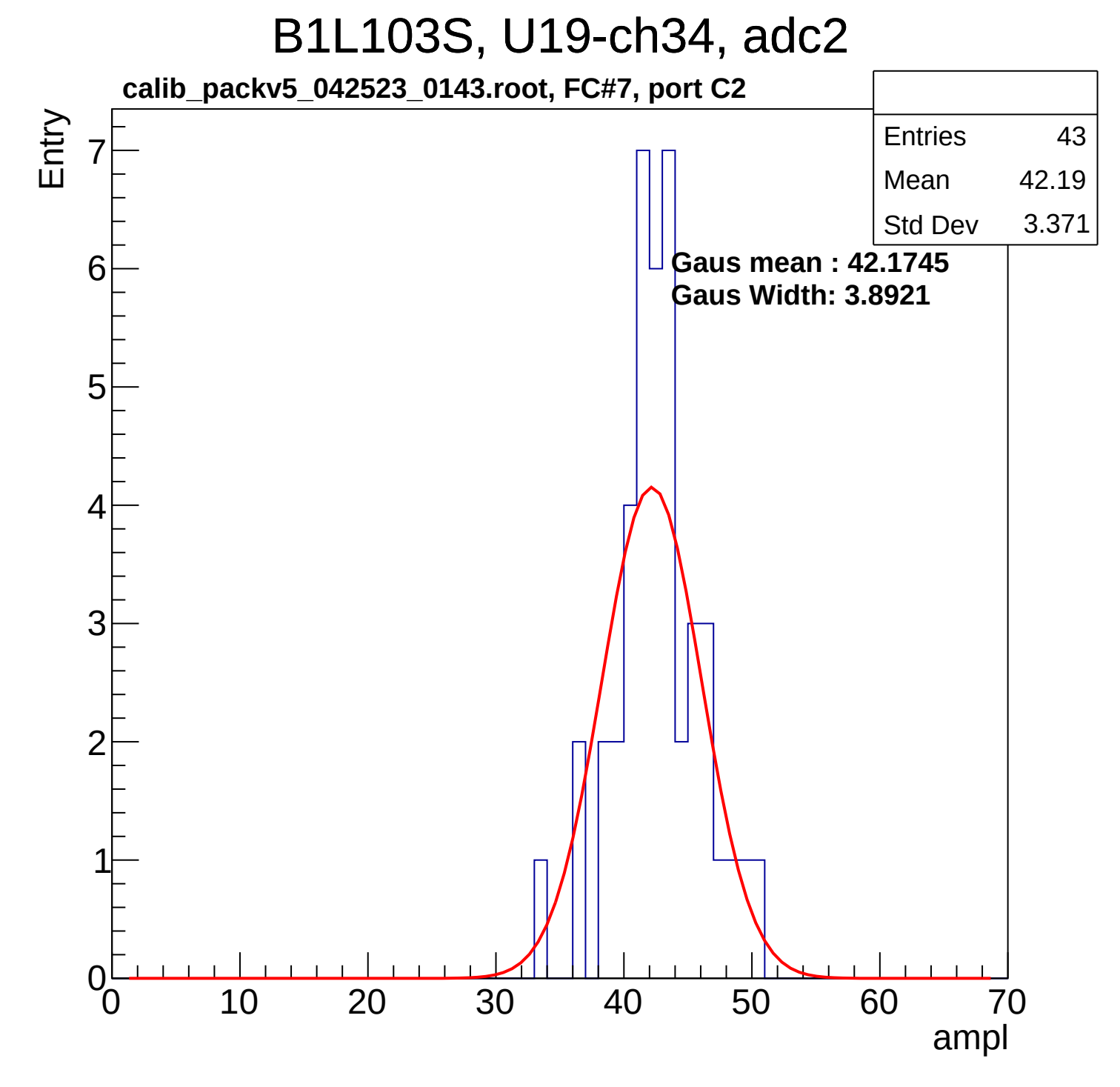
B1L103S, U19-ch34, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 42.19
Std Dev 3.371
Gaus mean : 42.1745
Gaus Width: 3.8921
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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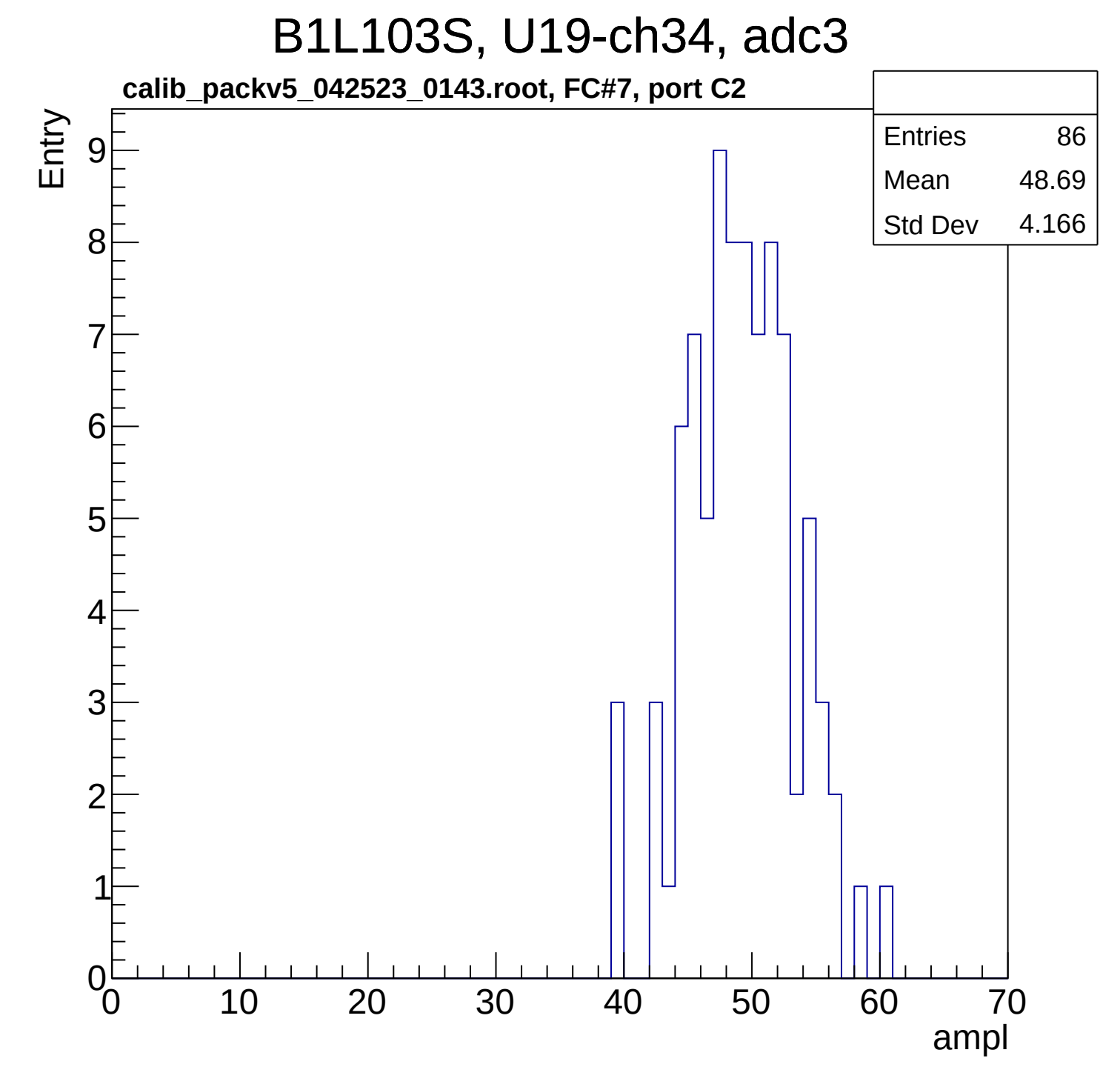
B1L103S, U19-ch34, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 48.69
Std Dev 4.166
Entry
ampl
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3
4
5
6
7
8
9
0
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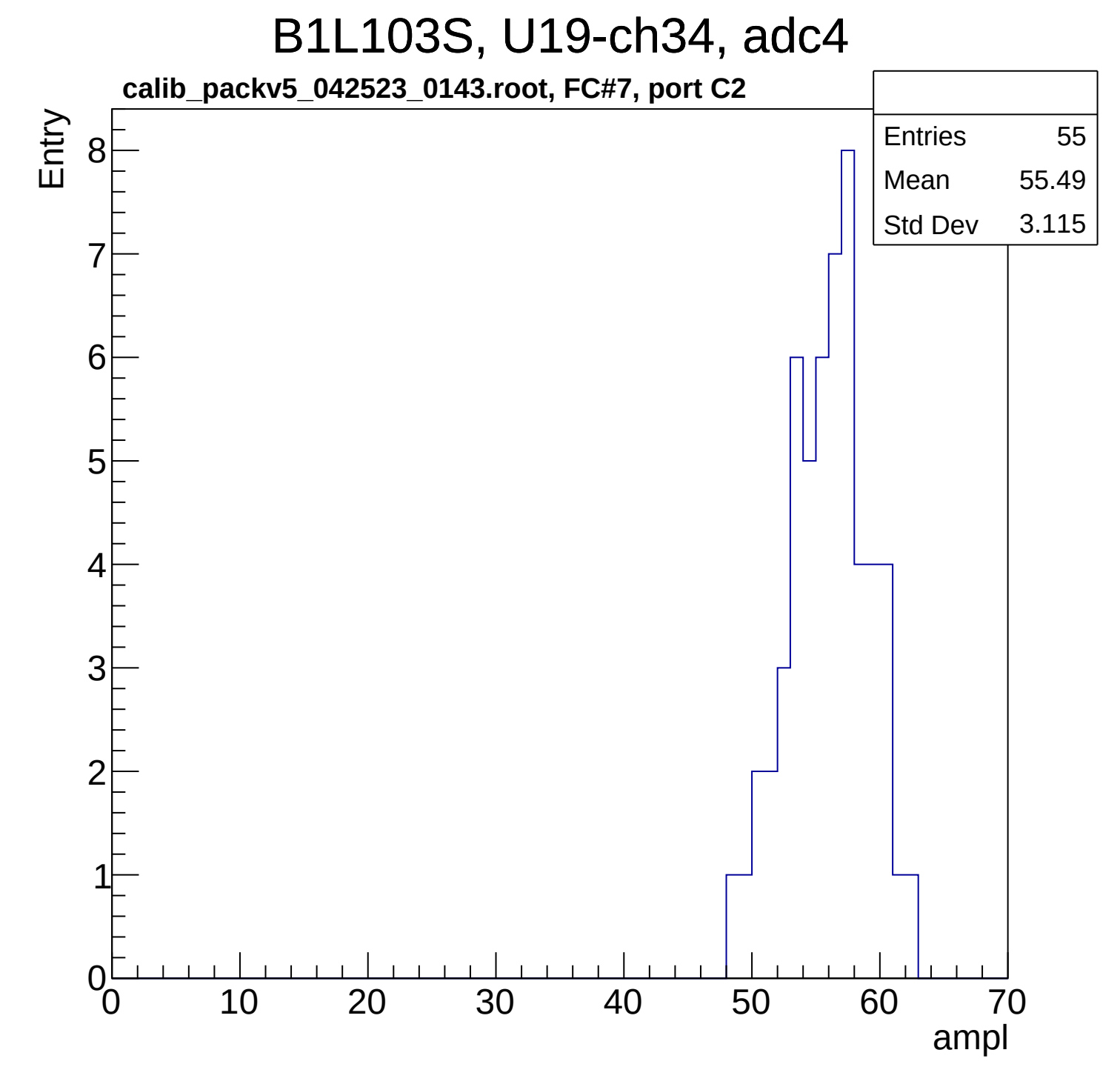
B1L103S, U19-ch34, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.49
Std Dev 3.115
Entry
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10
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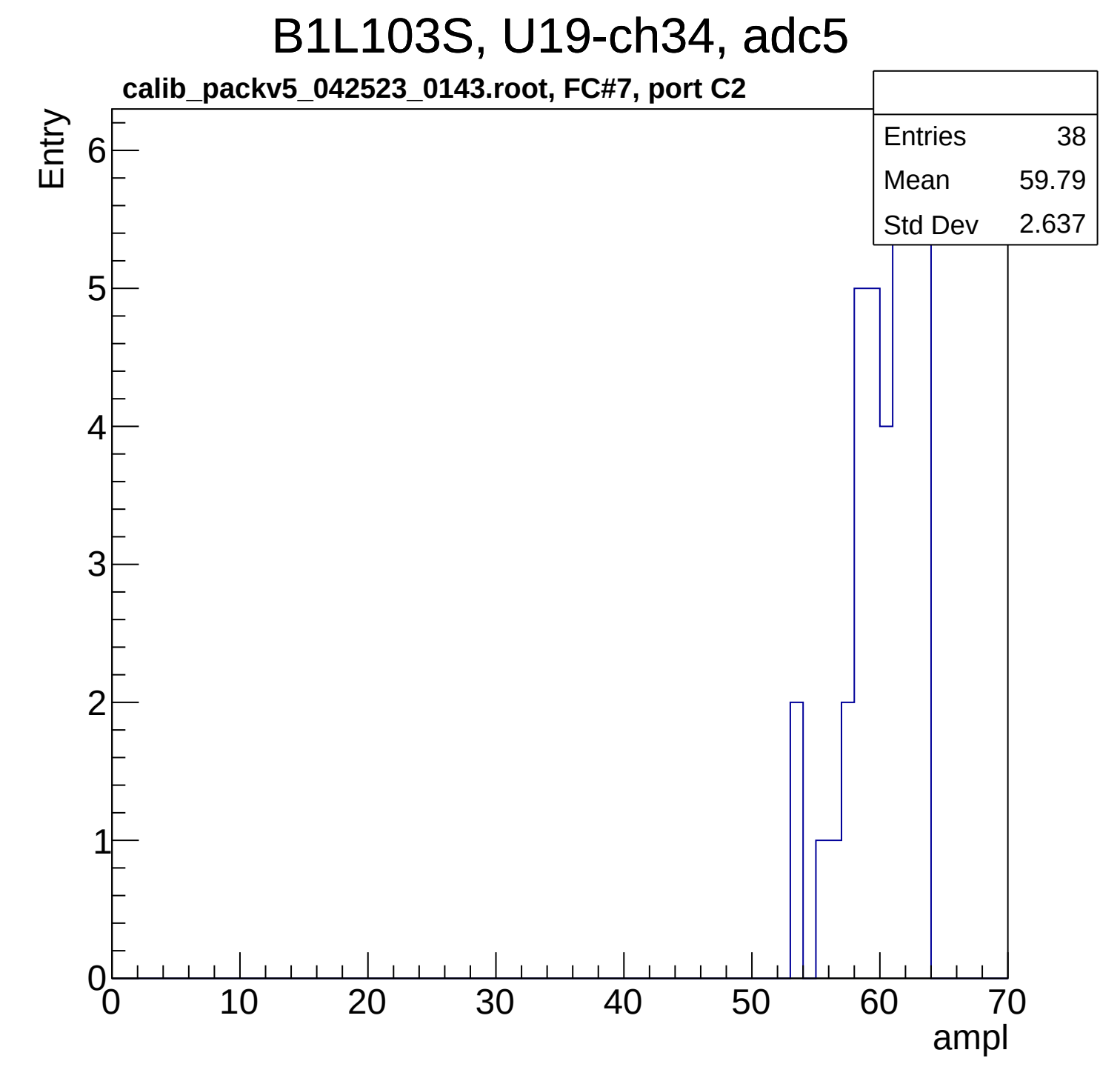
50
60
70
ampl


B1L103S, U19-ch34, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 59.79
Std Dev 2.637
Entry
ampl
0
1
2
3
4
5
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0
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20
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40
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60
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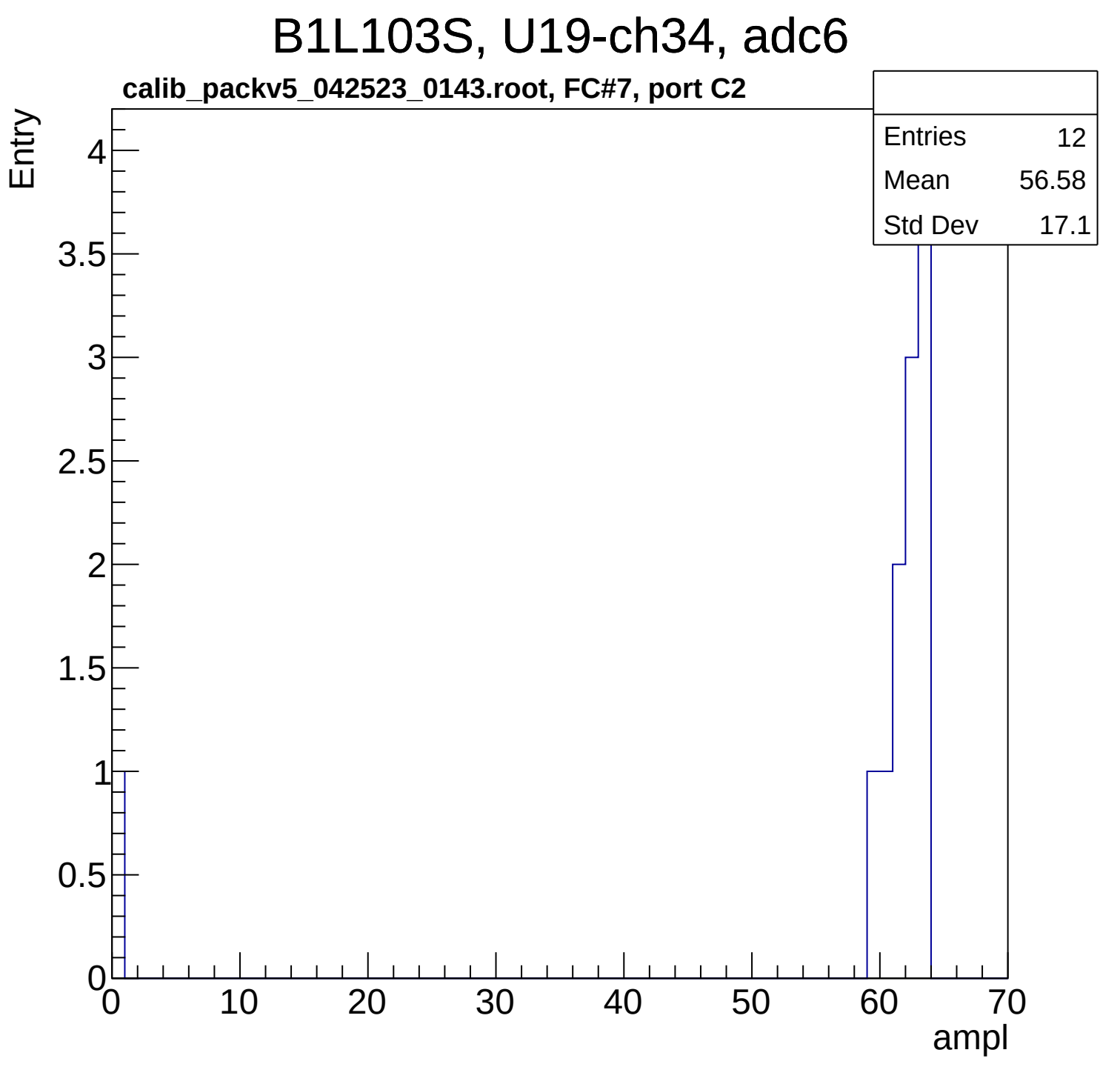
B1L103S, U19-ch34, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.58
Std Dev 17.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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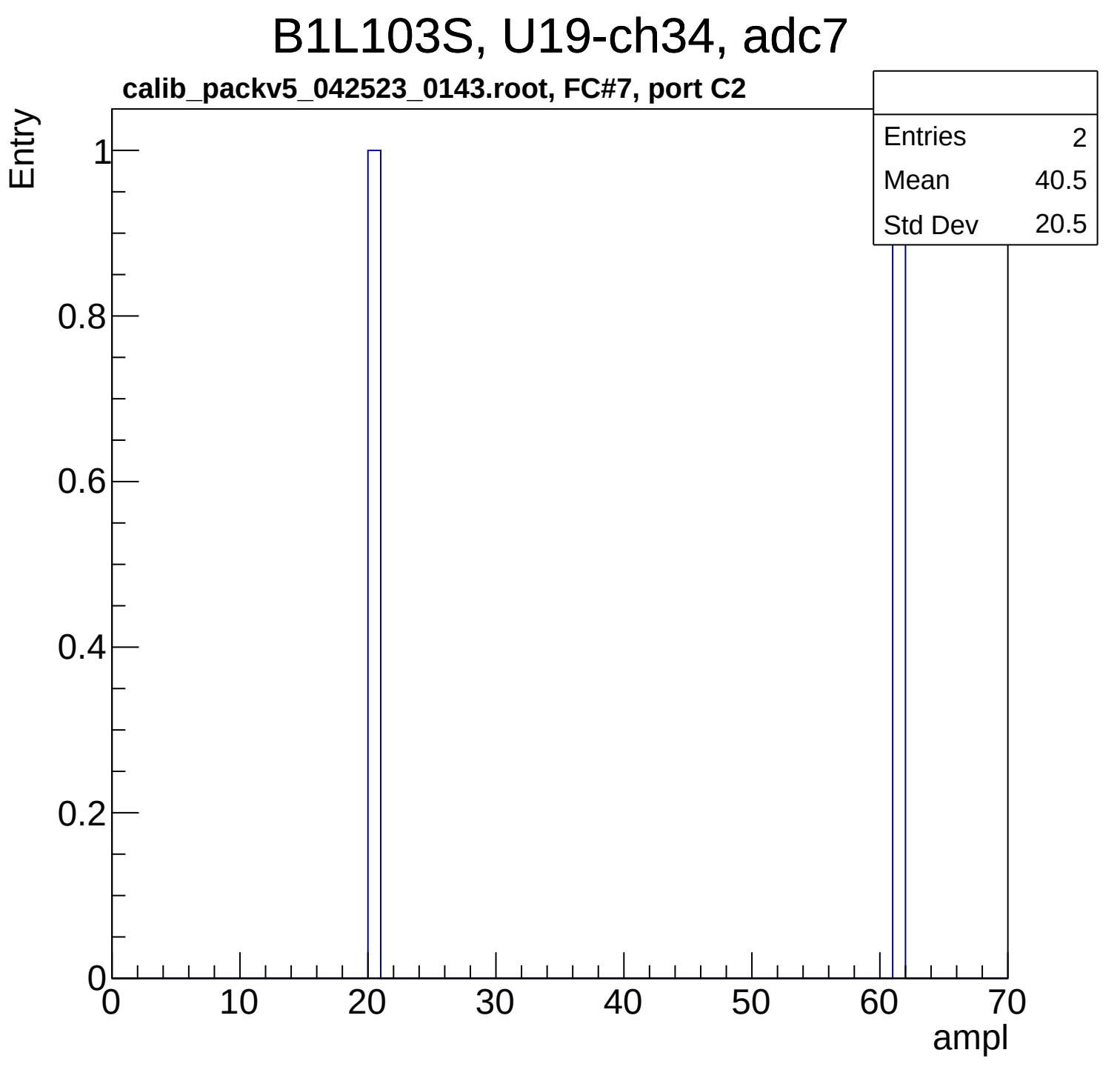
B1L103S, U19-ch34, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 40.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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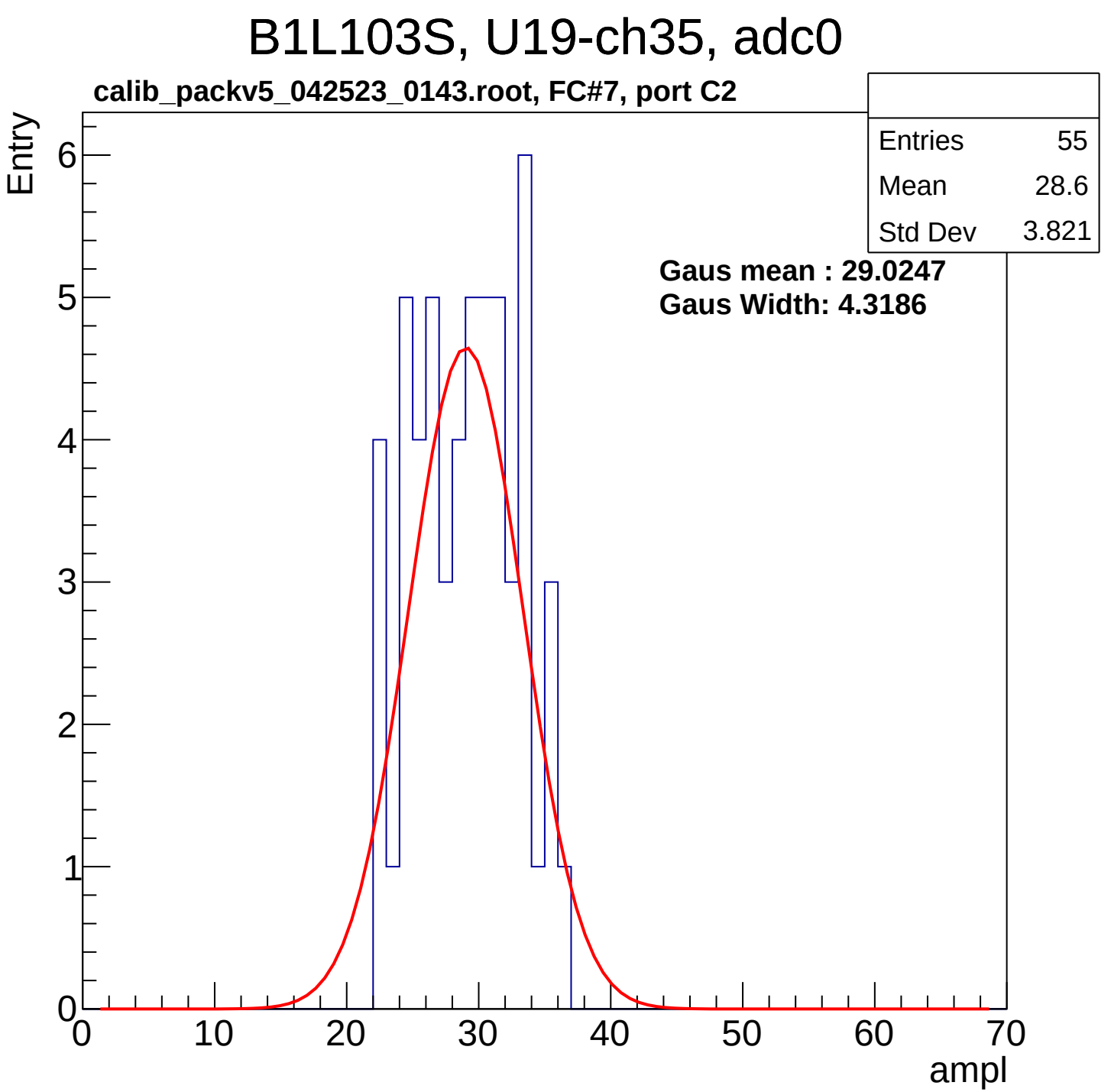
B1L103S, U19-ch35, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 28.6
Std Dev 3.821
Gaus mean : 29.0247
Gaus Width: 4.3186
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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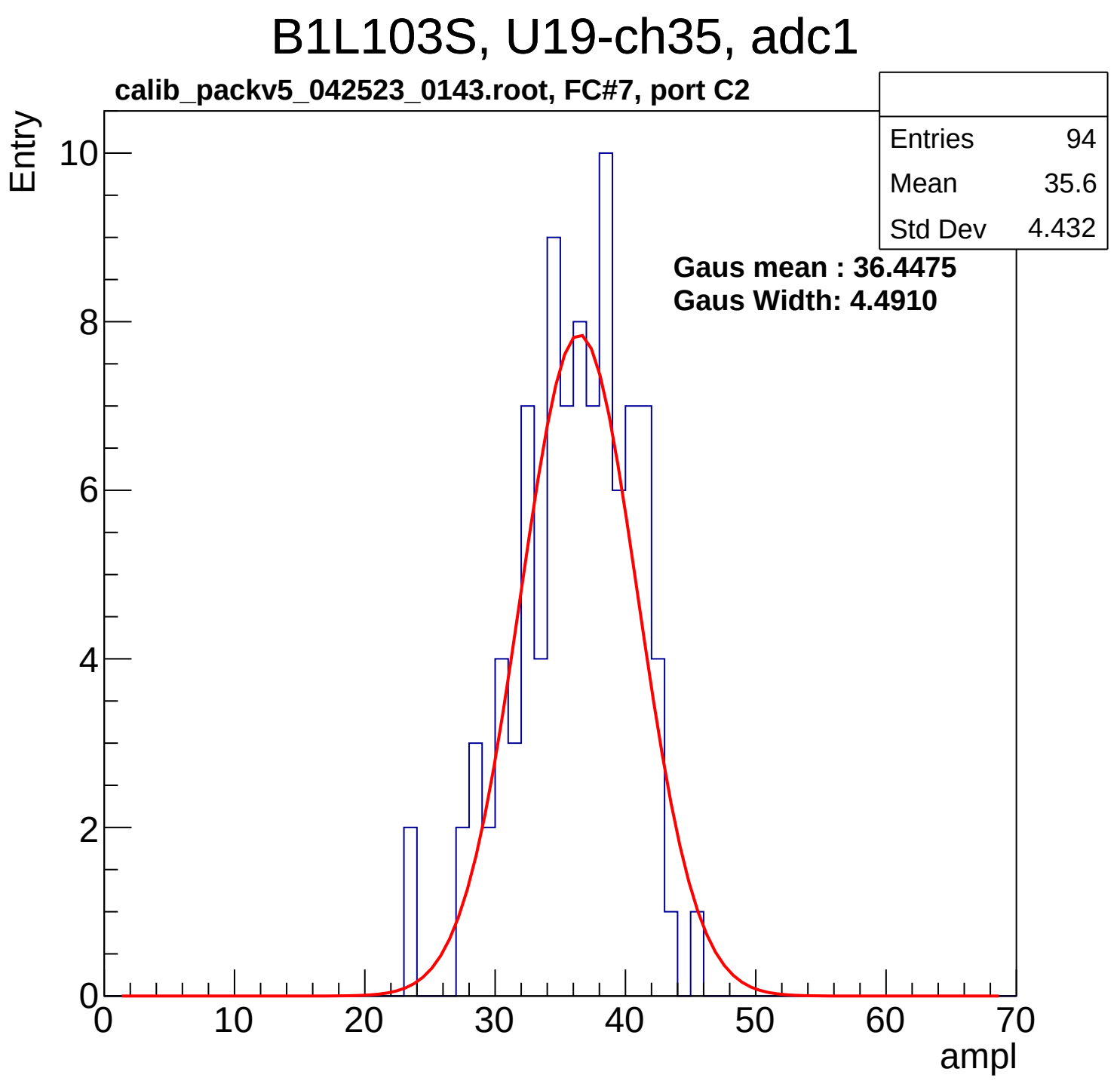
B1L103S, U19-ch35, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 94
Mean 35.6
Std Dev 4.432
Gaus mean : 36.4475
Gaus Width: 4.4910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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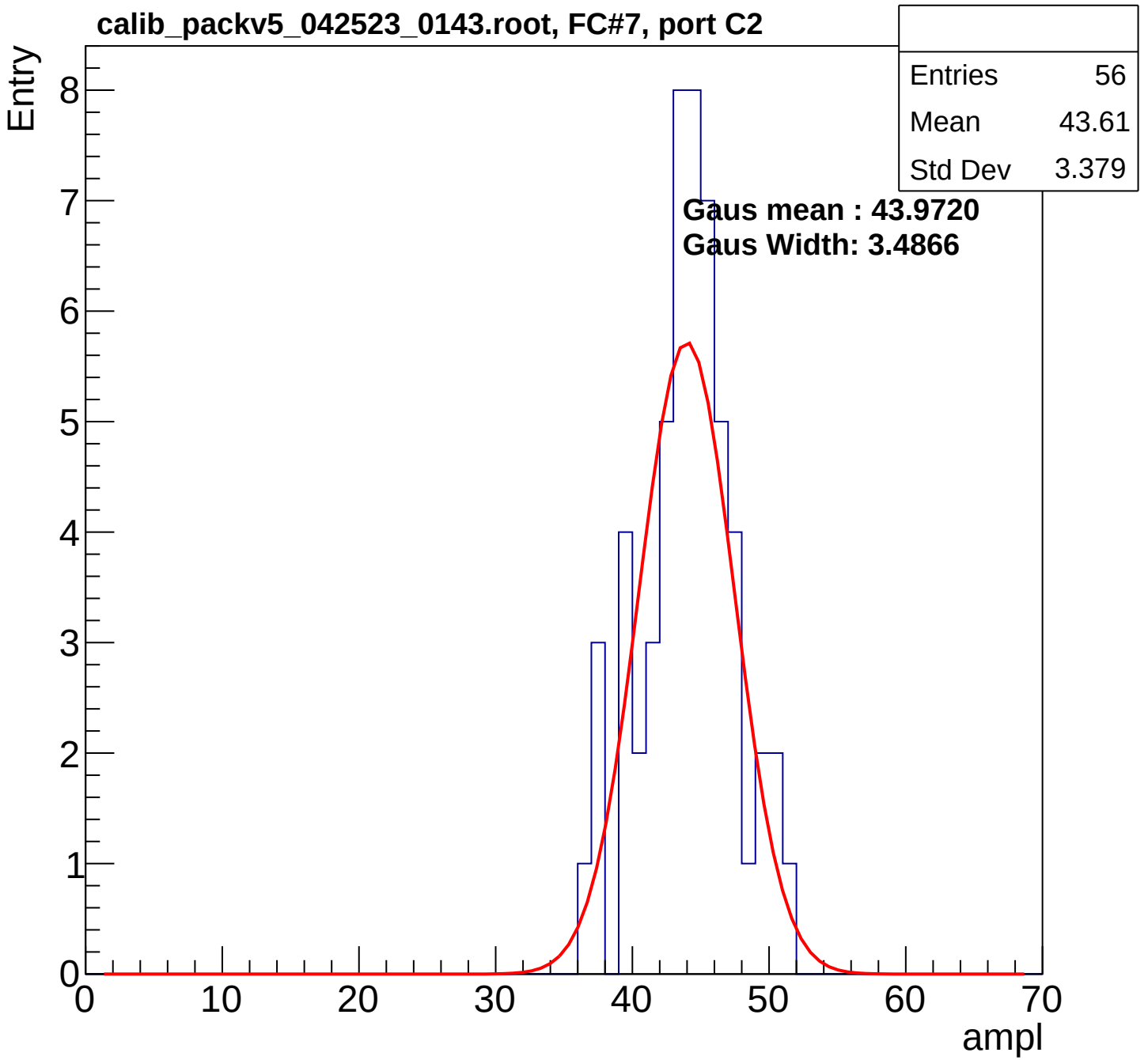
B1L103S, U19-ch35, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.61
Std Dev 3.379
Gaus mean : 43.9720
Gaus Width: 3.4866
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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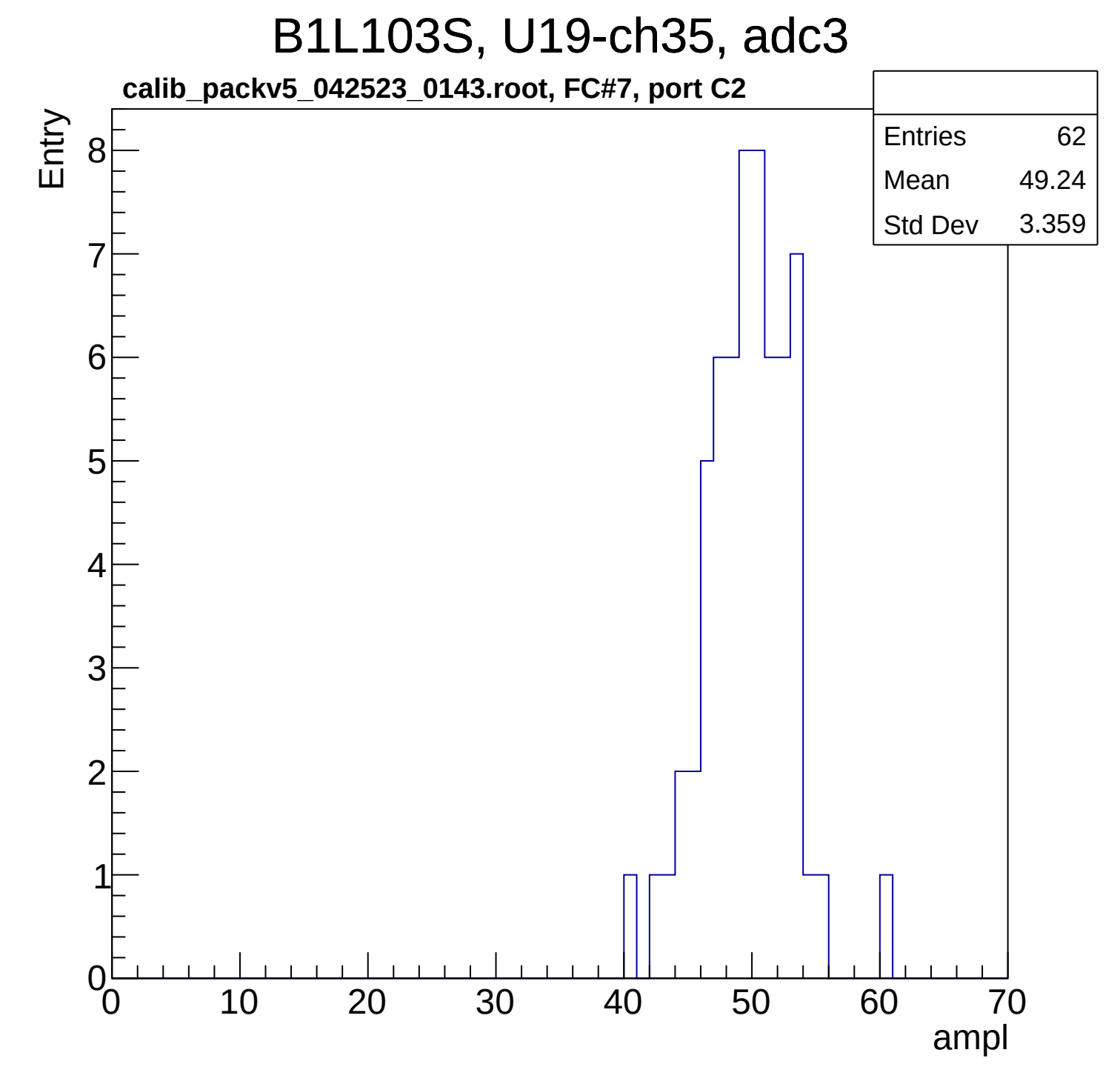
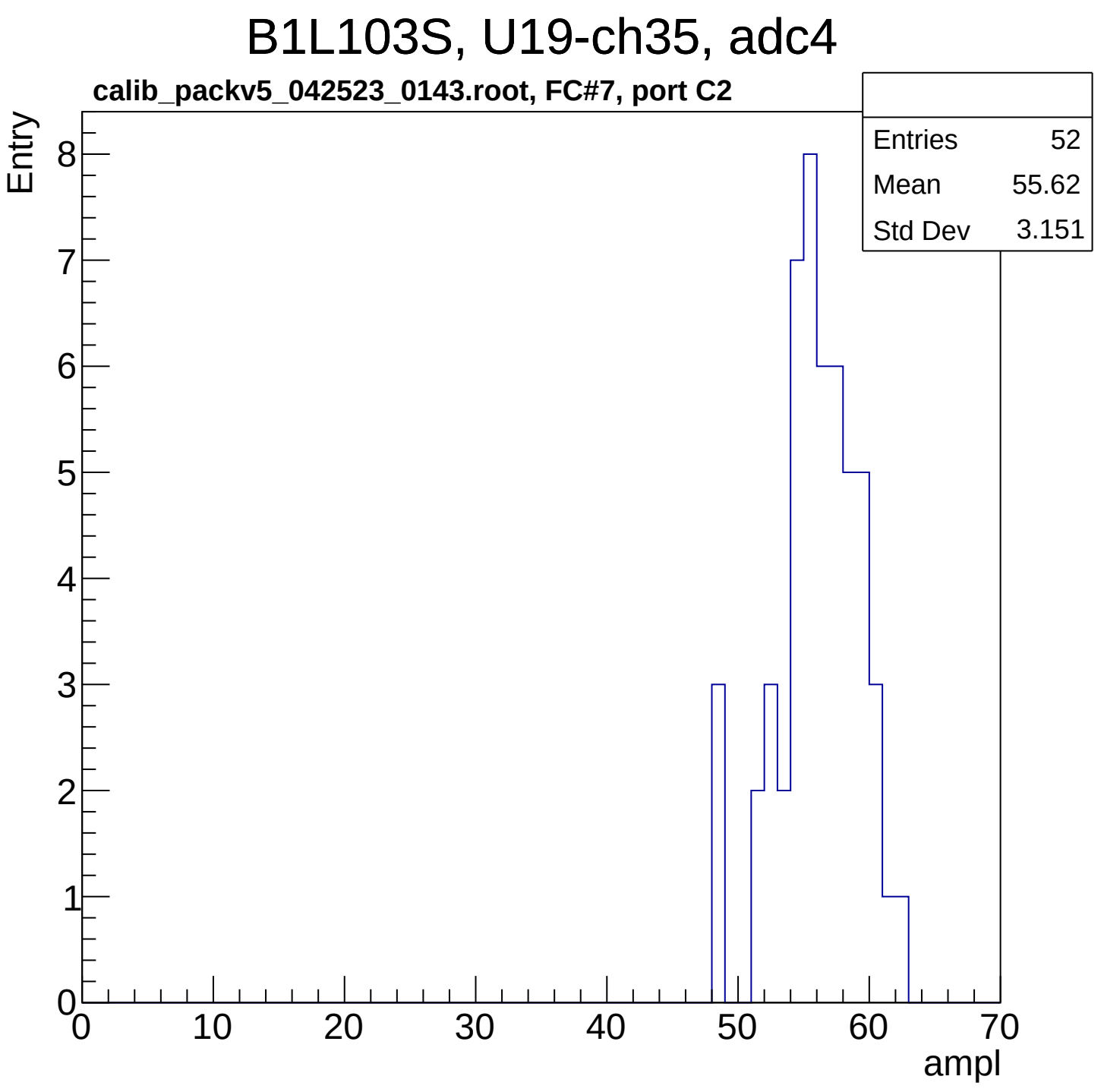

B1L103S, U19-ch35, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.24
Std Dev 3.359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch35, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.62
Std Dev 3.151
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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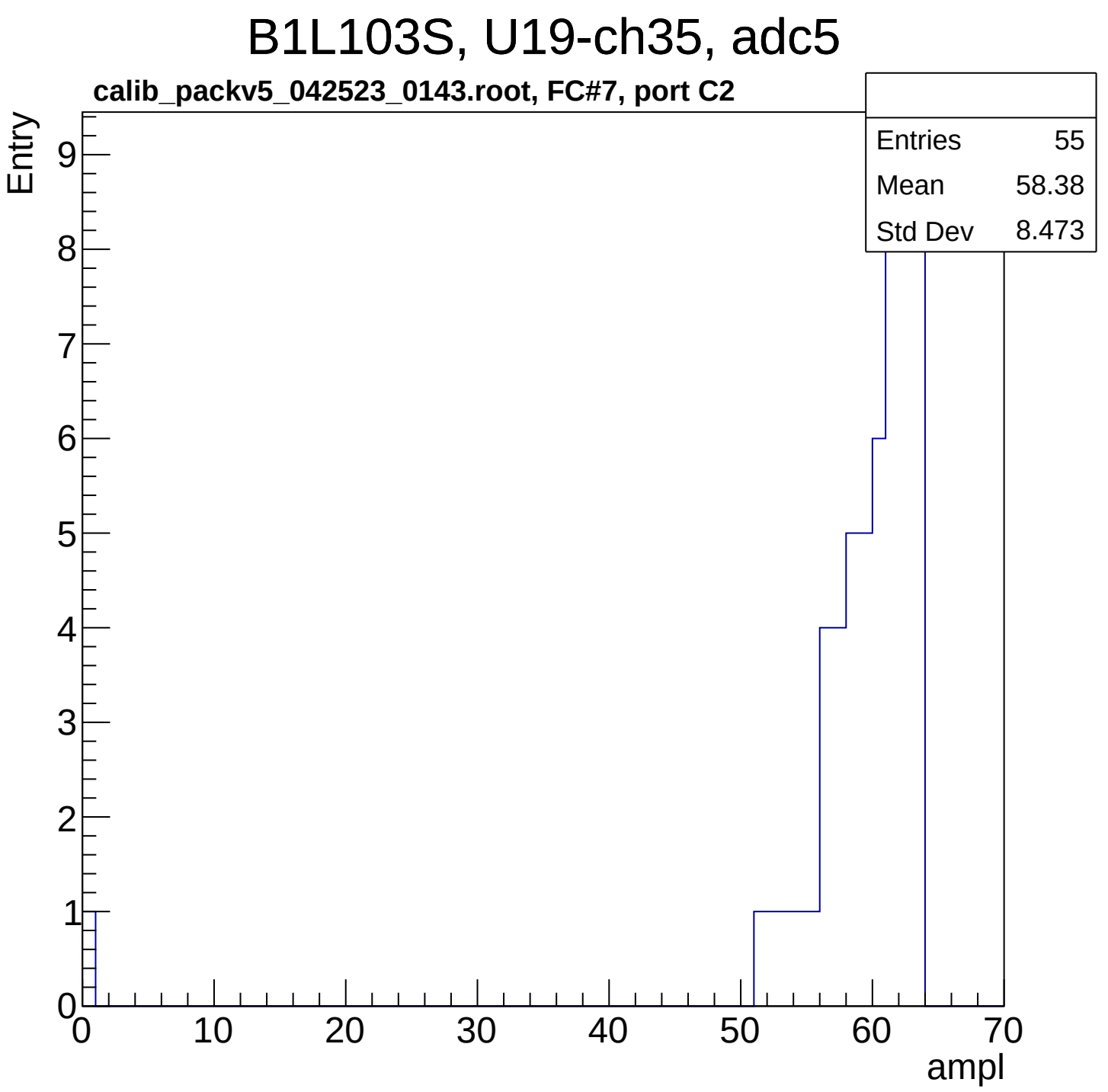
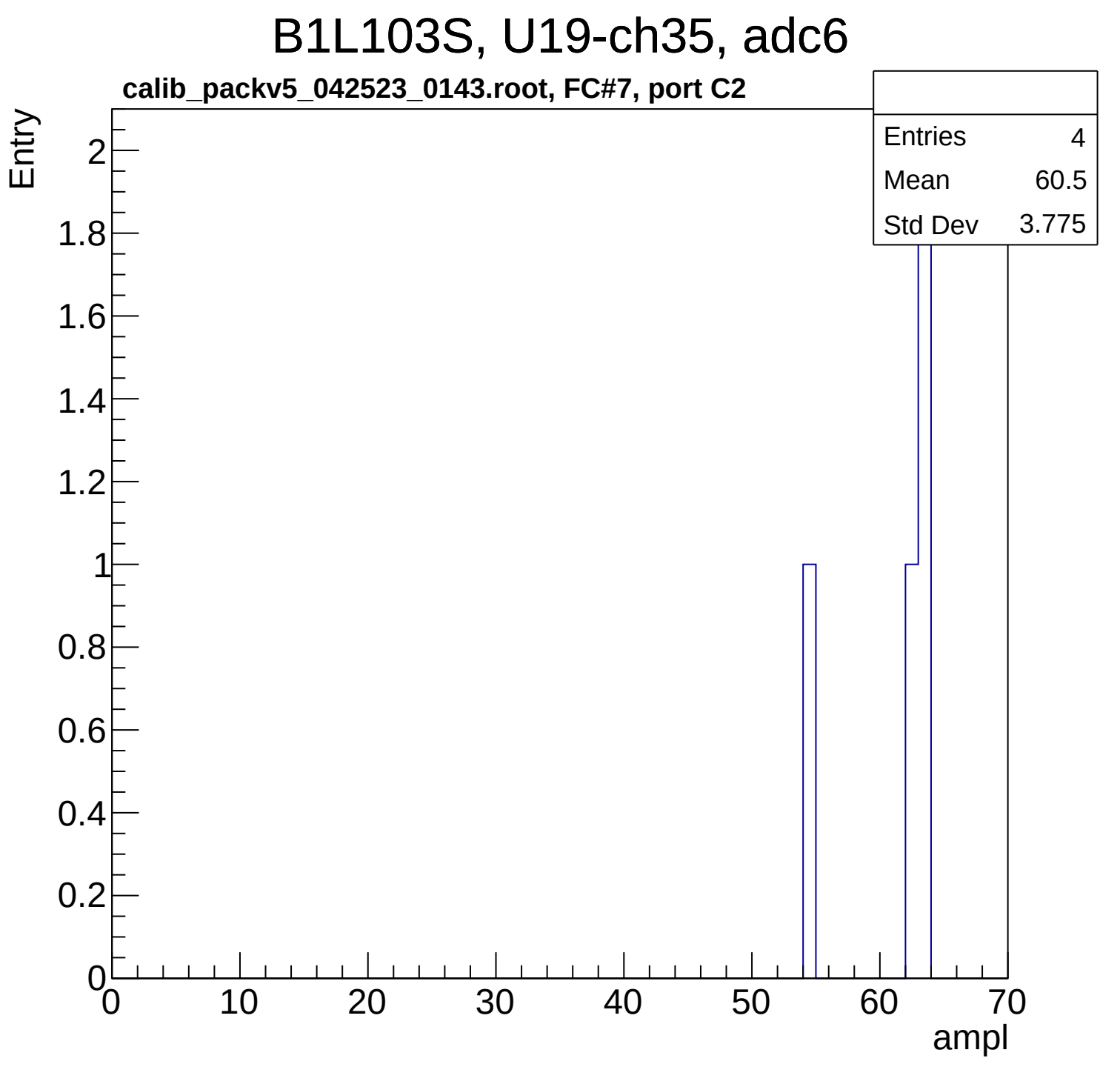
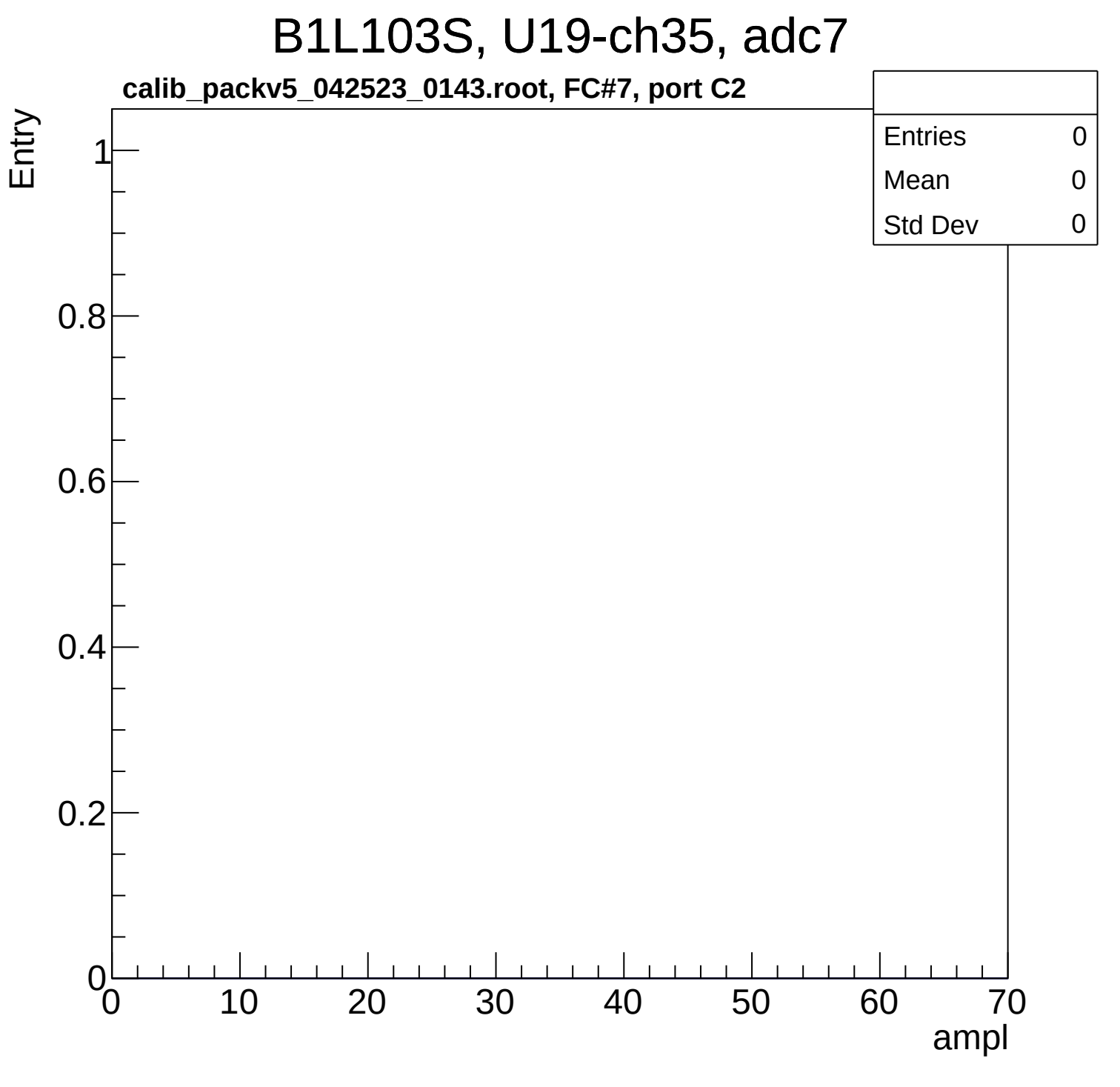
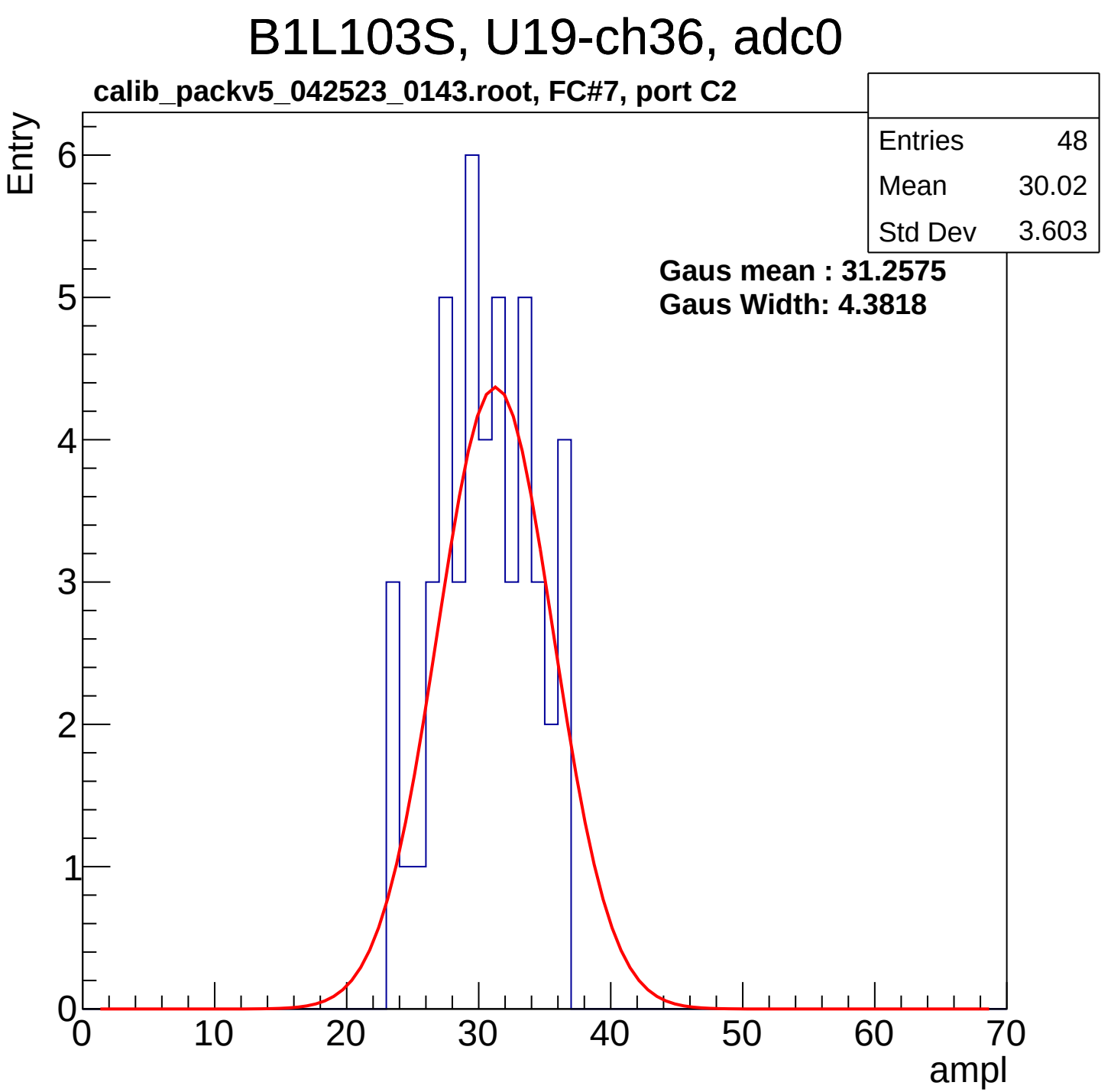
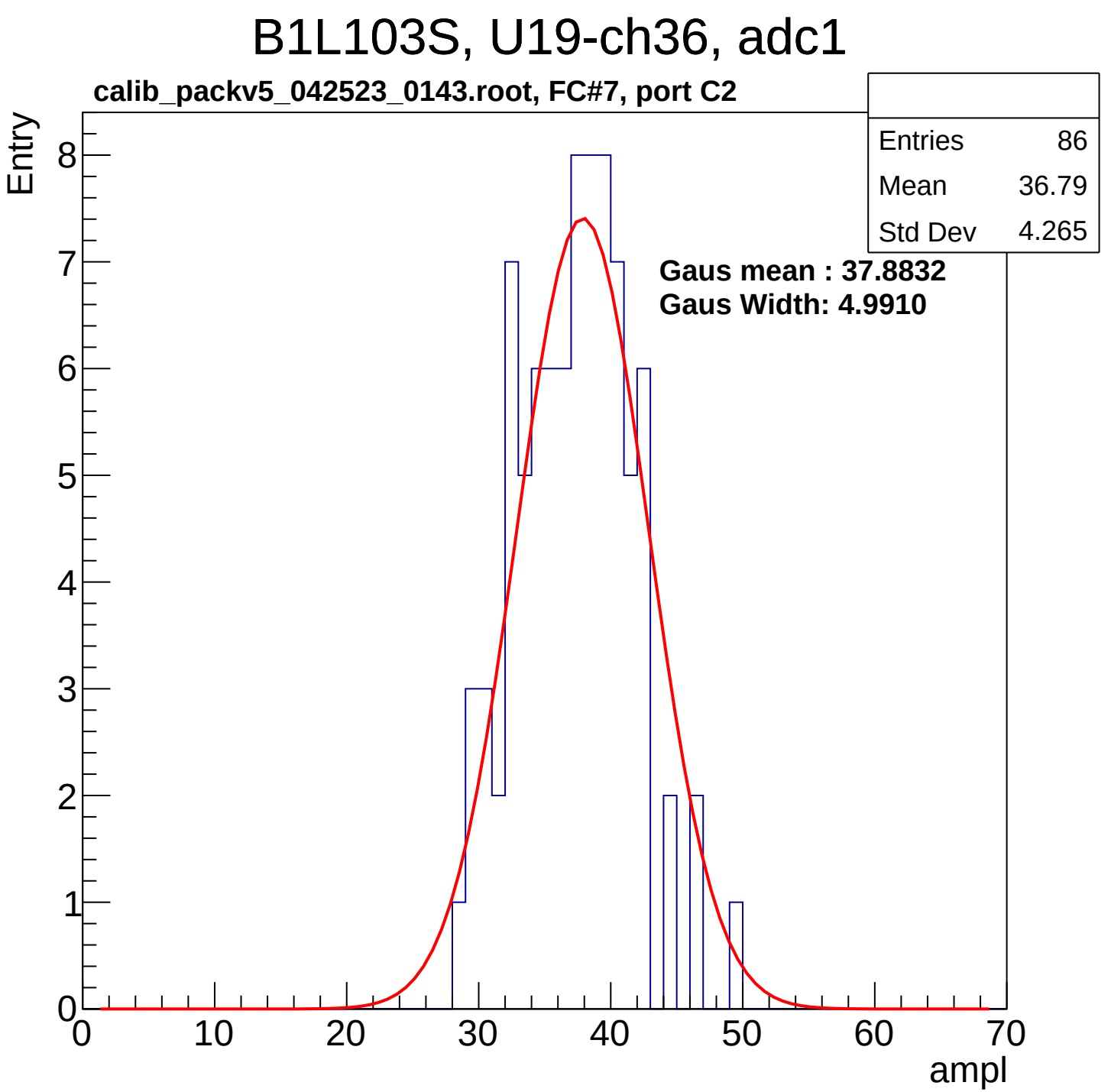

B1L103S, U19-ch35, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.38
Std Dev 8.473
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch35, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 60.5
Std Dev 3.775
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch35, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch36, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 30.02
Std Dev 3.603
Gaus mean : 31.2575
Gaus Width: 4.3818
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch36, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 36.79
Std Dev 4.265
Gaus mean : 37.8832
Gaus Width: 4.9910
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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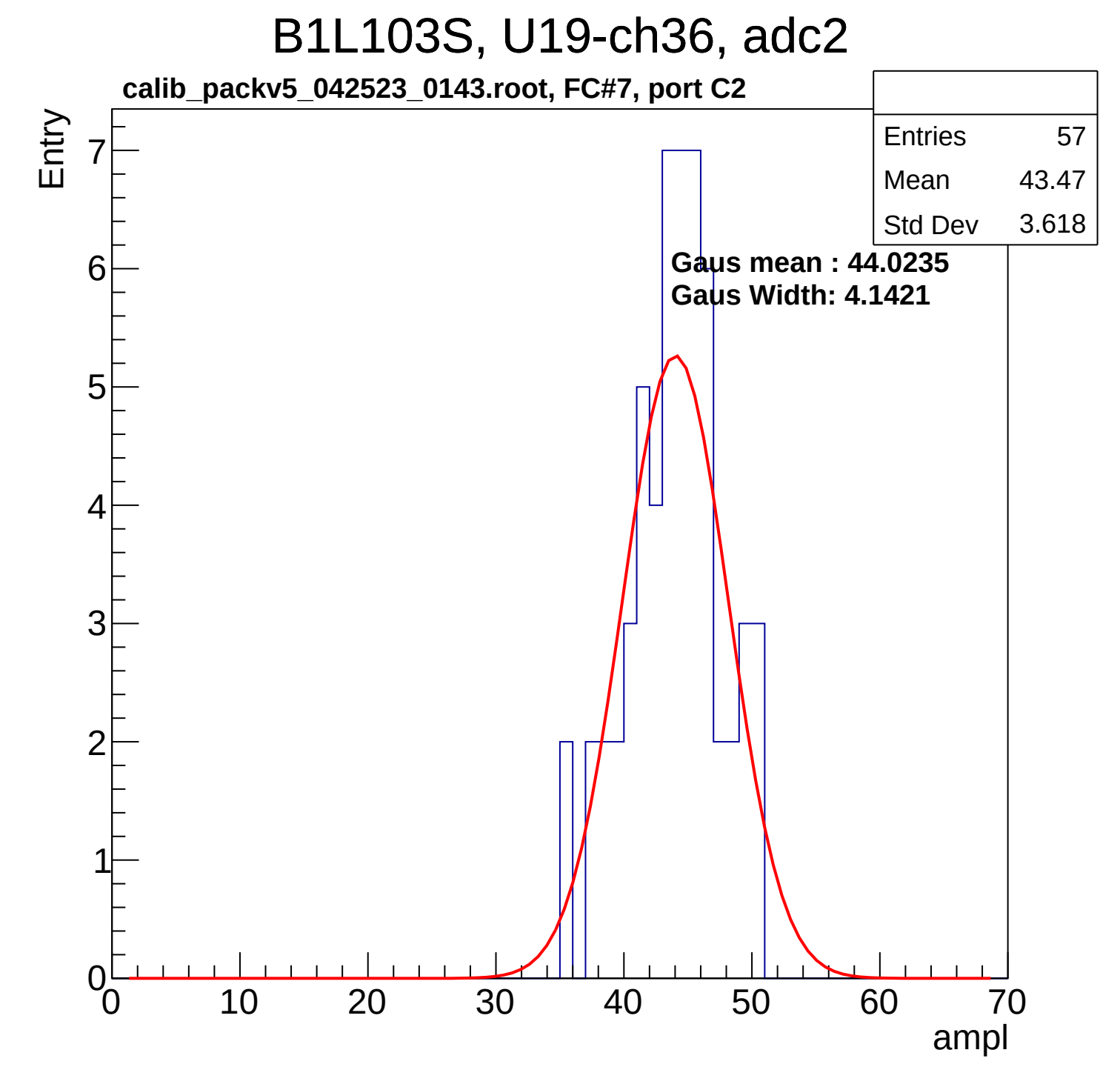
B1L103S, U19-ch36, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.47
Std Dev 3.618
Gaus mean : 44.0235
Gaus Width: 4.1421
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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60
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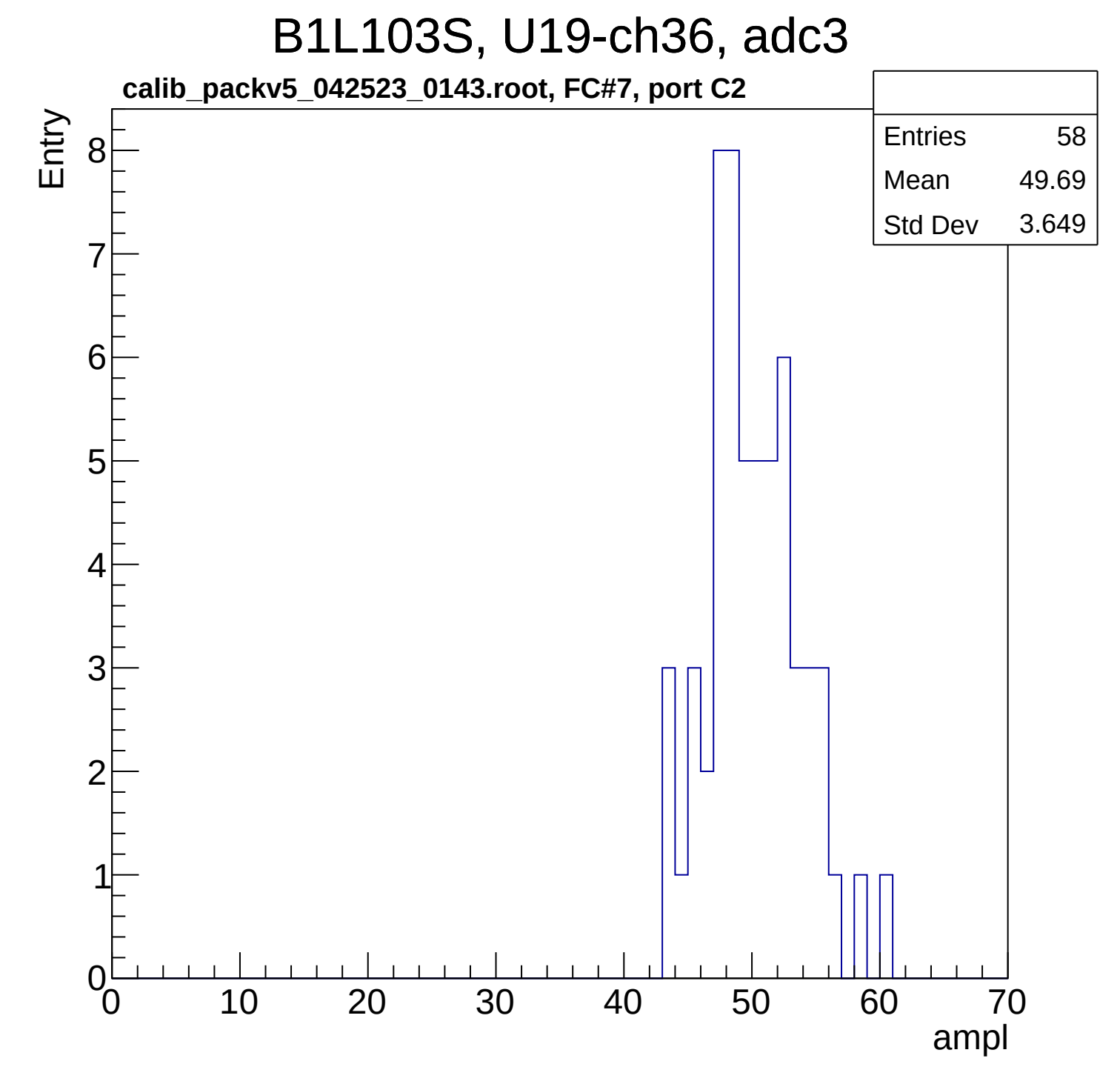
B1L103S, U19-ch36, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.69
Std Dev 3.649
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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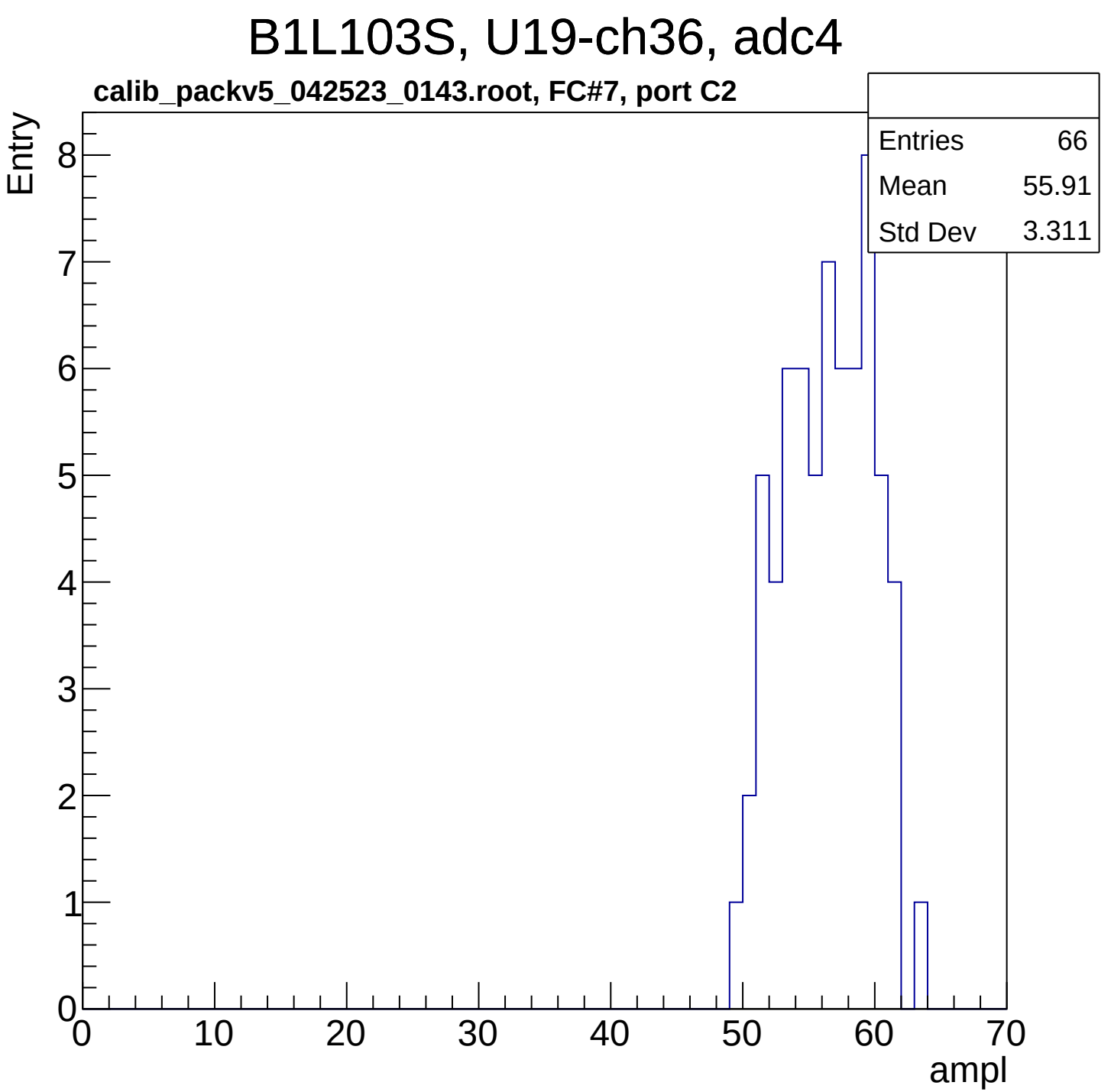
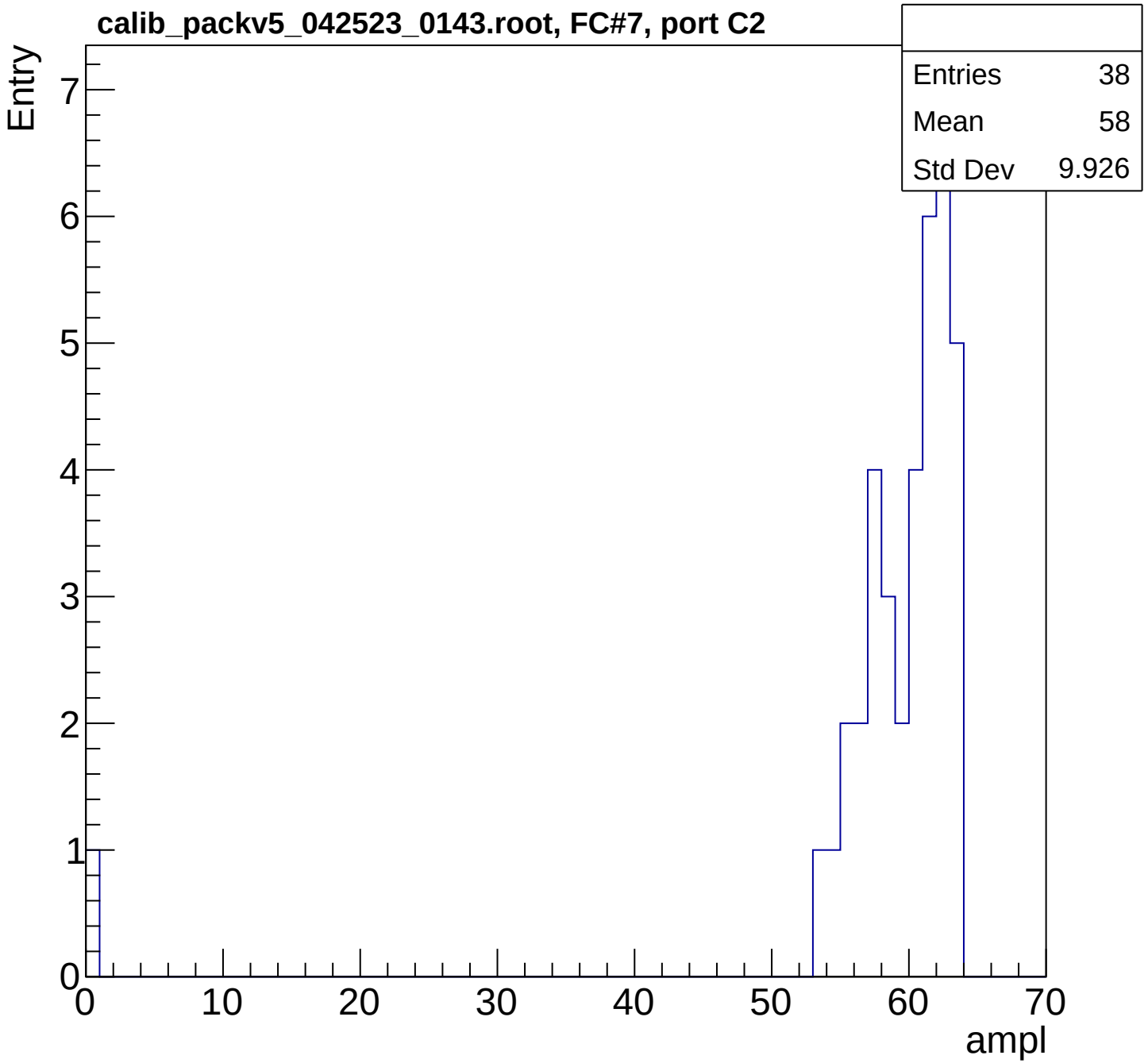

B1L103S, U19-ch36, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 55.91
Std Dev 3.311
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch36, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58
Std Dev 9.926
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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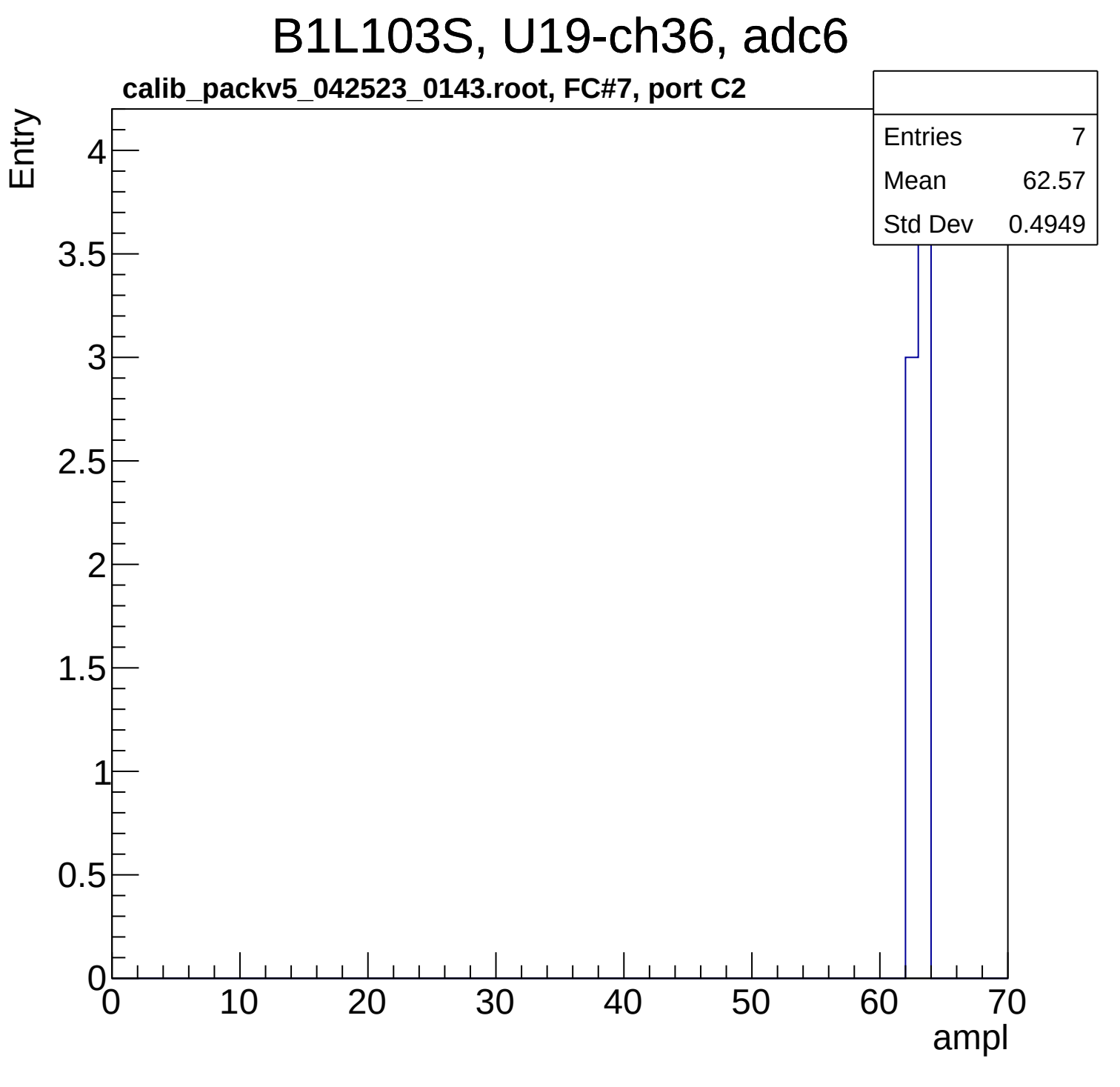
B1L103S, U19-ch36, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
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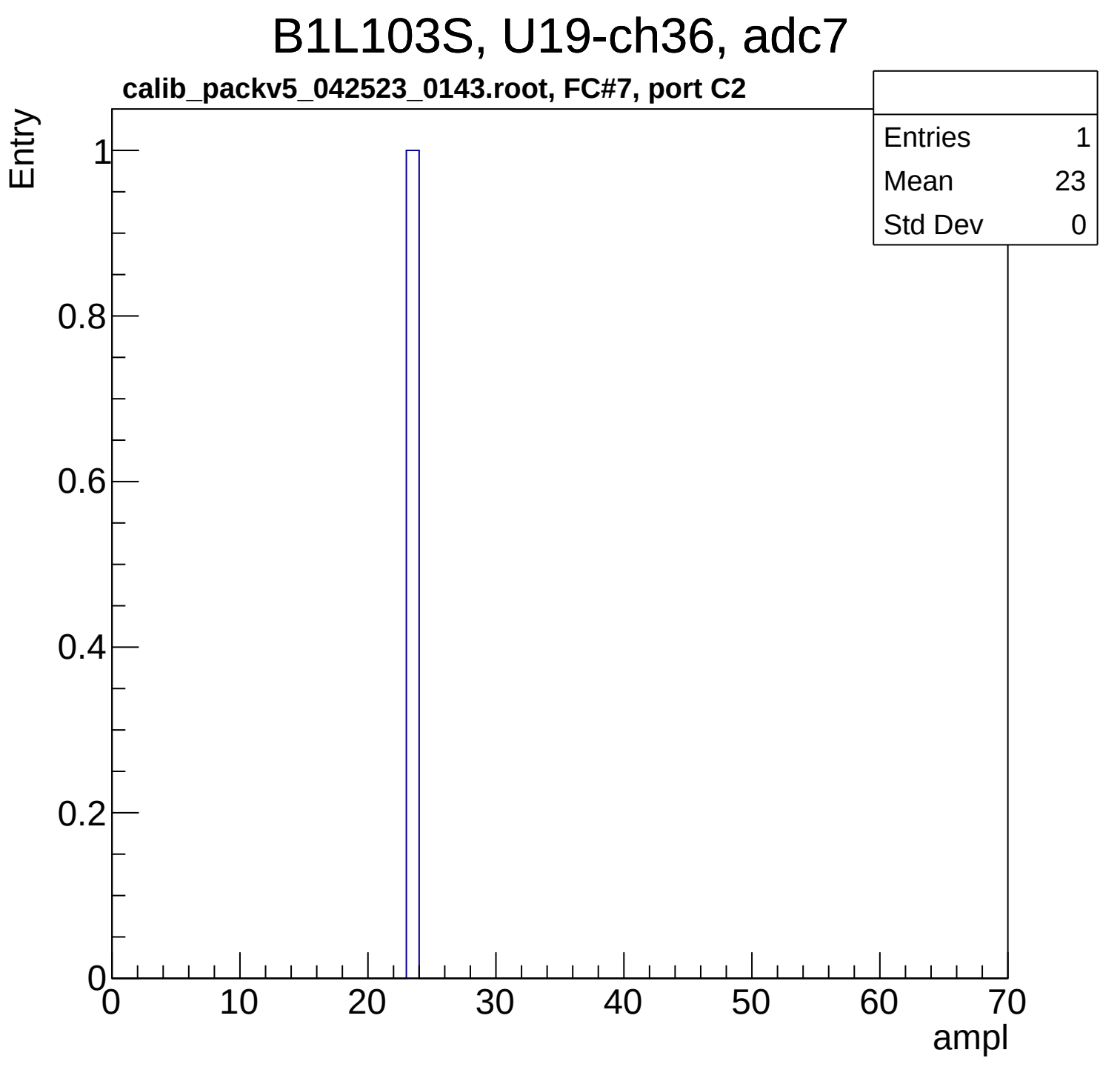
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U19-ch36, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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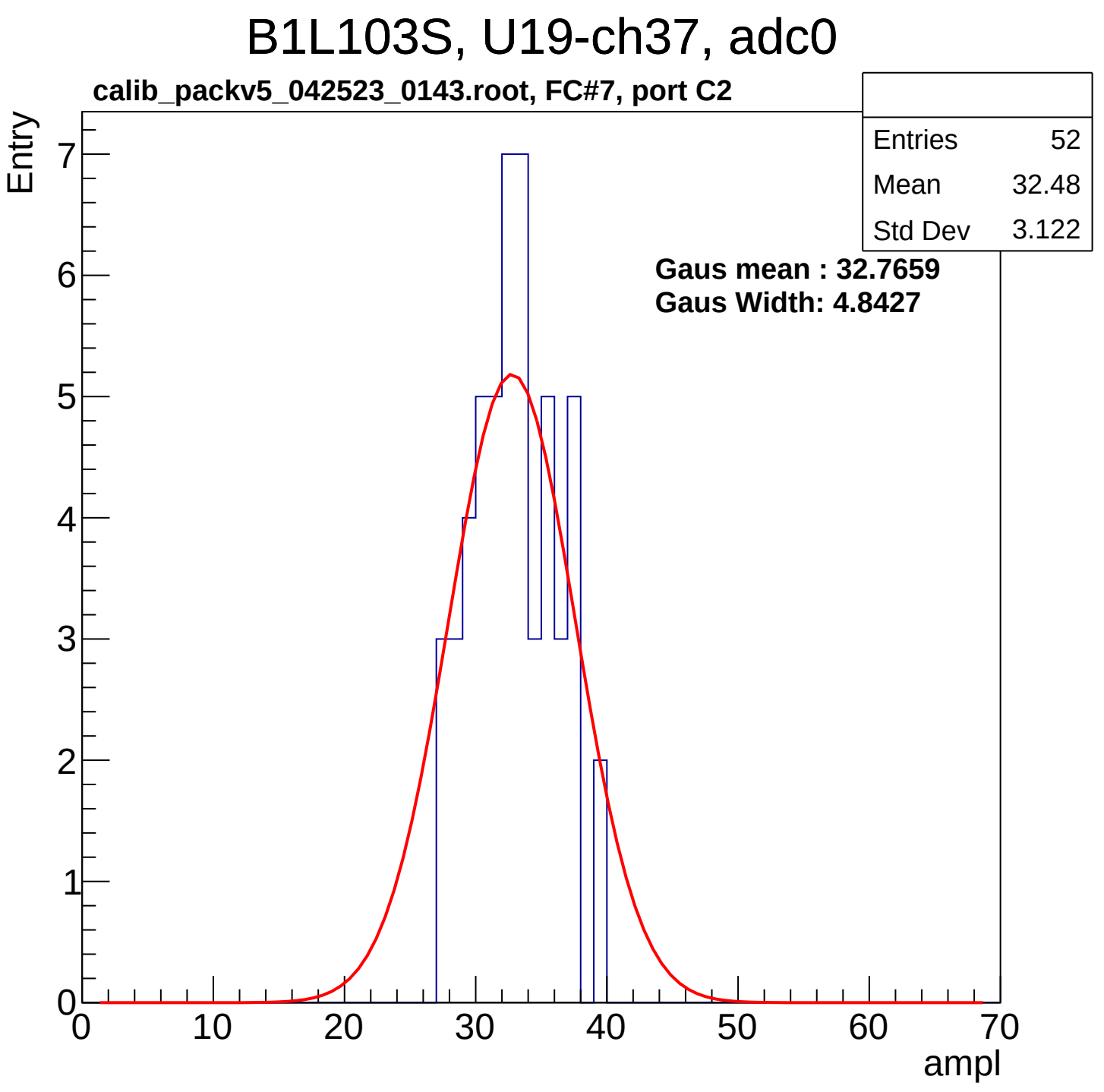
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch37, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 32.48
Std Dev 3.122
Gaus mean : 32.7659
Gaus Width: 4.8427
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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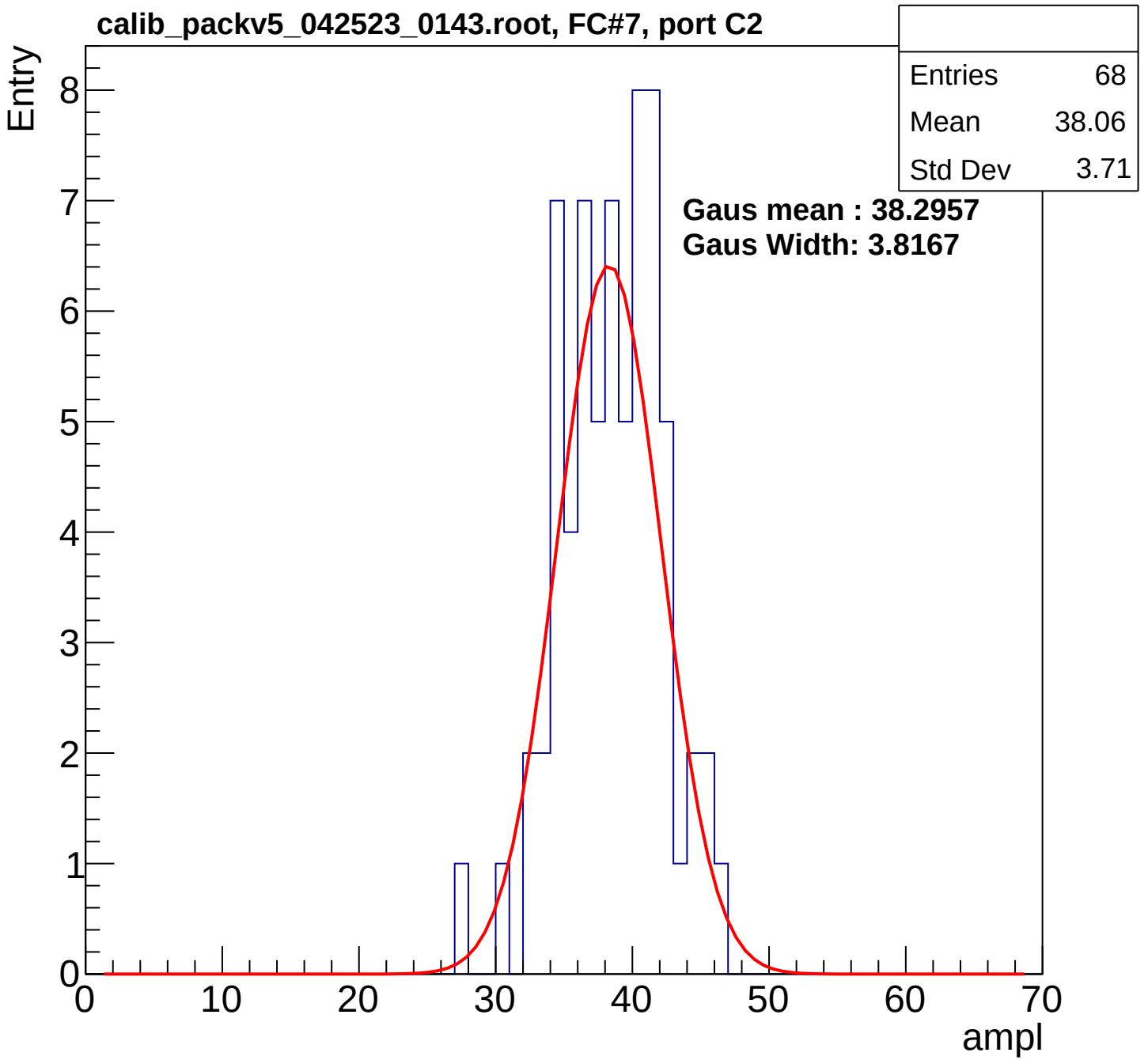
B1L103S, U19-ch37, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 38.06
Std Dev 3.71
Gaus mean : 38.2957
Gaus Width: 3.8167
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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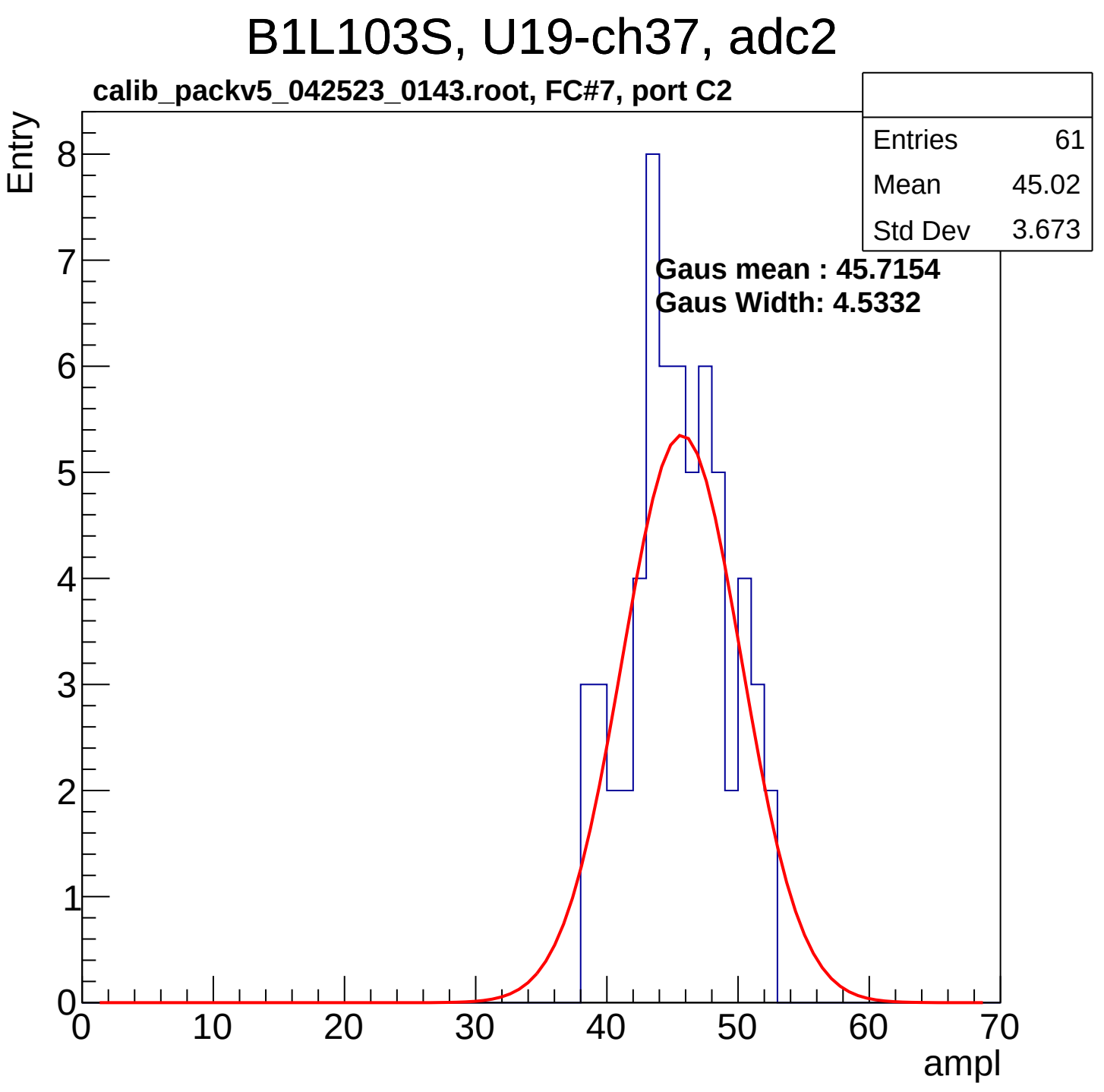
B1L103S, U19-ch37, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 45.02
Std Dev 3.673
Gaus mean : 45.7154
Gaus Width: 4.5332
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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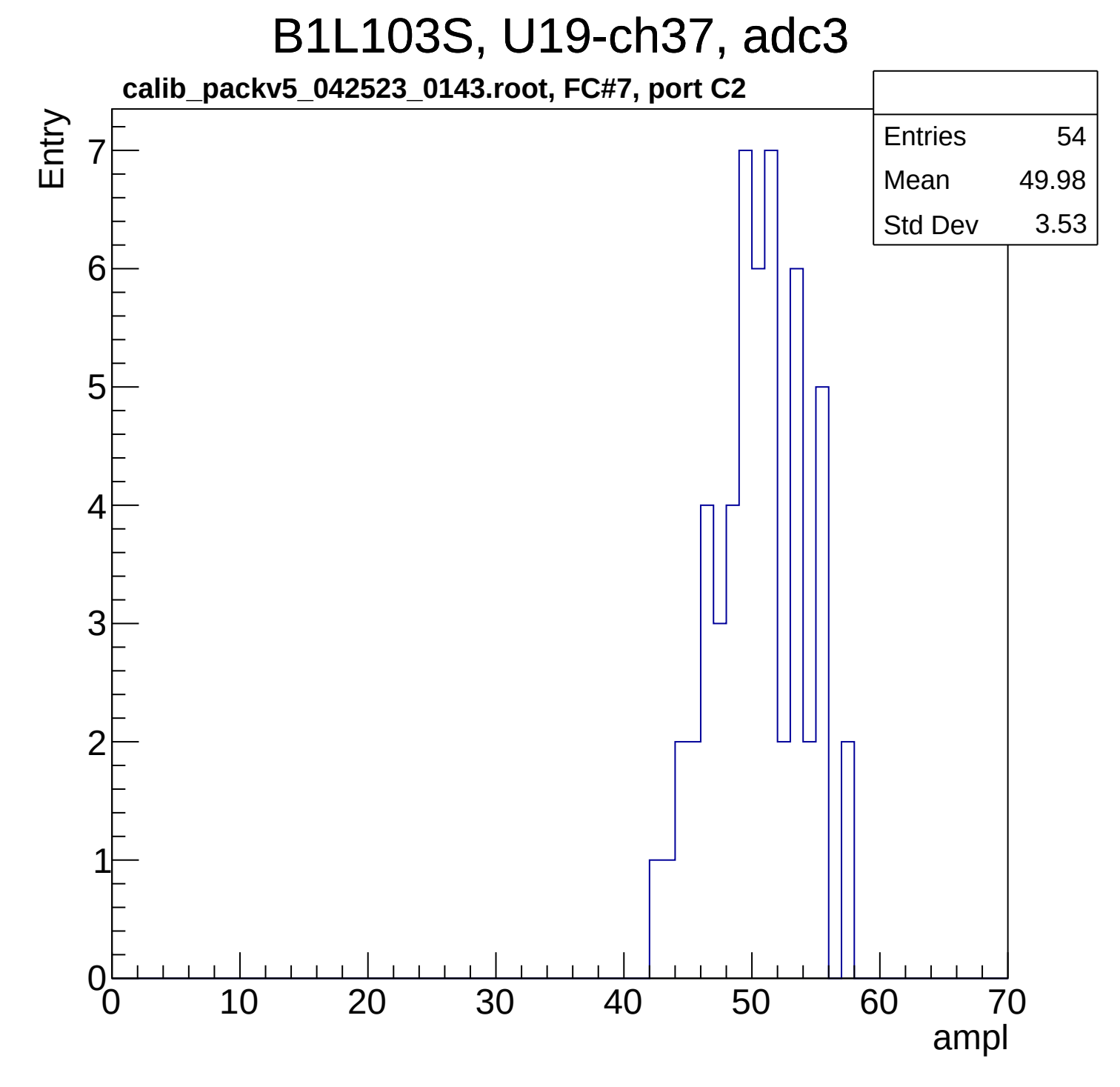
B1L103S, U19-ch37, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 49.98
Std Dev 3.53
Entry
ampl
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0
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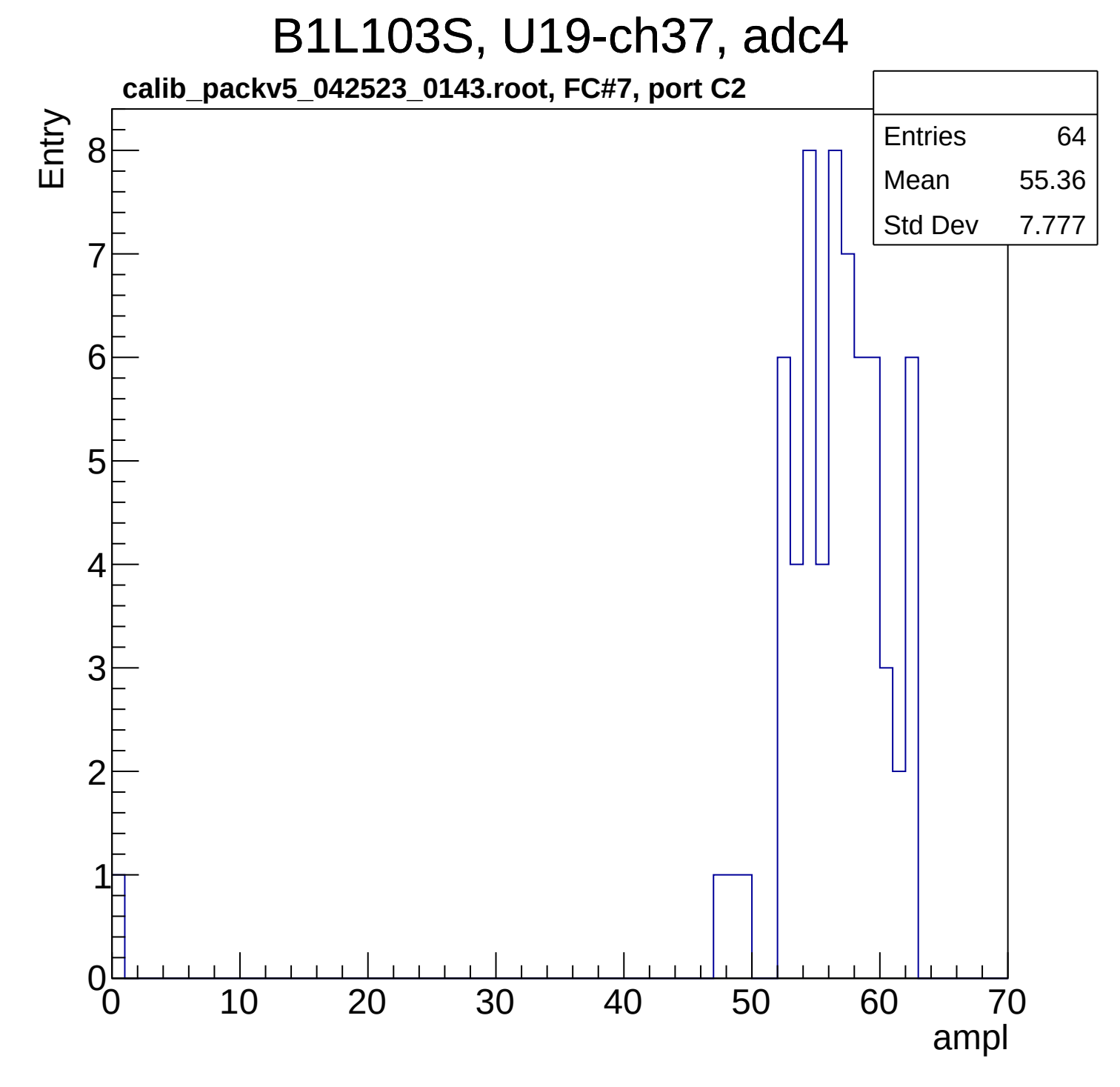
B1L103S, U19-ch37, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 55.36
Std Dev 7.777
Entry
ampl
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4
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0
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60
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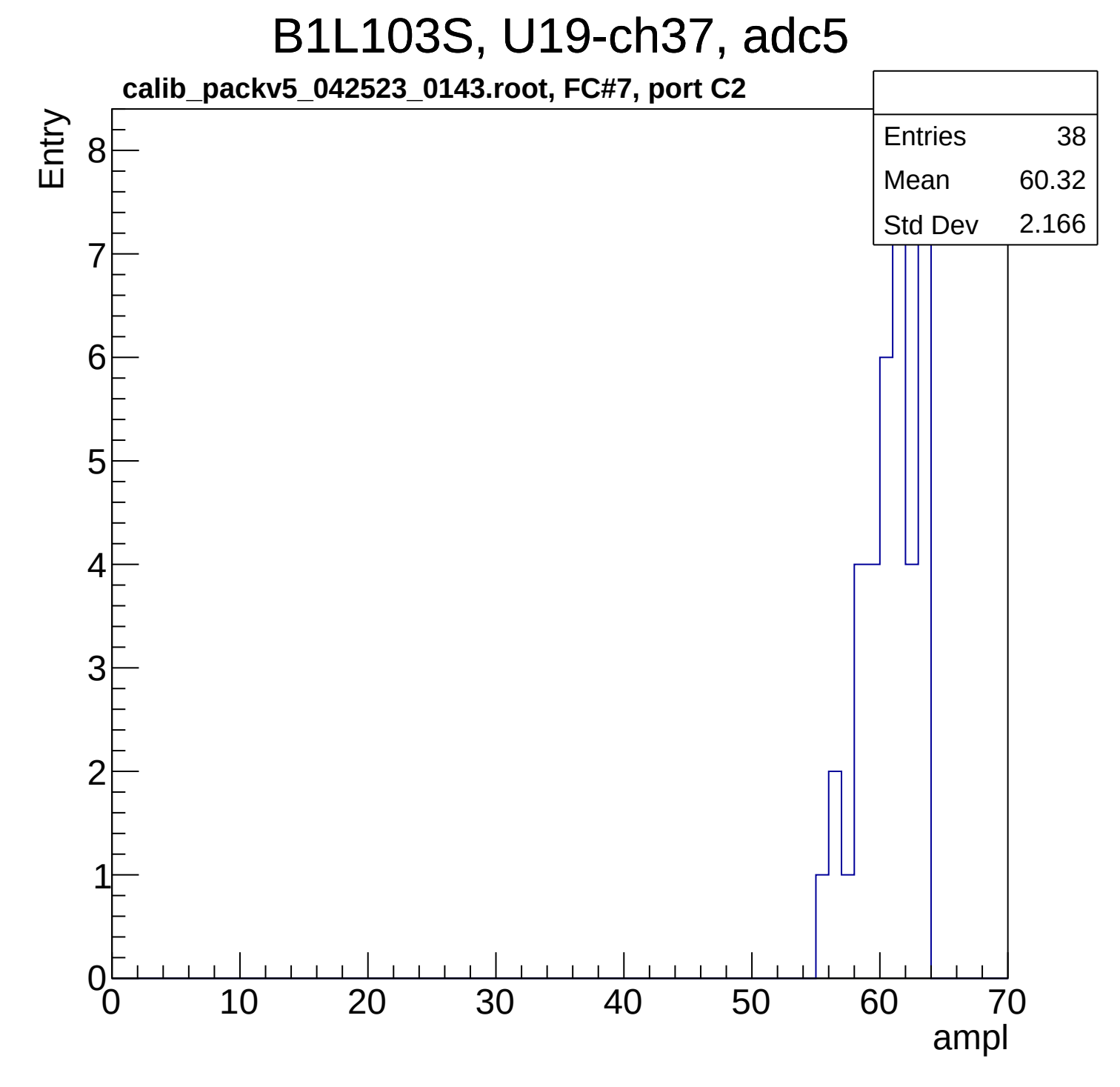
B1L103S, U19-ch37, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 60.32
Std Dev 2.166
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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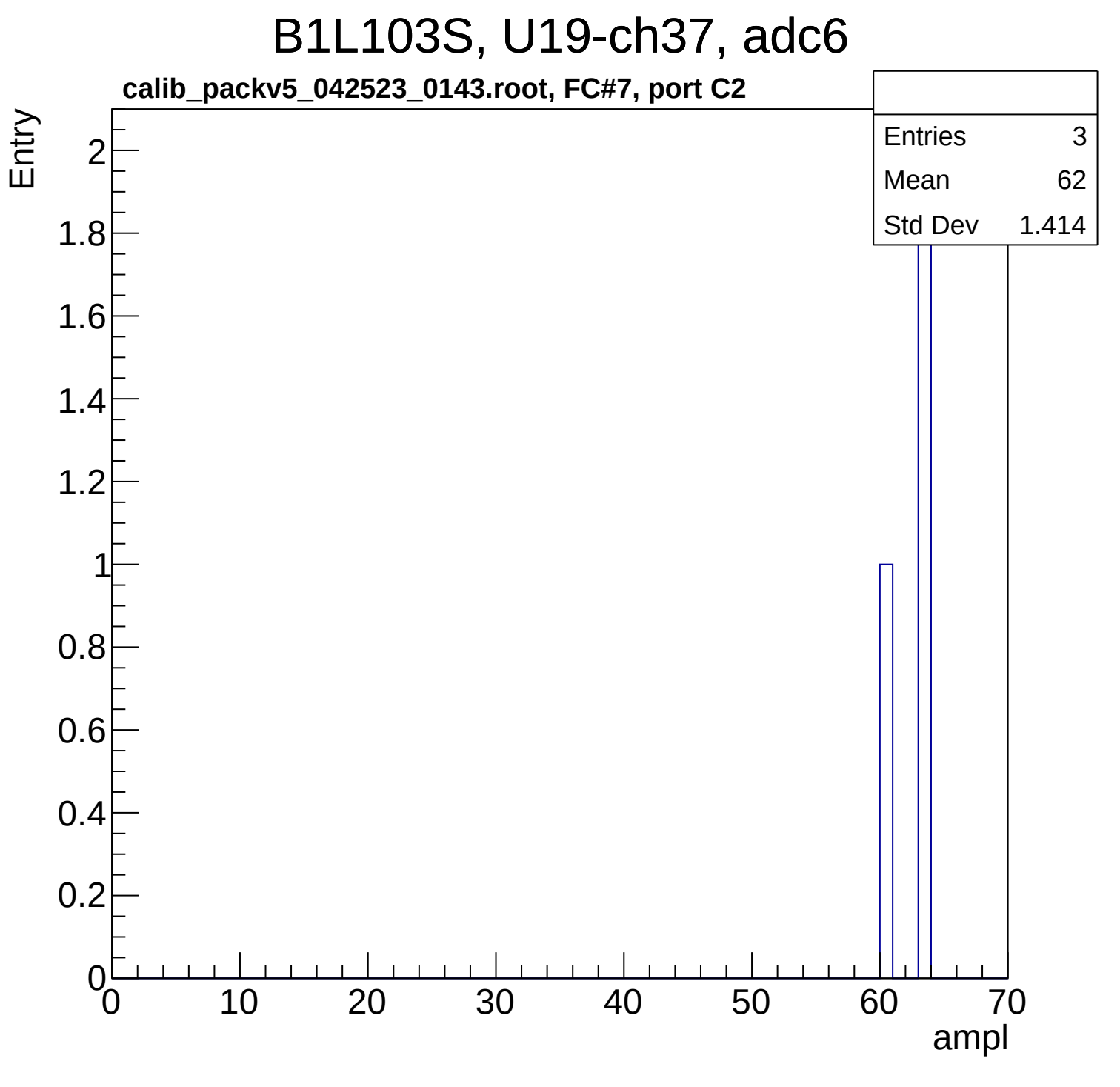
B1L103S, U19-ch37, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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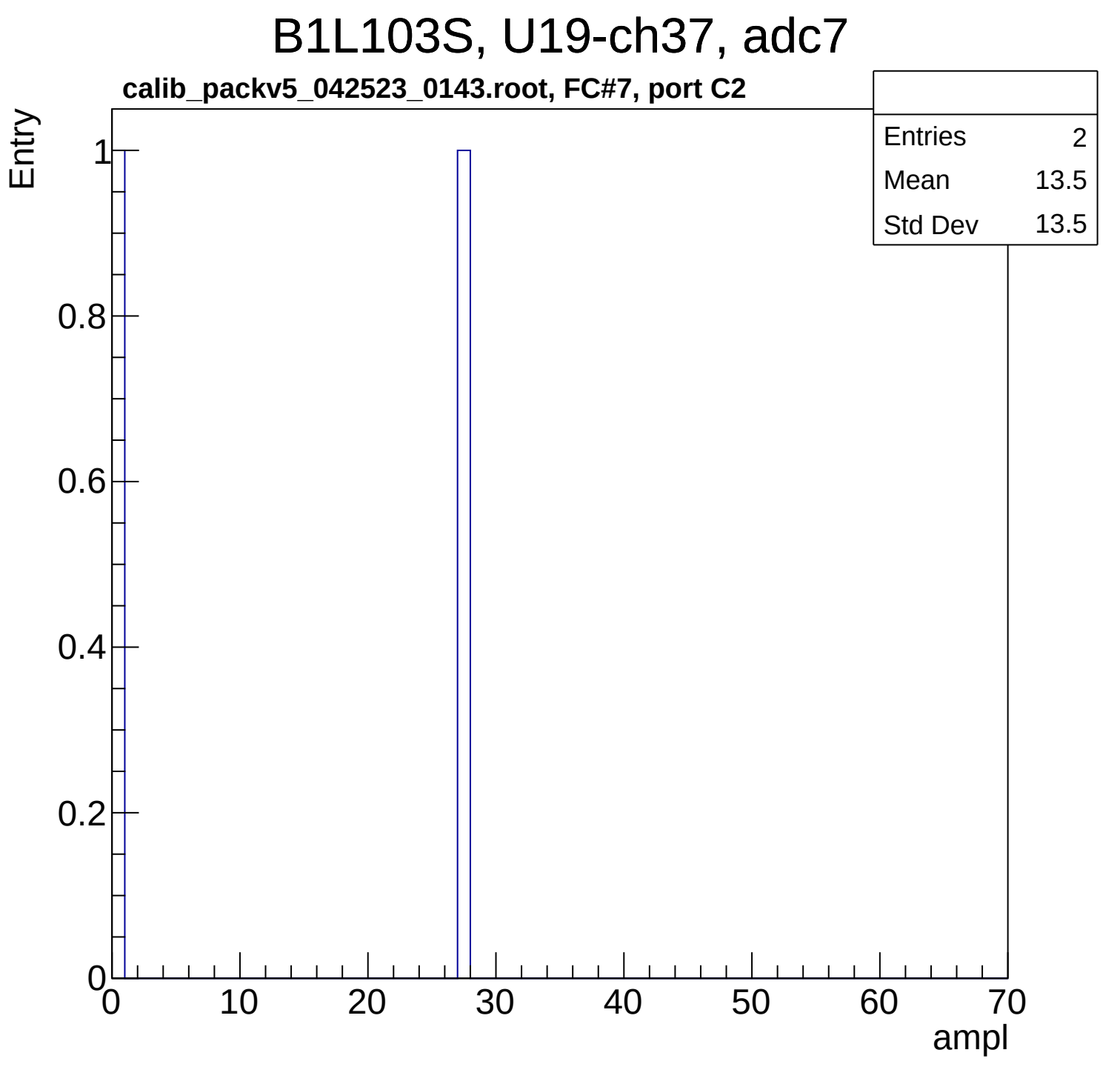
B1L103S, U19-ch37, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 13.5
Std Dev 13.5
Entry
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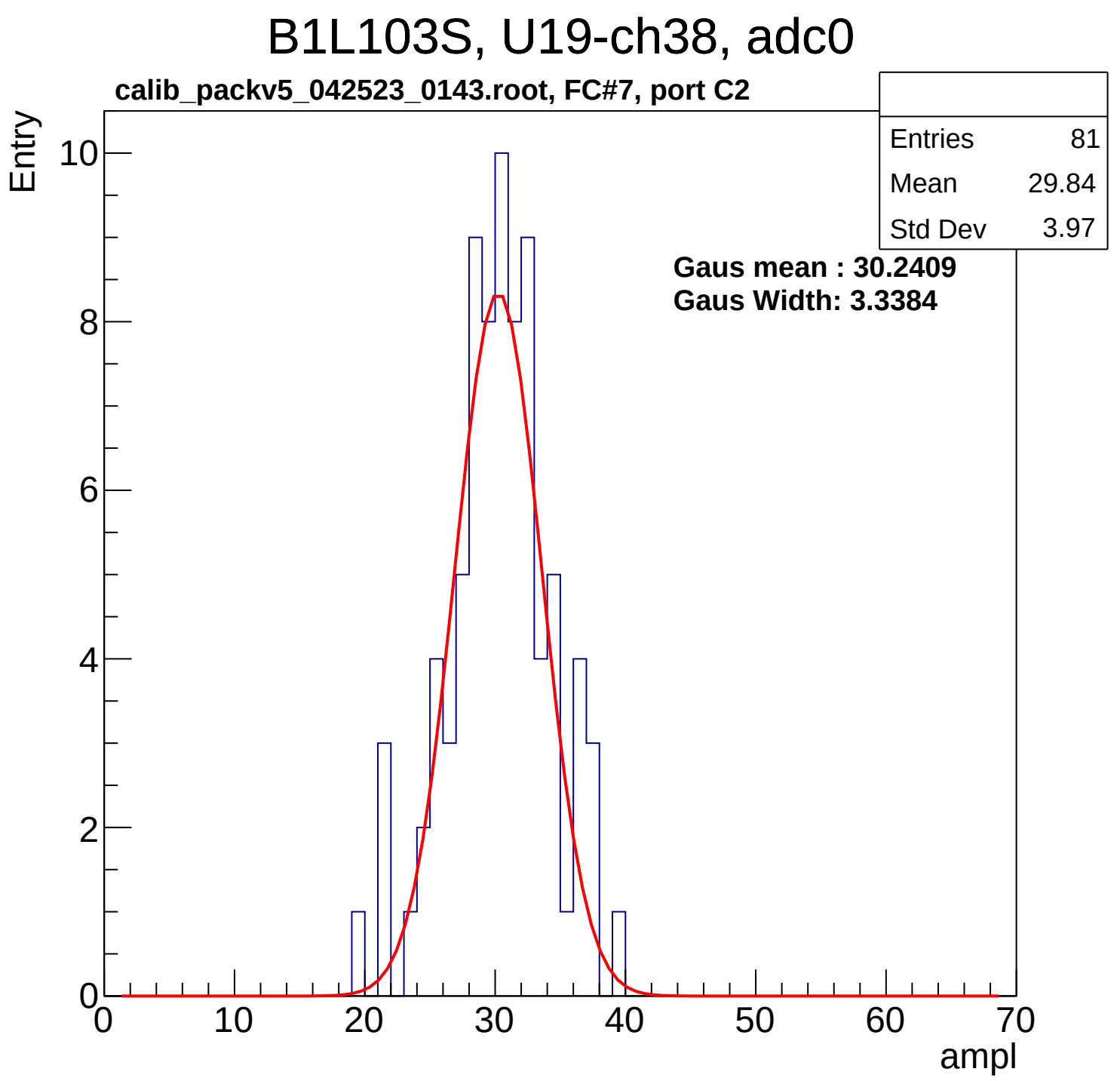
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch38, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 29.84
Std Dev 3.97
Gaus mean : 30.2409
Gaus Width: 3.3384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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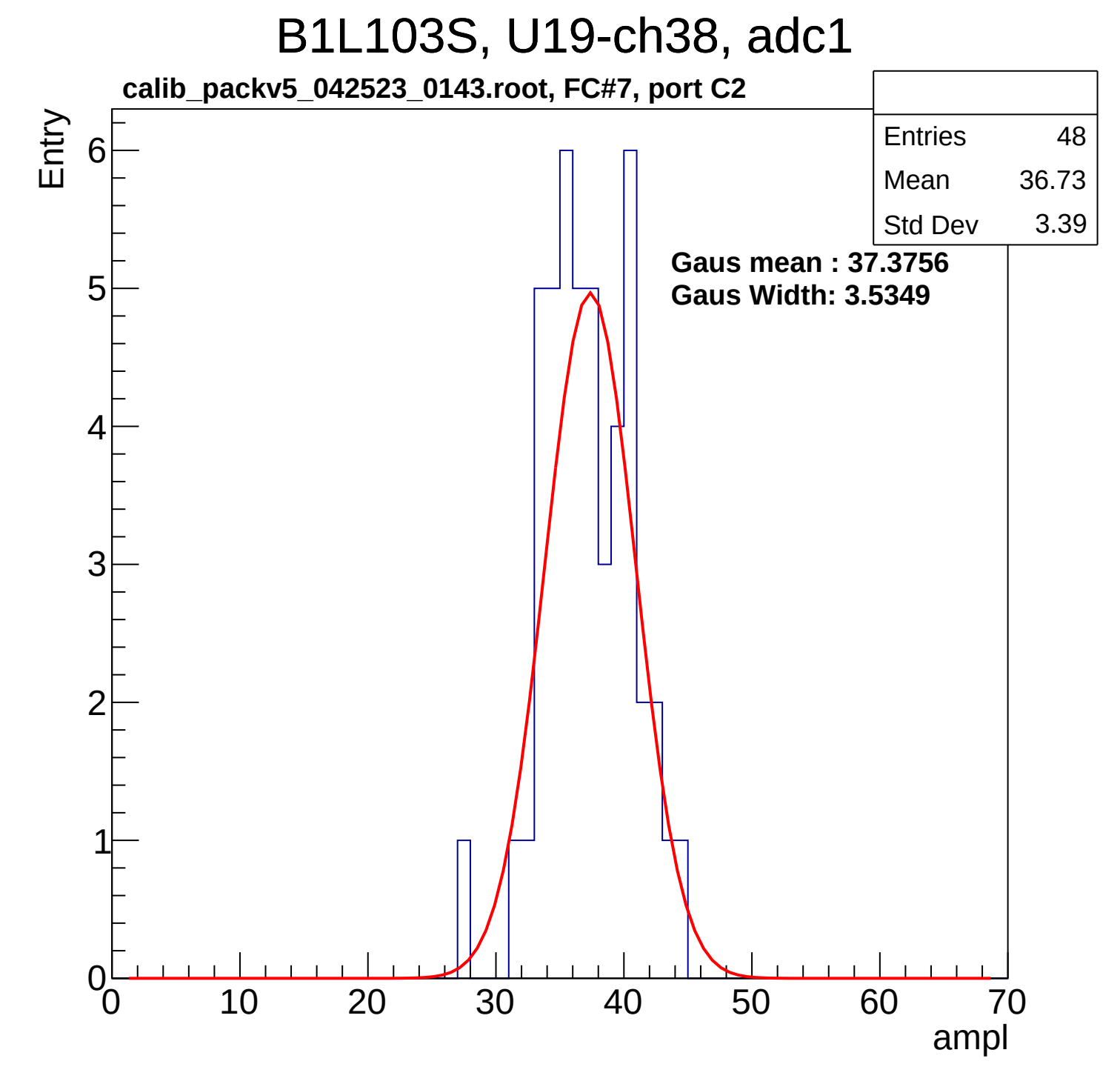
B1L103S, U19-ch38, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 36.73
Std Dev 3.39
Gaus mean : 37.3756
Gaus Width: 3.5349
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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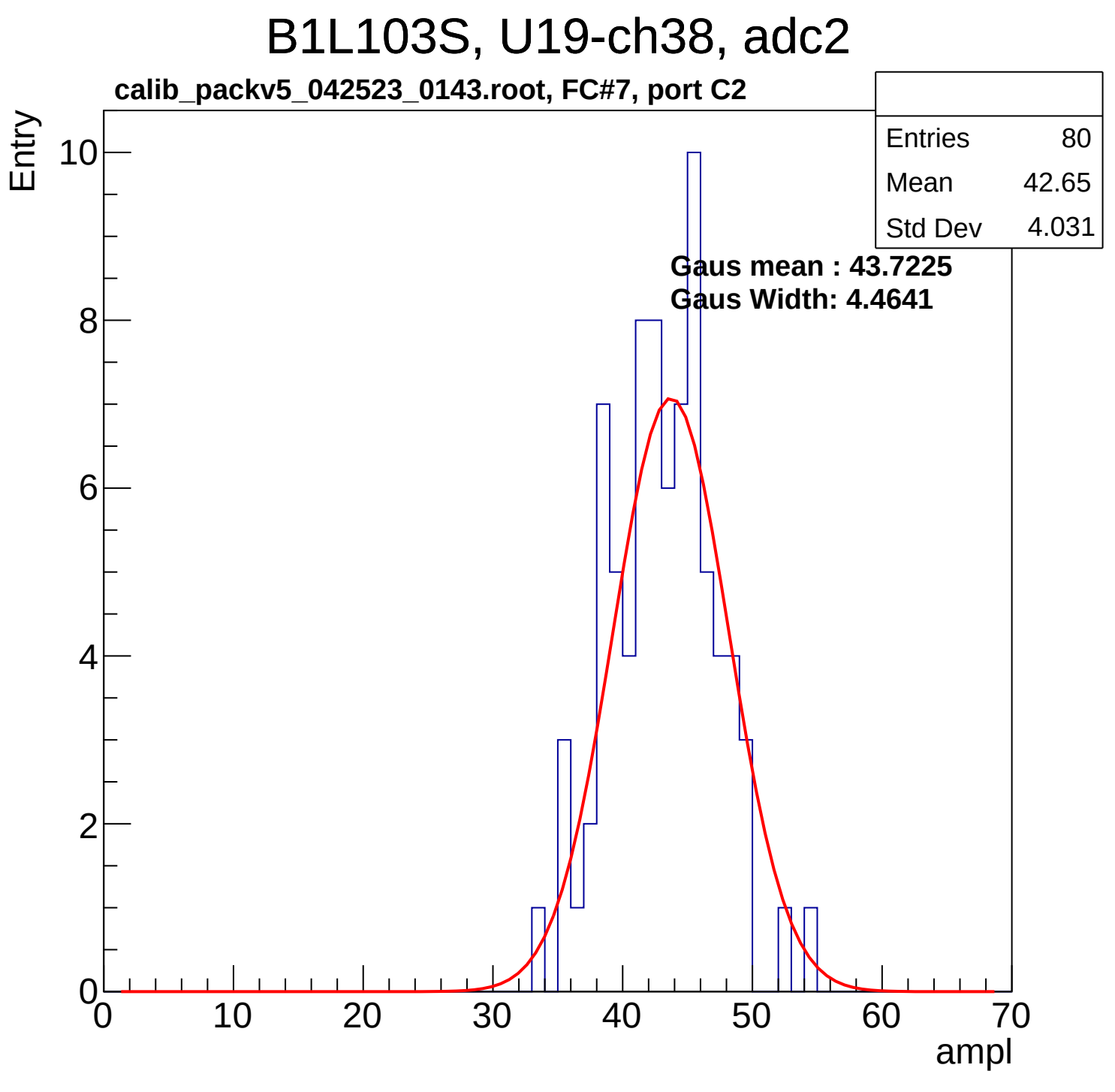
B1L103S, U19-ch38, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 42.65
Std Dev 4.031
Gaus mean : 43.7225
Gaus Width: 4.4641
Entry
ampl
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2
4
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10
0
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40
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60
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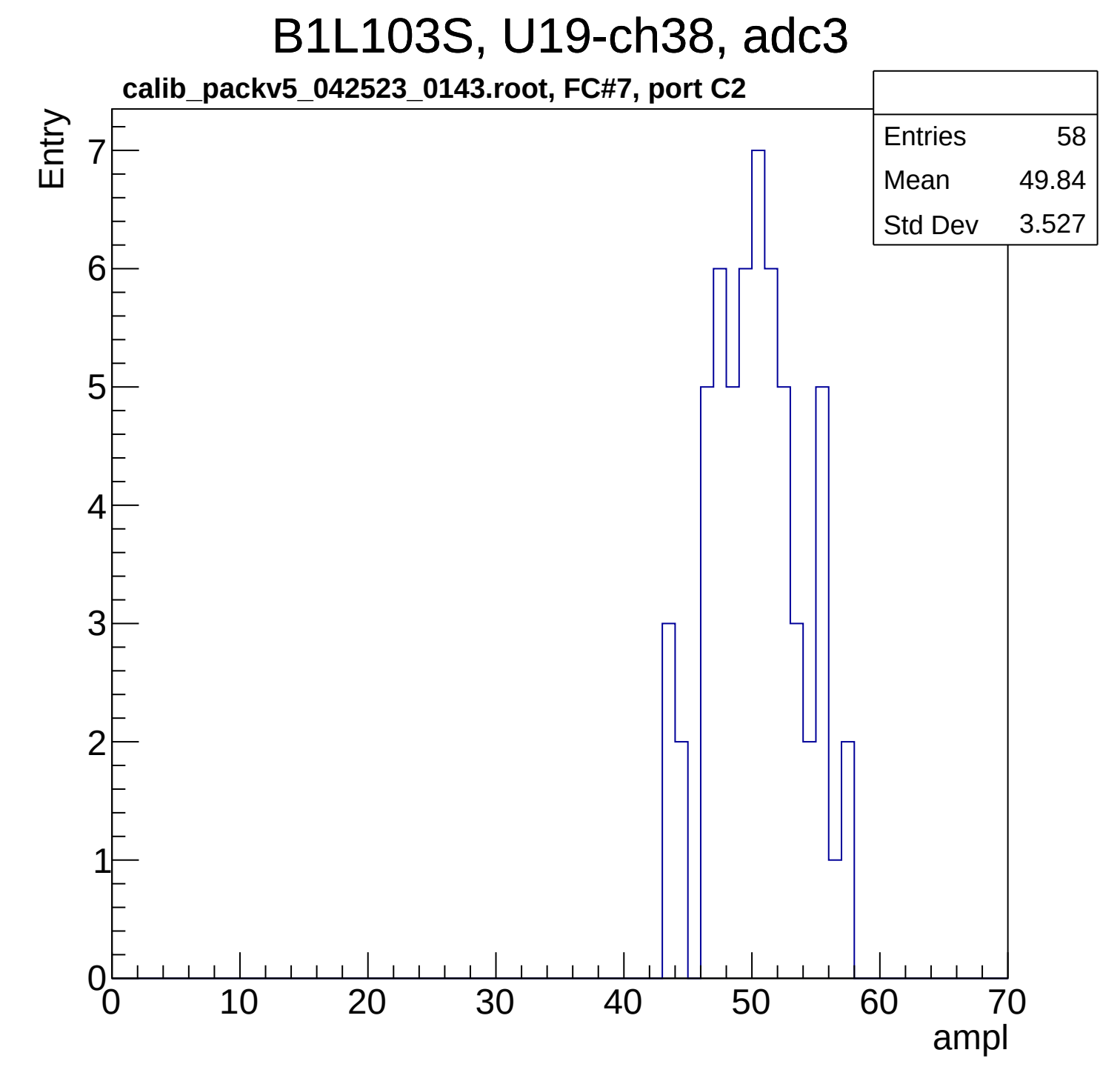
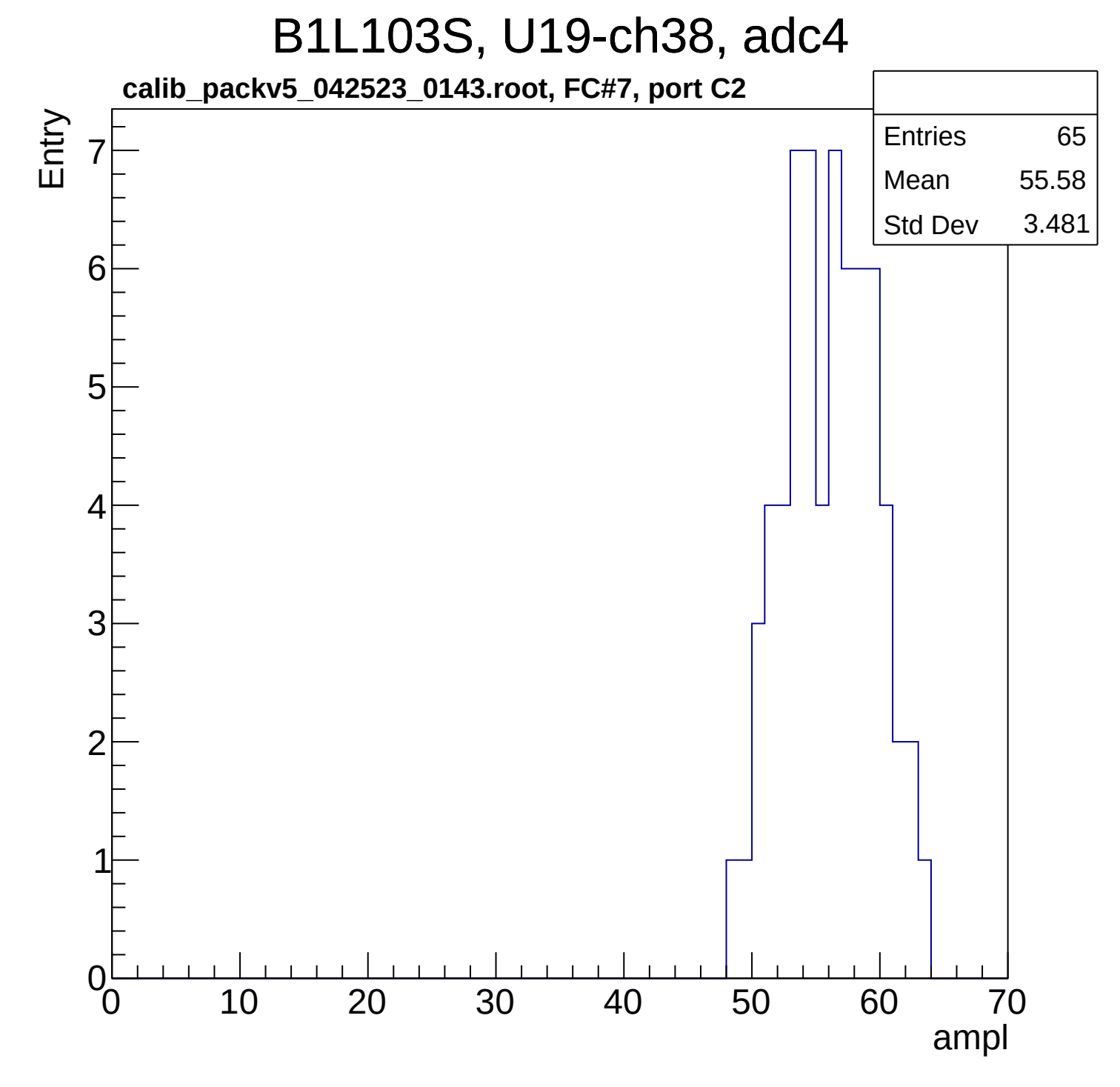
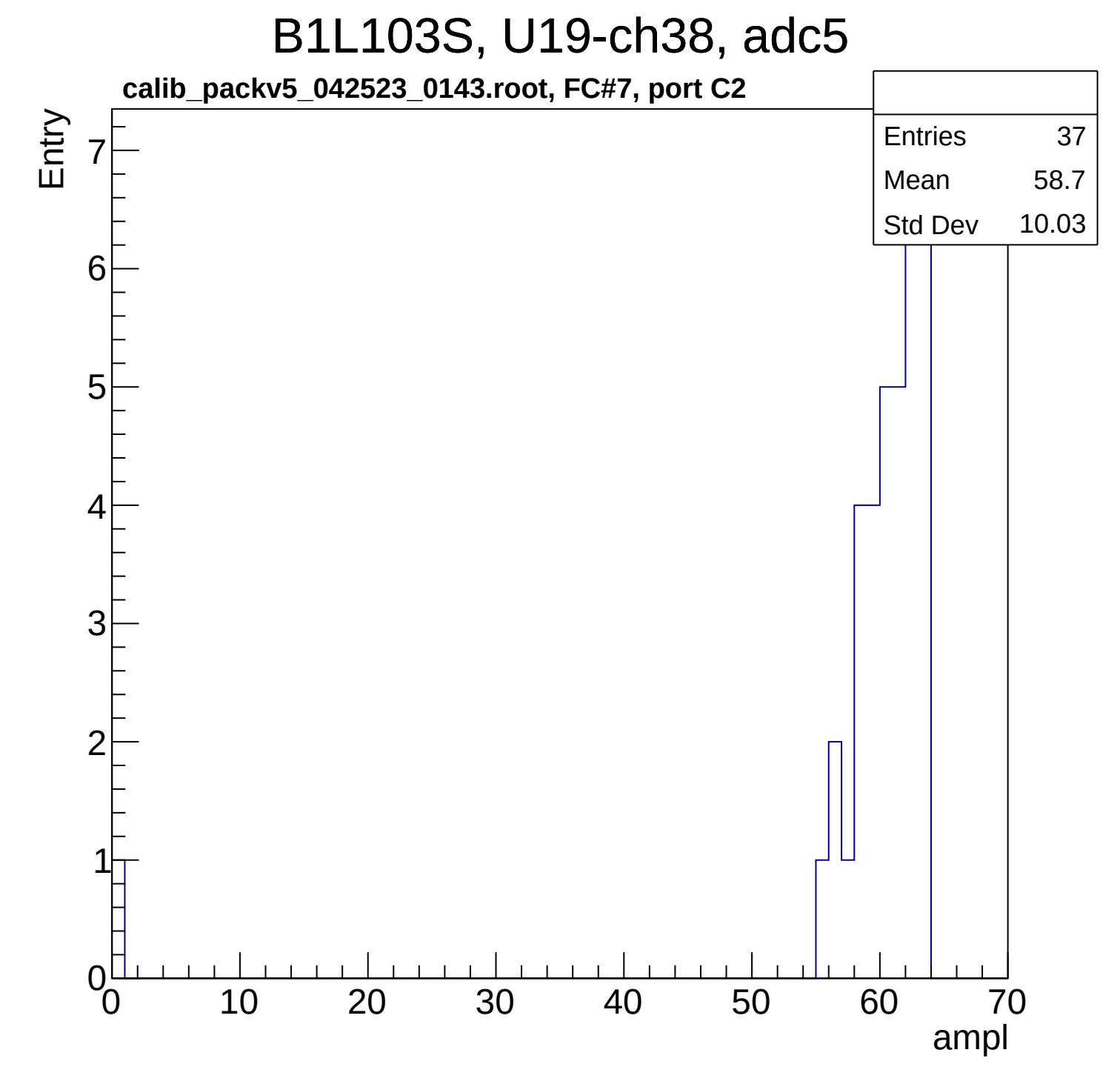

B1L103S, U19-ch38, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.84
Std Dev 3.527
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch38, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 55.58
Std Dev 3.481
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch38, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 58.7
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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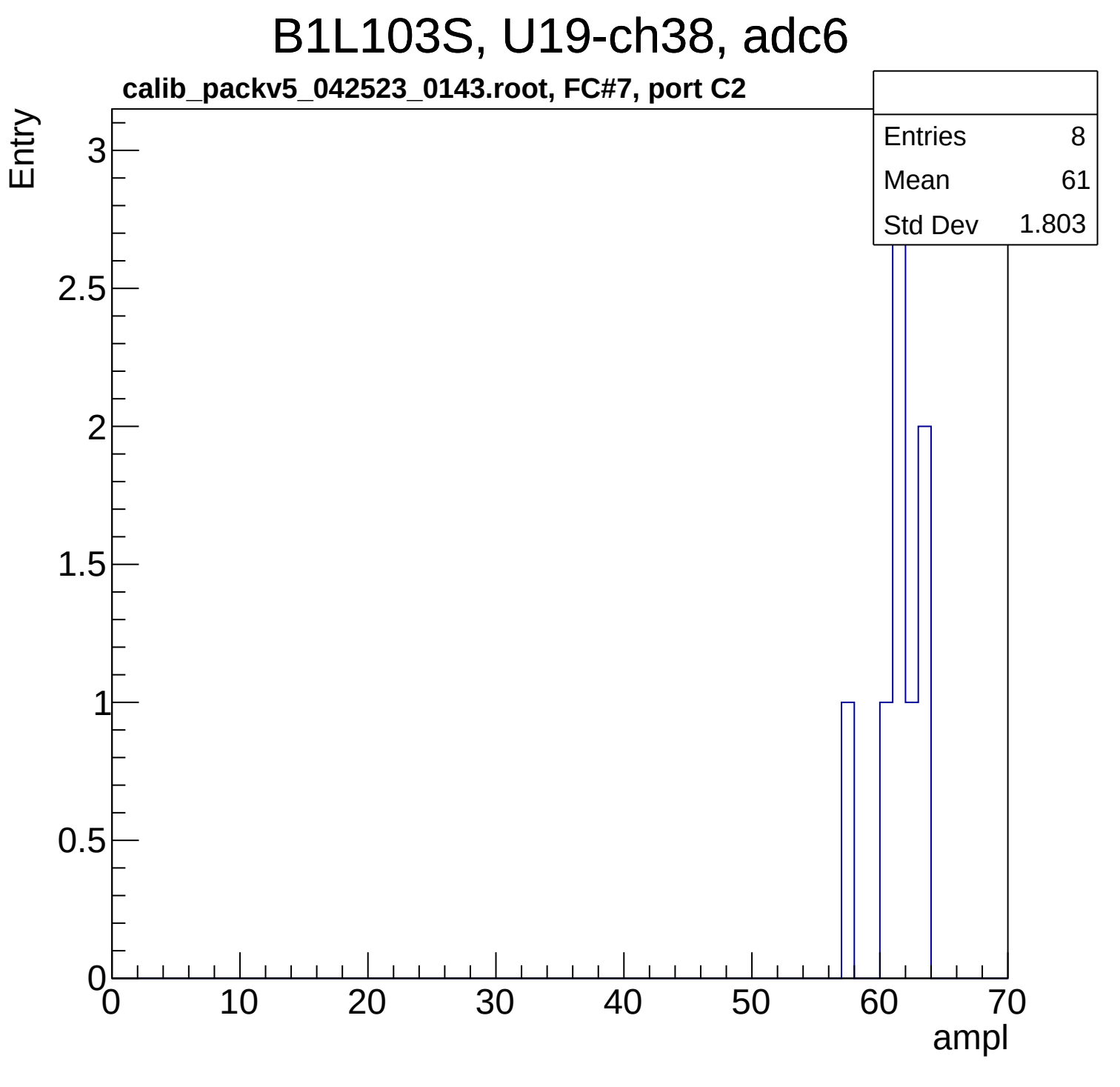
B1L103S, U19-ch38, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61
Std Dev 1.803
Entry
ampl
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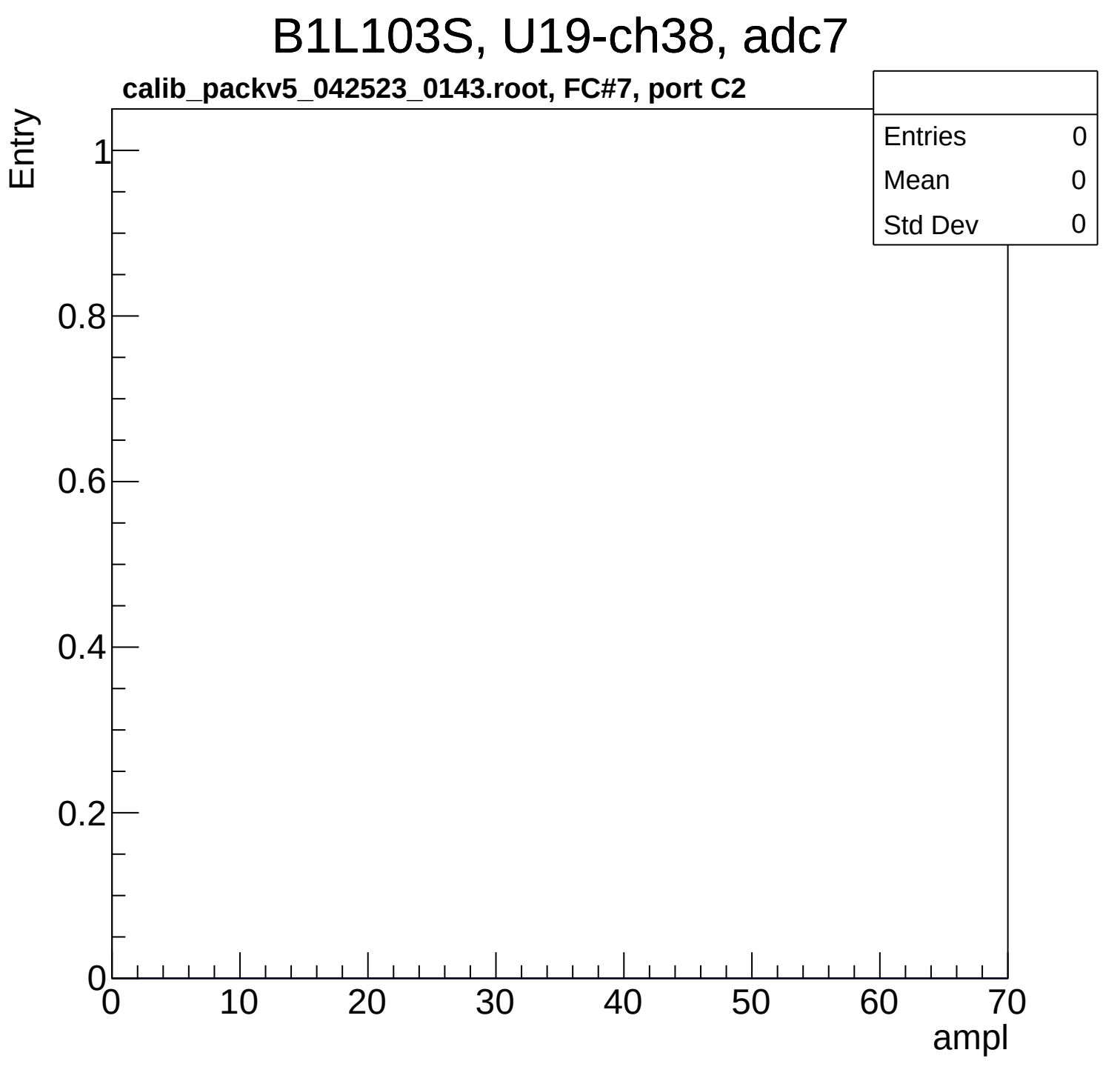
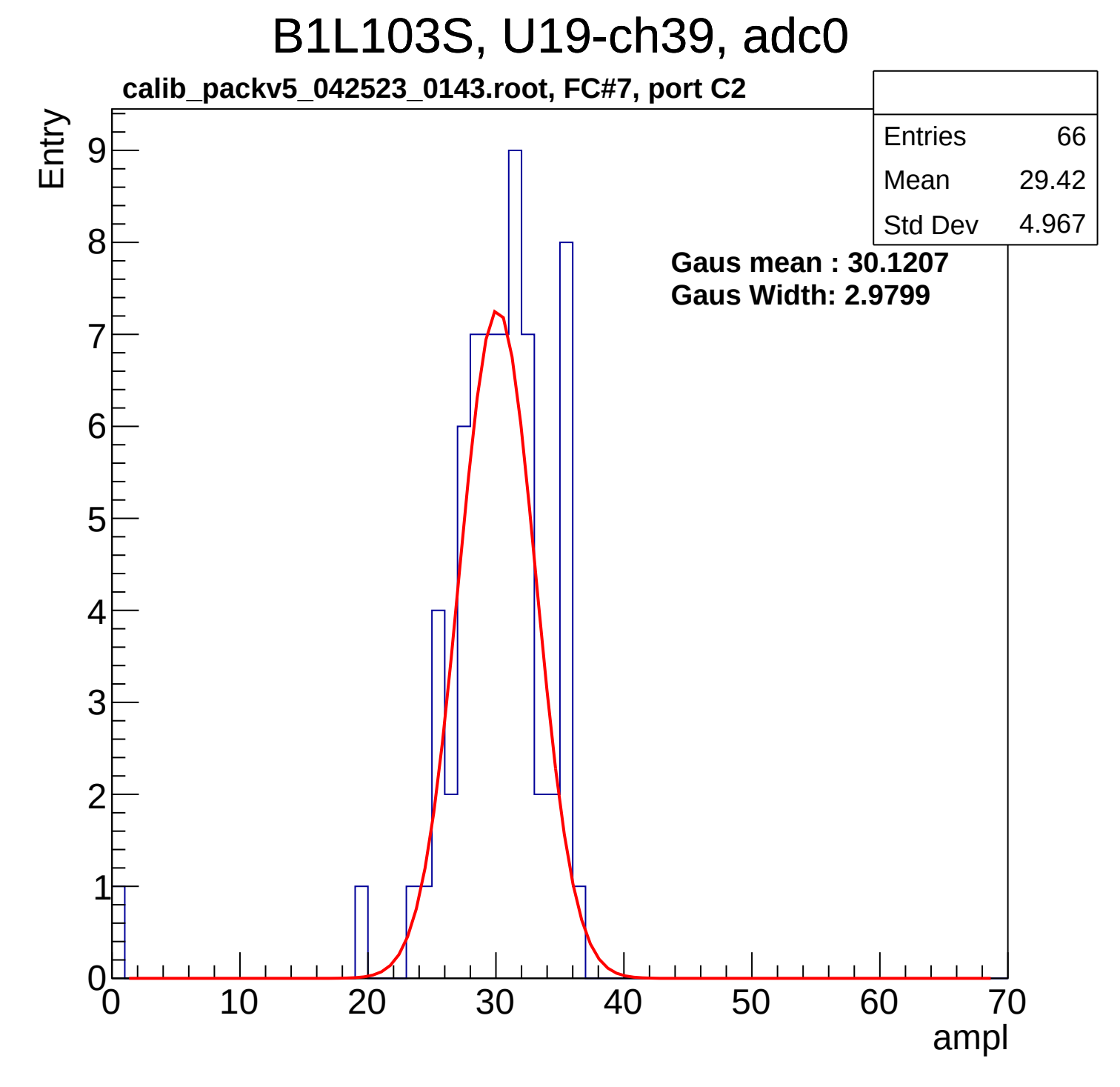
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch38, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch39, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.42
Std Dev 4.967
Gaus mean : 30.1207
Gaus Width: 2.9799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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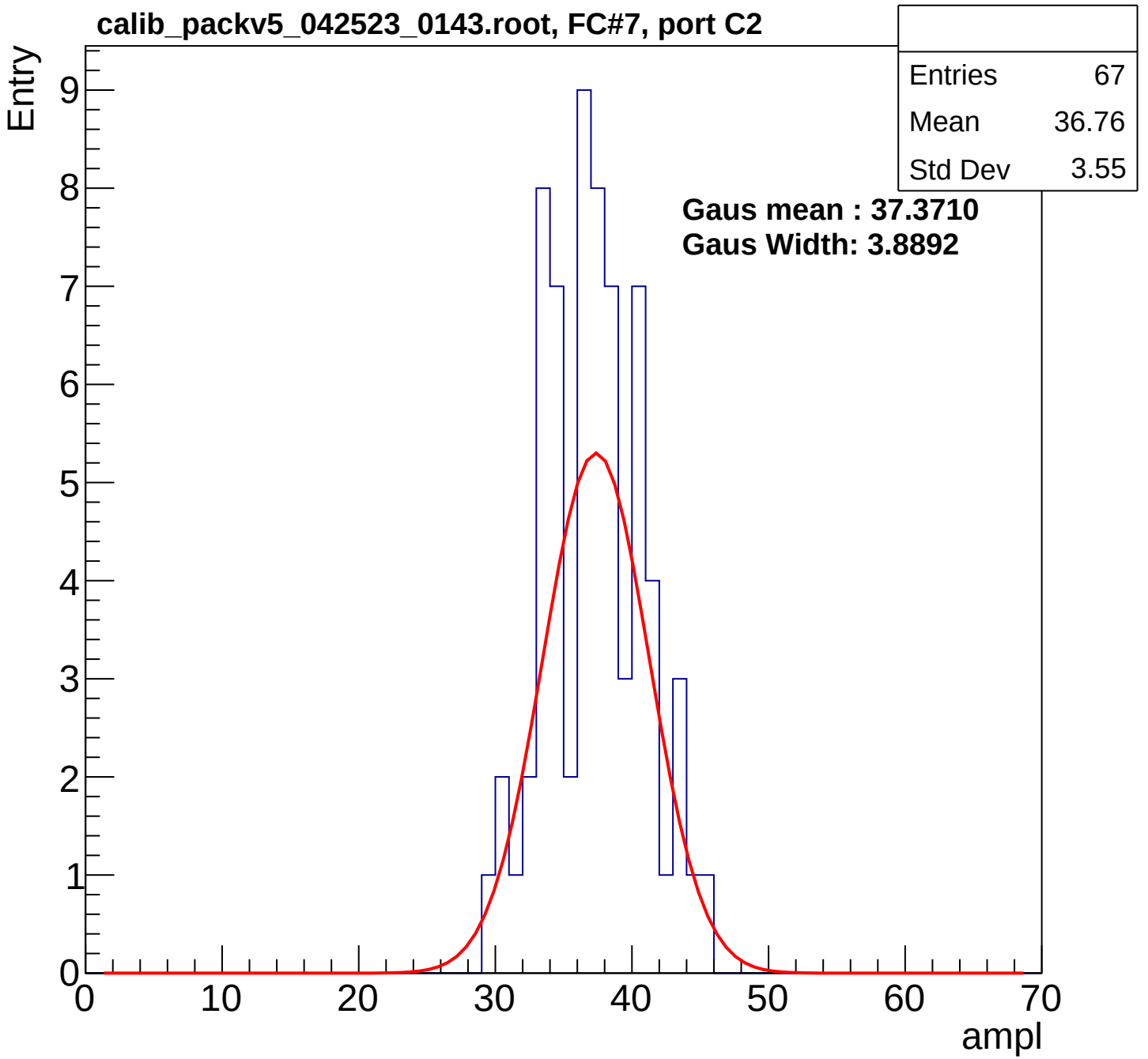
B1L103S, U19-ch39, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.76
Std Dev 3.55
Gaus mean : 37.3710
Gaus Width: 3.8892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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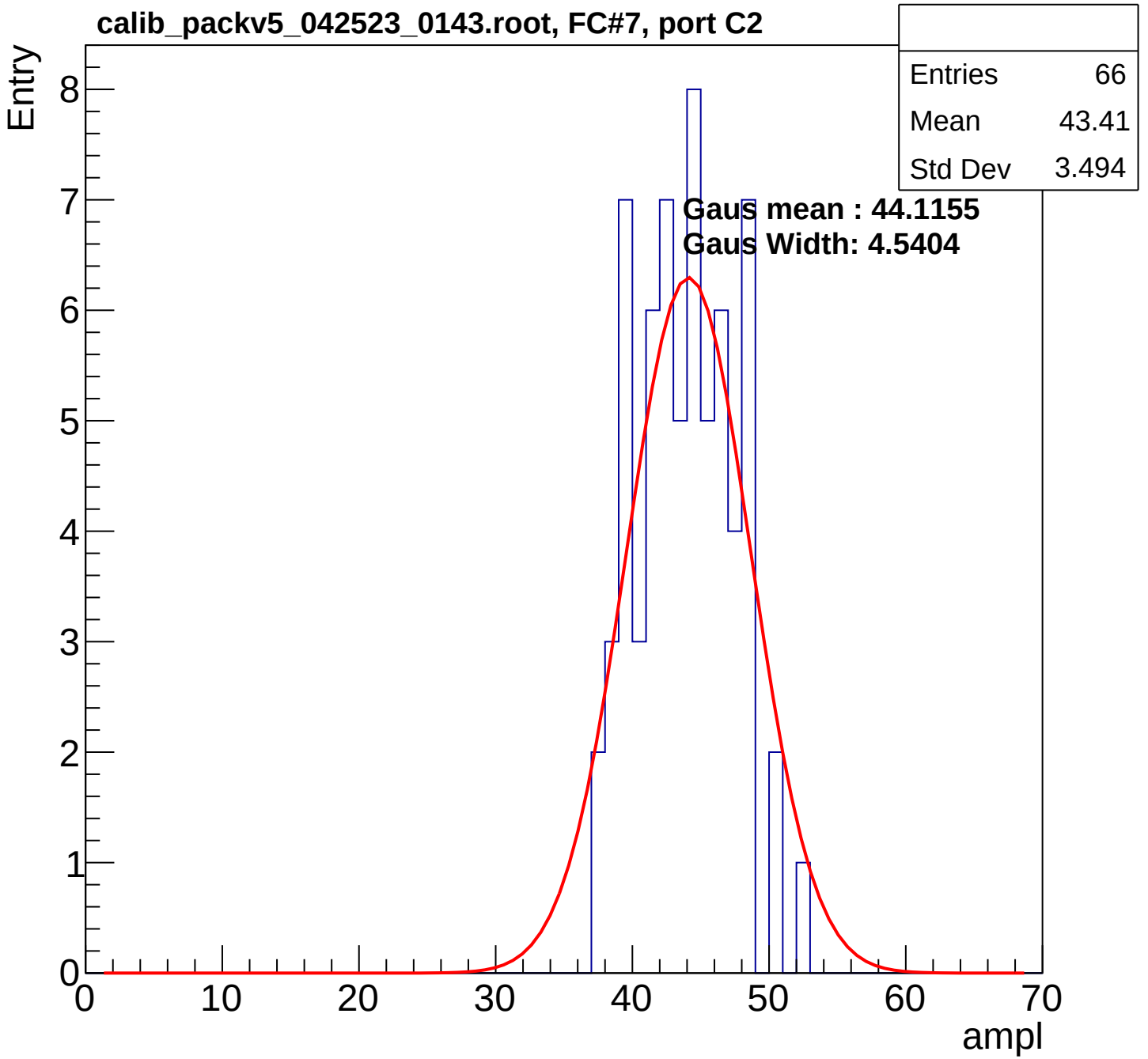
B1L103S, U19-ch39, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43.41
Std Dev 3.494
Gaus mean : 44.1155
Gaus Width: 4.5404
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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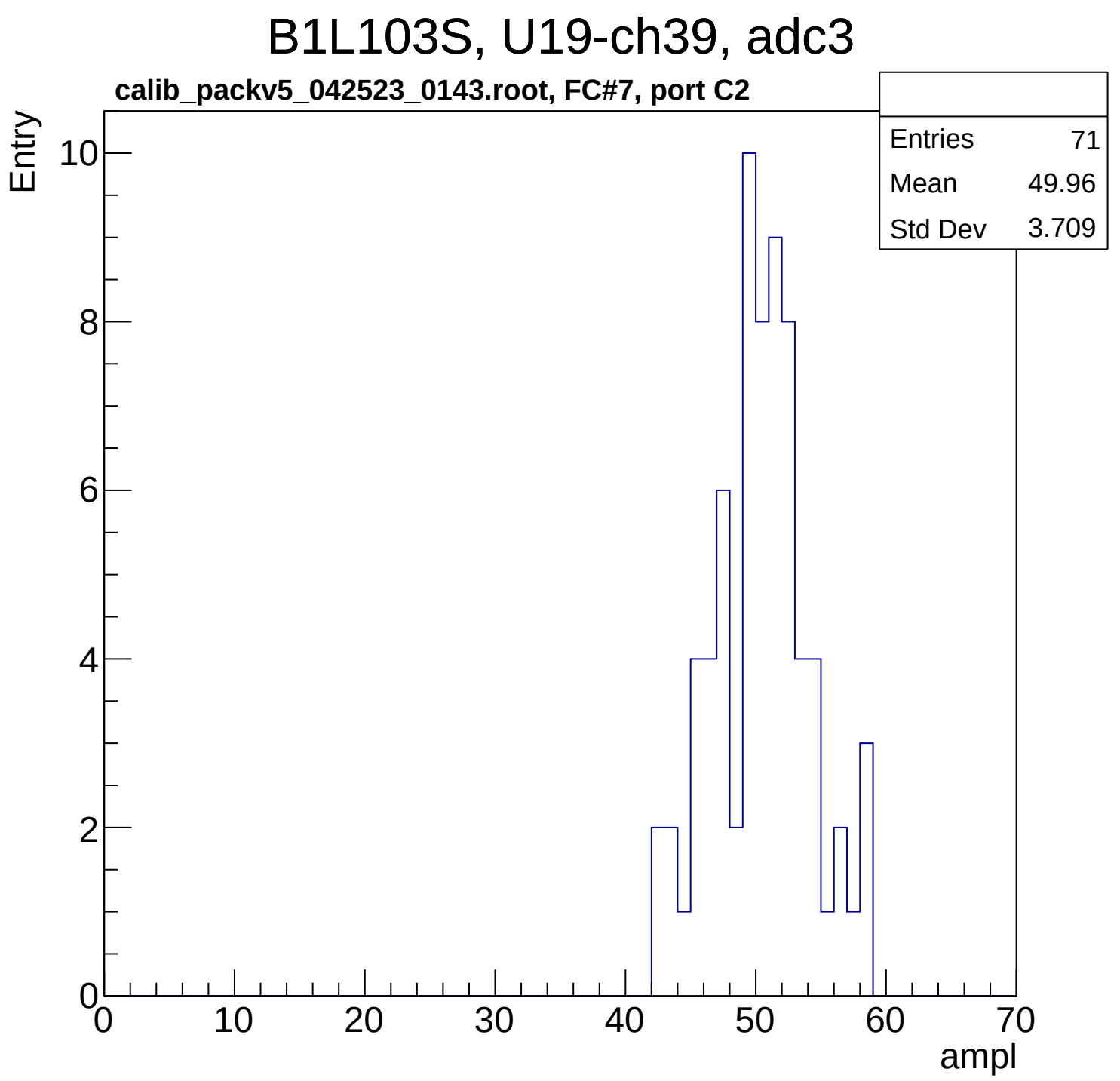
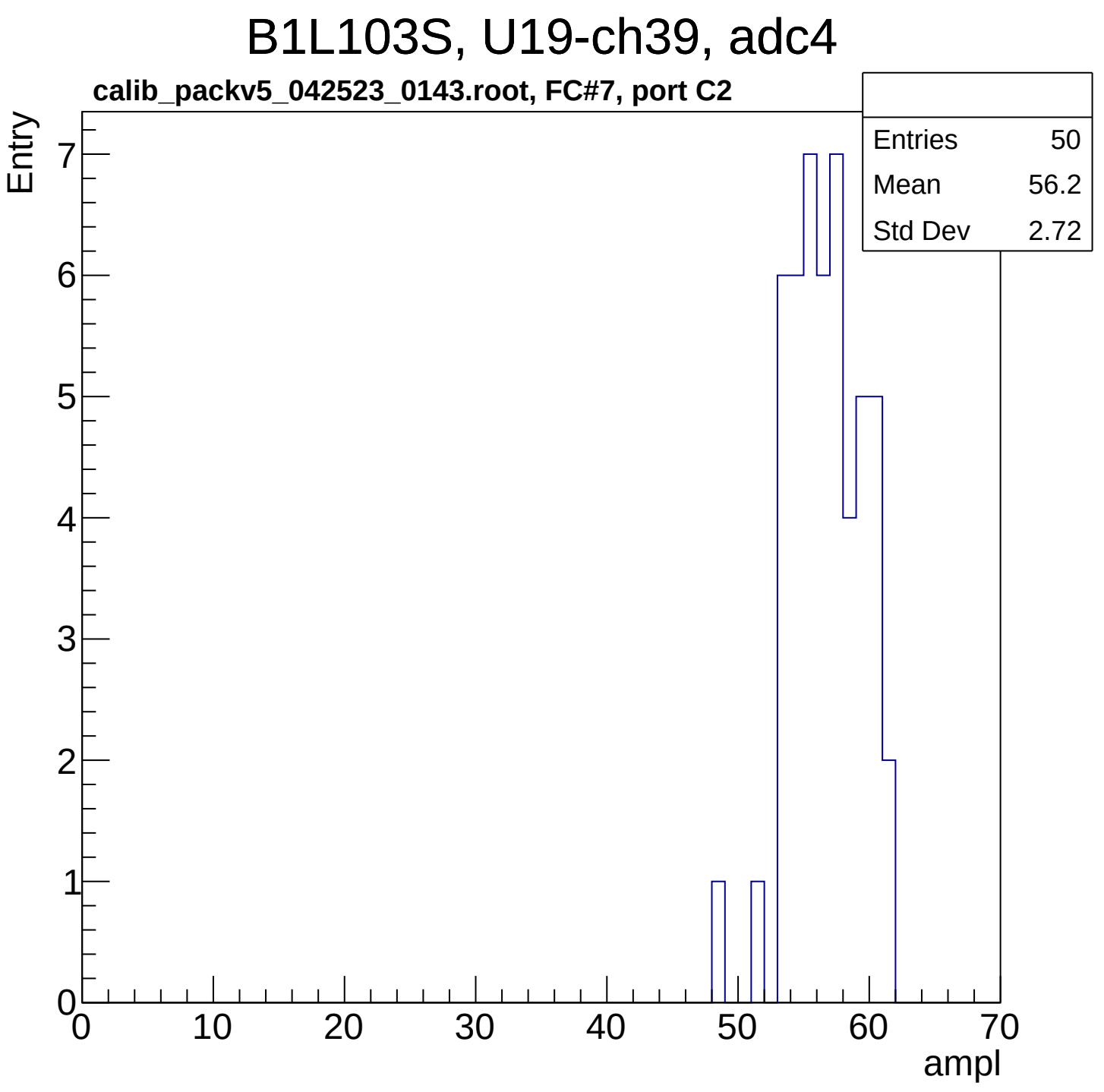
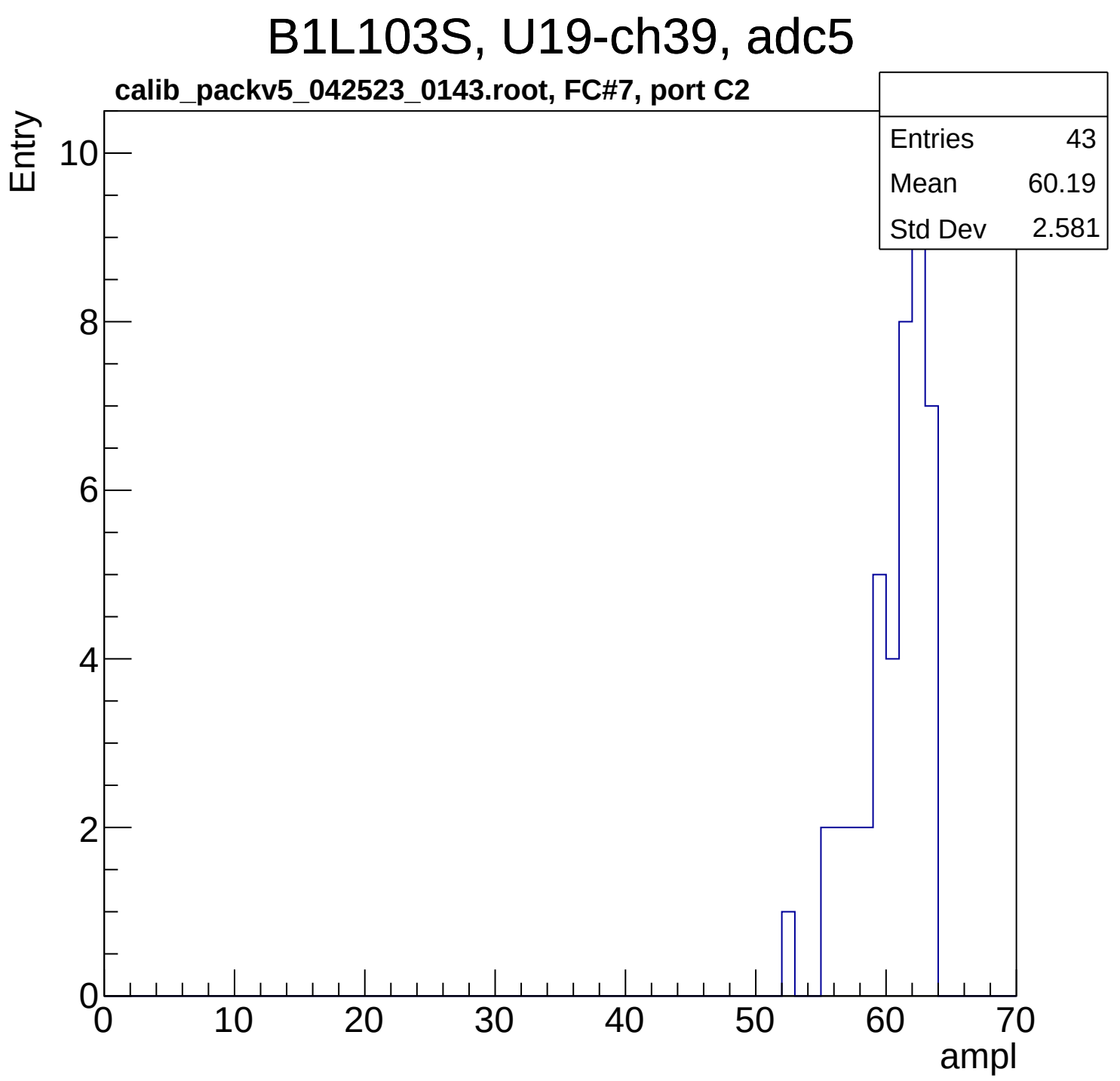
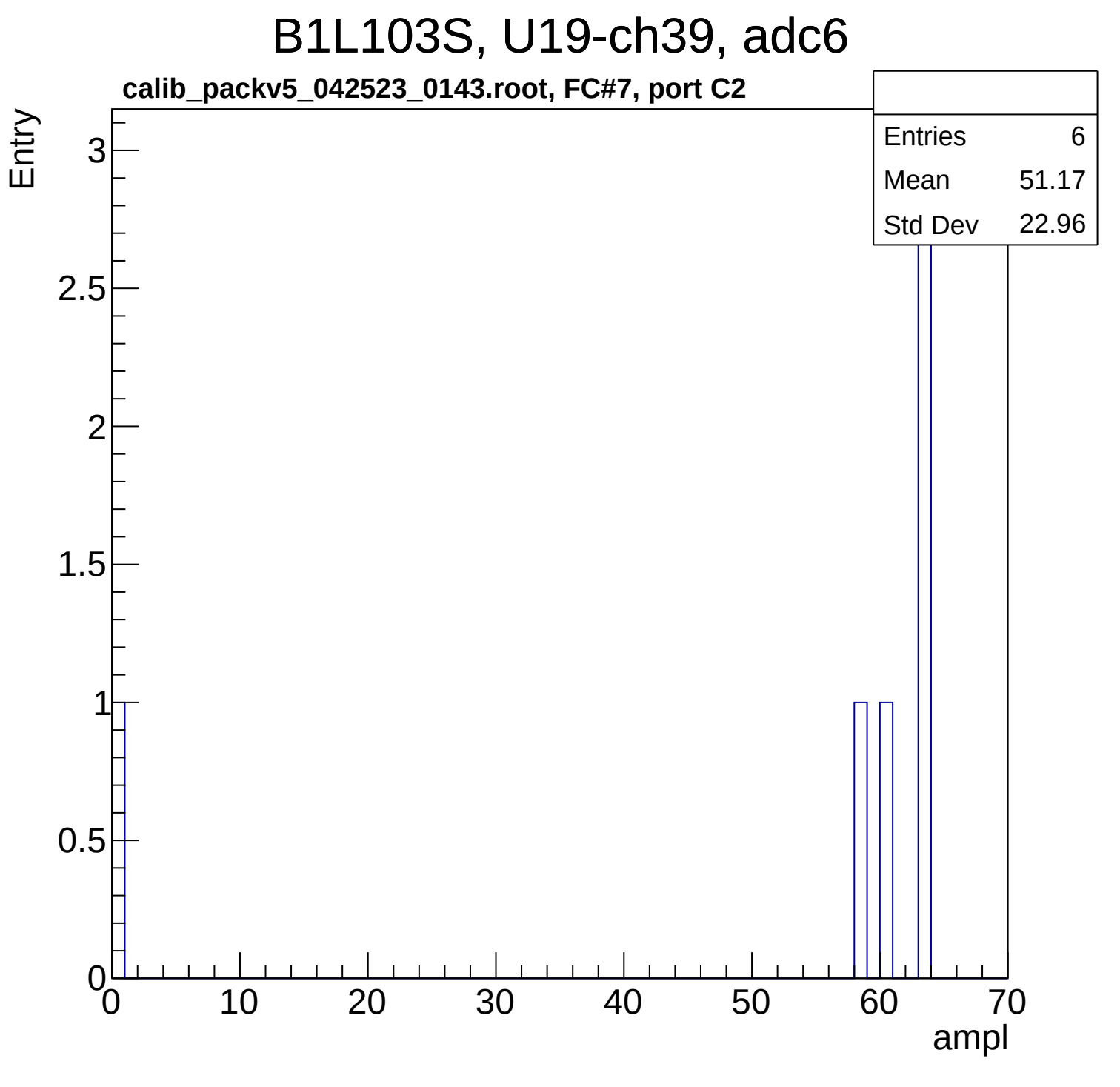

B1L103S, U19-ch39, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.96
Std Dev 3.709
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch39, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 56.2
Std Dev 2.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch39, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 60.19
Std Dev 2.581
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch39, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.17
Std Dev 22.96
Entry
ampl
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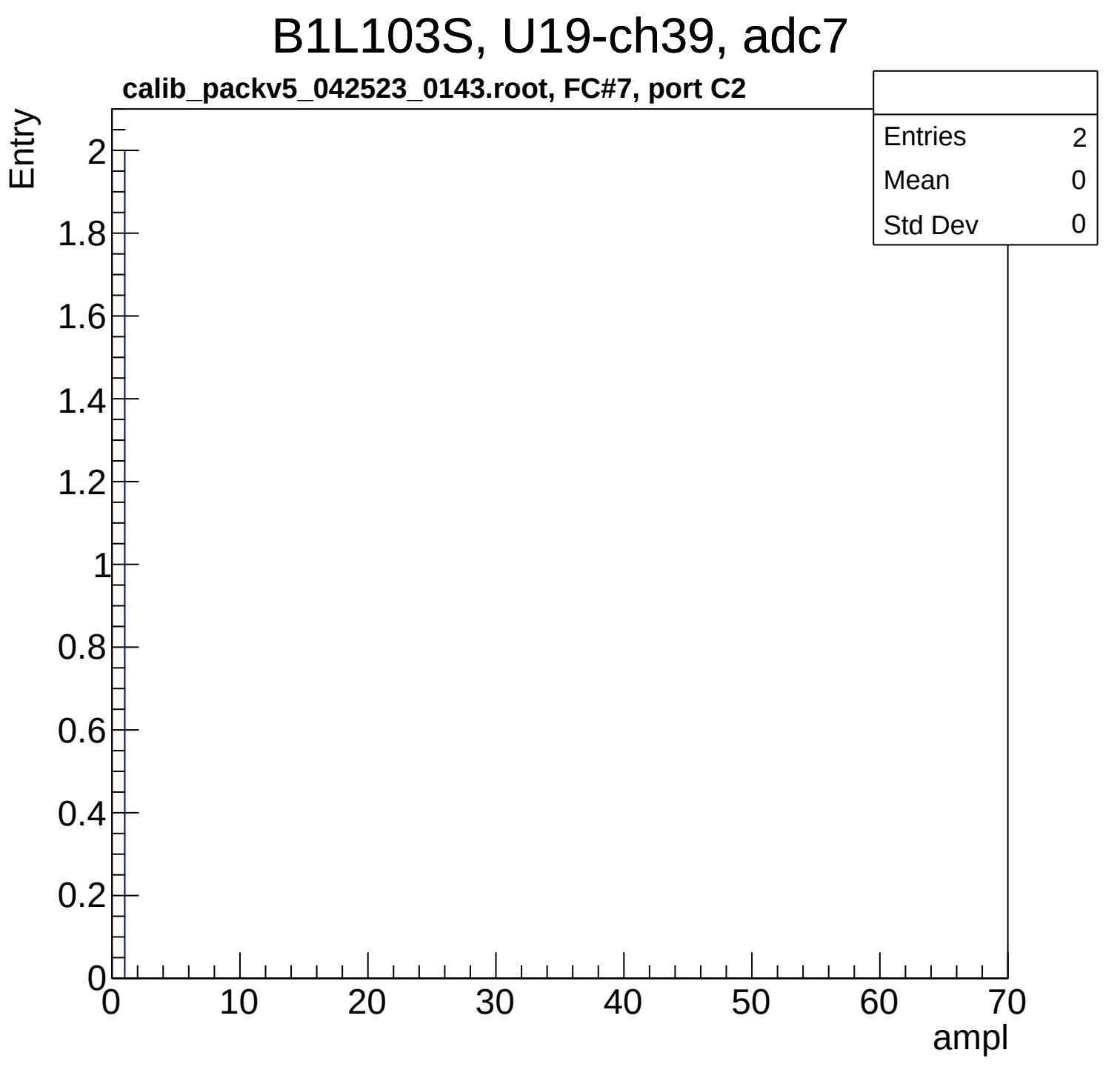
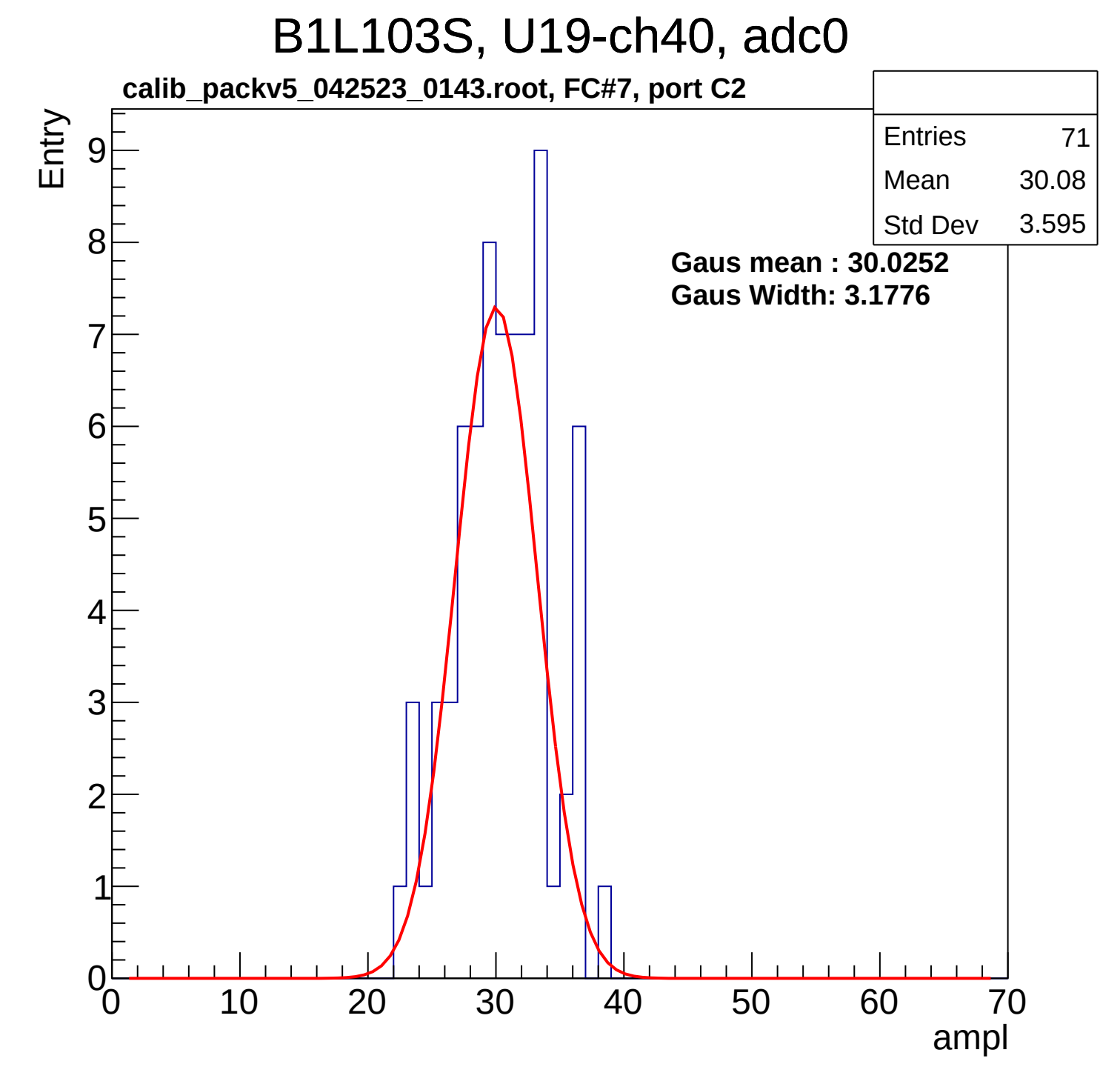
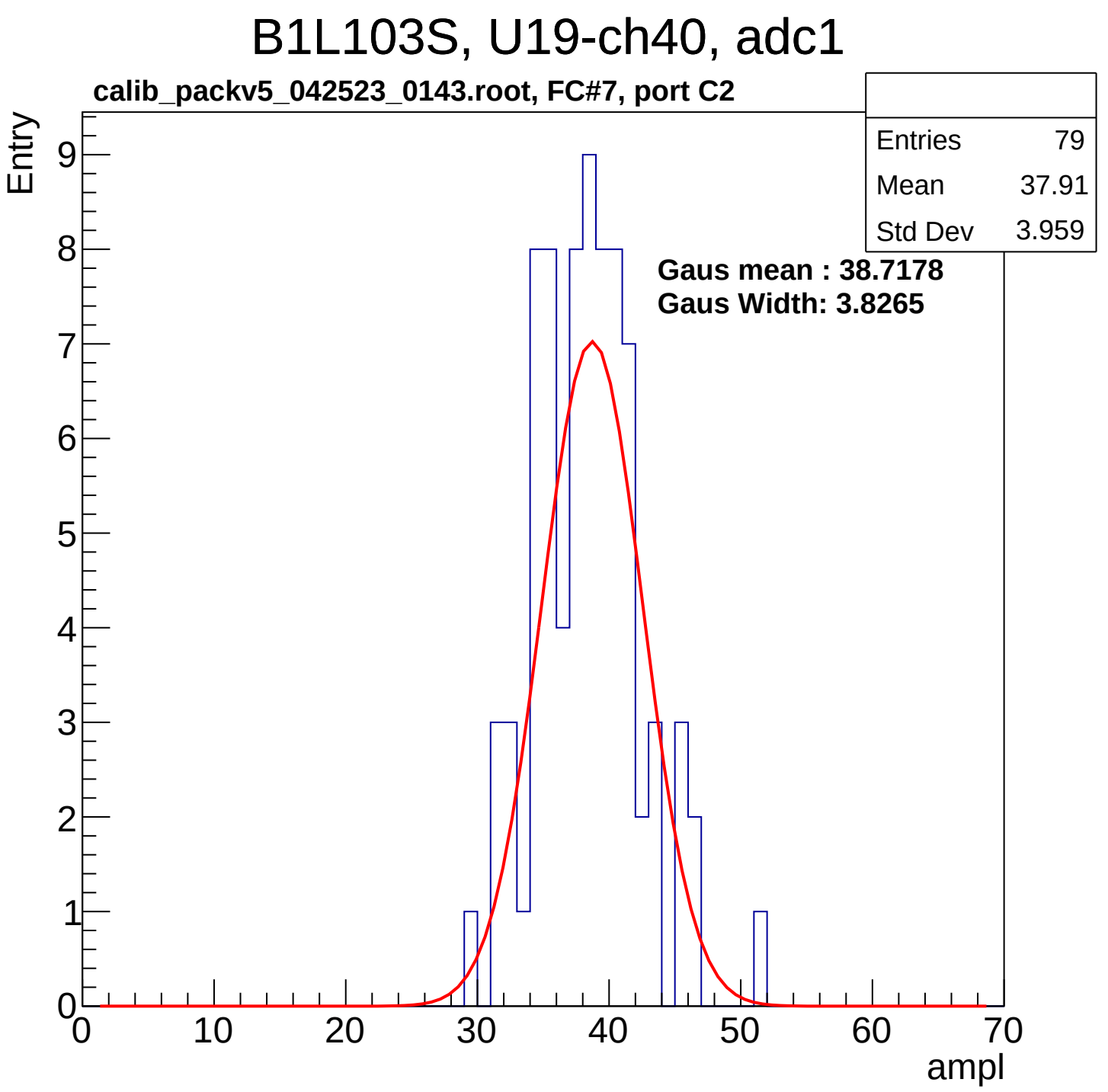
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch39, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch40, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 30.08
Std Dev 3.595
Gaus mean : 30.0252
Gaus Width: 3.1776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch40, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 37.91
Std Dev 3.959
Gaus mean : 38.7178
Gaus Width: 3.8265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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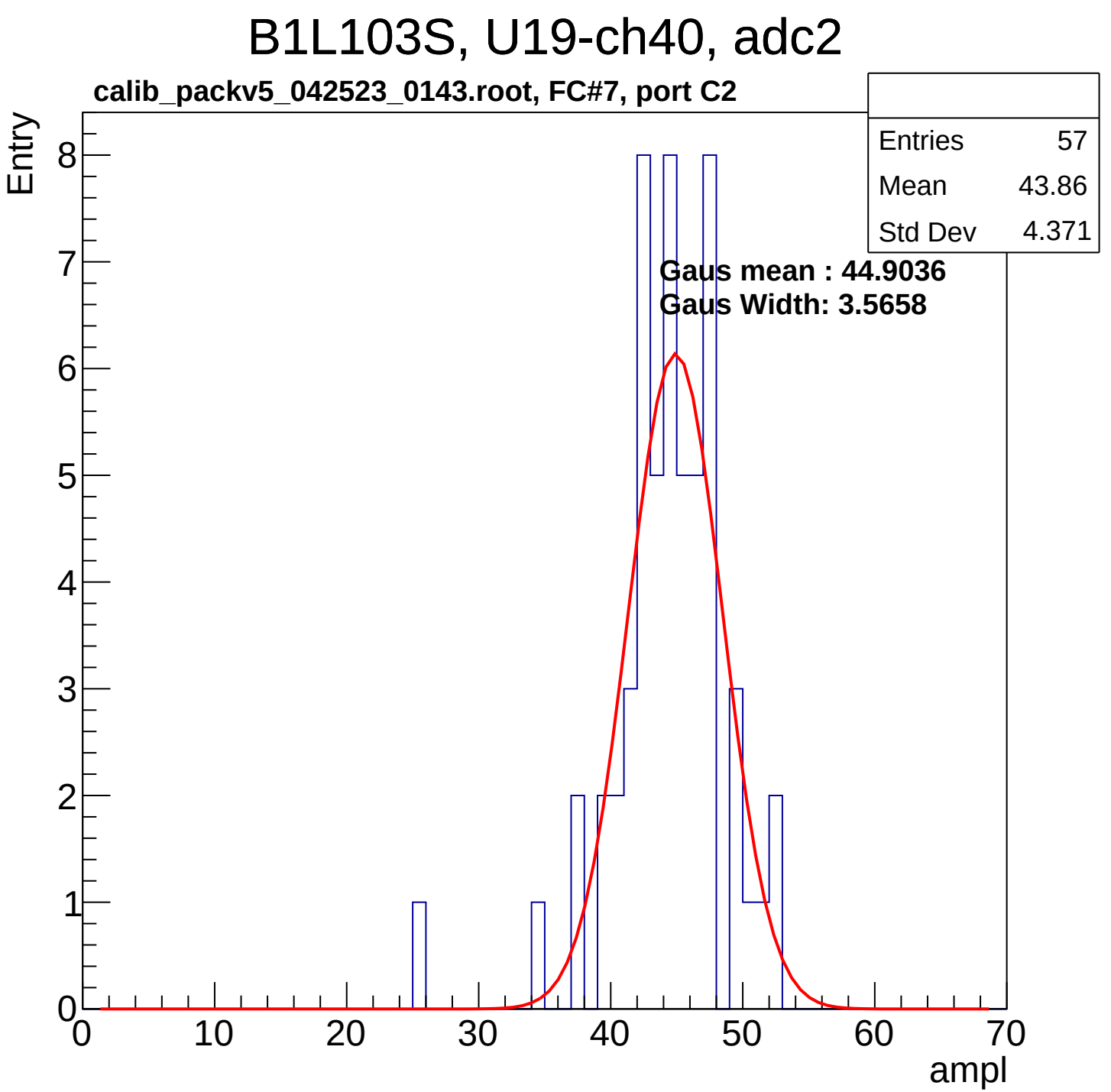
B1L103S, U19-ch40, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.86
Std Dev 4.371
Gaus mean : 44.9036
Gaus Width: 3.5658
Entry
ampl
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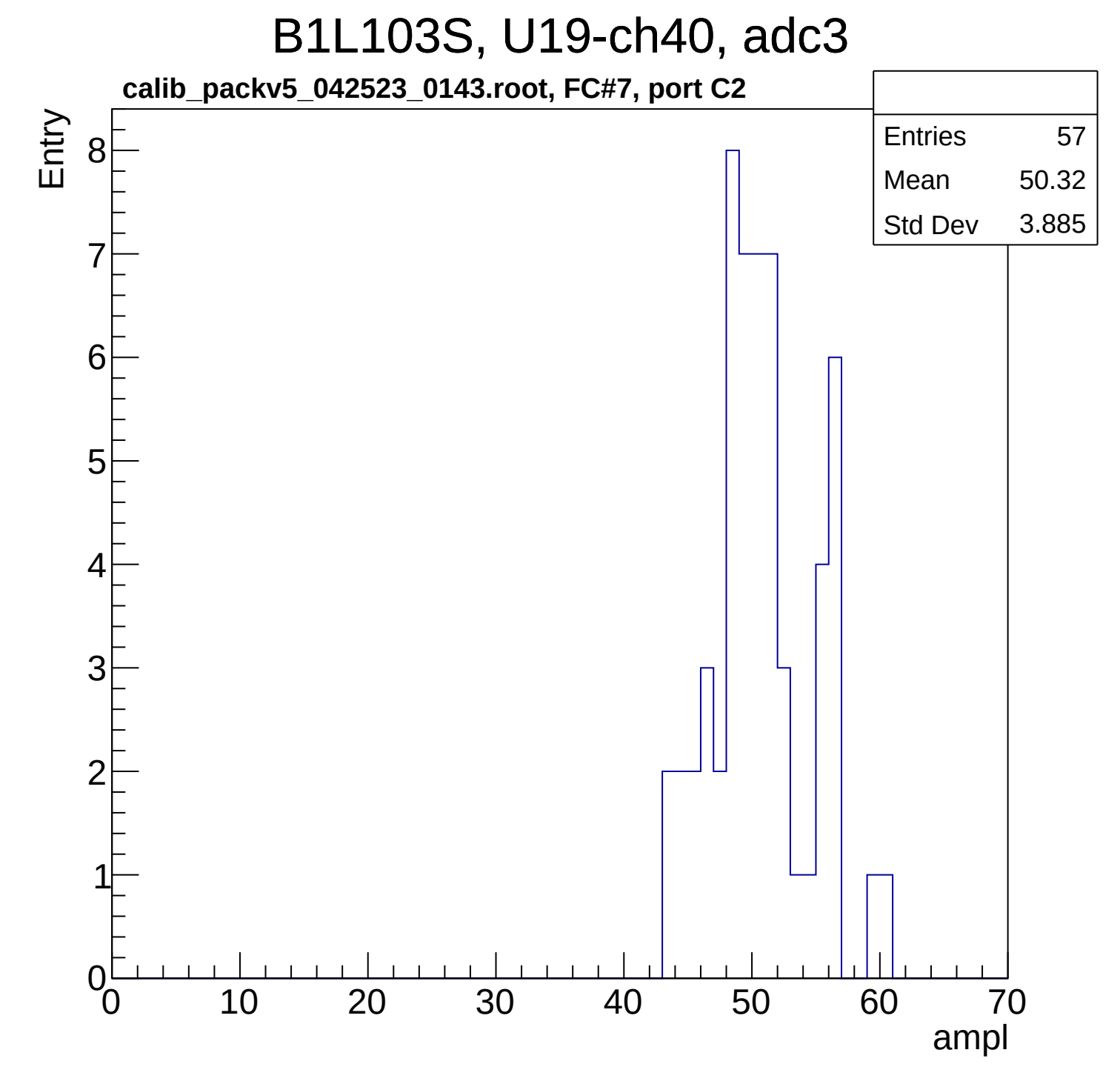
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch40, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 50.32
Std Dev 3.885
Entry
ampl
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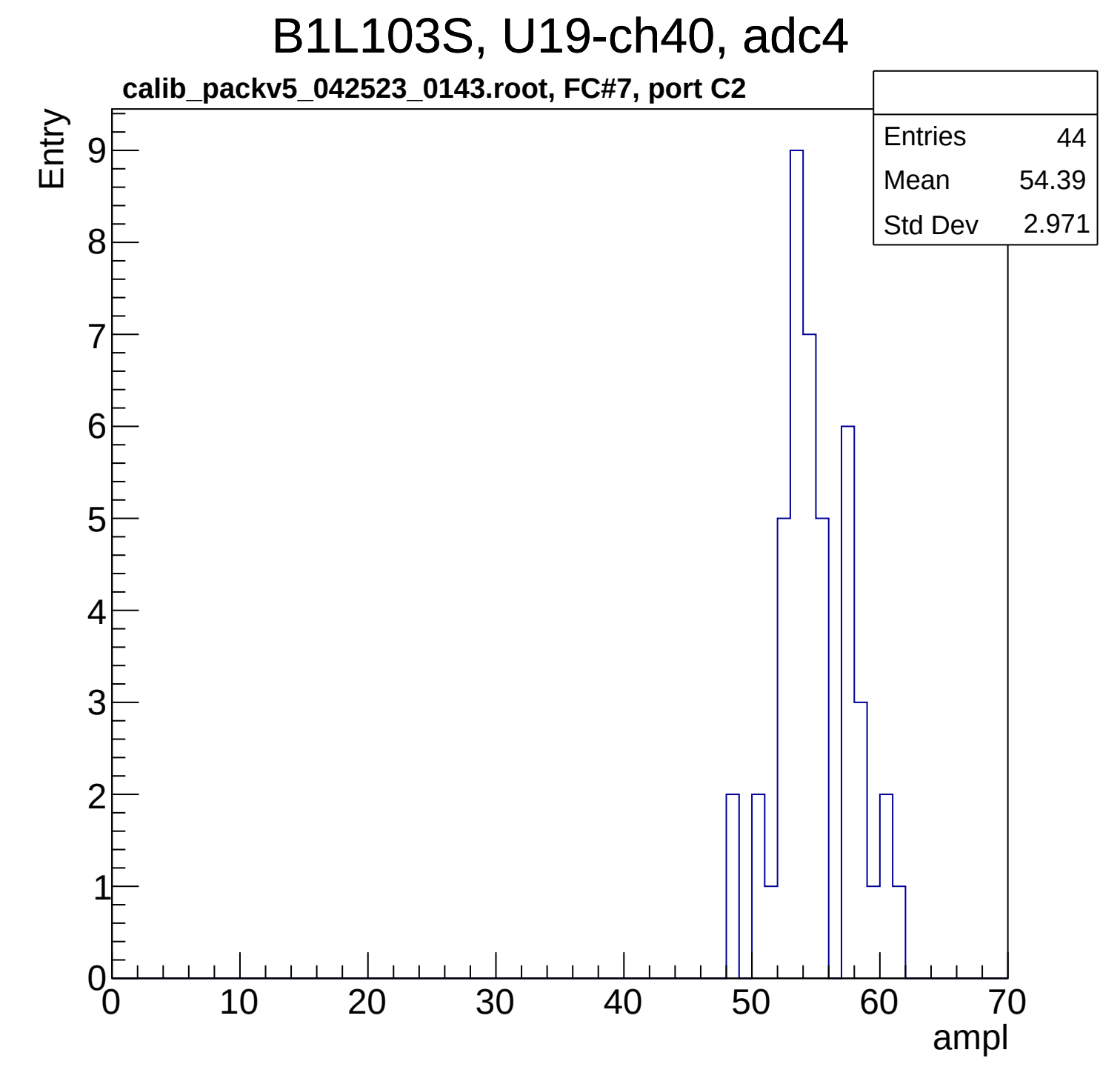
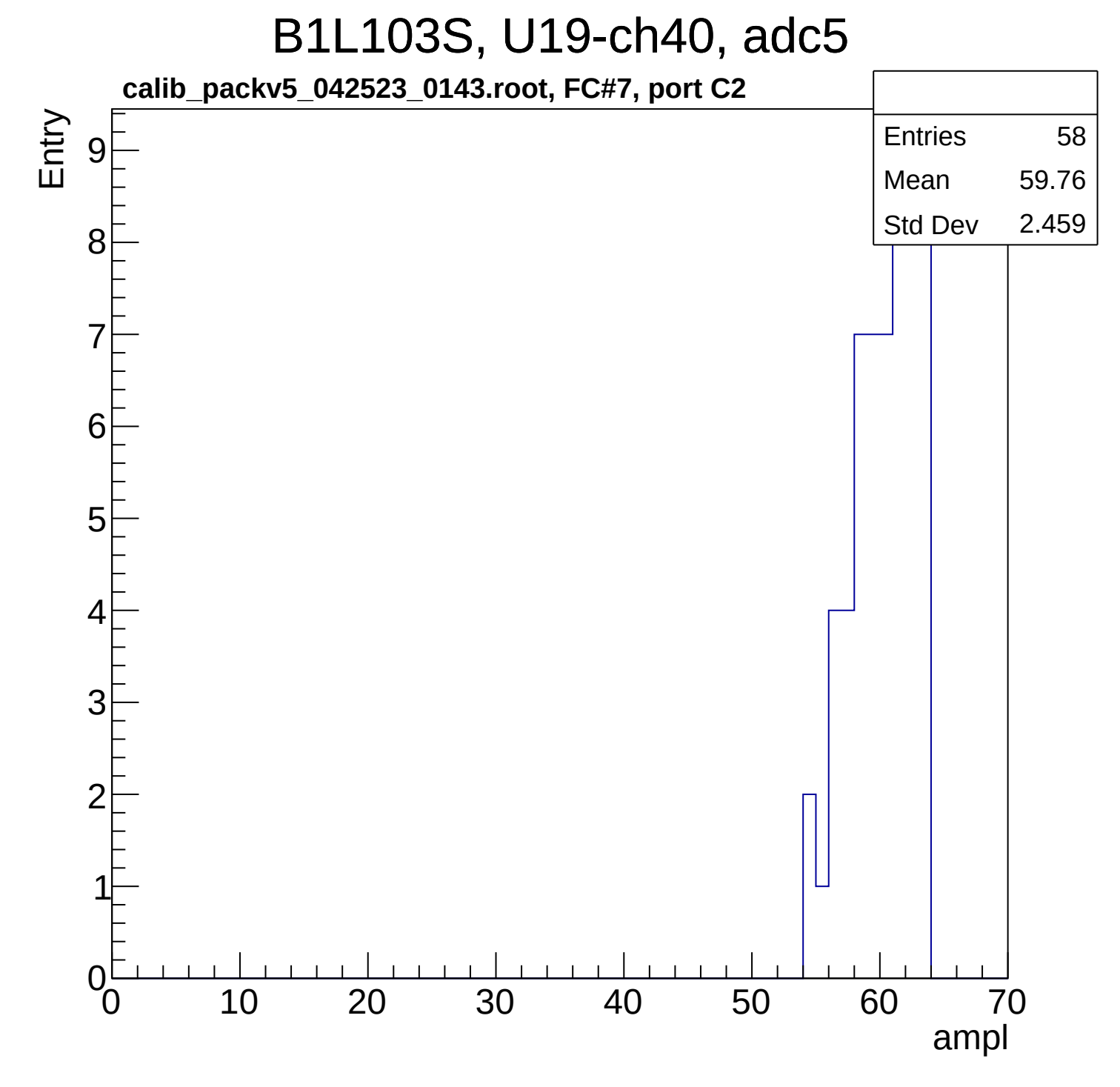
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch40, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 54.39
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch40, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 59.76
Std Dev 2.459
Entry
ampl
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2
3
4
5
6
7
8
9
0
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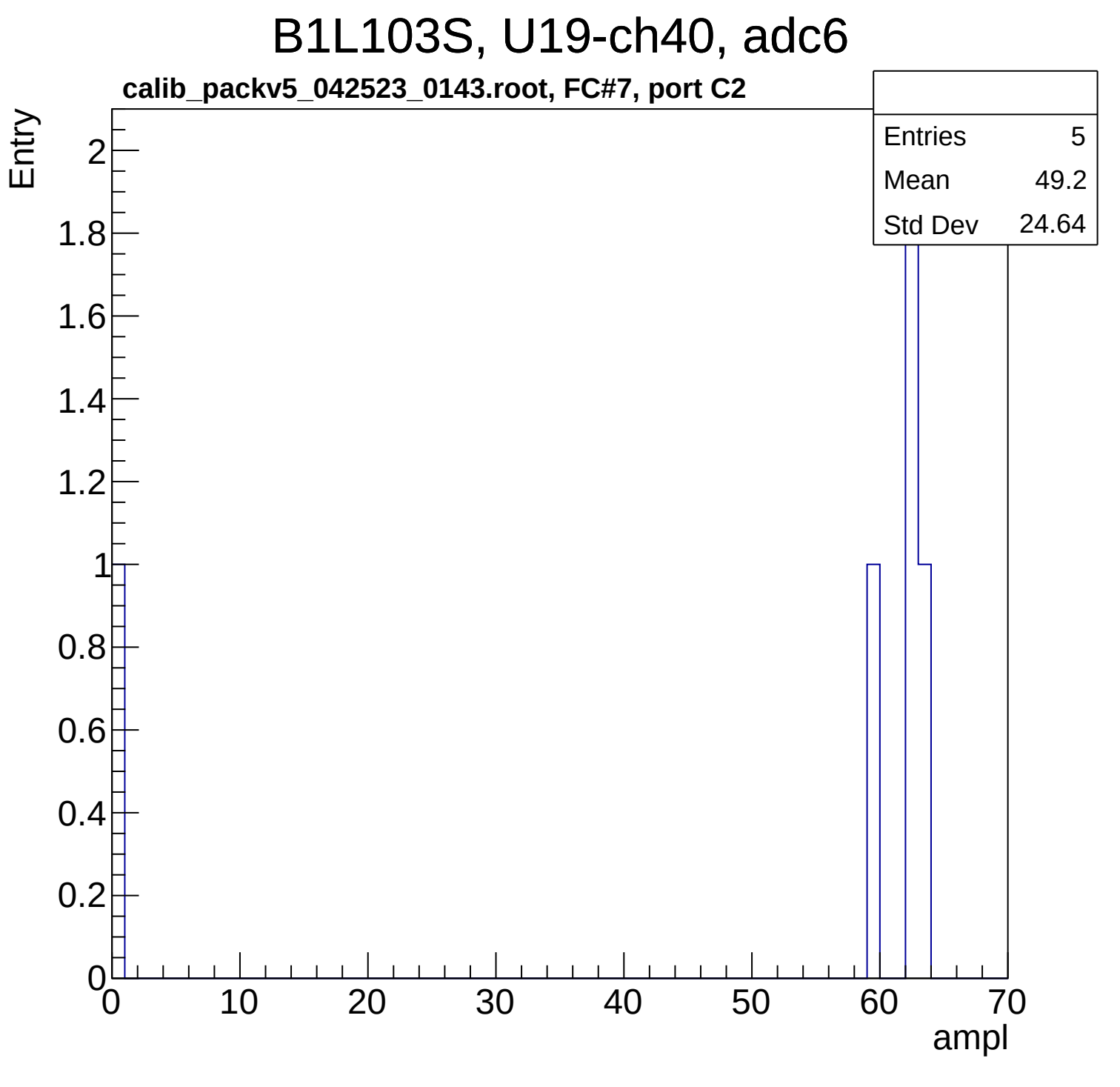
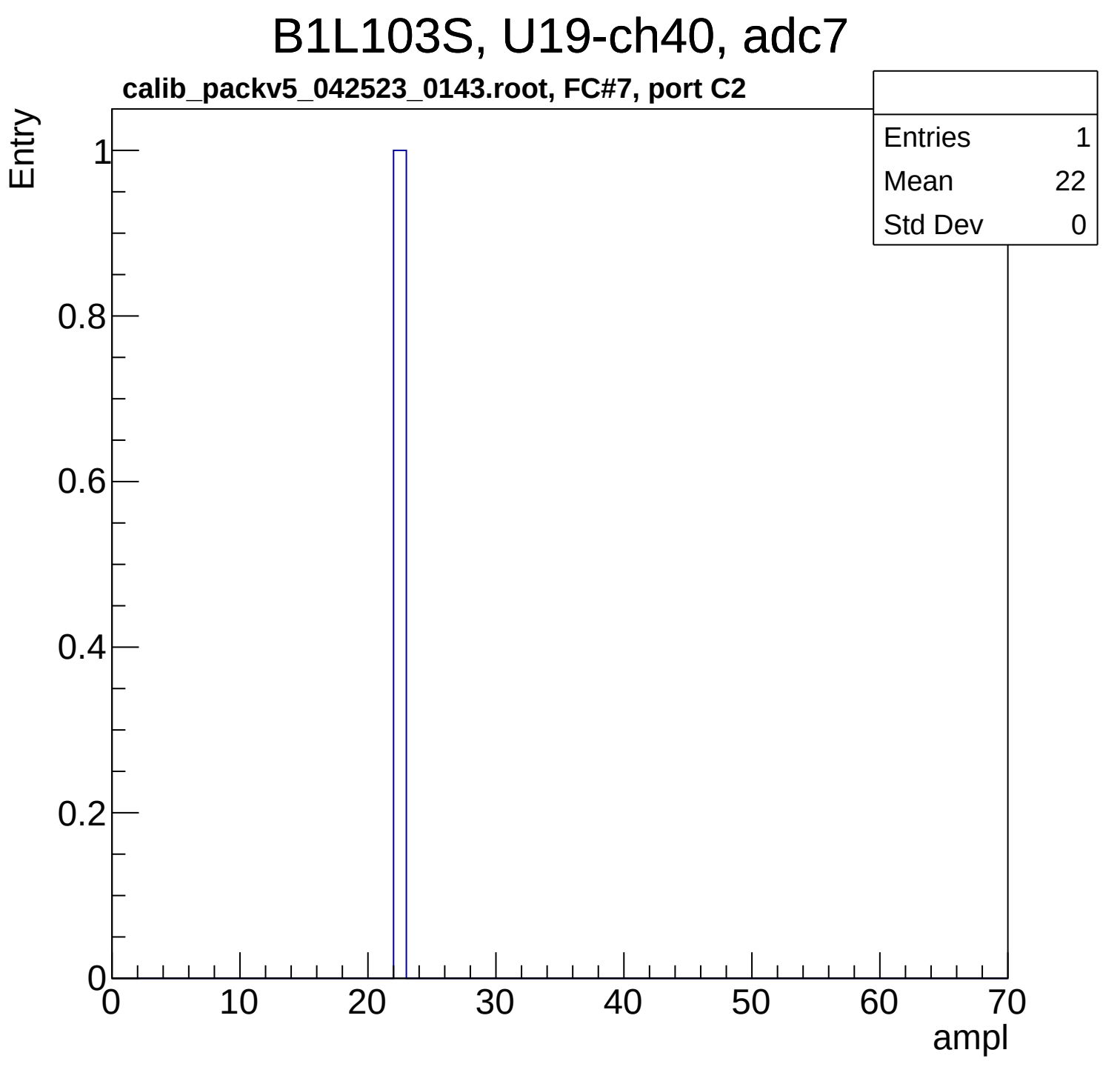
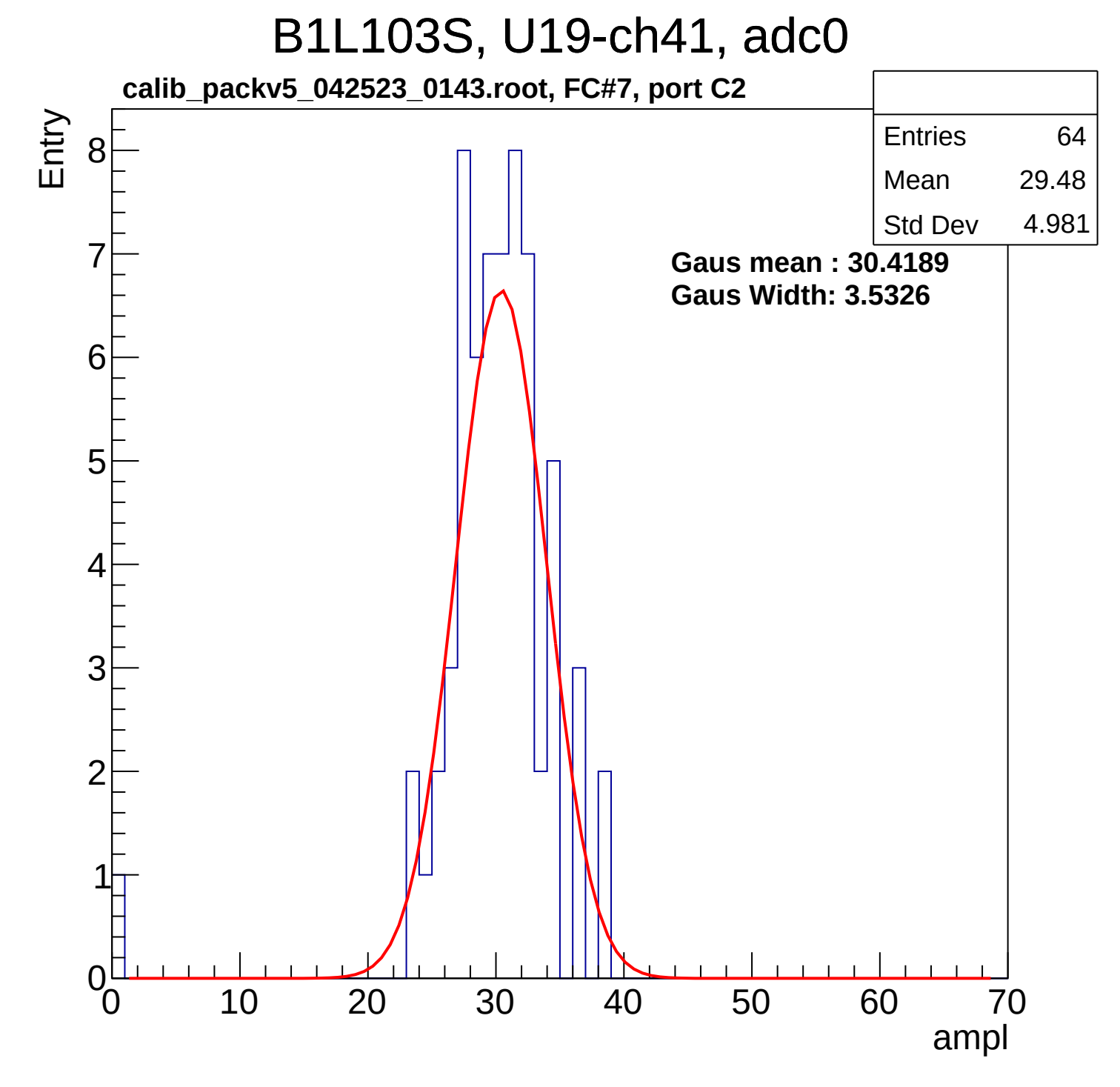
20
30
40
50
60
70


B1L103S, U19-ch40, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.2
Std Dev 24.64
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch40, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch41, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 29.48
Std Dev 4.981
Gaus mean : 30.4189
Gaus Width: 3.5326
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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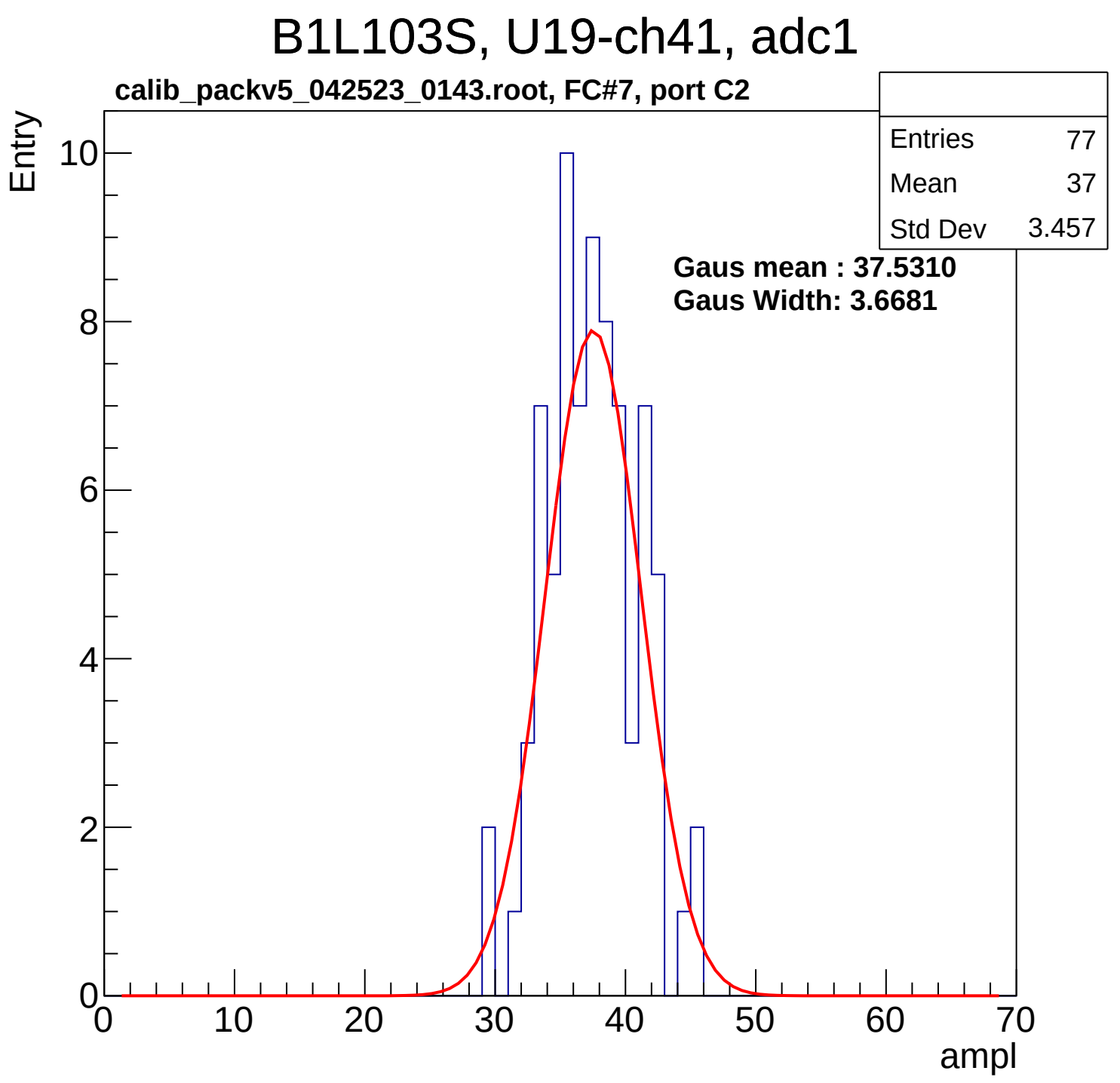
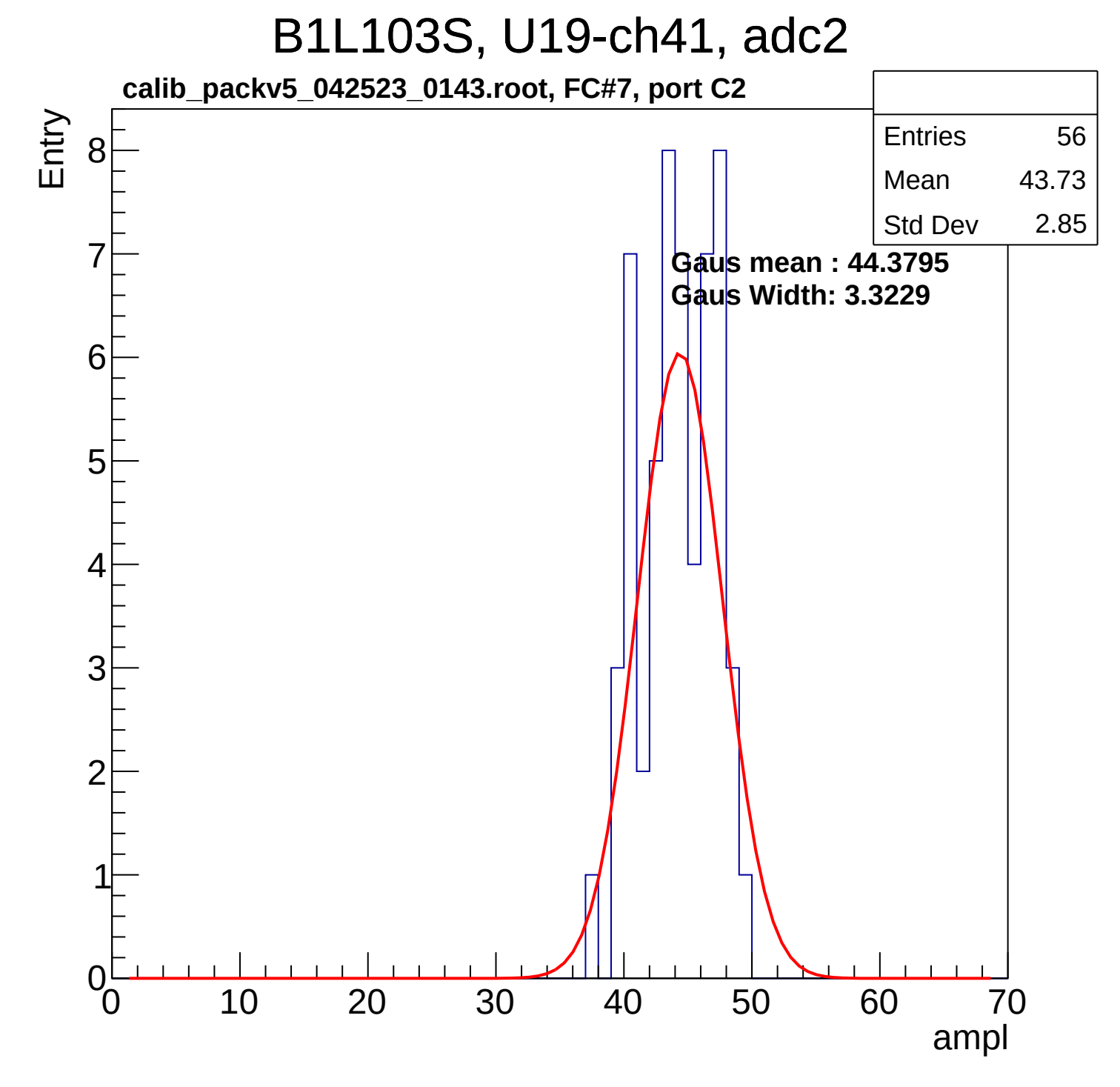

B1L103S, U19-ch41, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 37
Std Dev 3.457
Gaus mean : 37.5310
Gaus Width: 3.6681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch41, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.73
Std Dev 2.85
Gaus mean : 44.3795
Gaus Width: 3.3229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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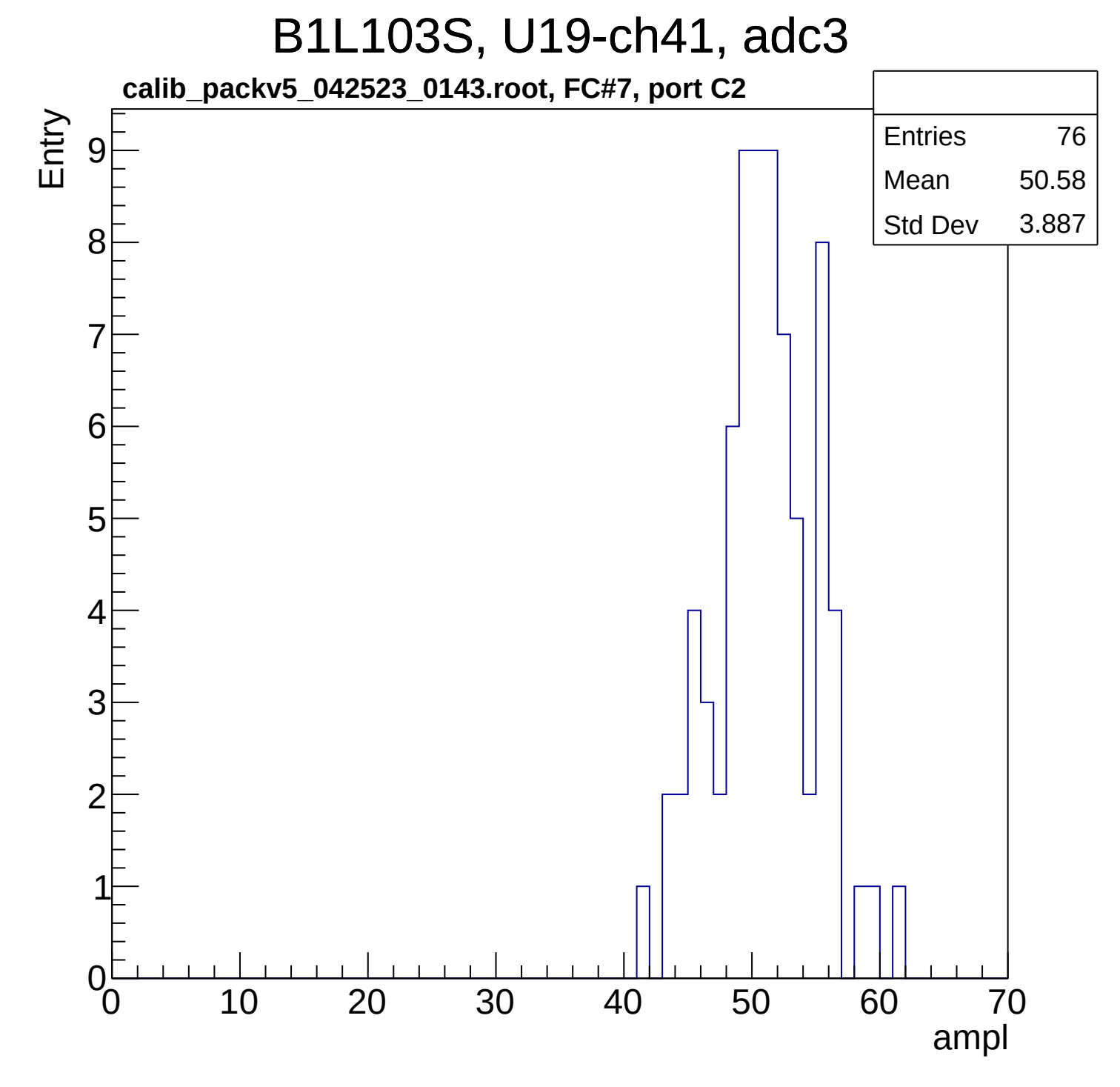
B1L103S, U19-ch41, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 50.58
Std Dev 3.887
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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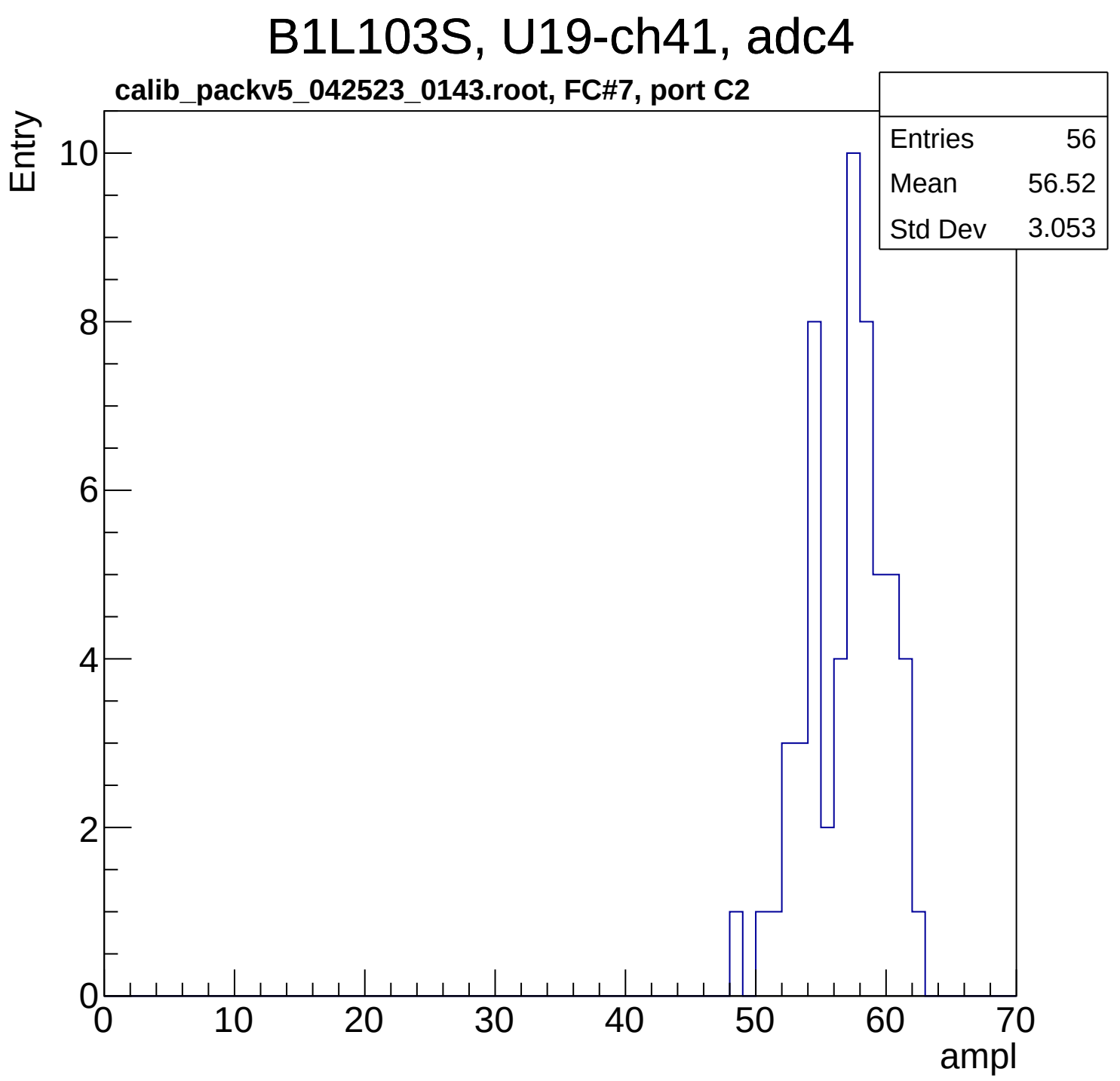
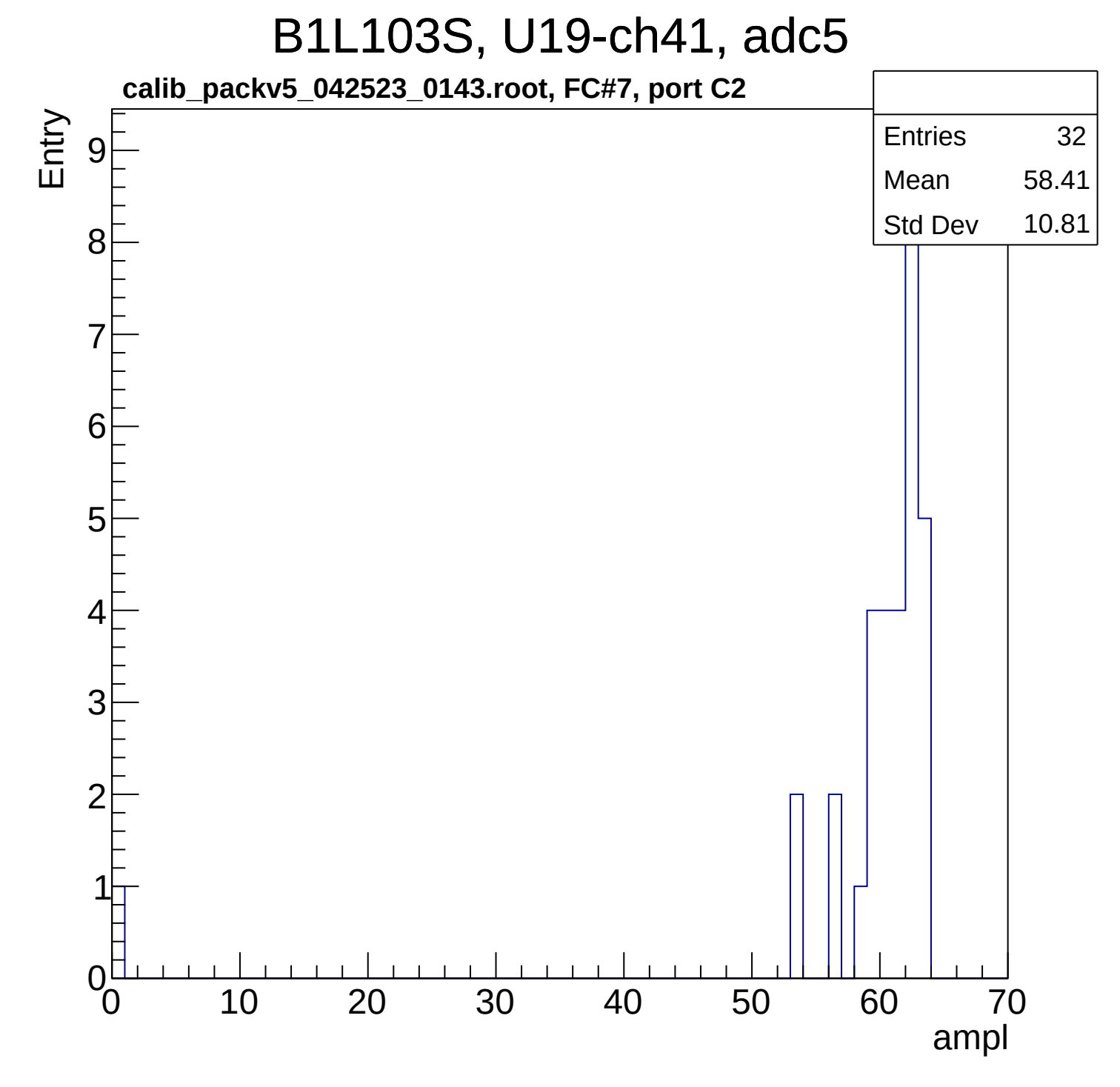

B1L103S, U19-ch41, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 56.52
Std Dev 3.053
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch41, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.41
Std Dev 10.81
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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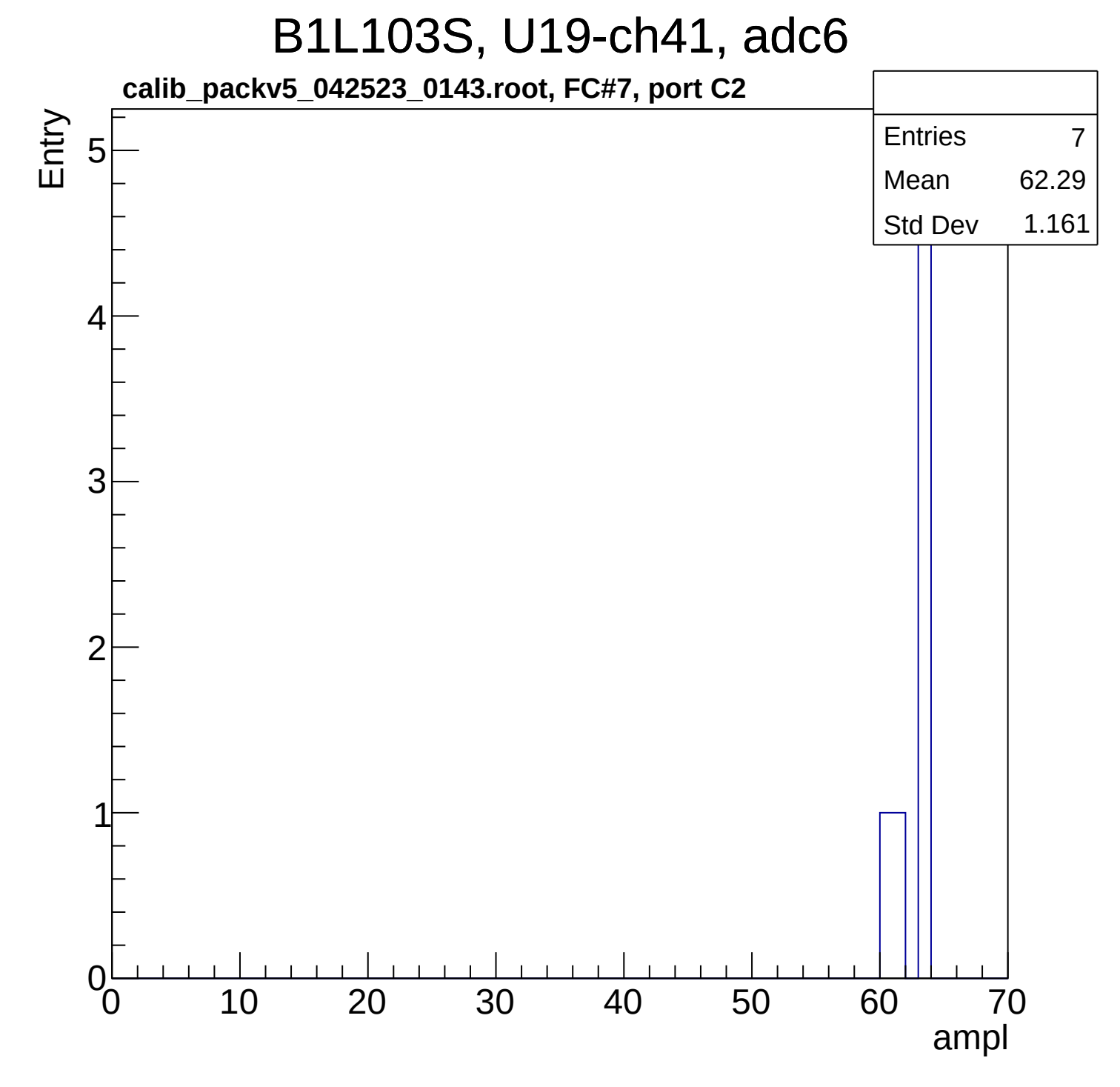
B1L103S, U19-ch41, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.29
Std Dev 1.161
Entry
ampl
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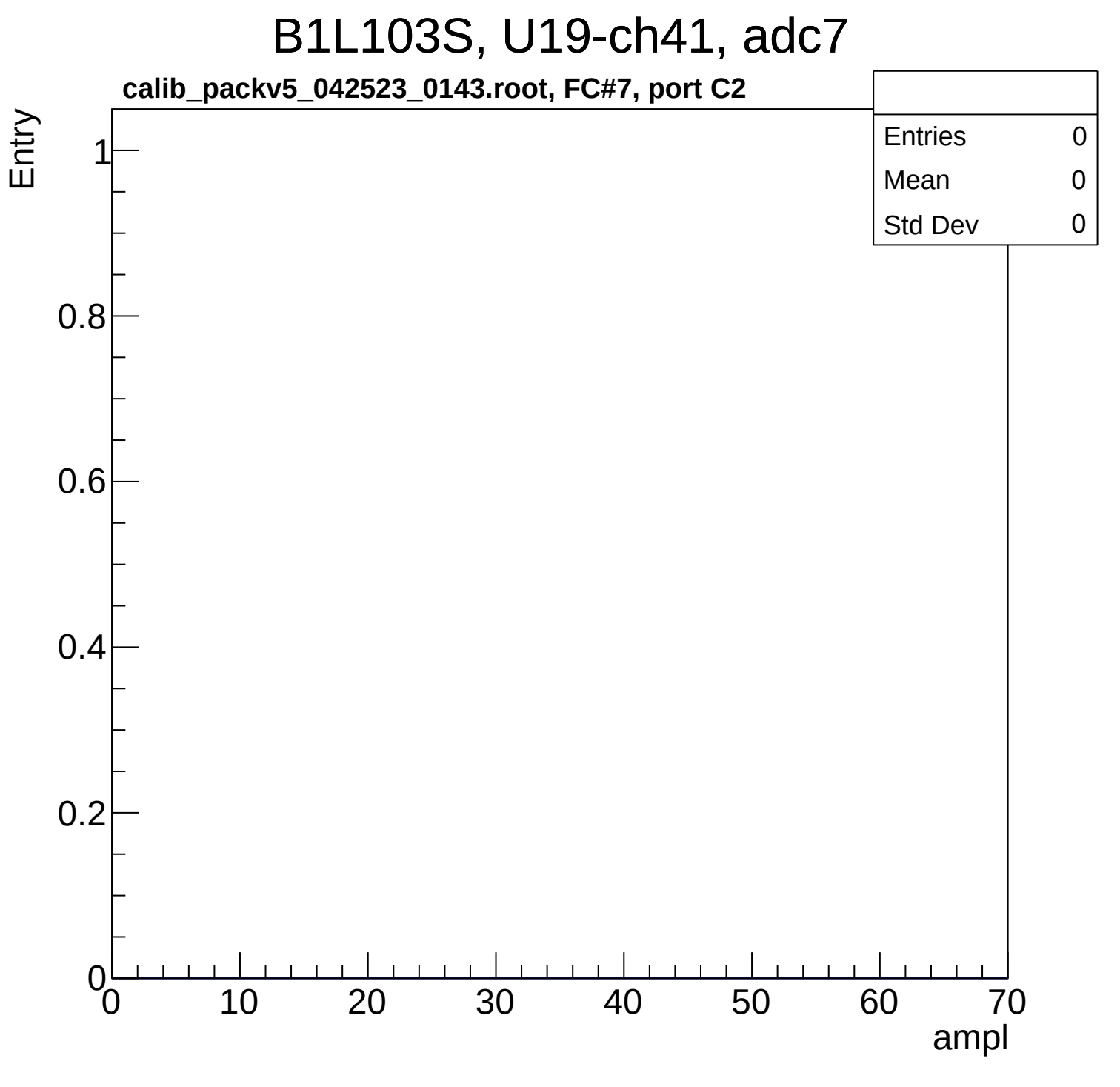
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch41, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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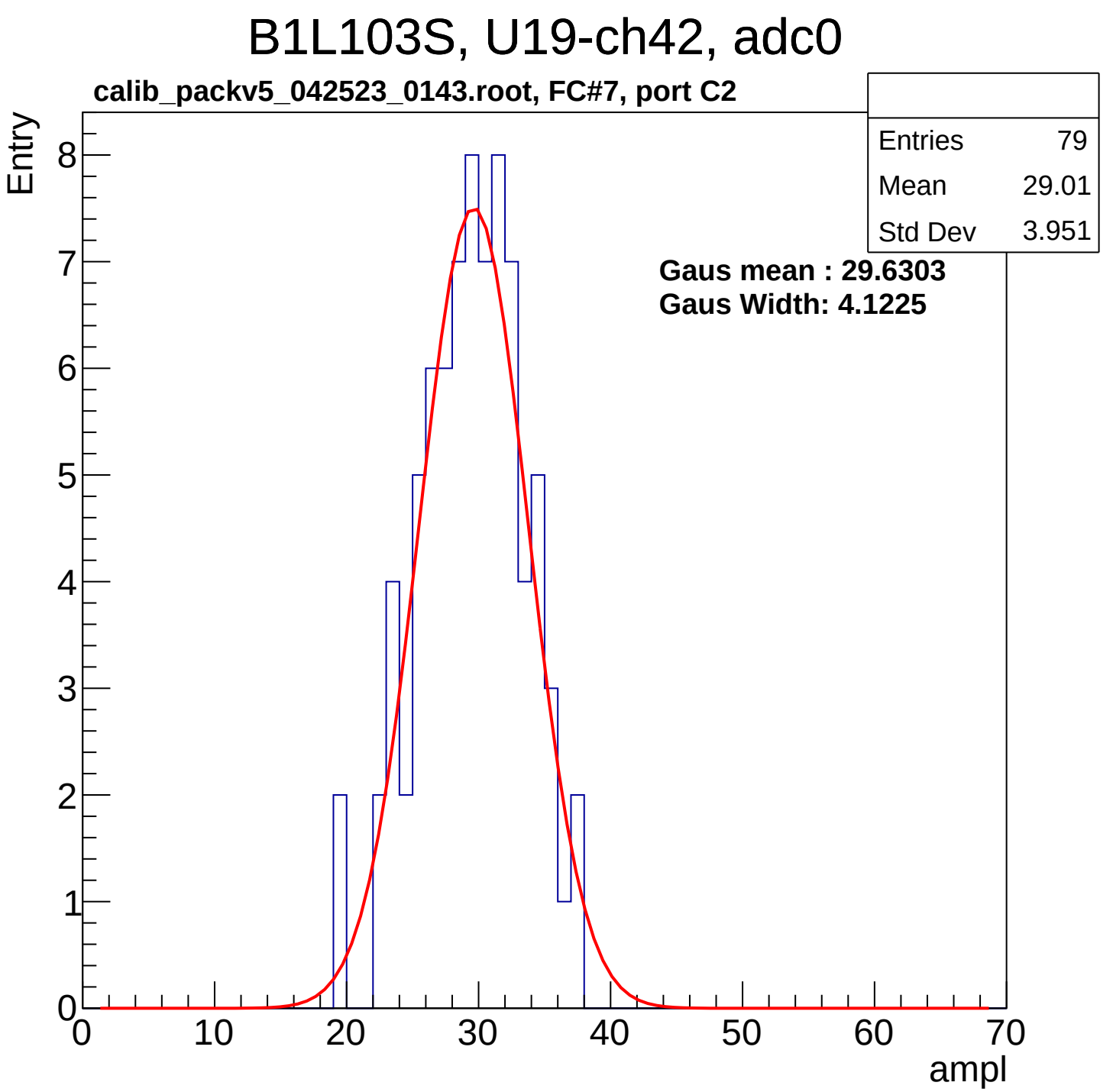
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch42, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.01
Std Dev 3.951
Gaus mean : 29.6303
Gaus Width: 4.1225
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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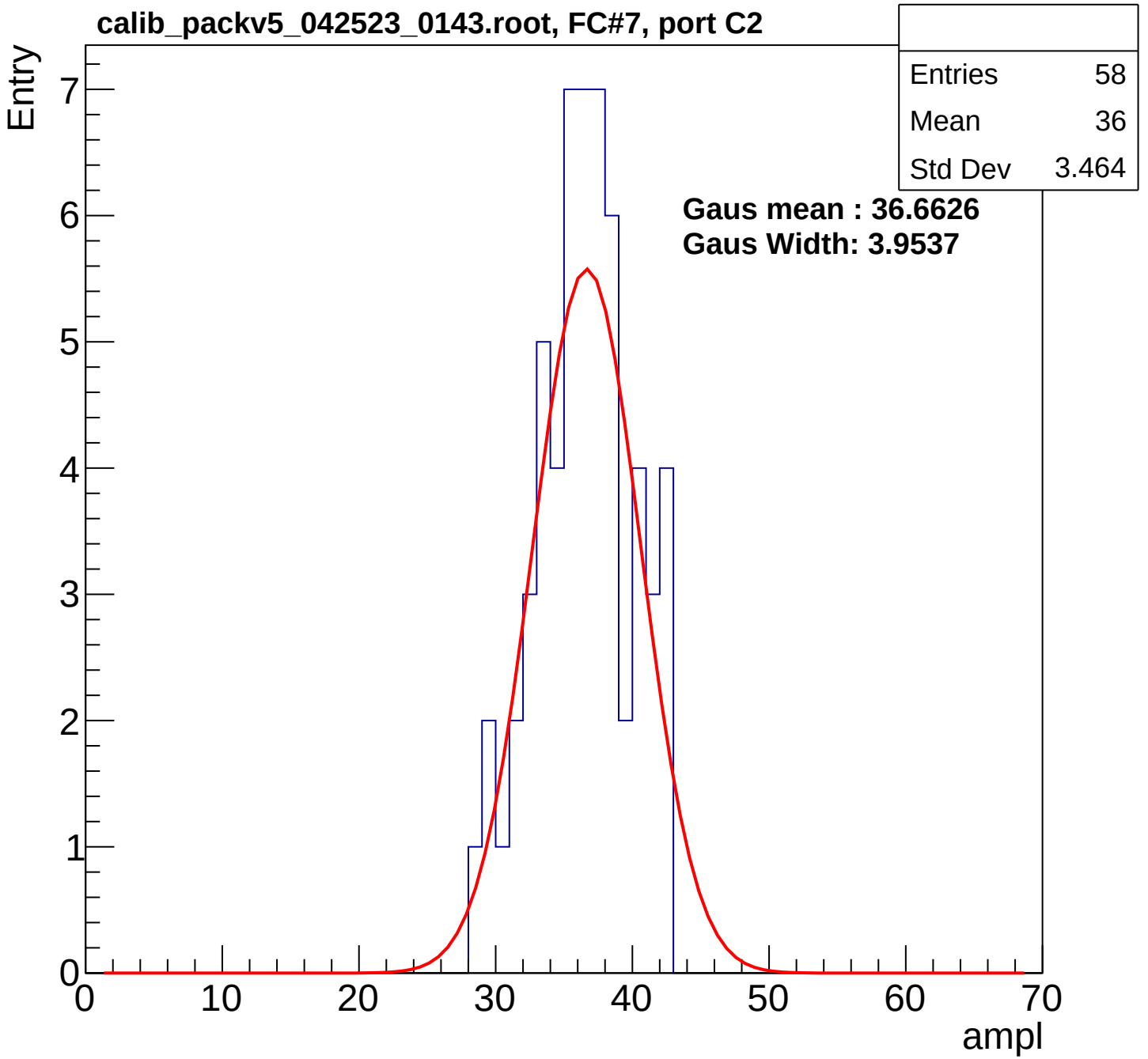
B1L103S, U19-ch42, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 36
Std Dev 3.464
Gaus mean : 36.6626
Gaus Width: 3.9537
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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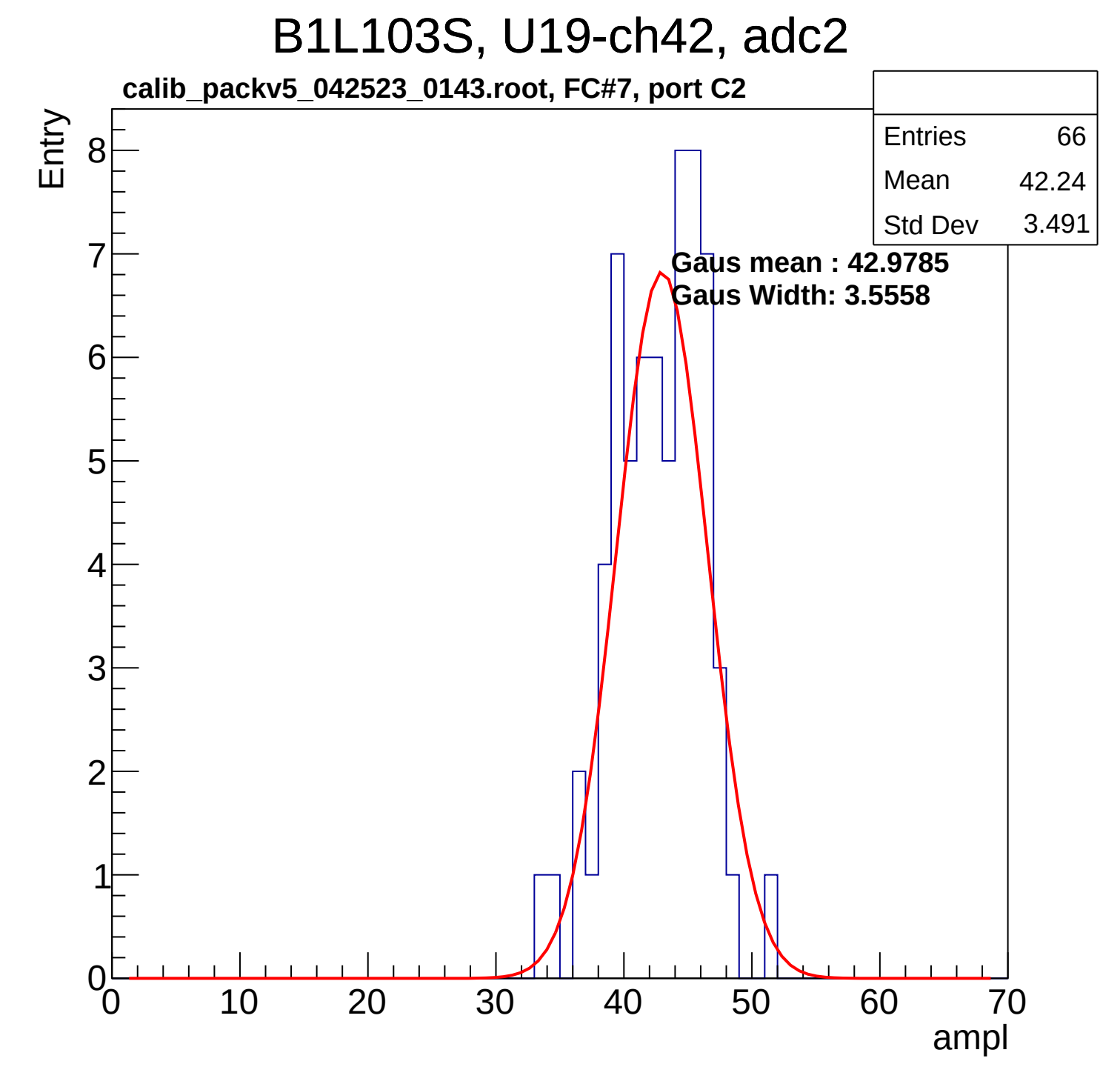
60
70


B1L103S, U19-ch42, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.24
Std Dev 3.491
Gaus mean : 42.9785
Gaus Width: 3.5558
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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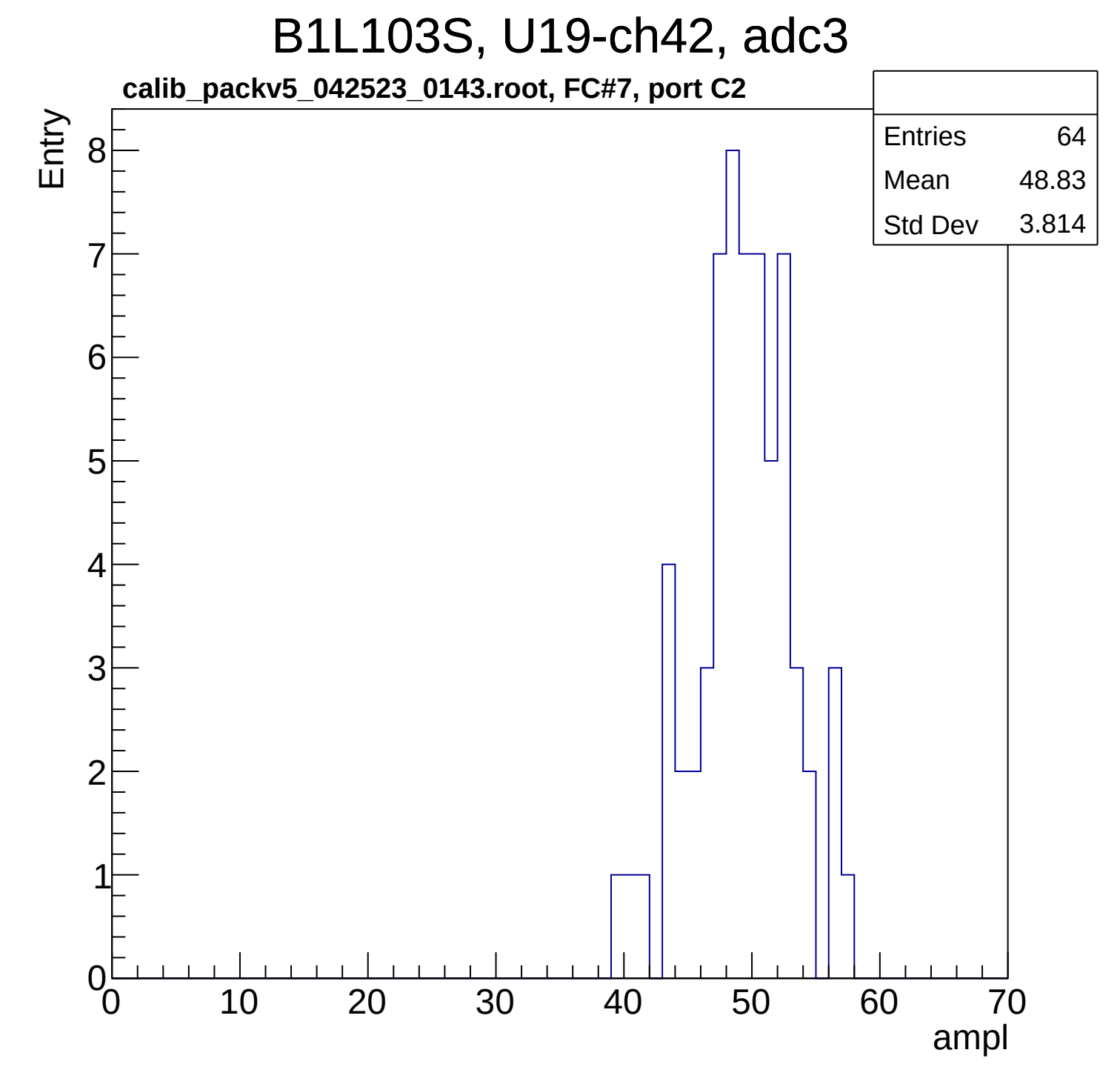
B1L103S, U19-ch42, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.83
Std Dev 3.814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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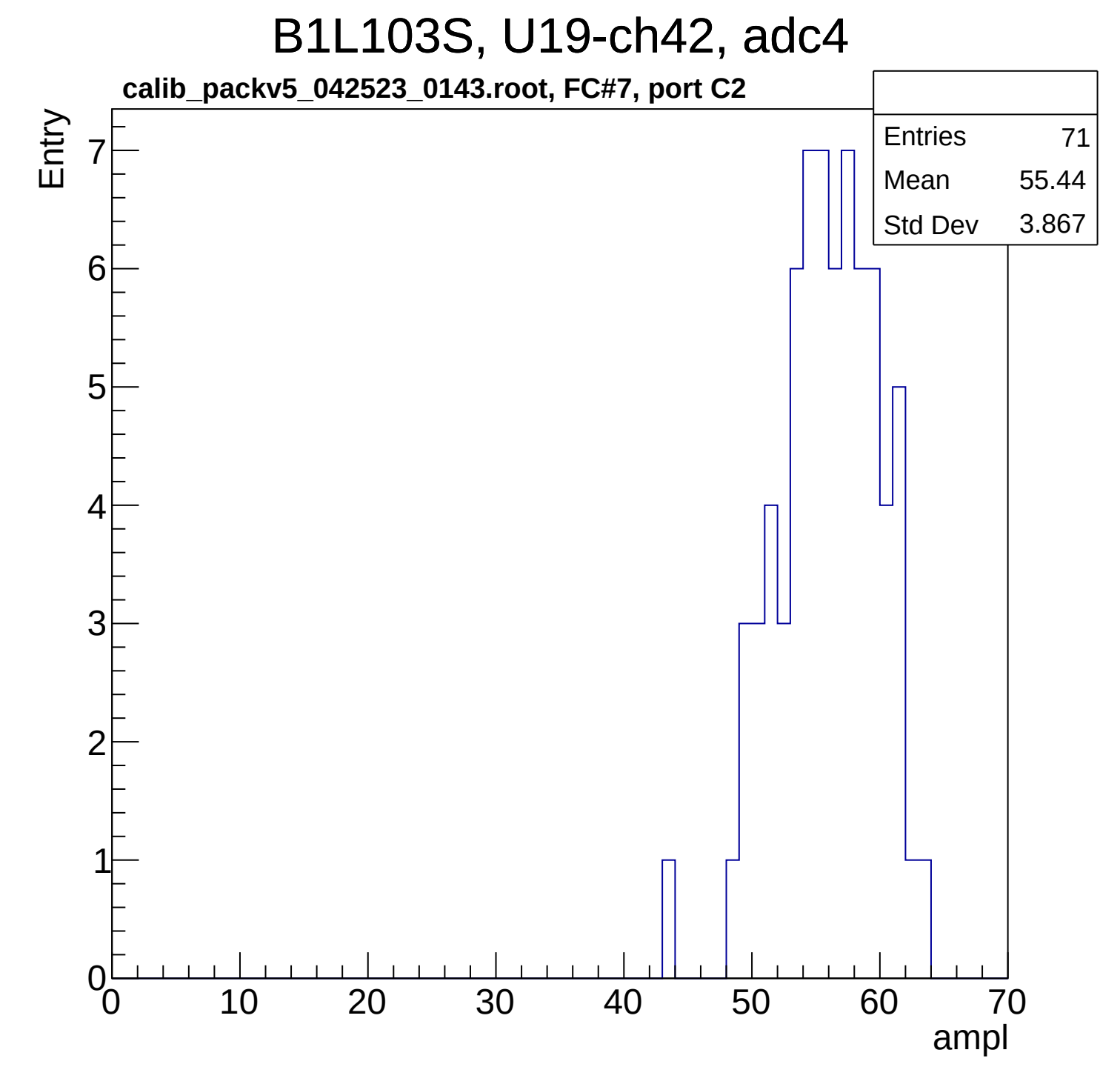
B1L103S, U19-ch42, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 55.44
Std Dev 3.867
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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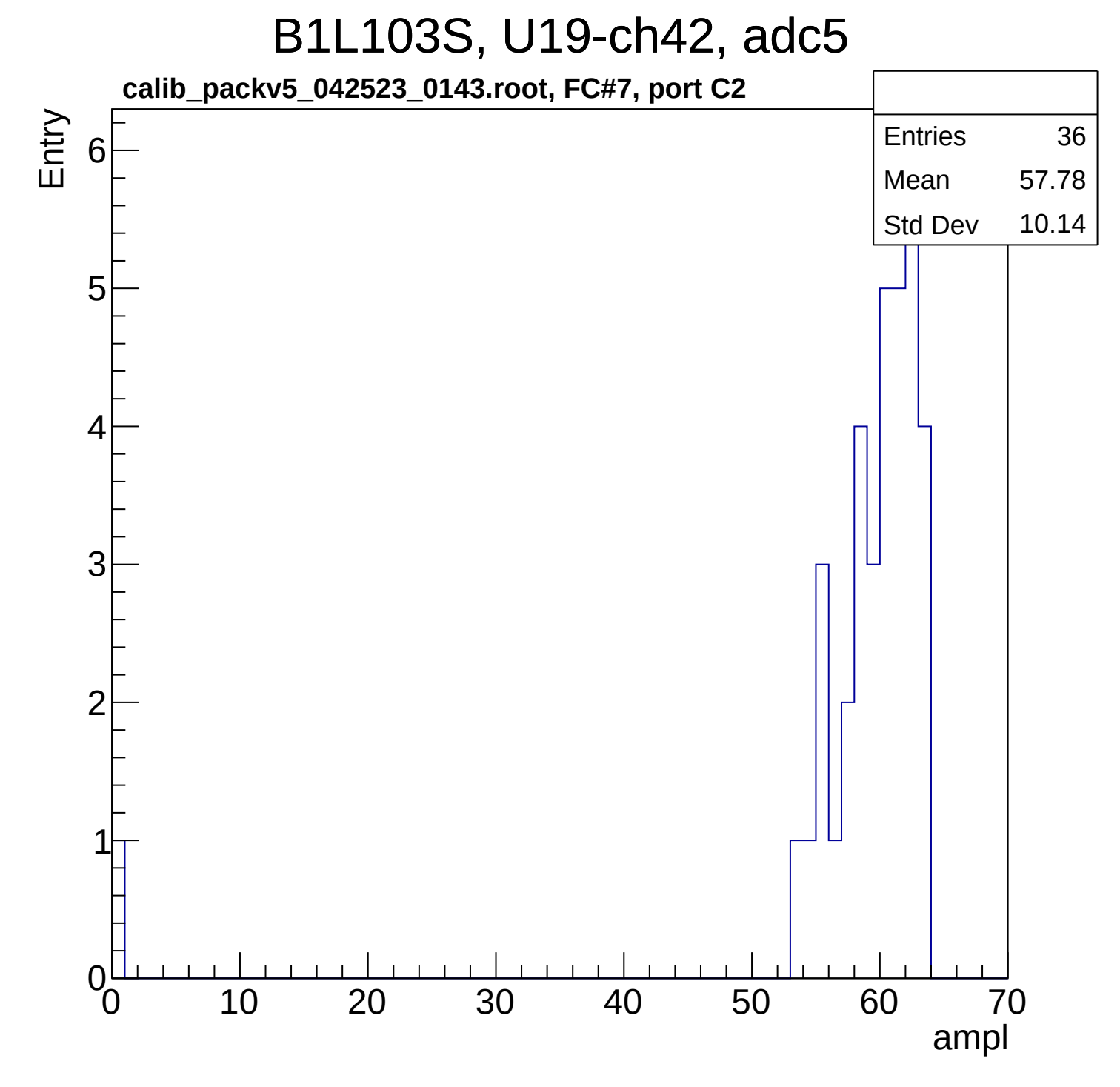
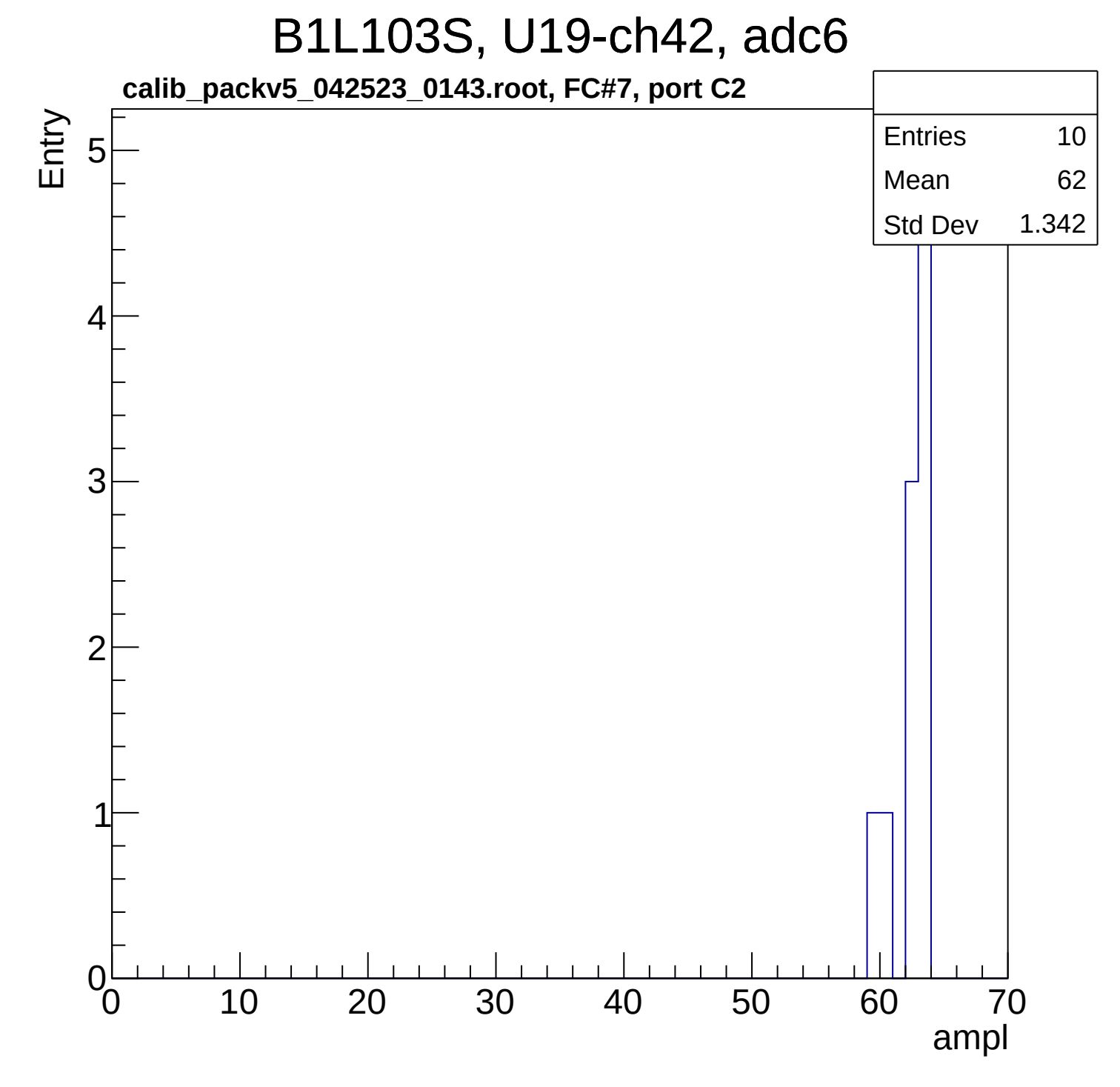
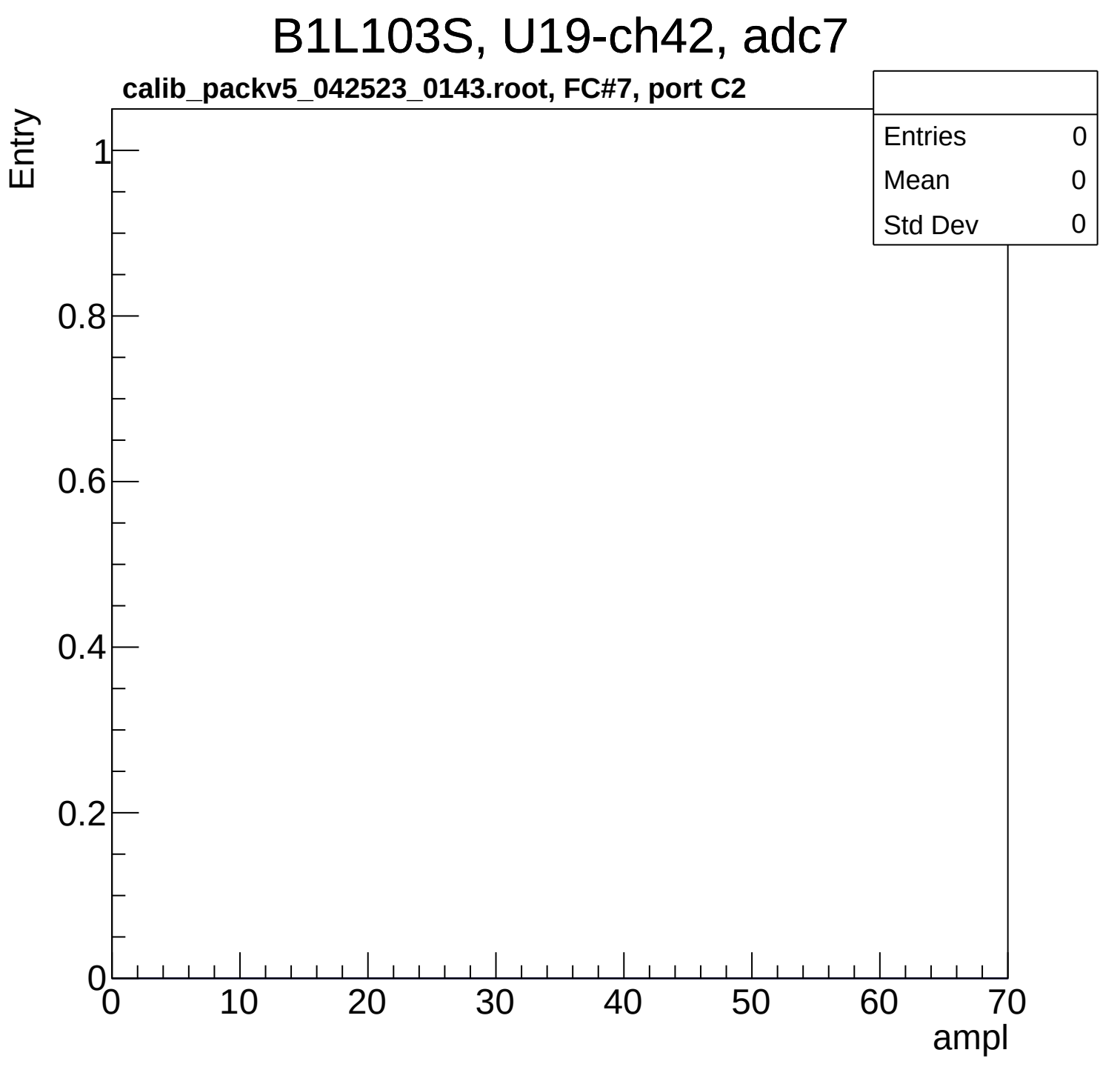
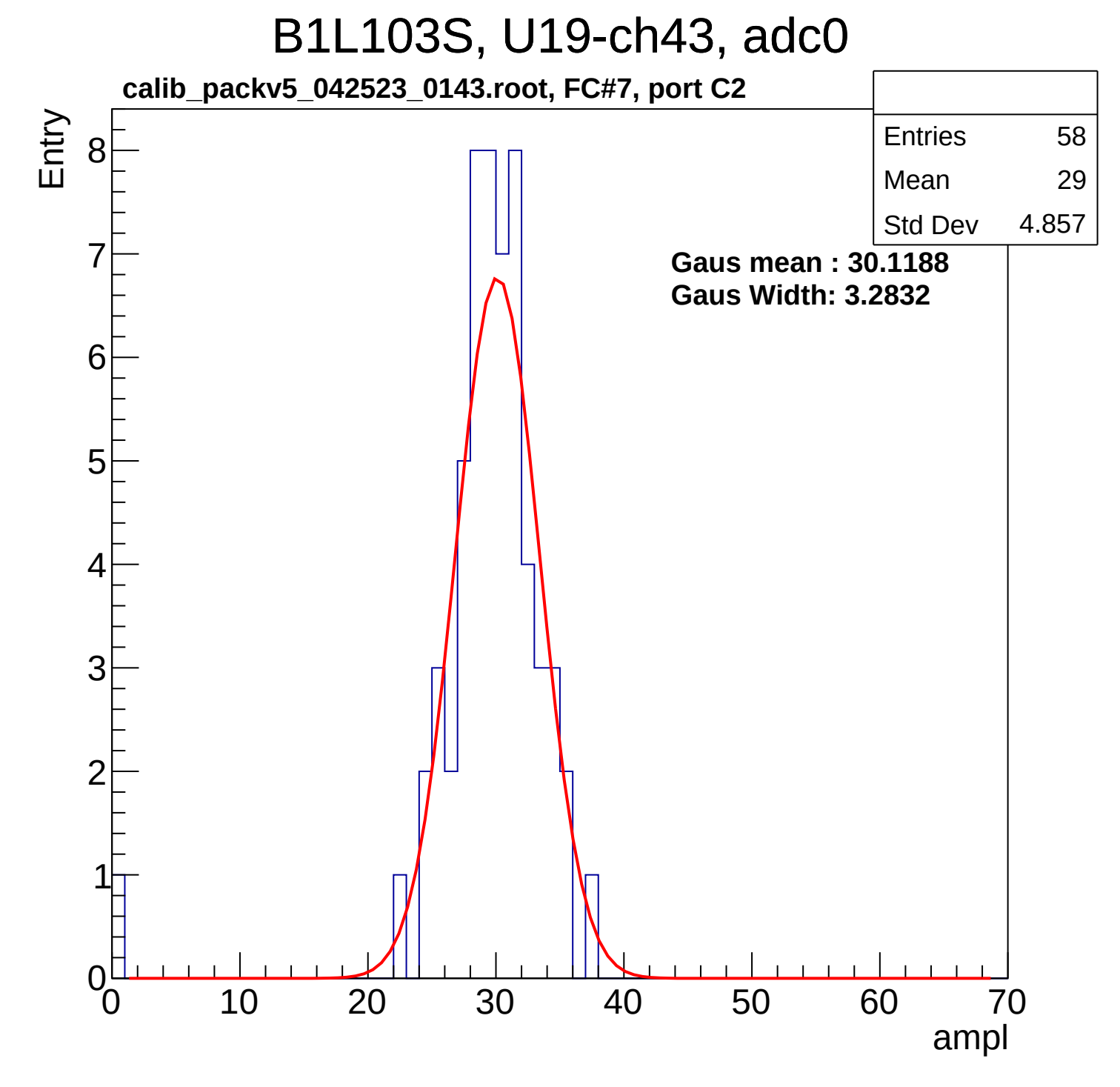
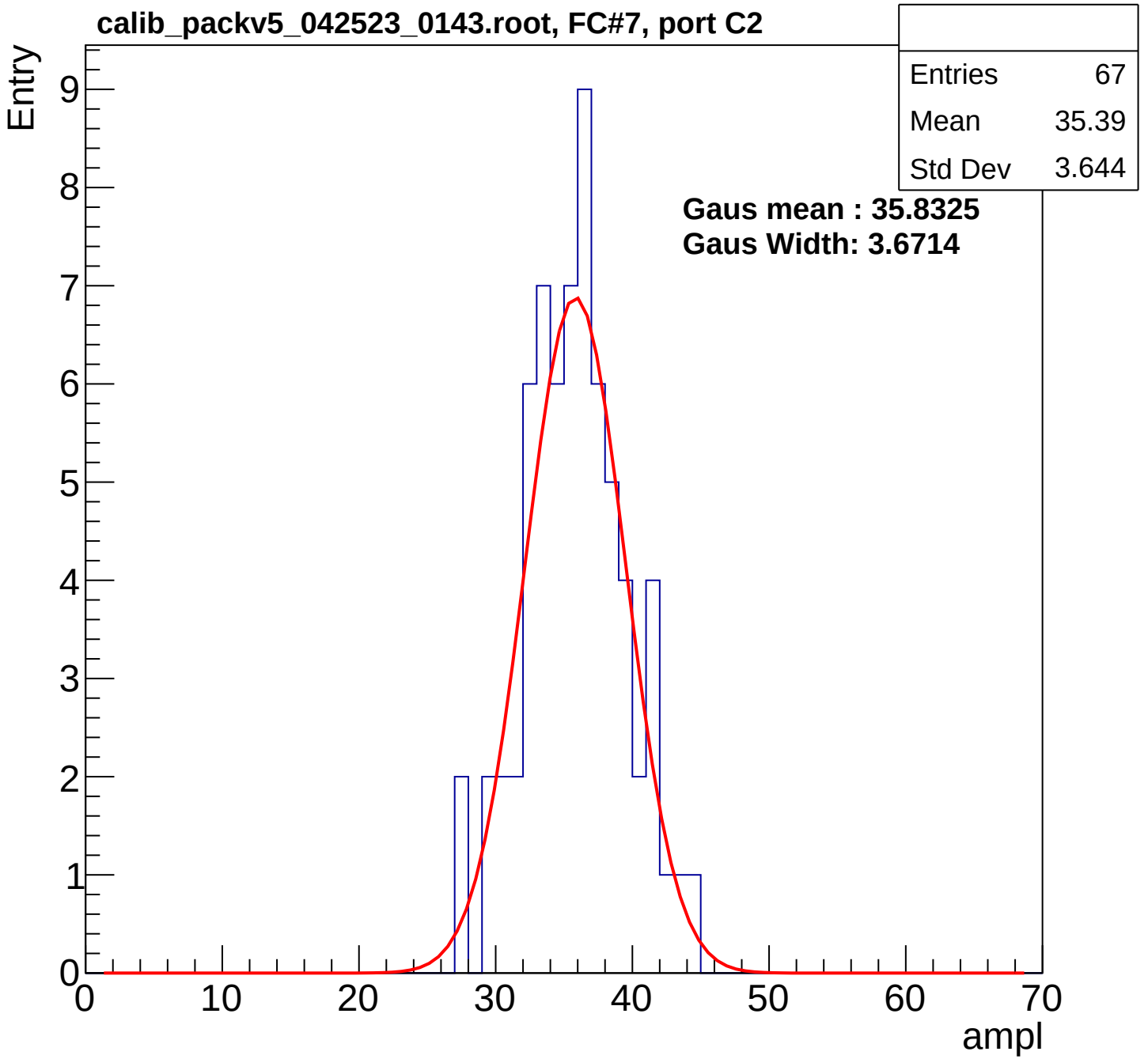
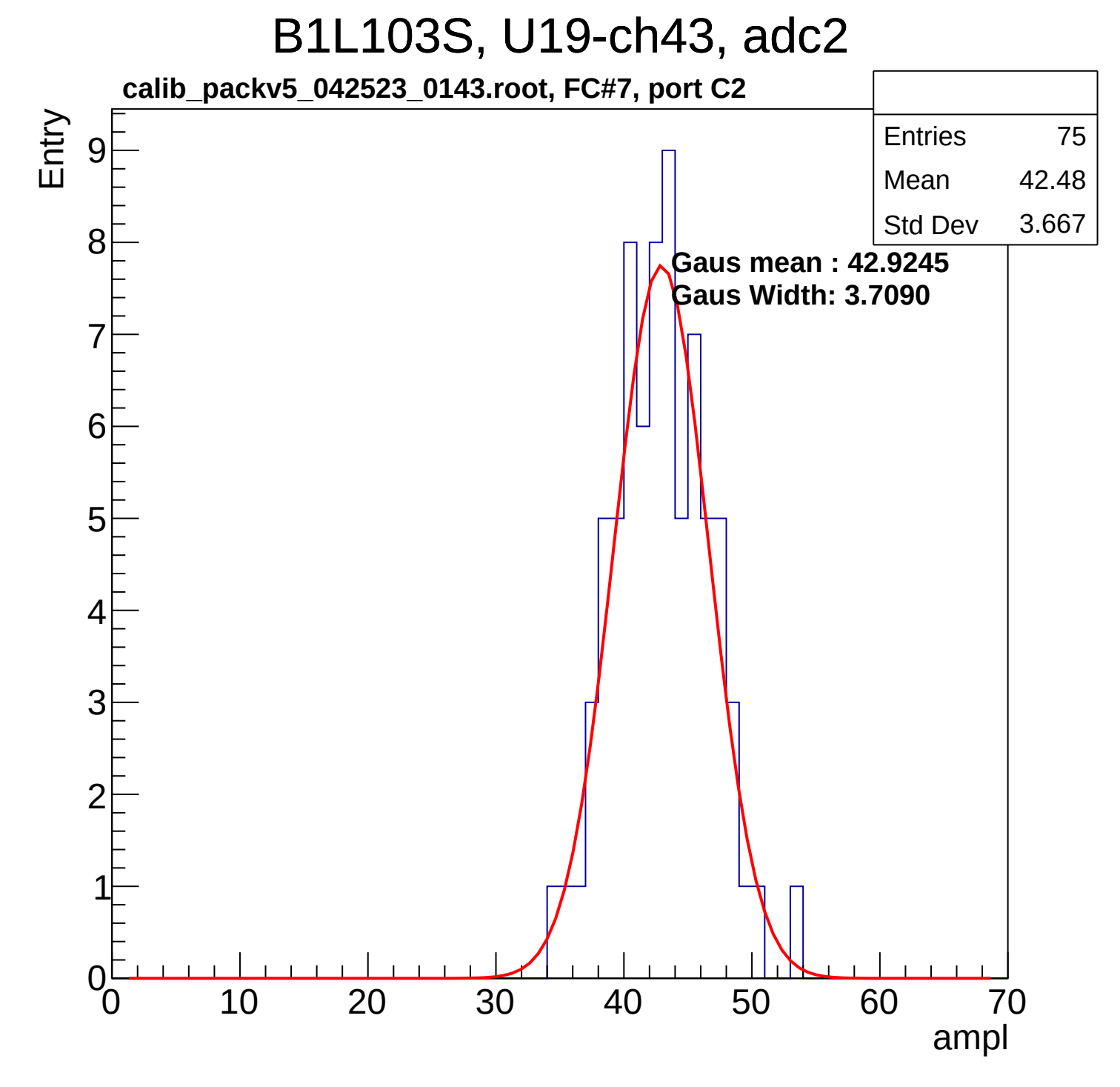

B1L103S, U19-ch42, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 57.78
Std Dev 10.14
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch42, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 62
Std Dev 1.342
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch42, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch43, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 29
Std Dev 4.857
Gaus mean : 30.1188
Gaus Width: 3.2832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch43, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.39
Std Dev 3.644
Gaus mean : 35.8325
Gaus Width: 3.6714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch43, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 42.48
Std Dev 3.667
Gaus mean : 42.9245
Gaus Width: 3.7090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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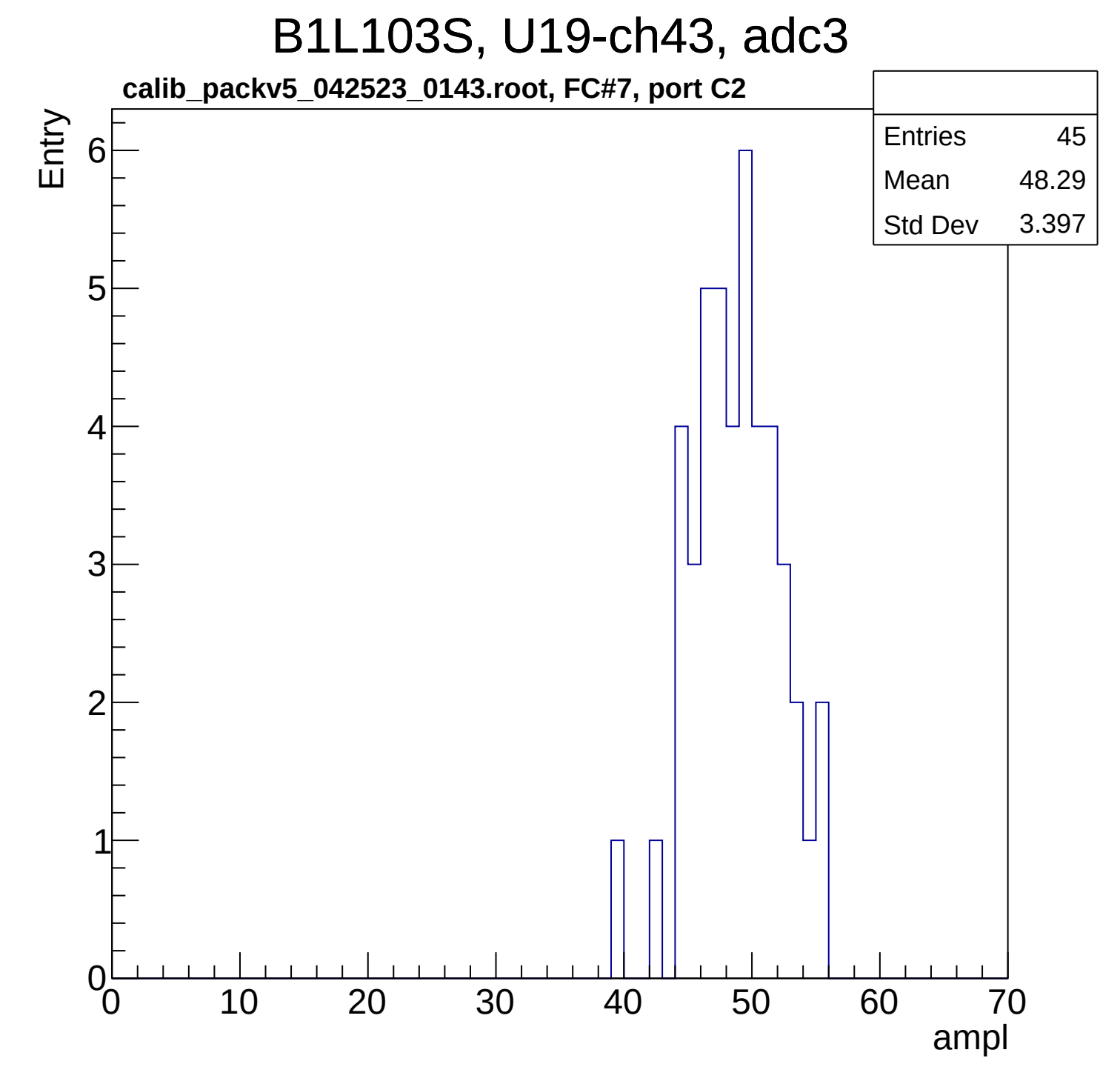
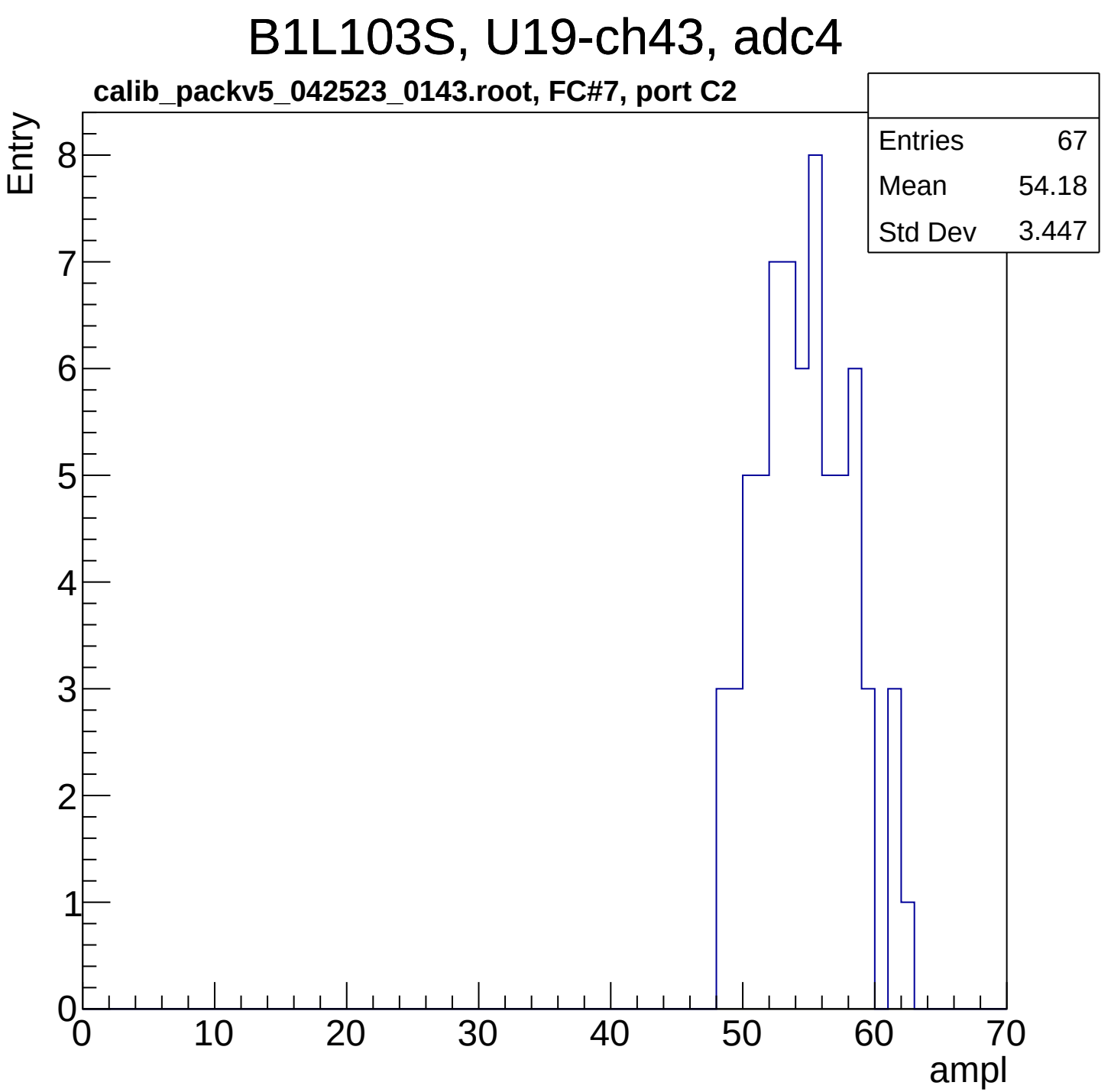
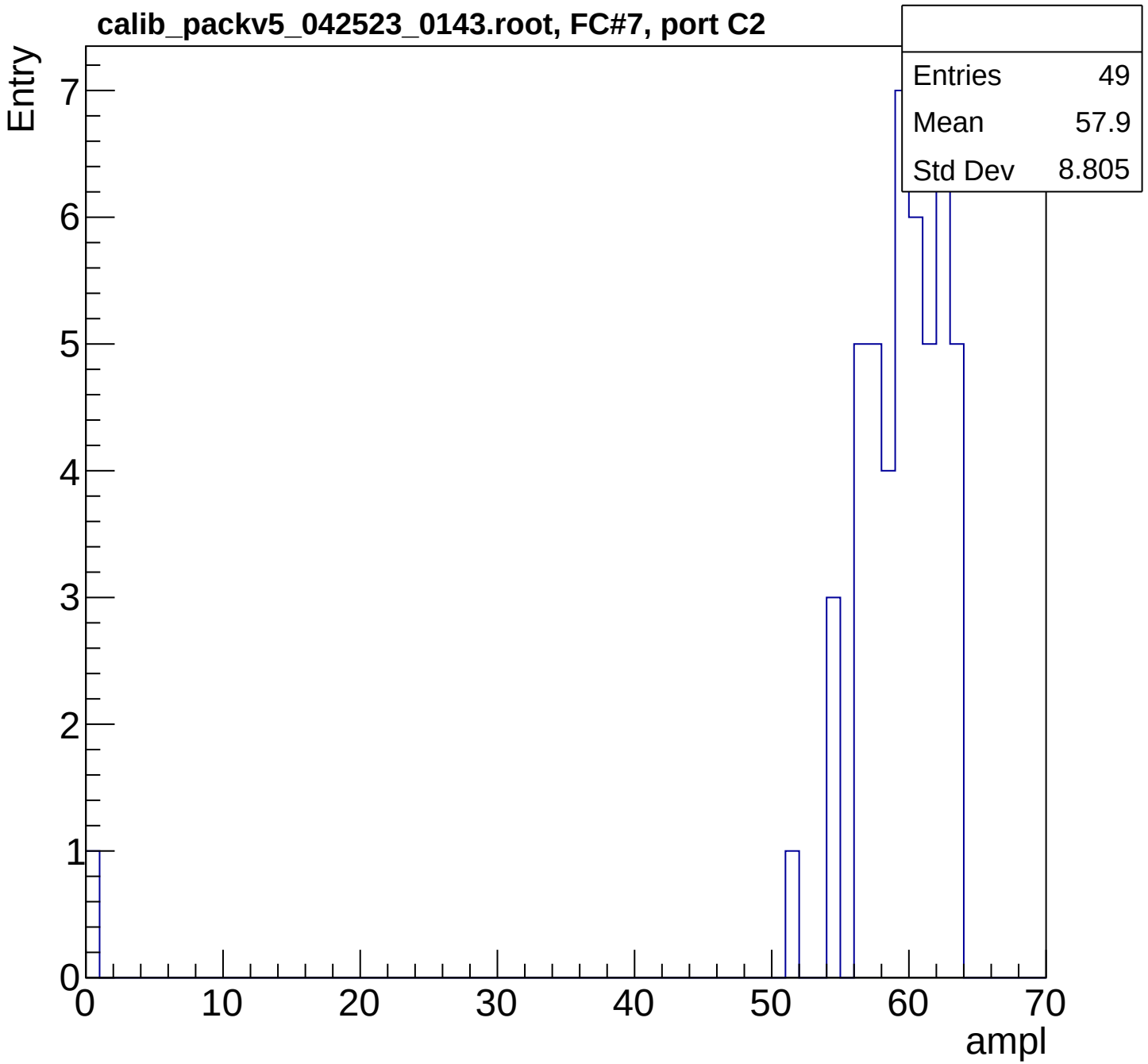

B1L103S, U19-ch43, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 48.29
Std Dev 3.397
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch43, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.18
Std Dev 3.447
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch43, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 57.9
Std Dev 8.805
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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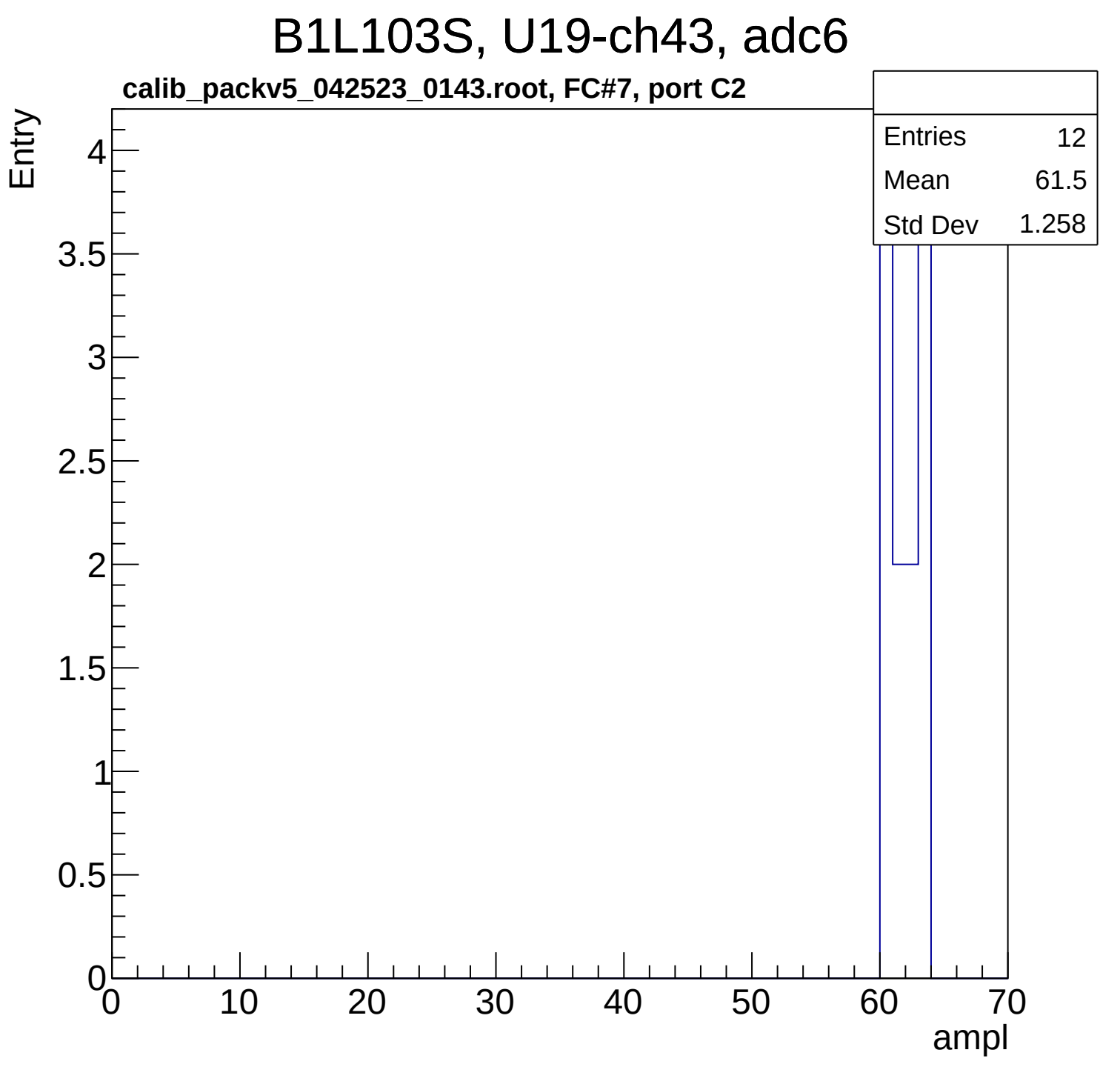
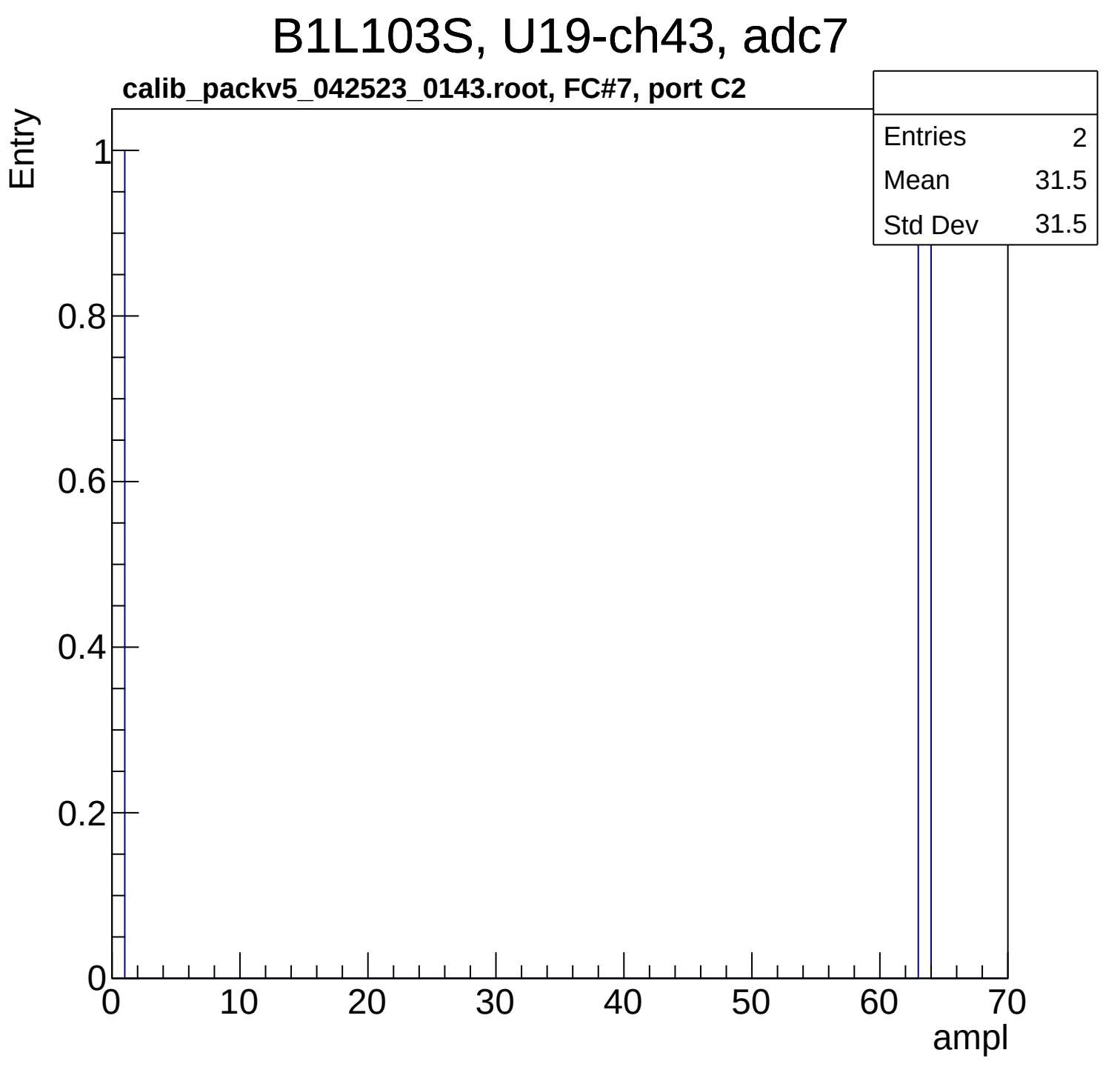
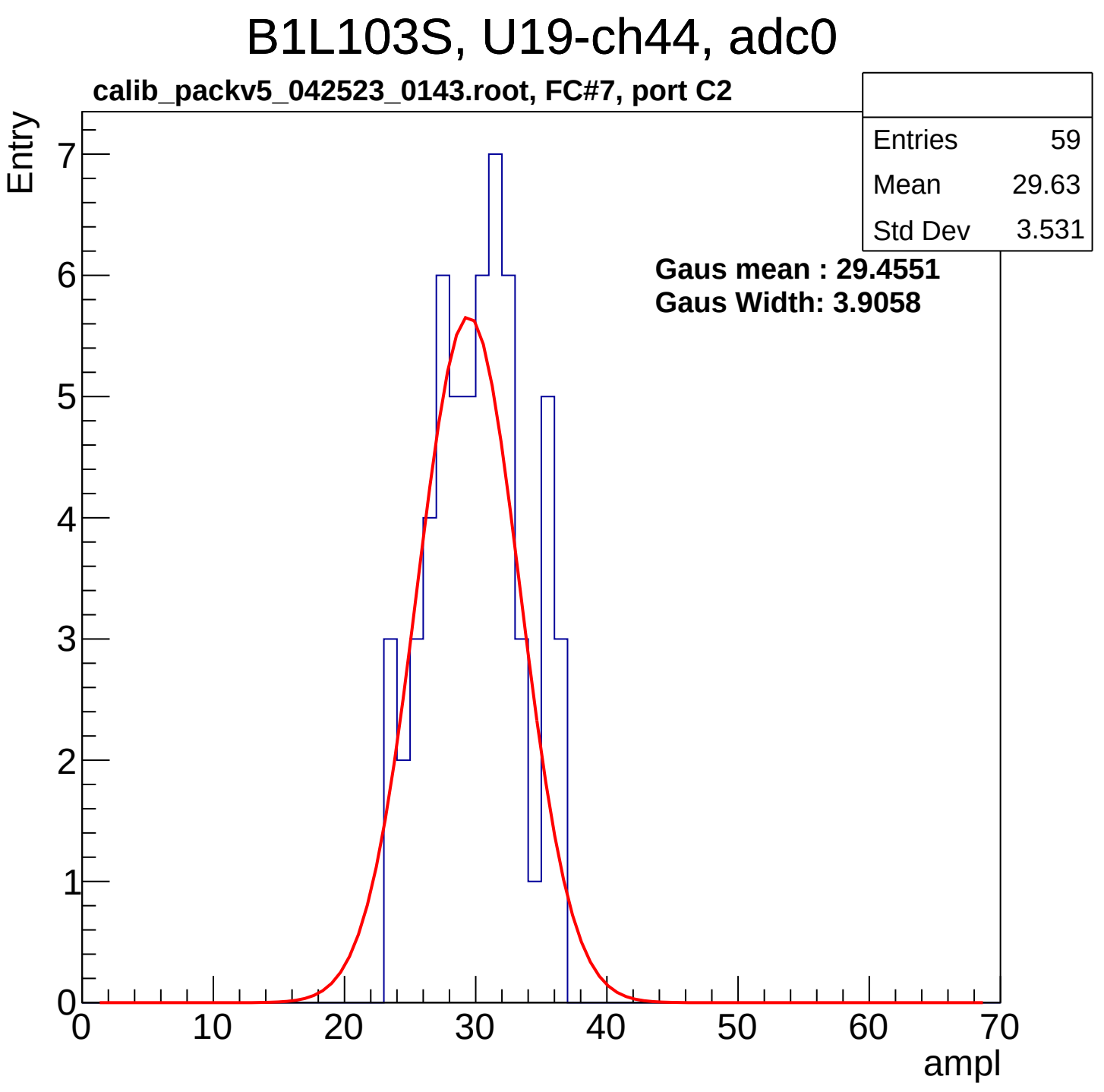
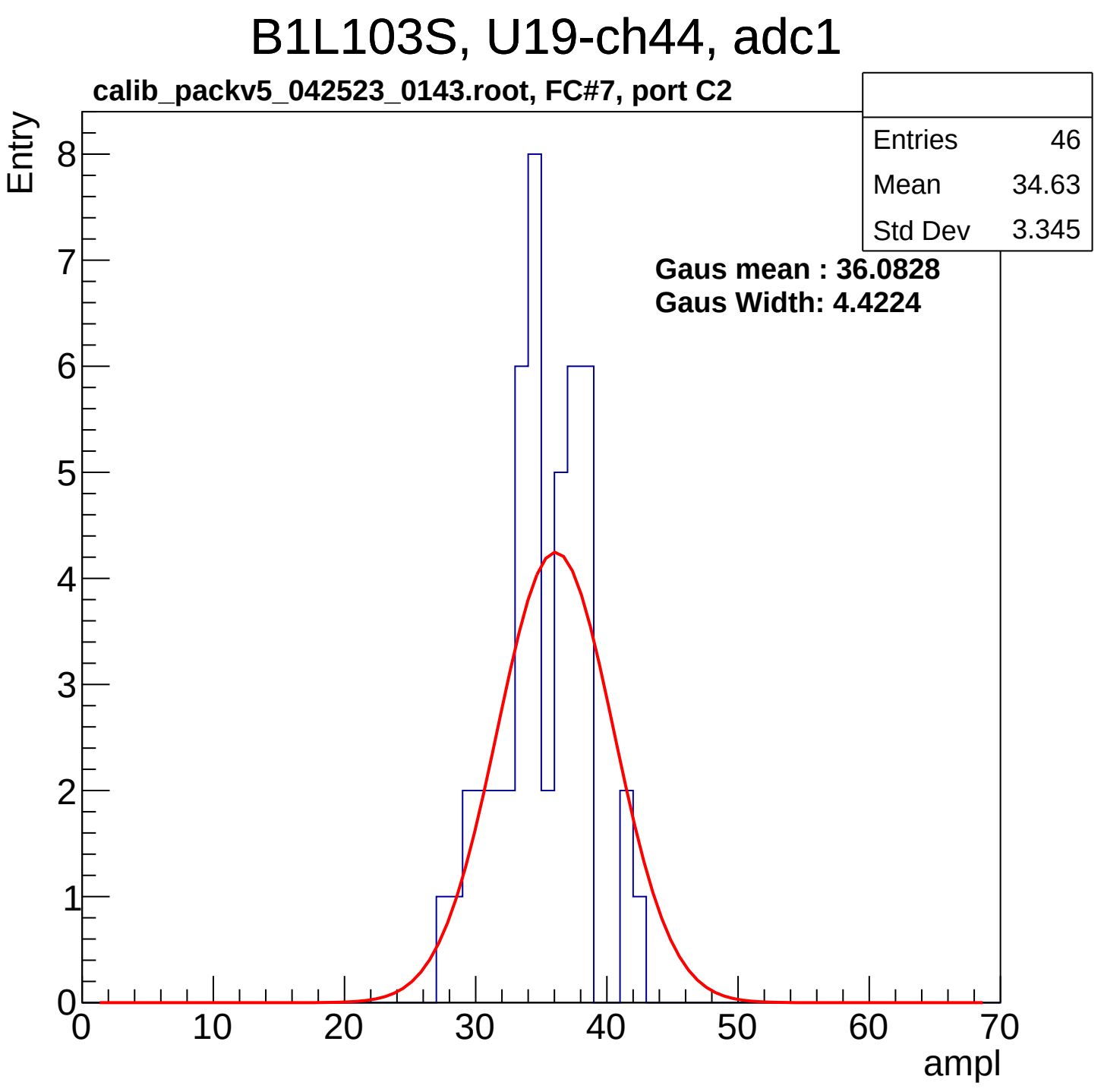
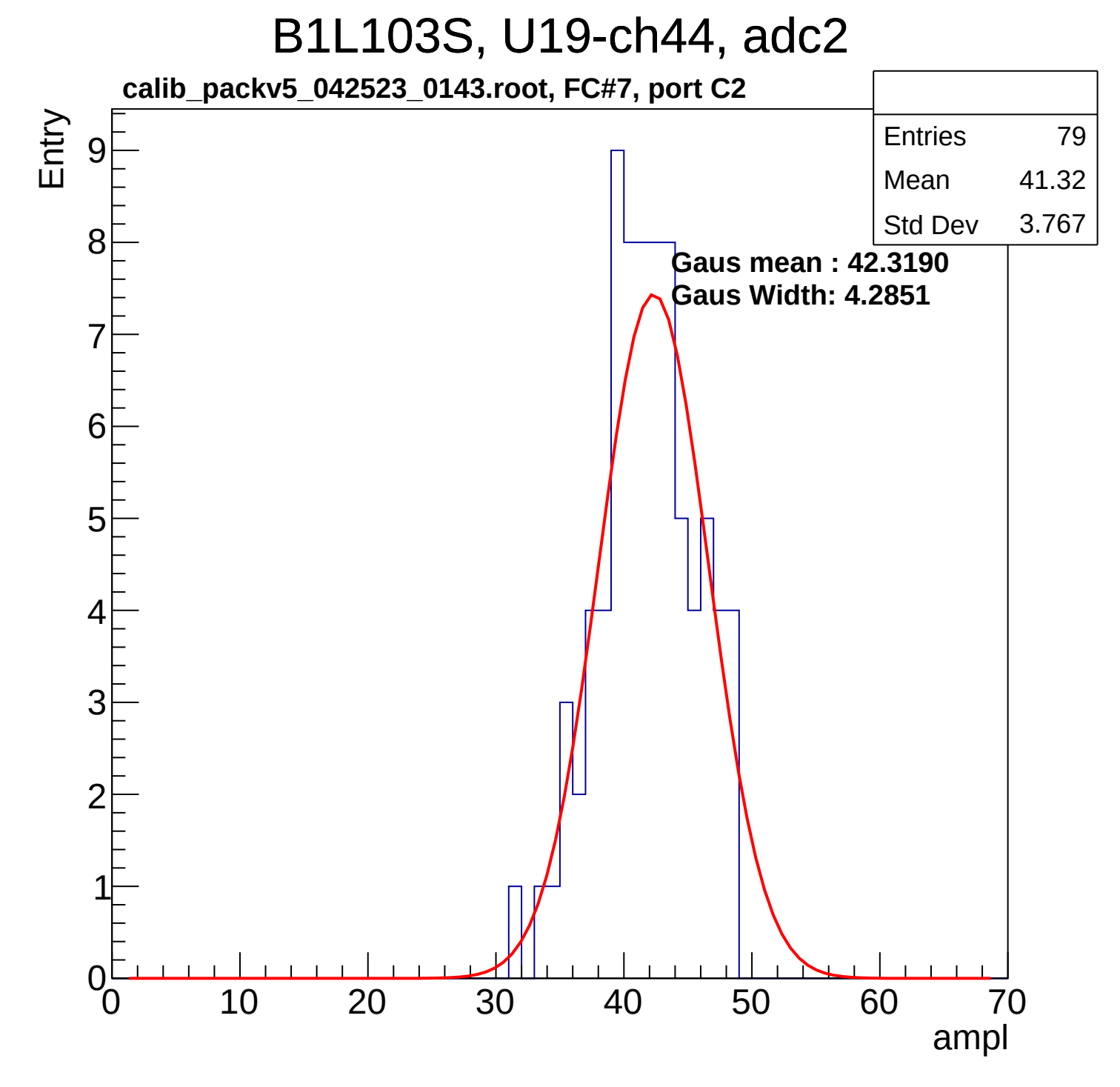
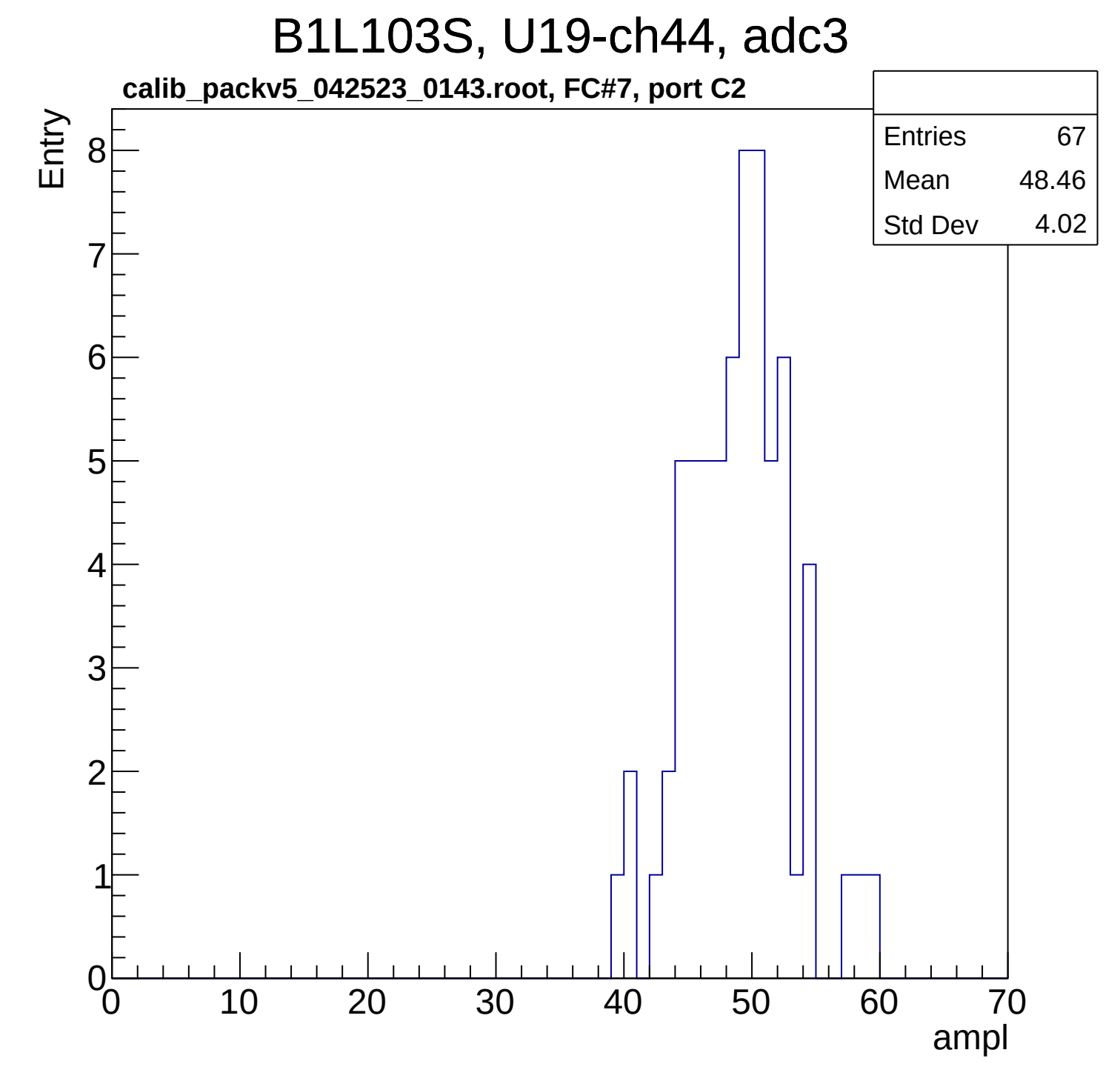

B1L103S, U19-ch43, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.5
Std Dev 1.258
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

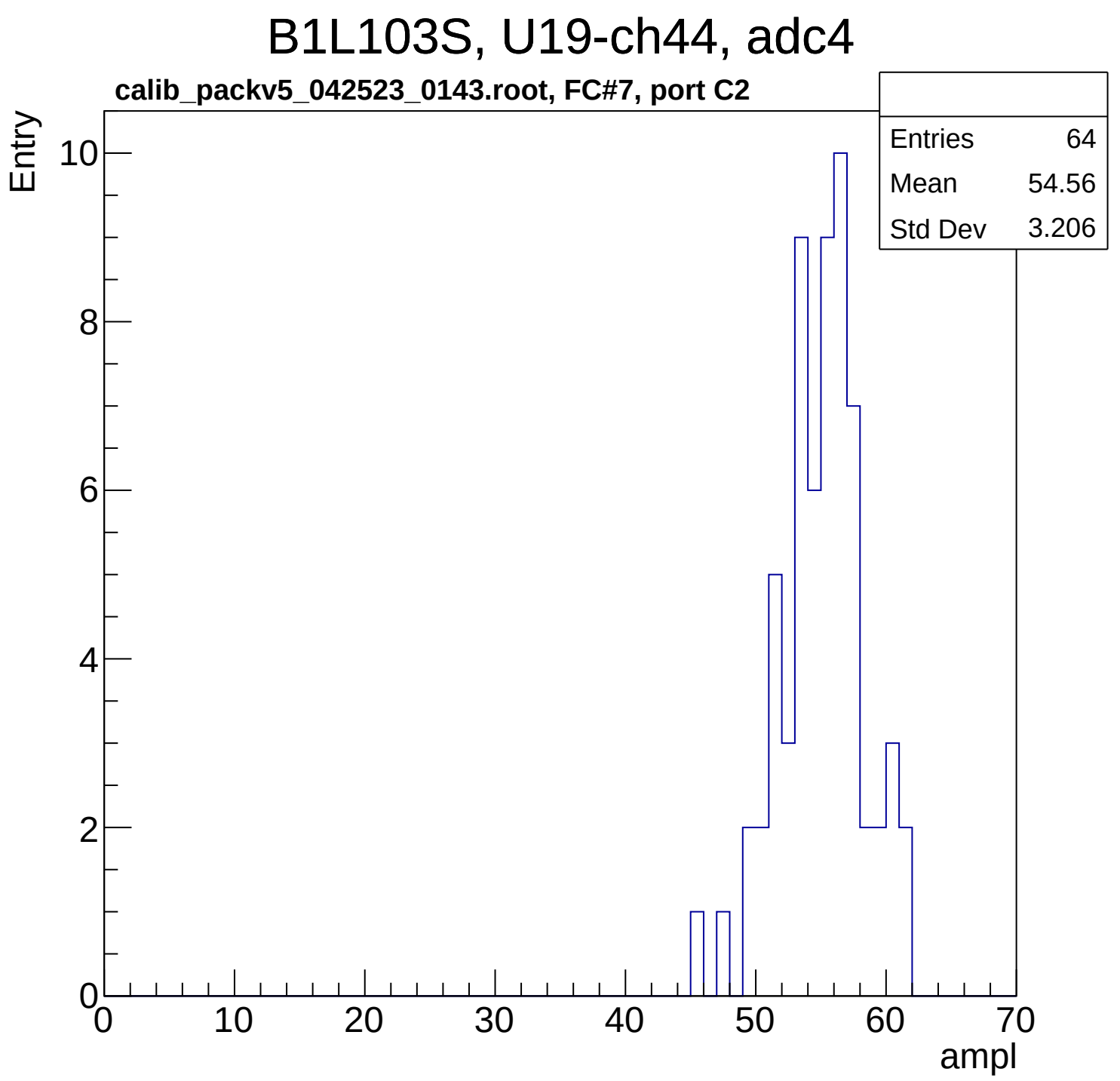

B1L103S, U19-ch43, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

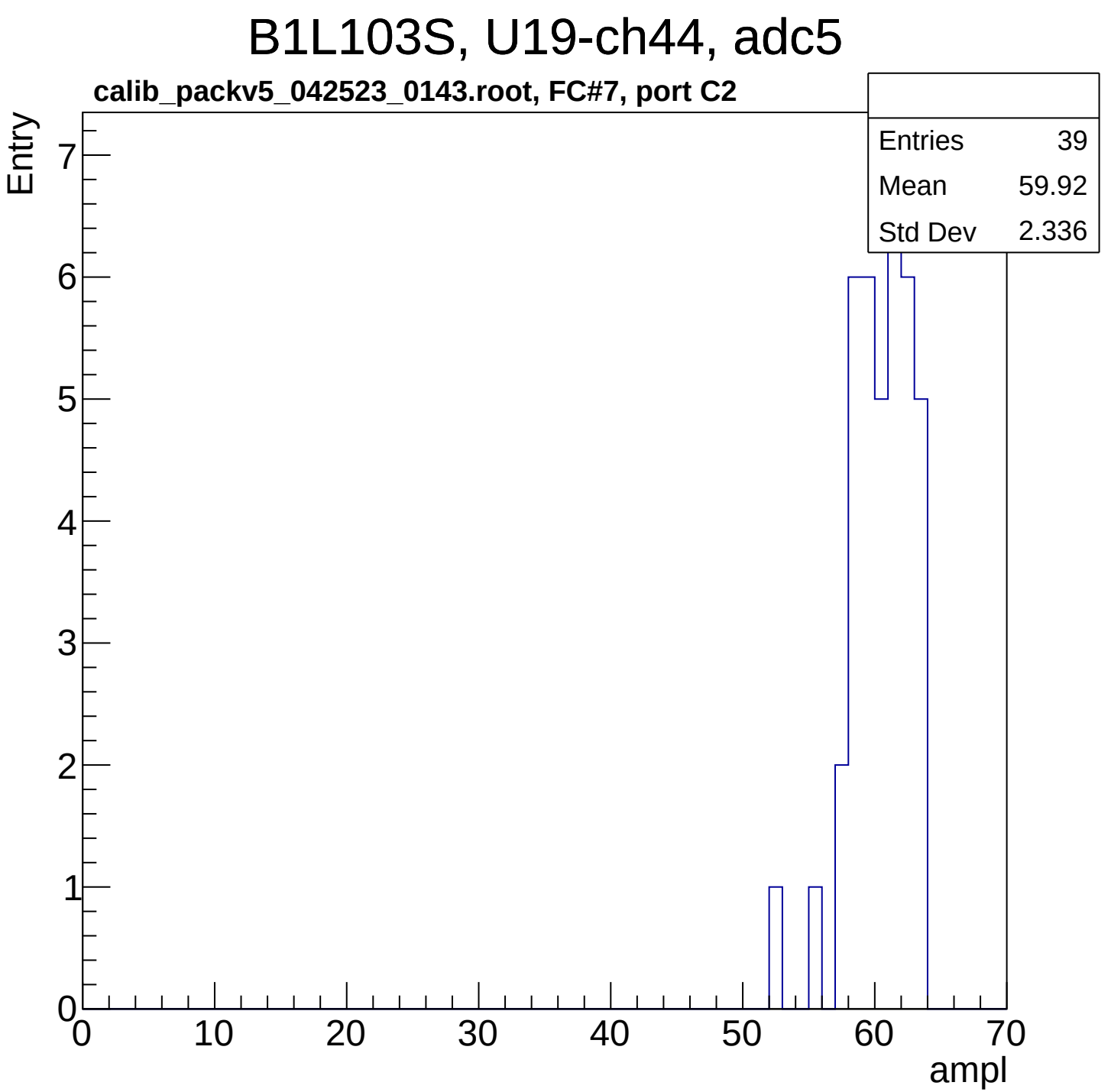

B1L103S, U19-ch44, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 29.63
Std Dev 3.531
Gaus mean : 29.4551
Gaus Width: 3.9058
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

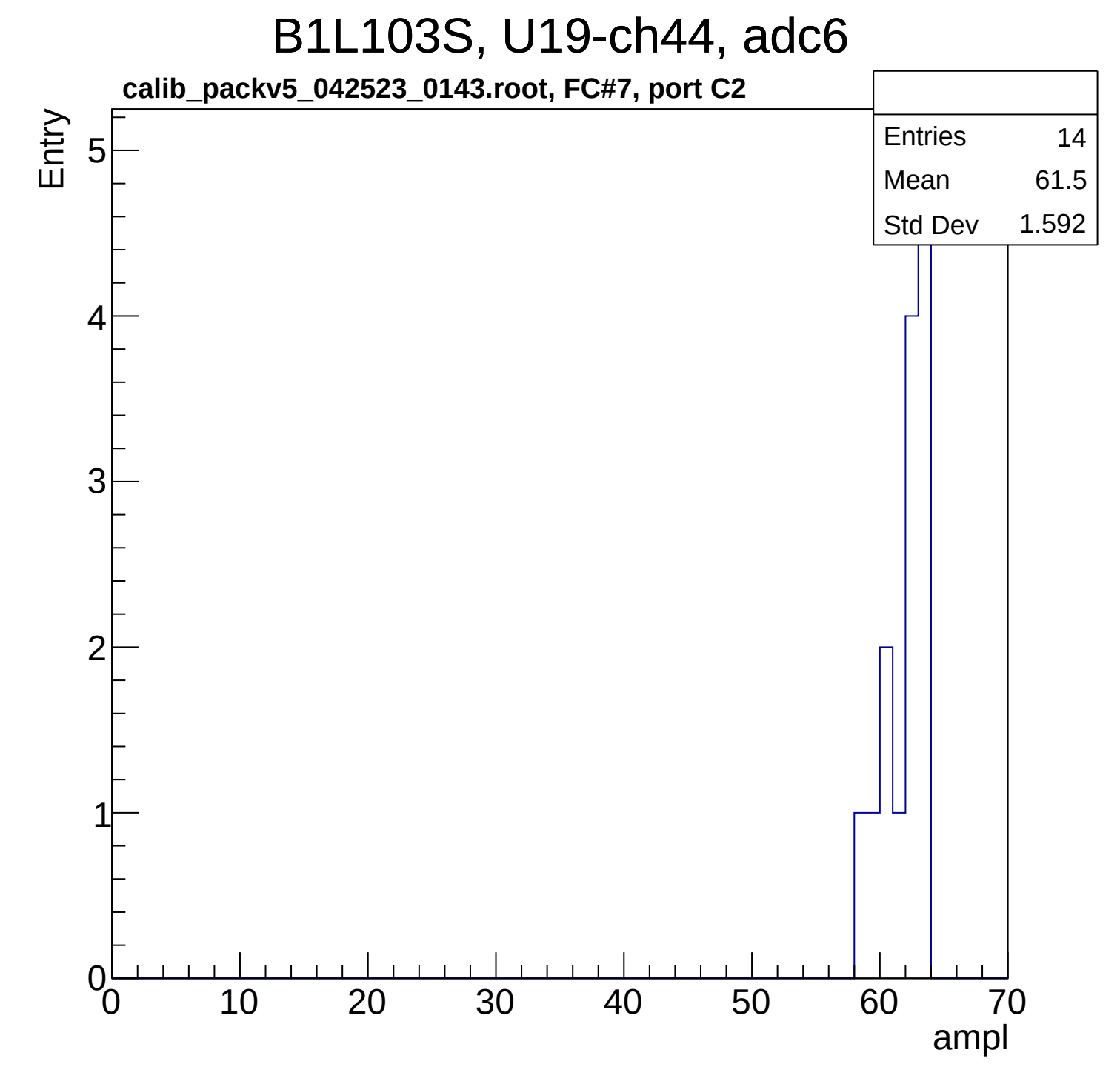

B1L103S, U19-ch44, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 34.63
Std Dev 3.345
Gaus mean : 36.0828
Gaus Width: 4.4224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

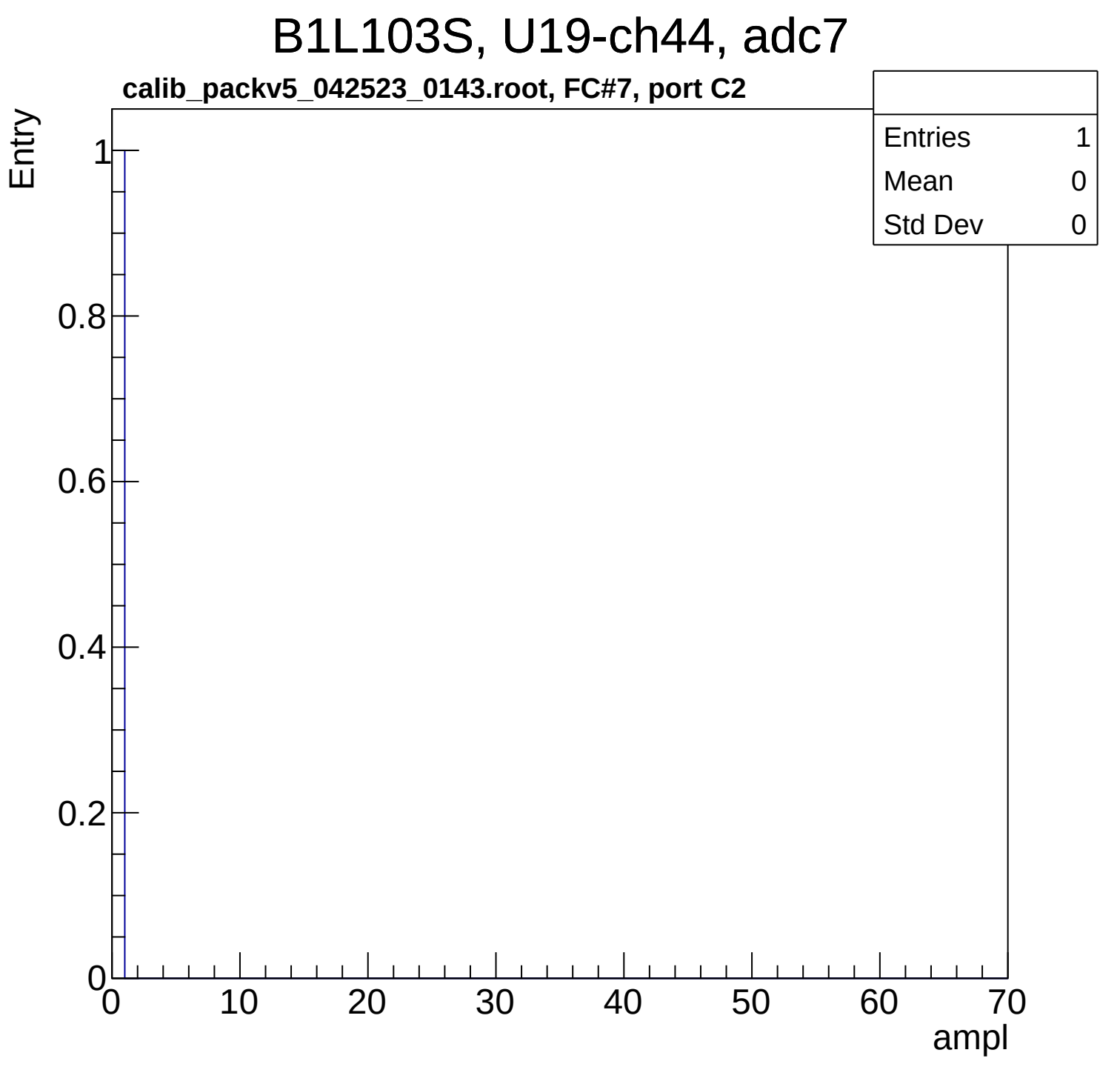

B1L103S, U19-ch44, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 41.32
Std Dev 3.767
Gaus mean : 42.3190
Gaus Width: 4.2851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

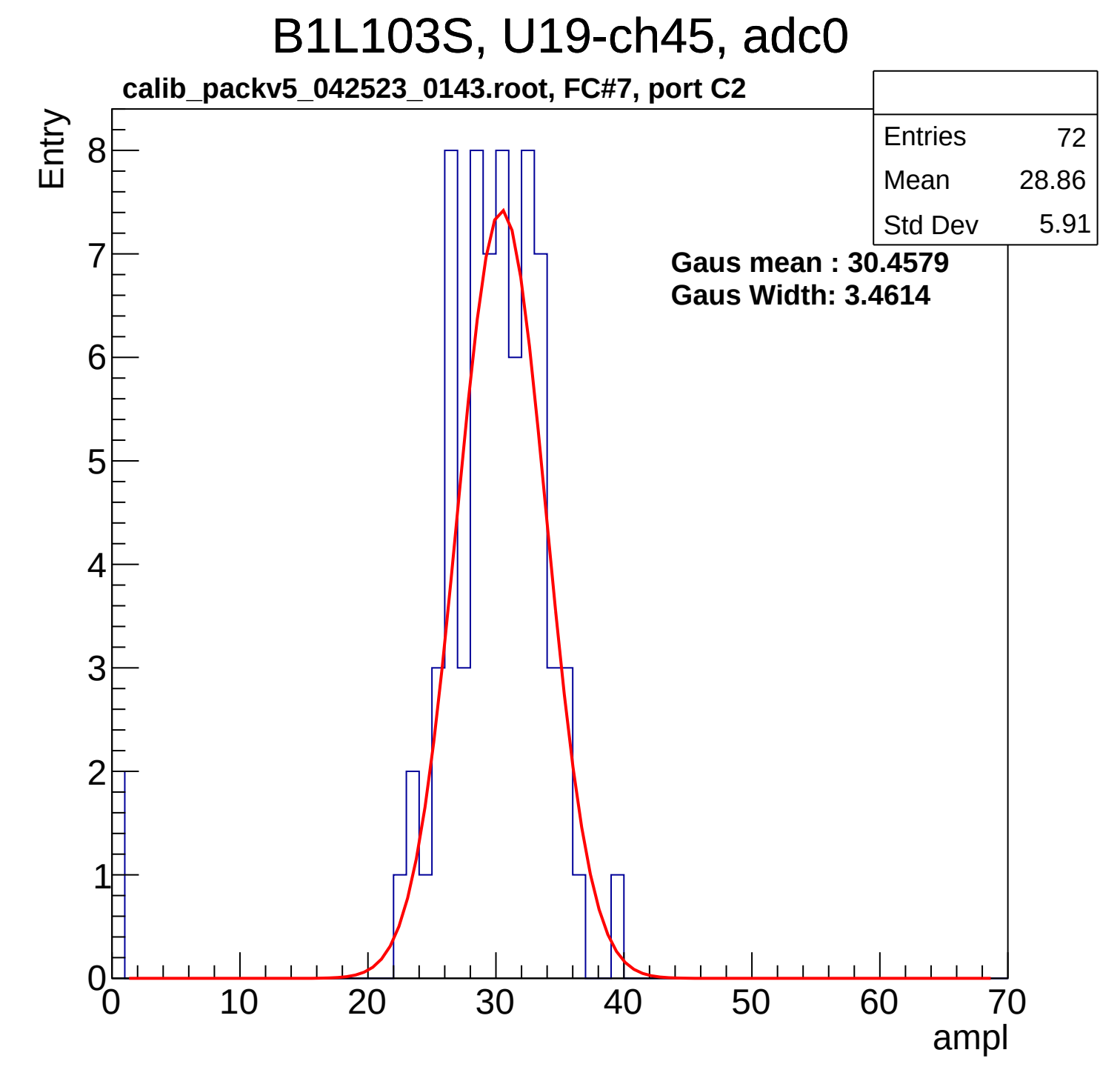

B1L103S, U19-ch44, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.46
Std Dev 4.02
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

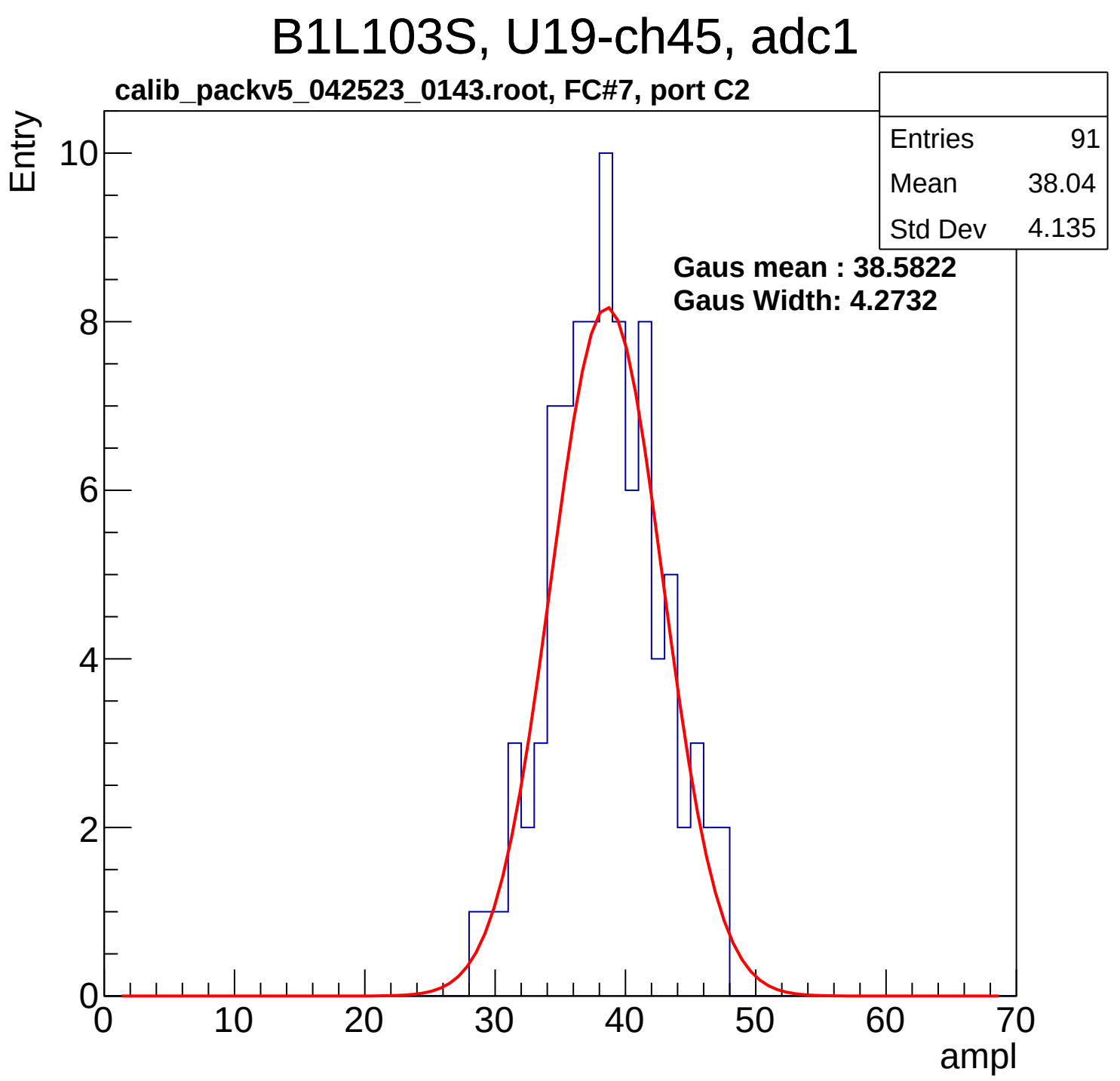

B1L103S, U19-ch44, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 54.56
Std Dev 3.206
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

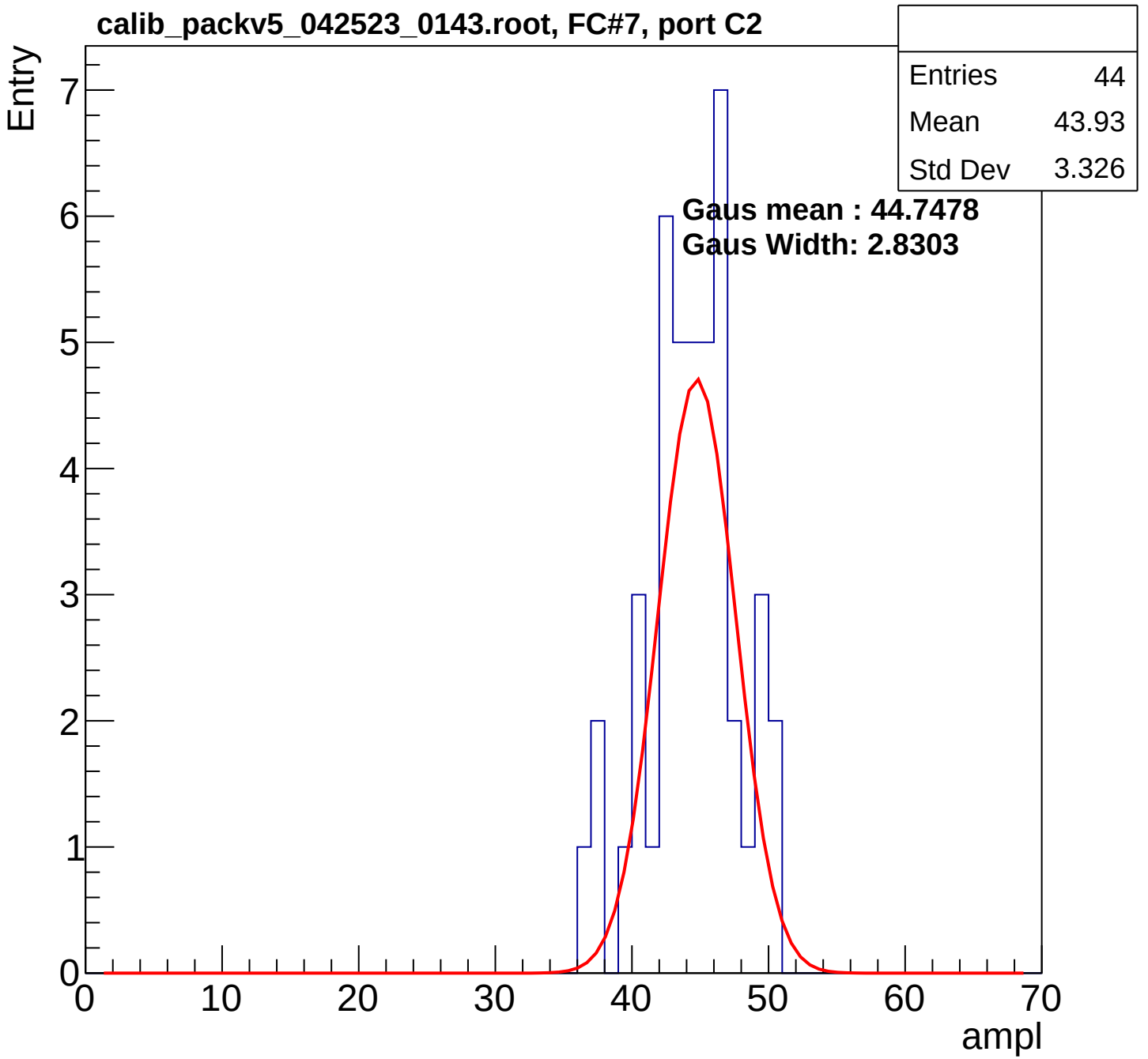

B1L103S, U19-ch44, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 59.92
Std Dev 2.336
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

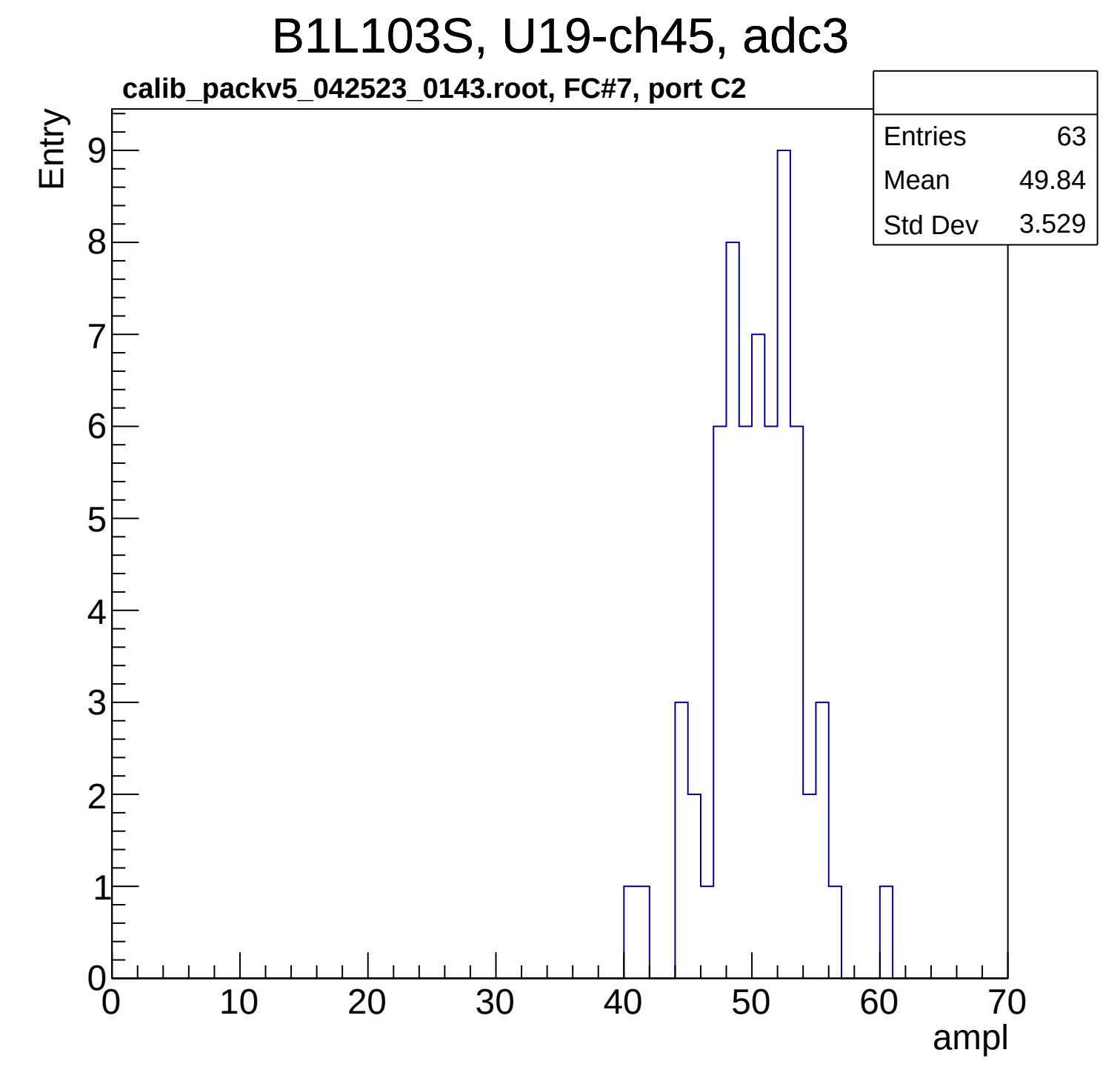

B1L103S, U19-ch44, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 61.5
Std Dev 1.592
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch44, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch45, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 28.86
Std Dev 5.91
Gaus mean : 30.4579
Gaus Width: 3.4614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch45, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 38.04
Std Dev 4.135
Gaus mean : 38.5822
Gaus Width: 4.2732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch45, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 43.93
Std Dev 3.326
Gaus mean : 44.7478
Gaus Width: 2.8303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch45, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.84
Std Dev 3.529
Entry
ampl
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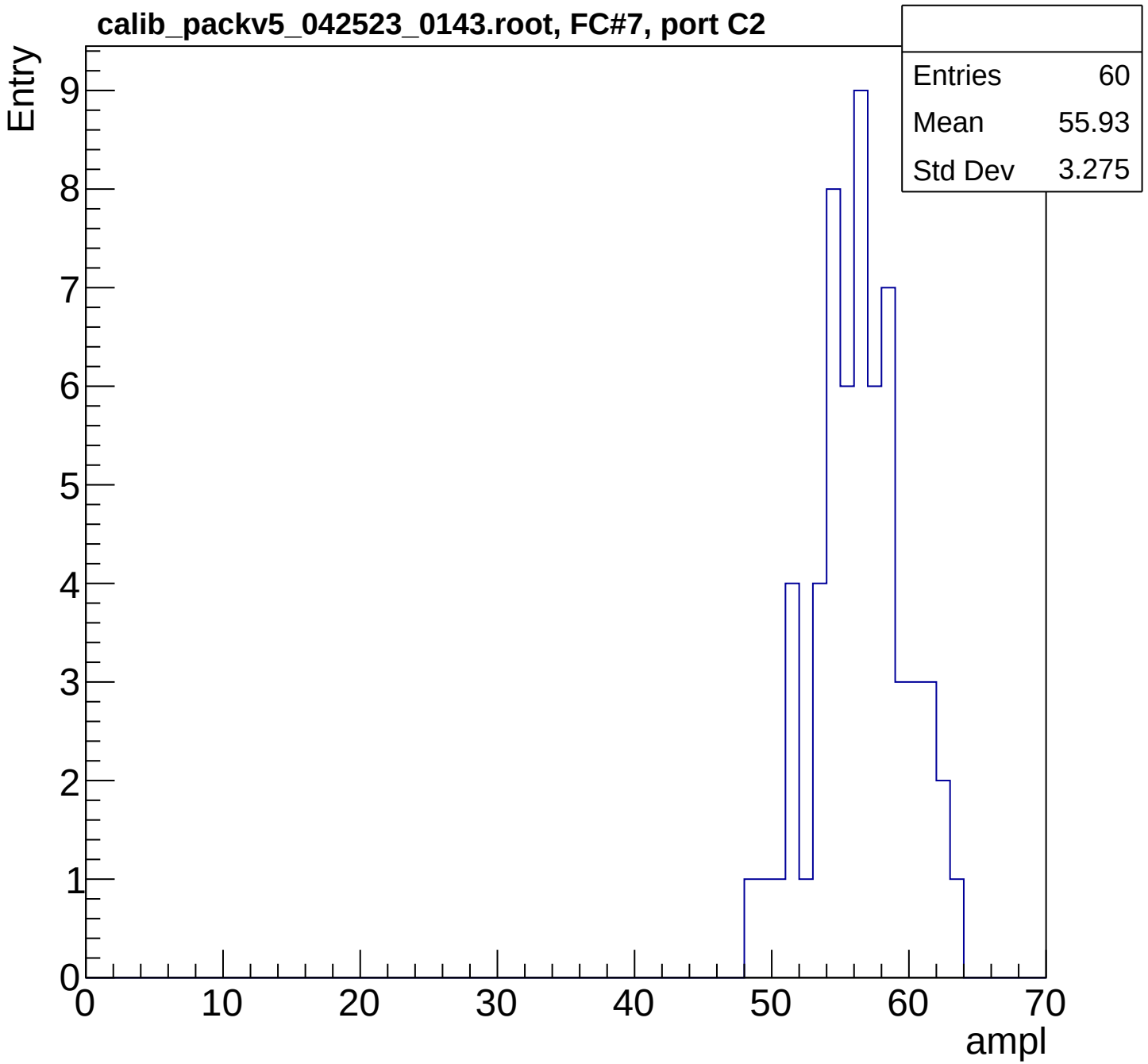
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch45, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 55.93
Std Dev 3.275
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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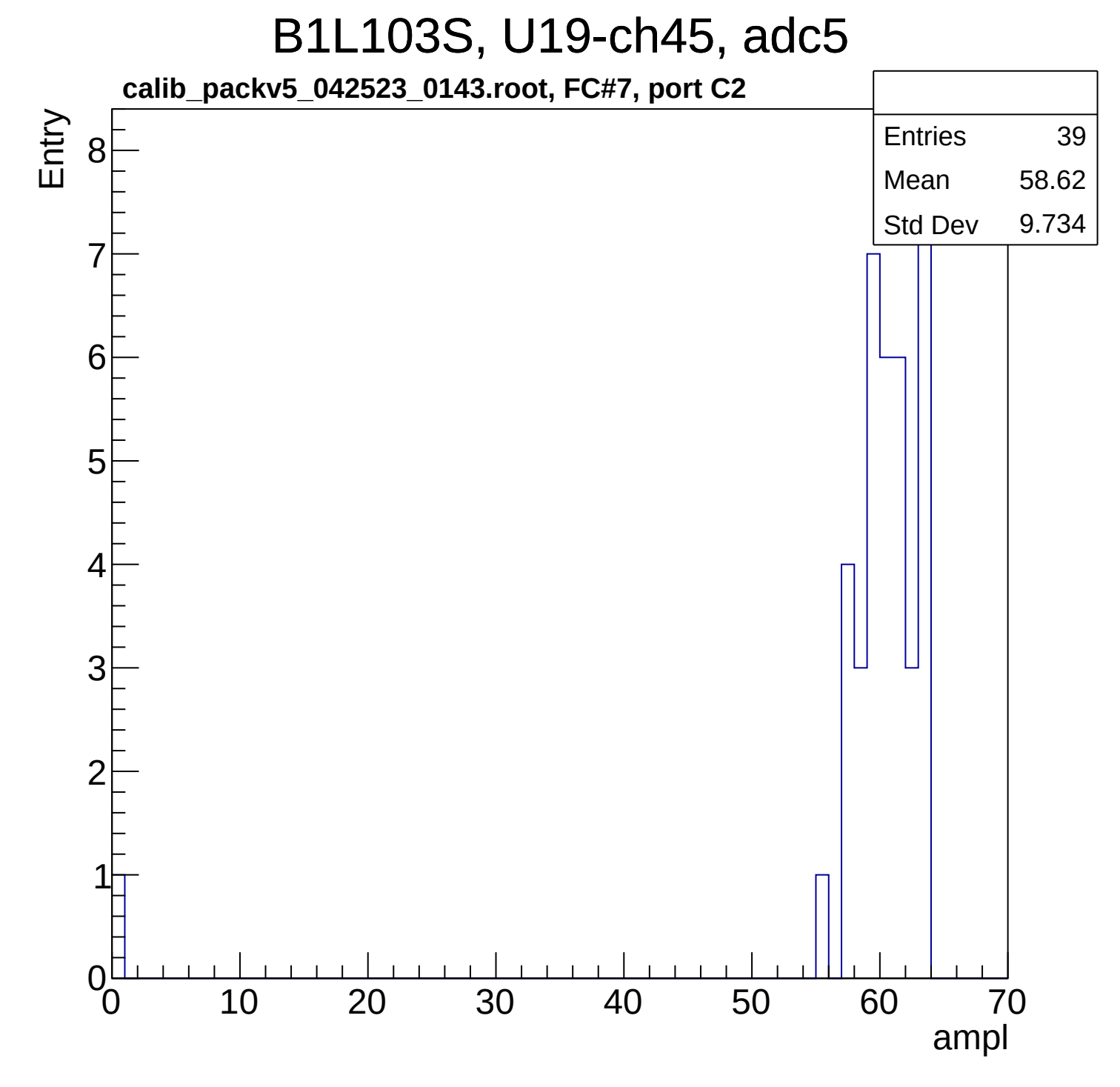
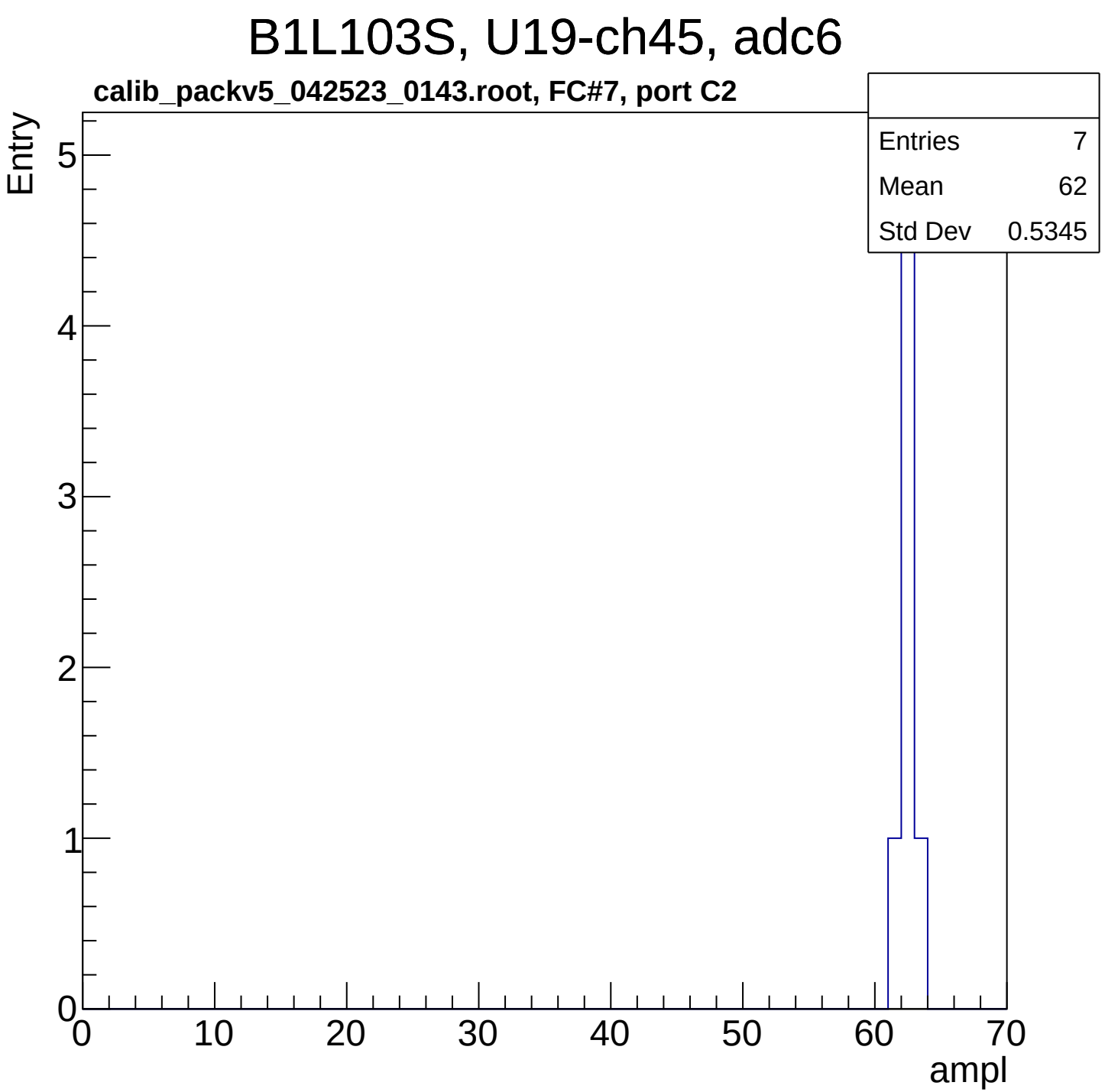
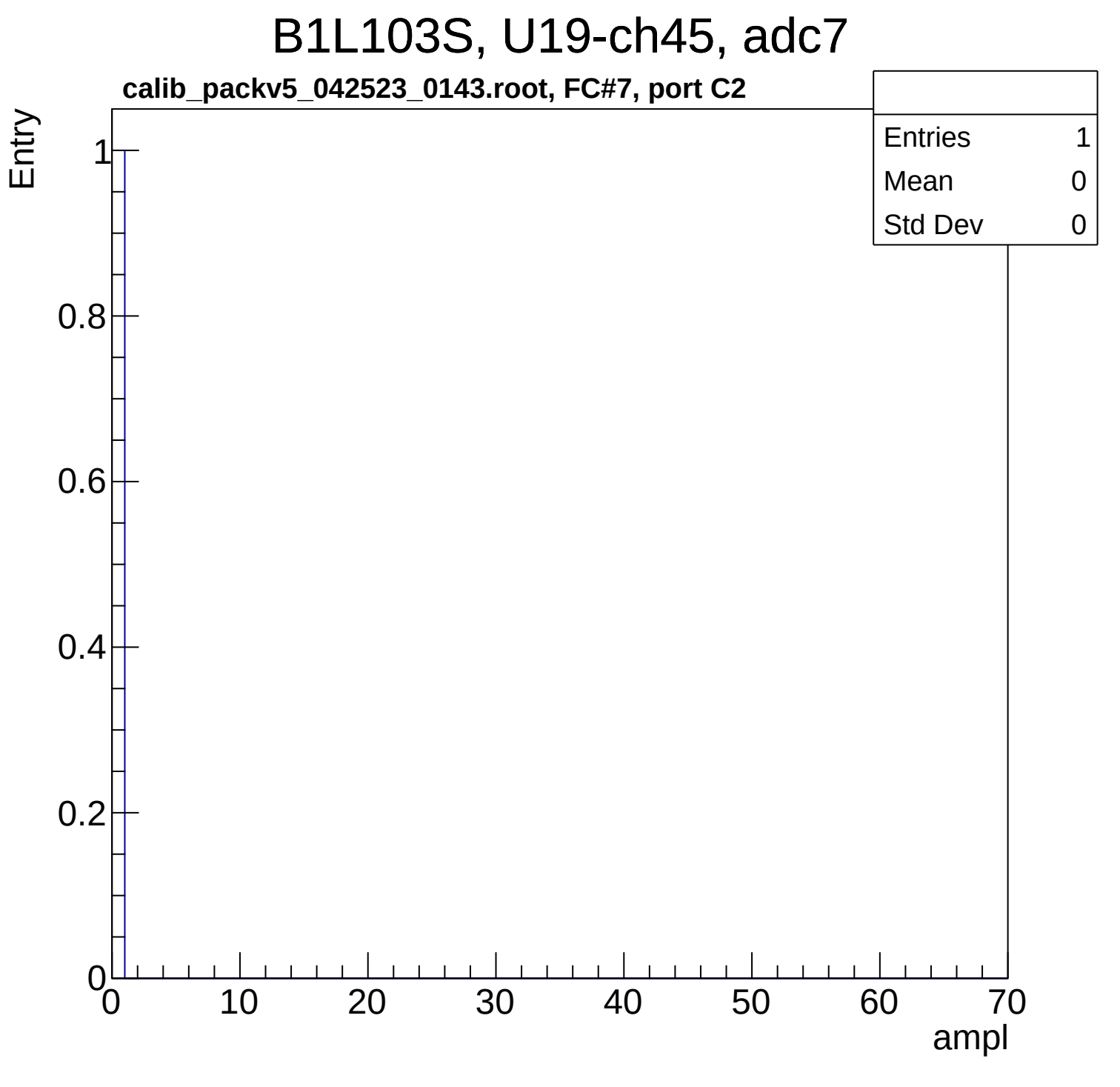

B1L103S, U19-ch45, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.62
Std Dev 9.734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch45, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62
Std Dev 0.5345
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch45, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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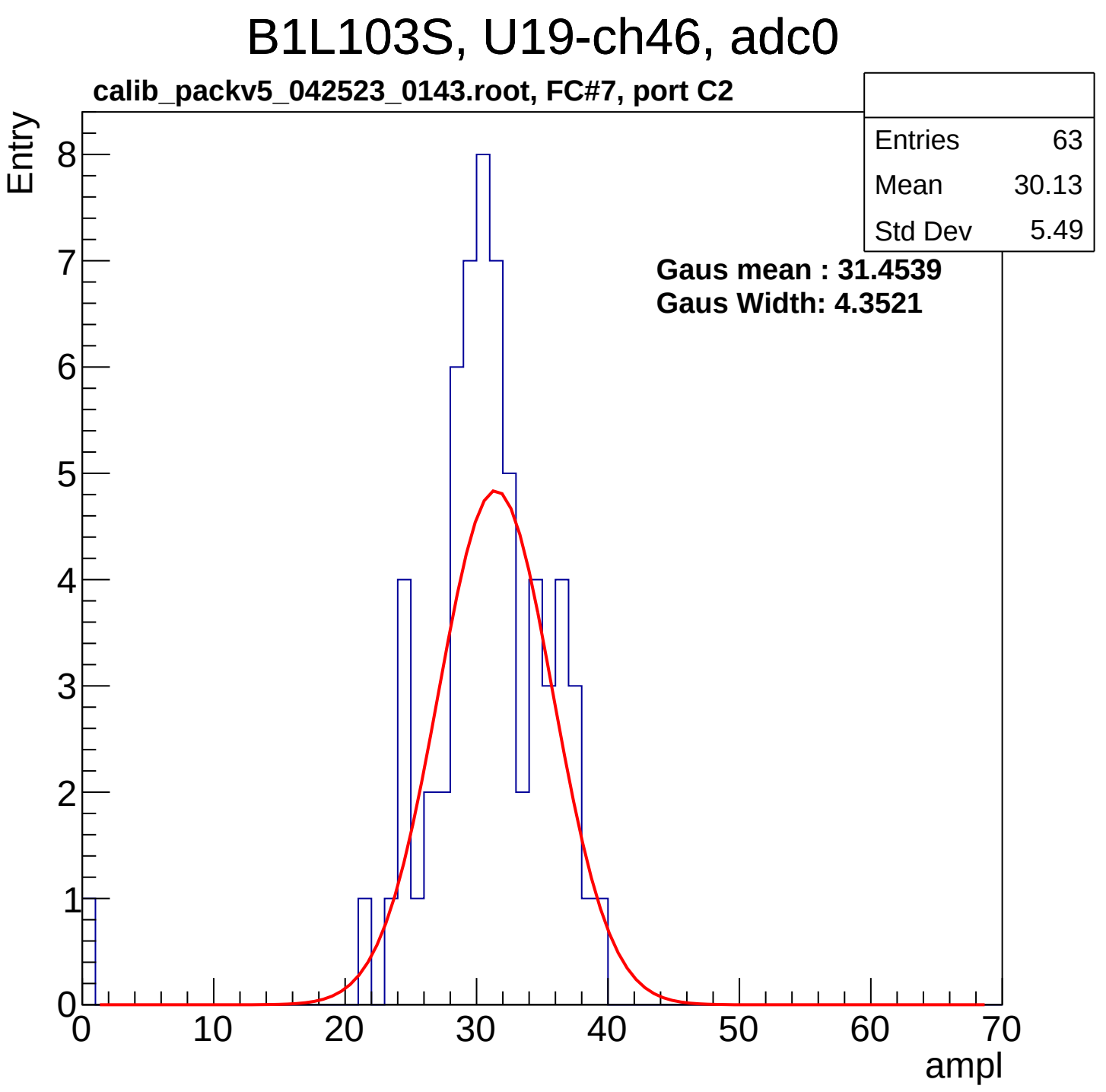
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch46, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 30.13
Std Dev 5.49
Gaus mean : 31.4539
Gaus Width: 4.3521
Entry
ampl
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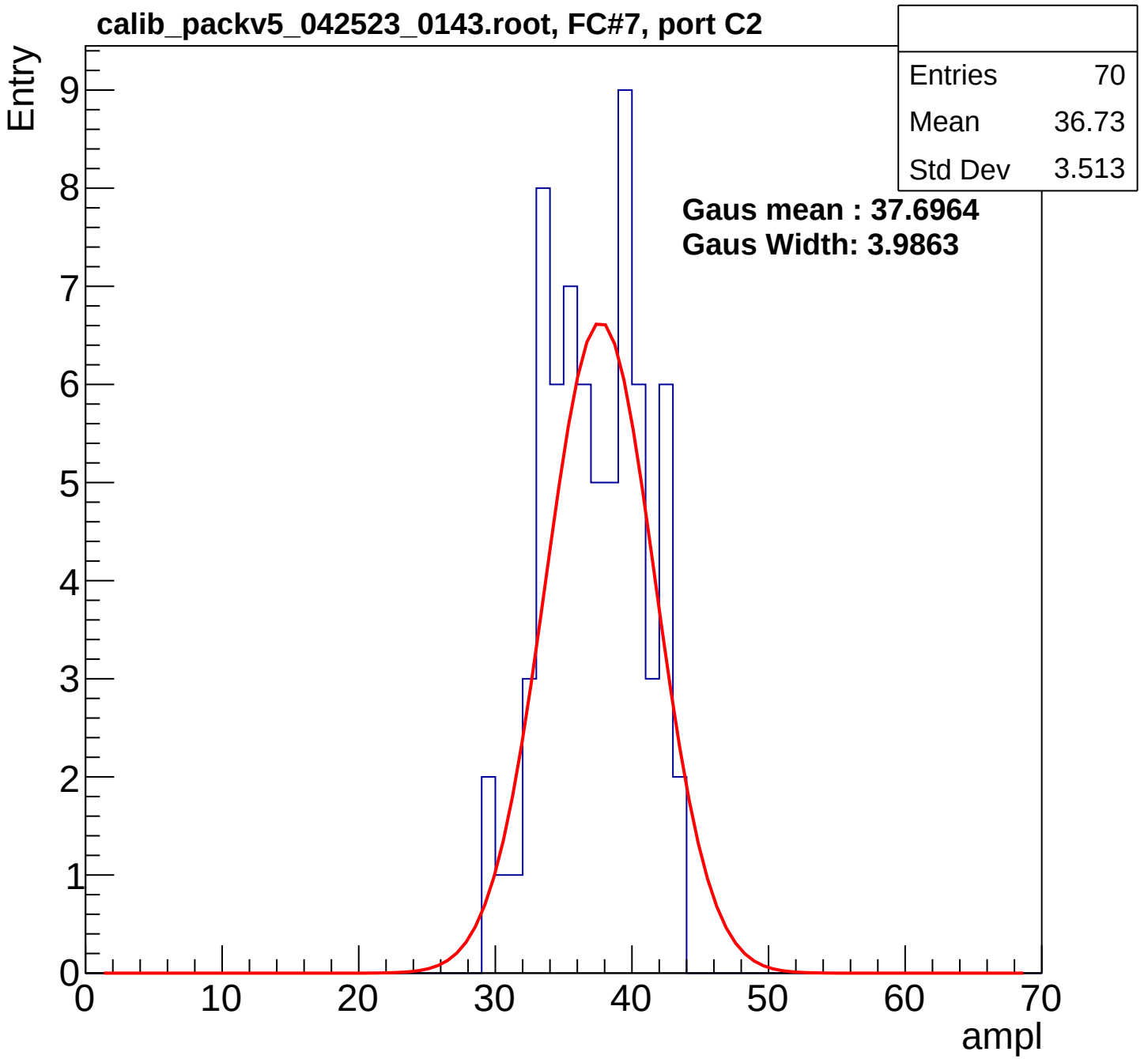
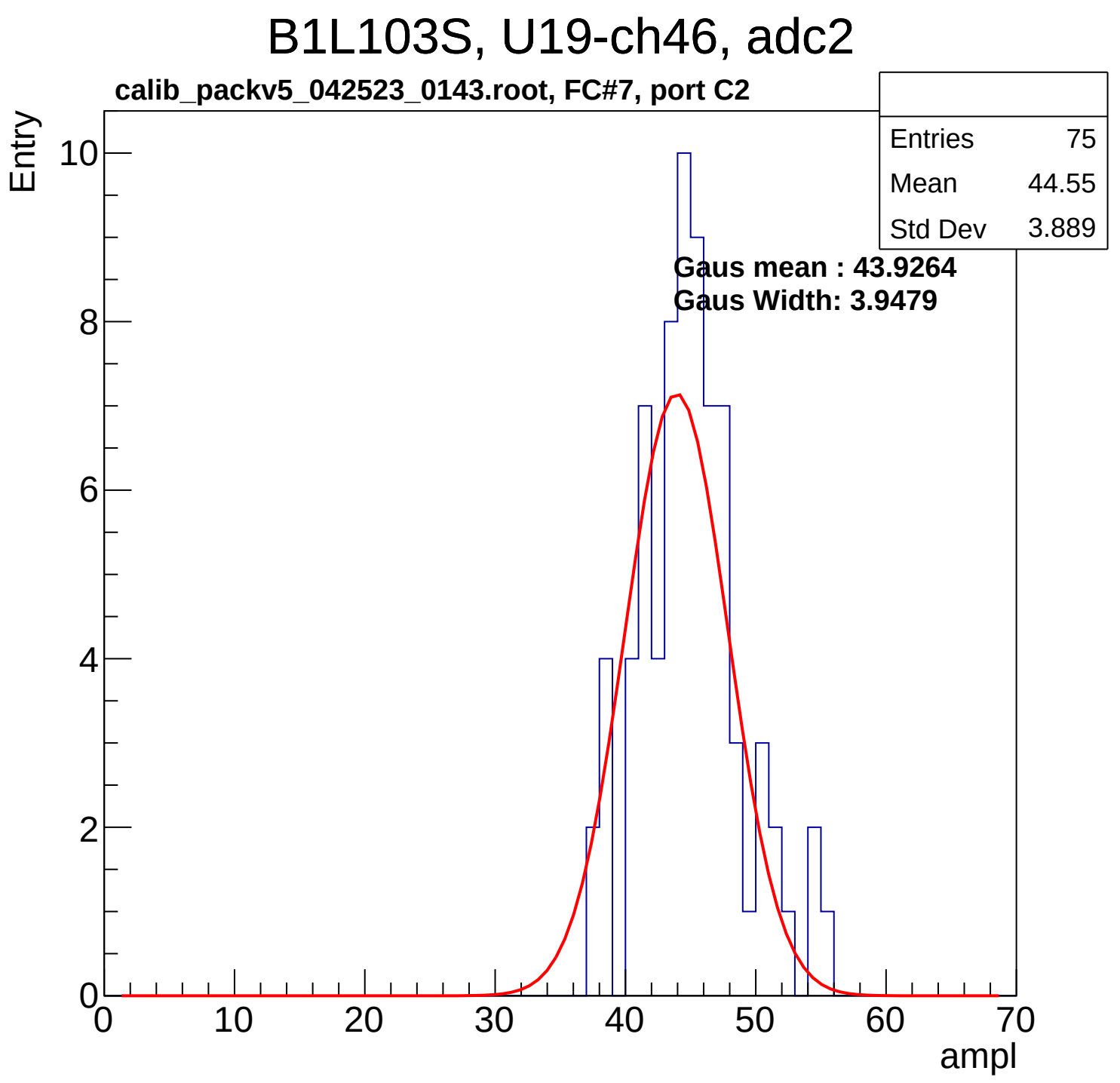
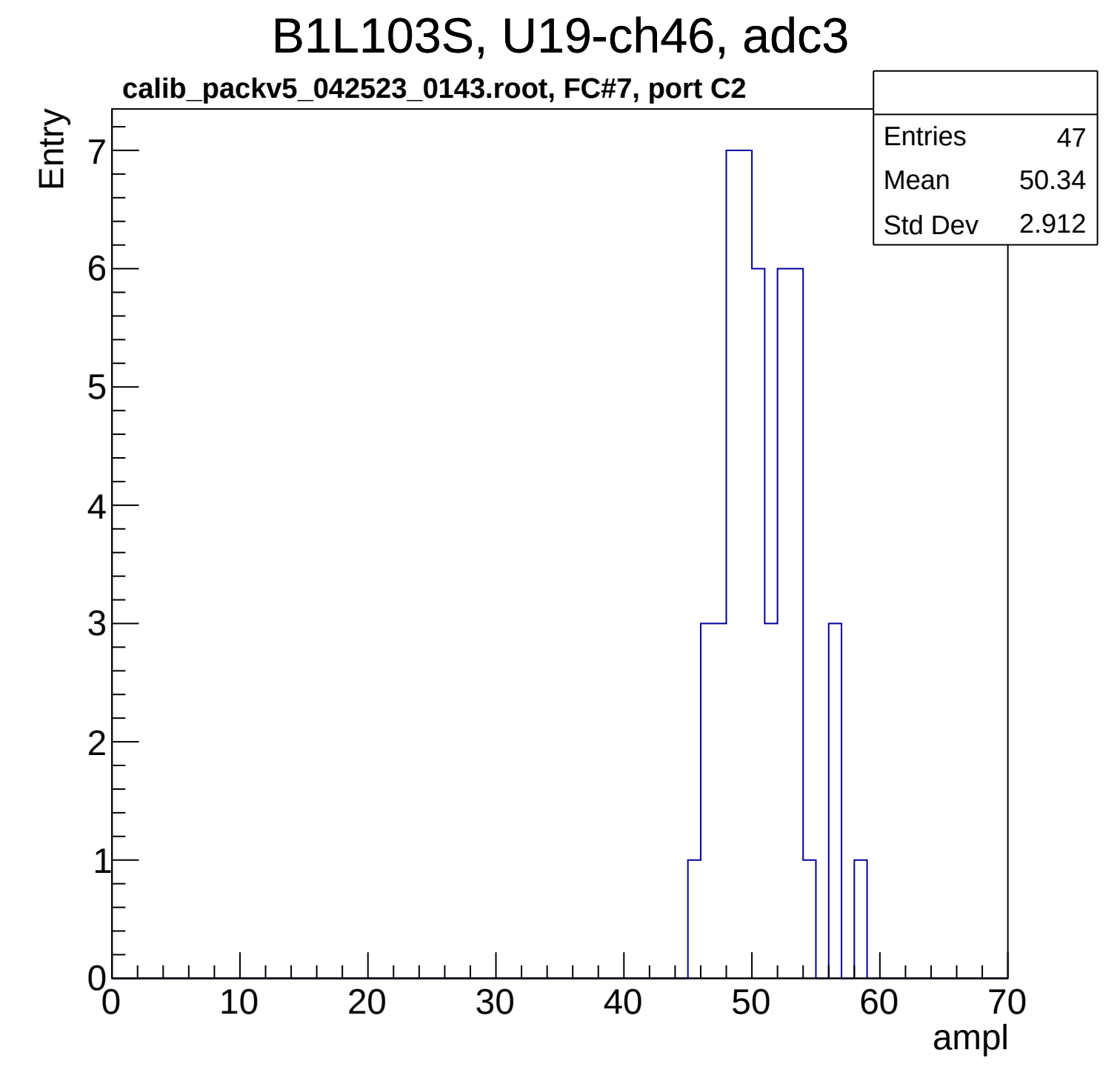
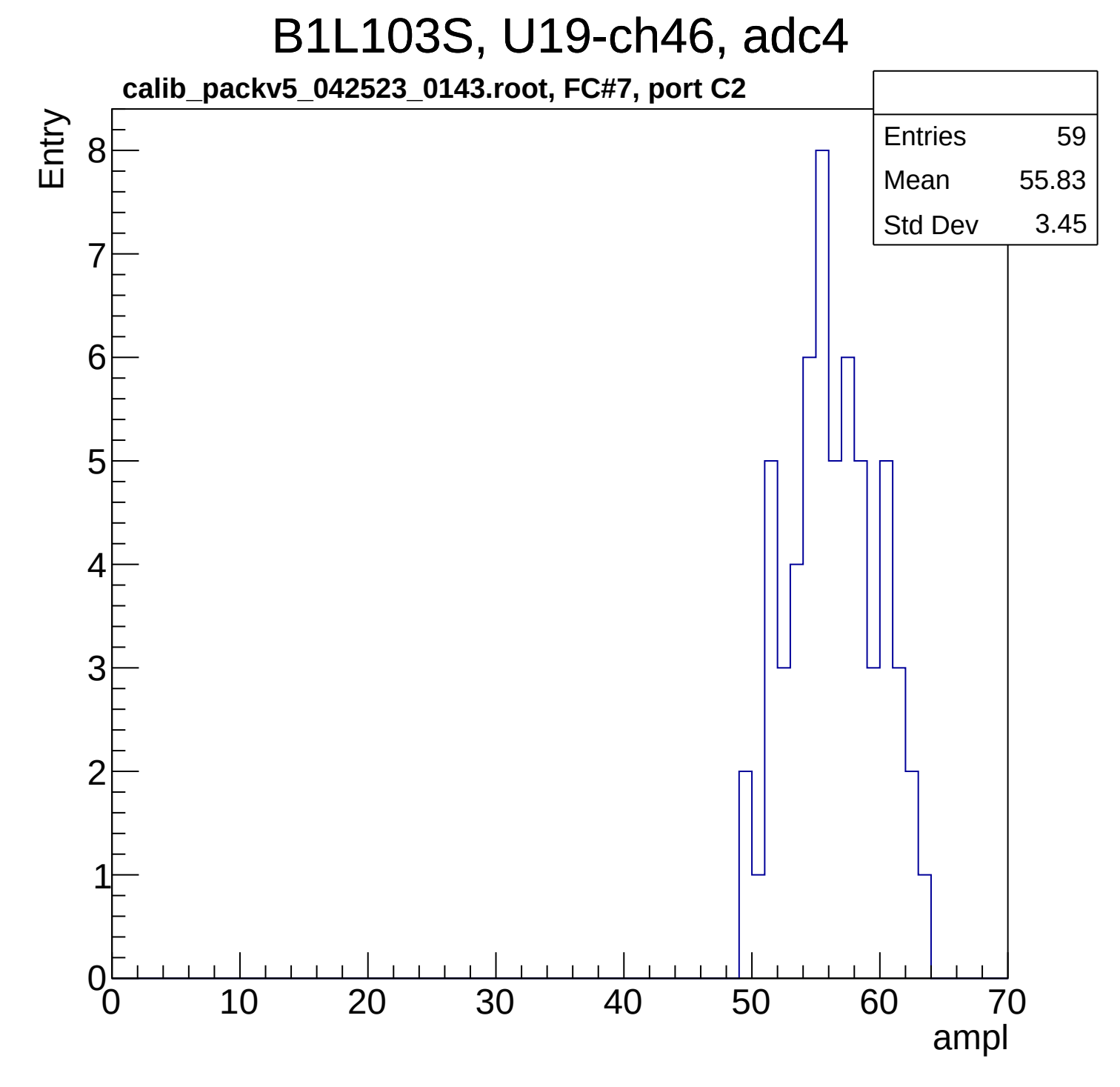
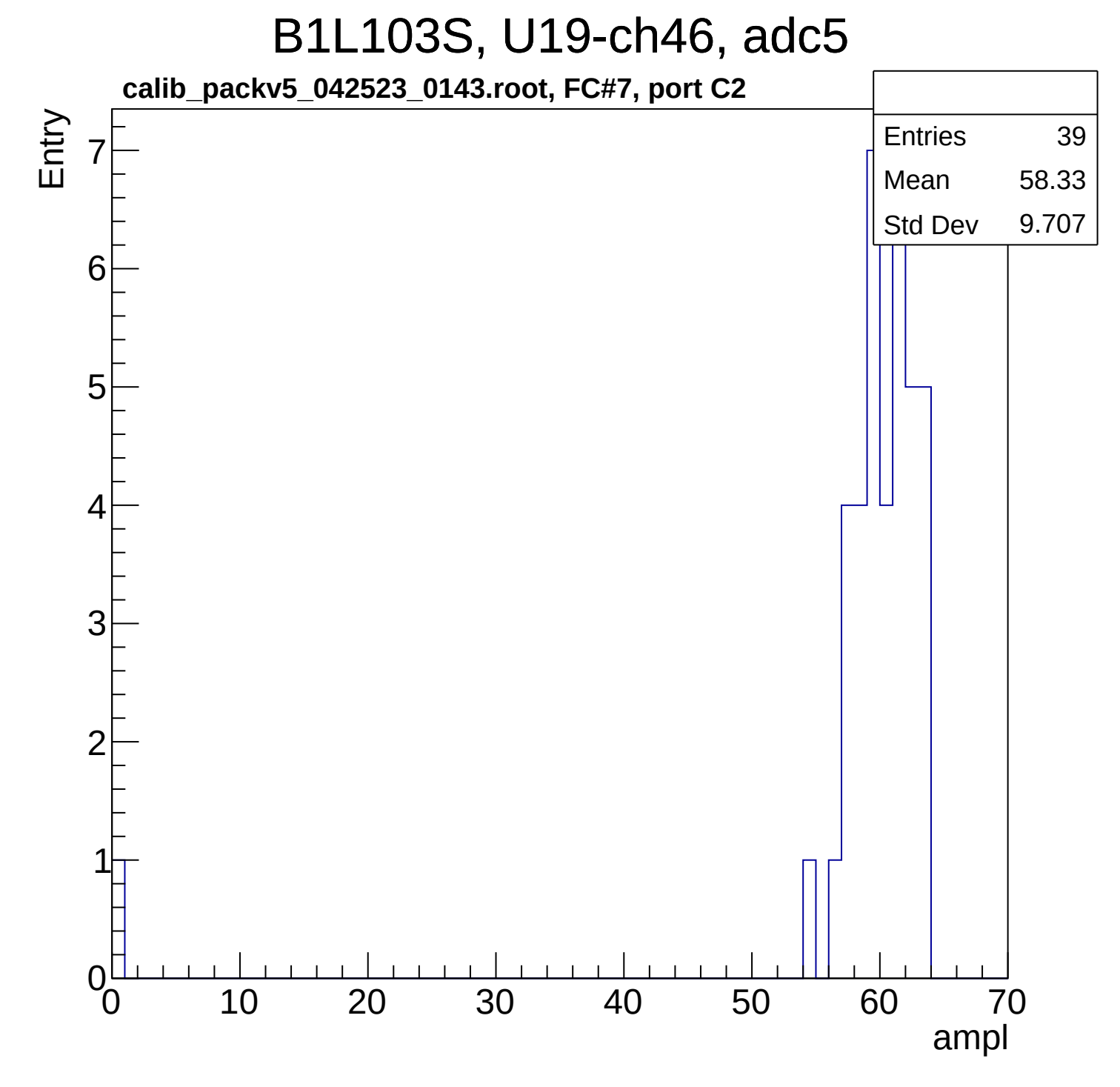
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

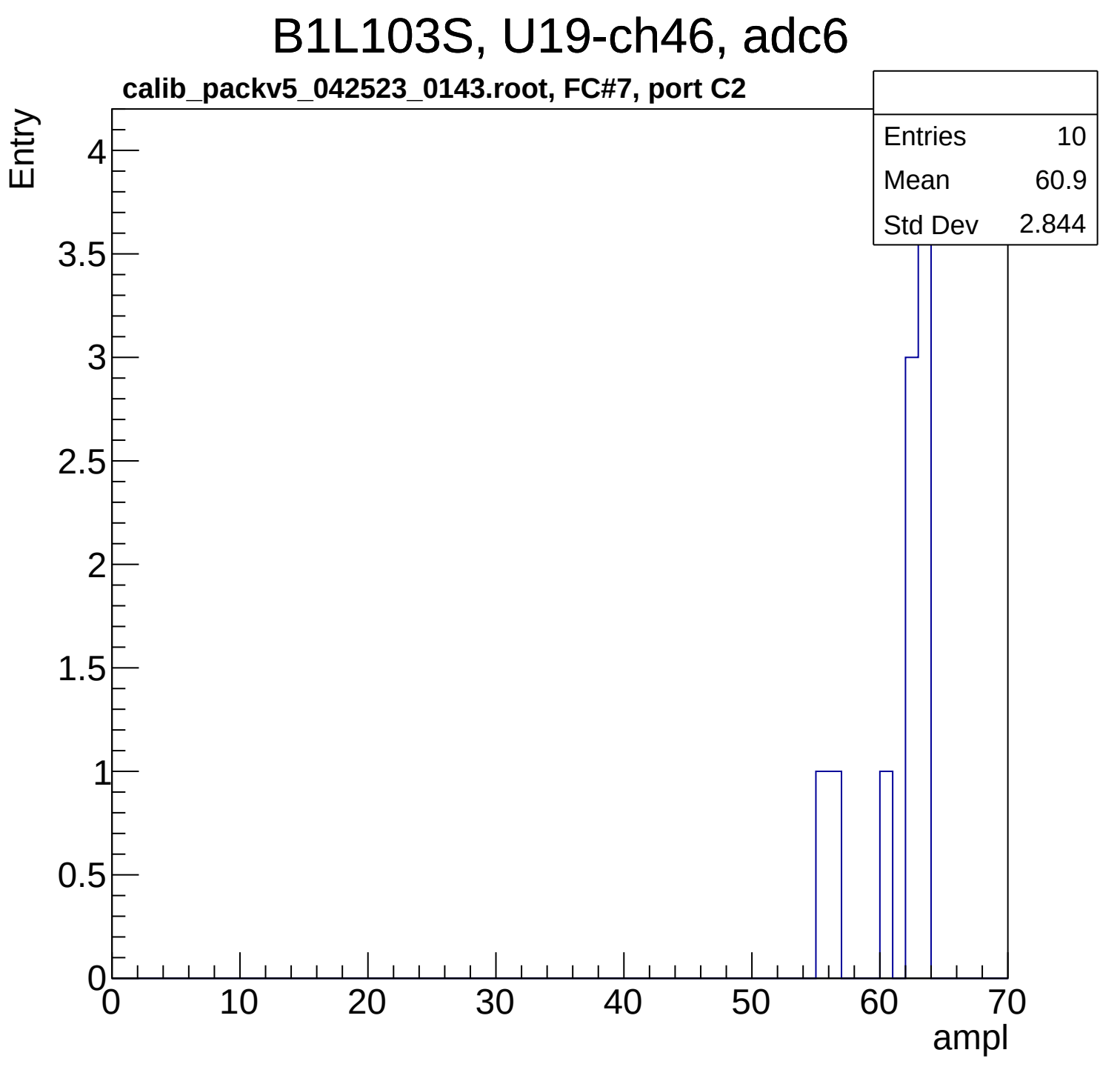

B1L103S, U19-ch46, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 36.73
Std Dev 3.513
Gaus mean : 37.6964
Gaus Width: 3.9863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

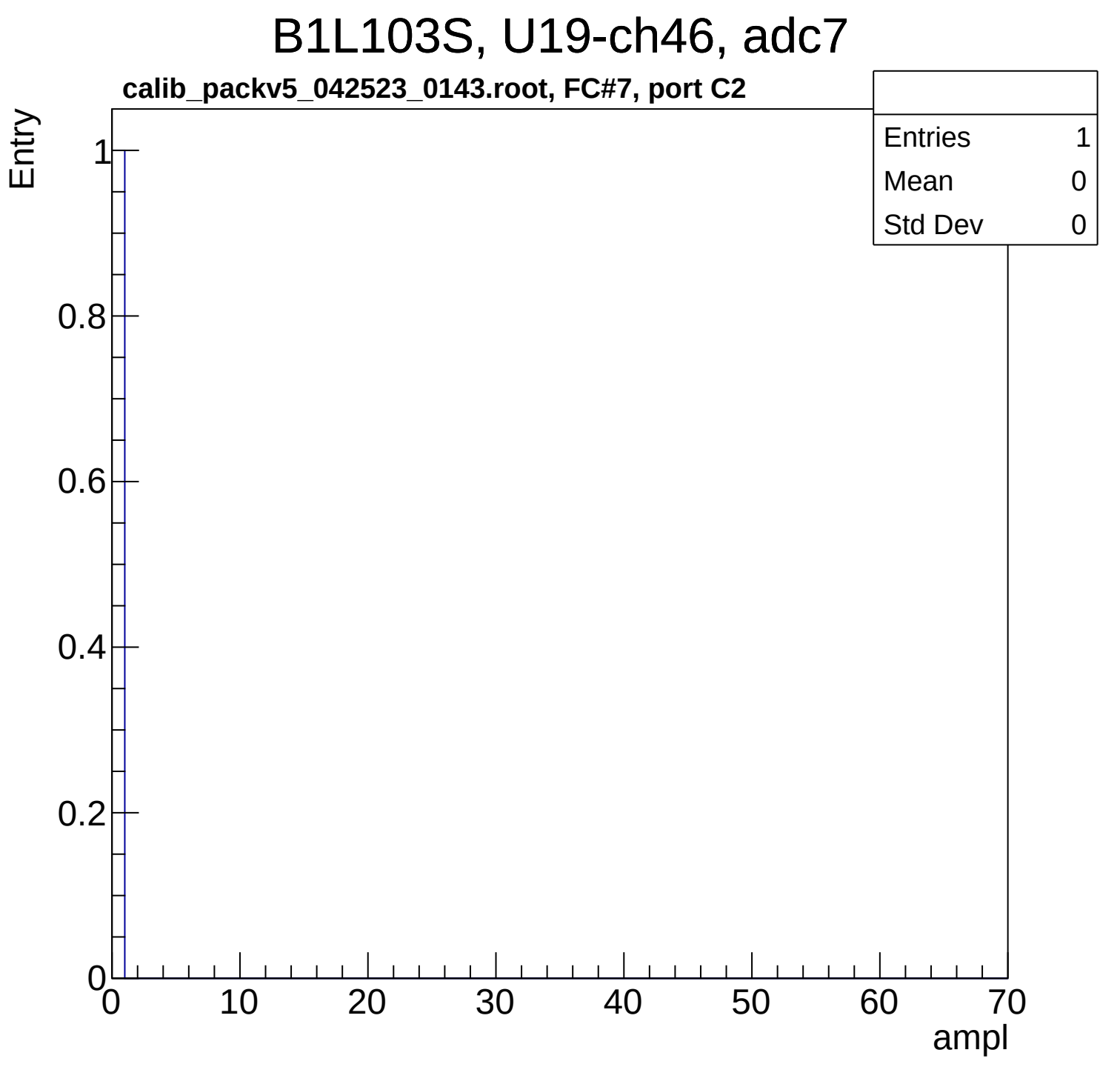

B1L103S, U19-ch46, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 44.55
Std Dev 3.889
Gaus mean : 43.9264
Gaus Width: 3.9479
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

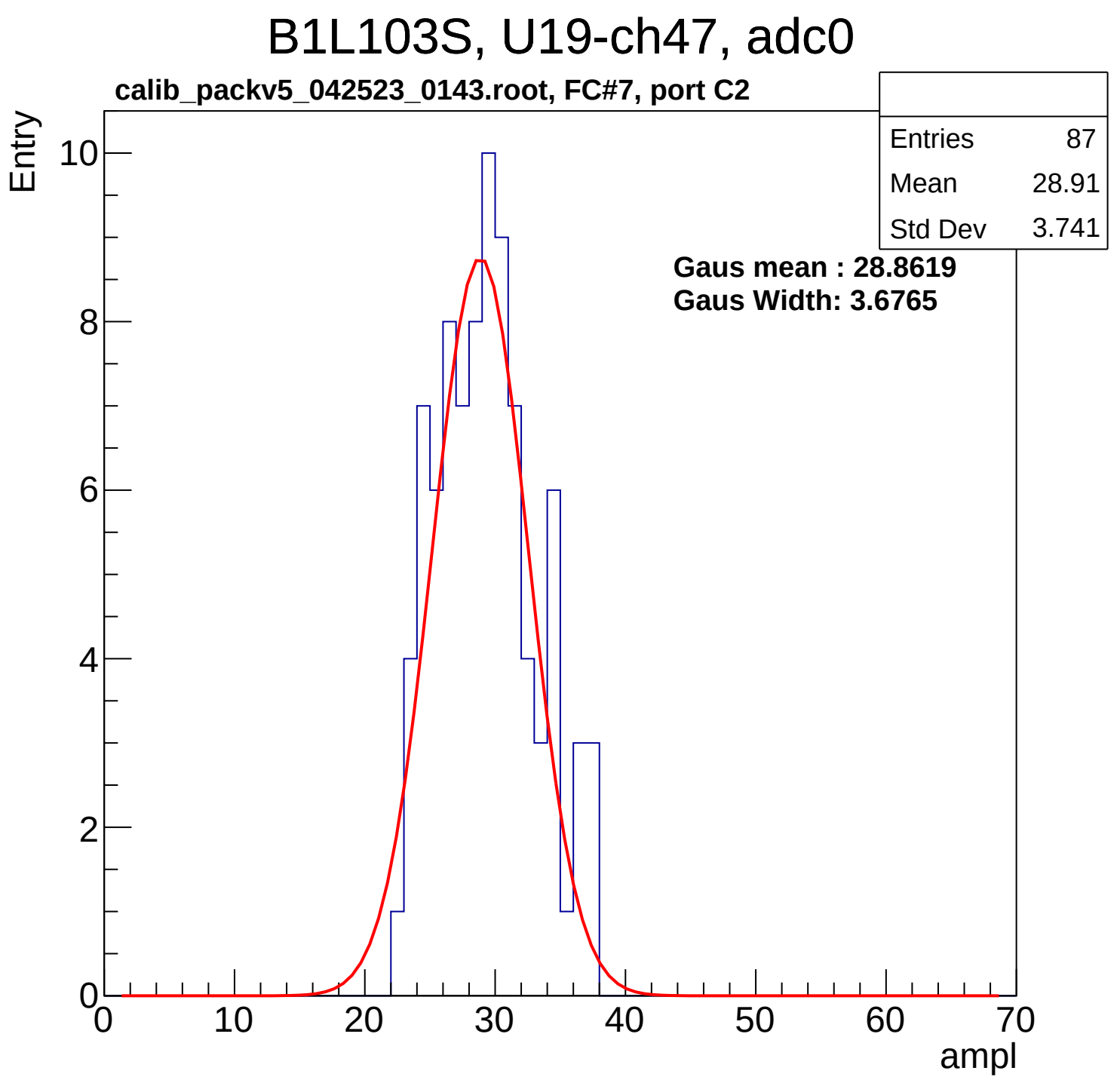

B1L103S, U19-ch46, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 50.34
Std Dev 2.912
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

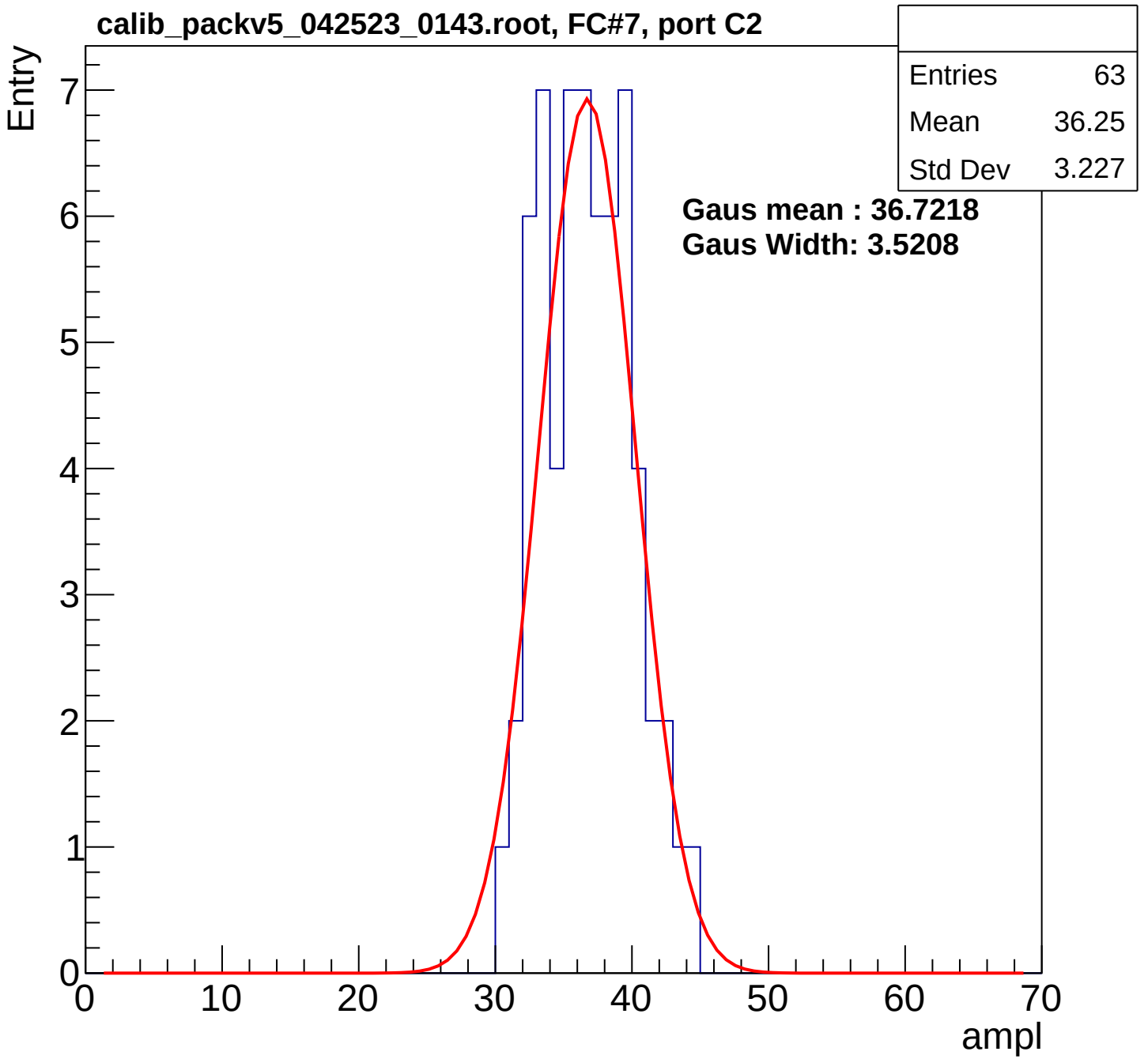

B1L103S, U19-ch46, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.83
Std Dev 3.45
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

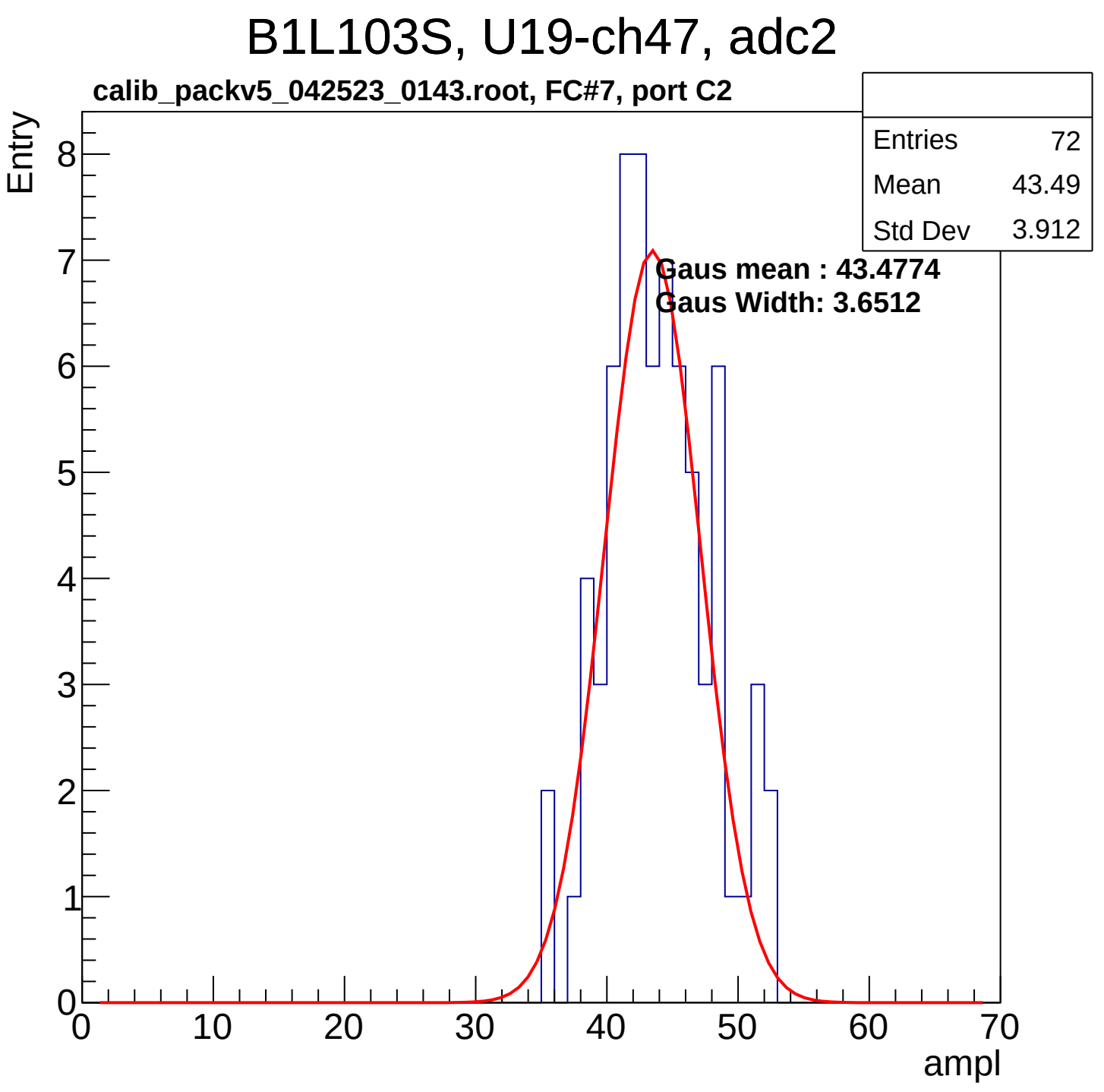

B1L103S, U19-ch46, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.33
Std Dev 9.707
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

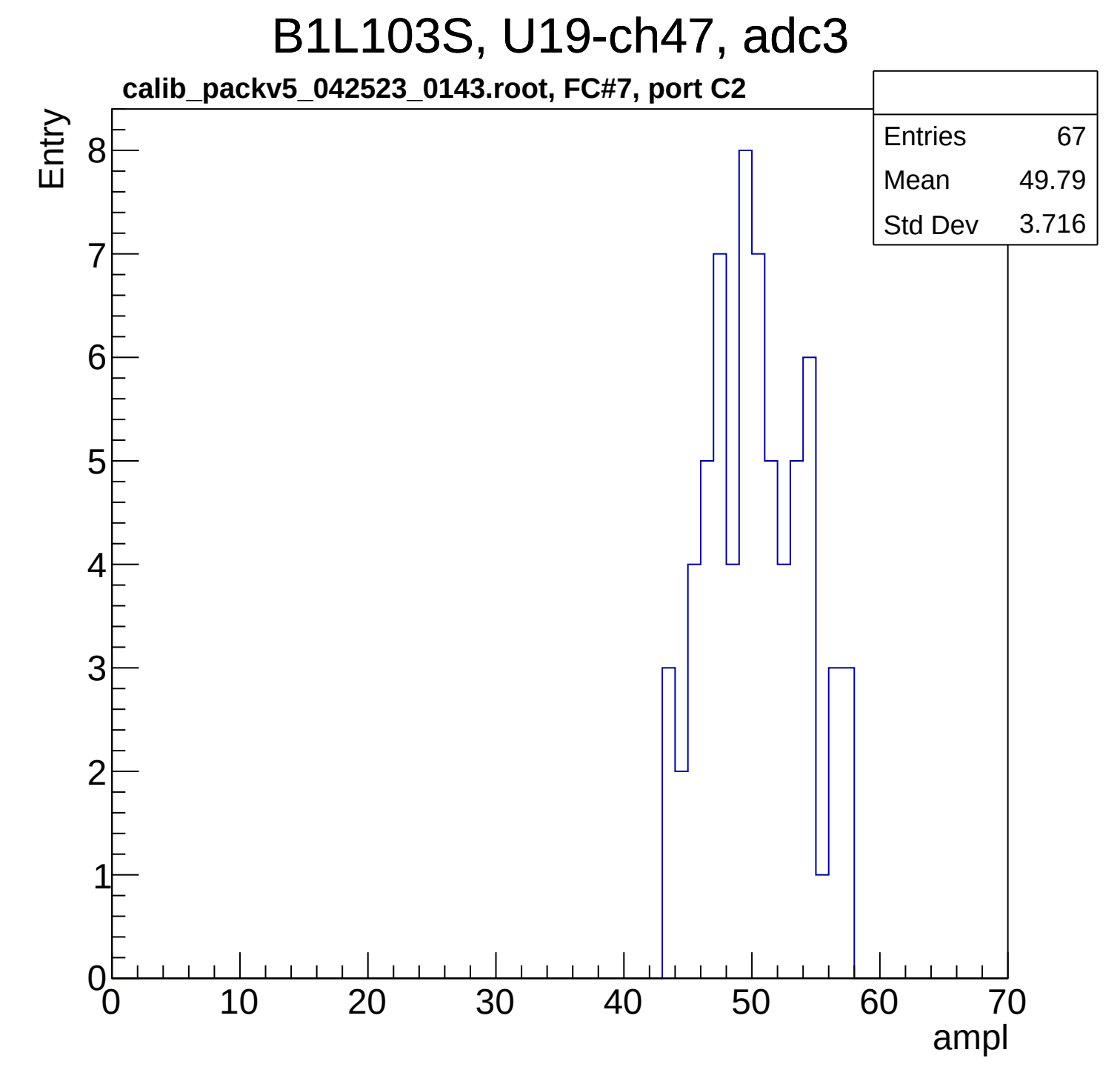

B1L103S, U19-ch46, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 60.9
Std Dev 2.844
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

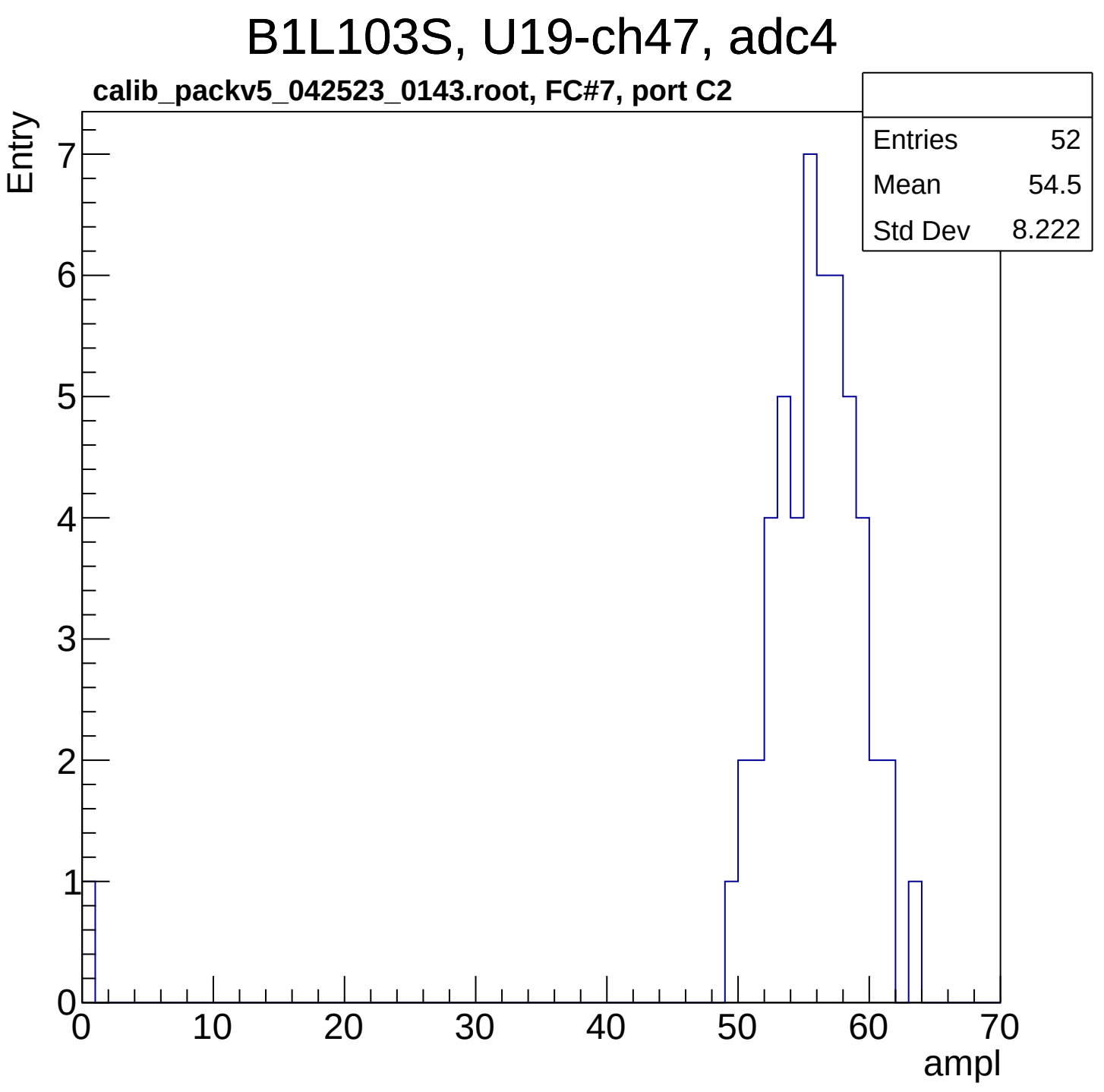

B1L103S, U19-ch46, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

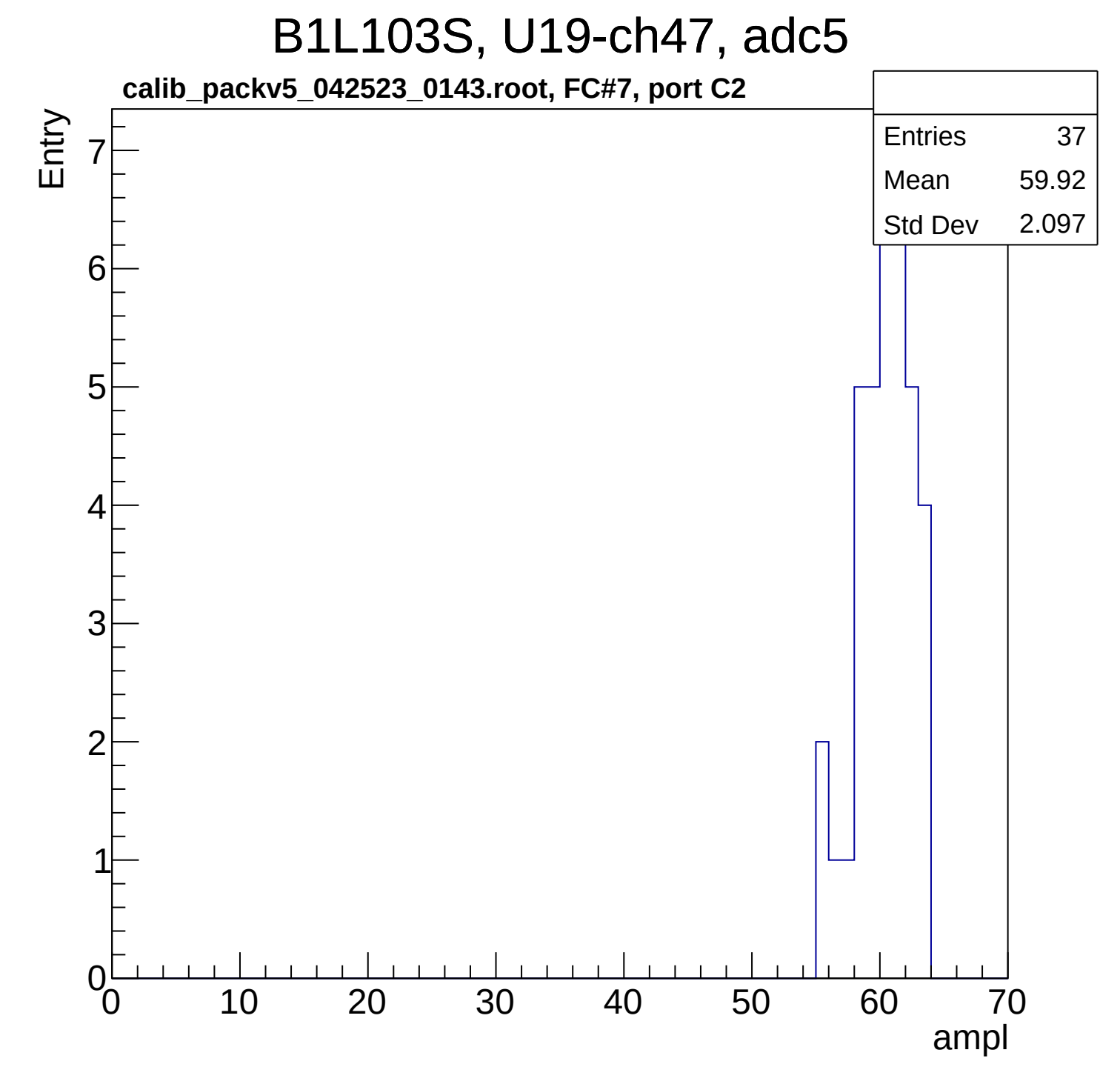

B1L103S, U19-ch47, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 28.91
Std Dev 3.741
Gaus mean : 28.8619
Gaus Width: 3.6765
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

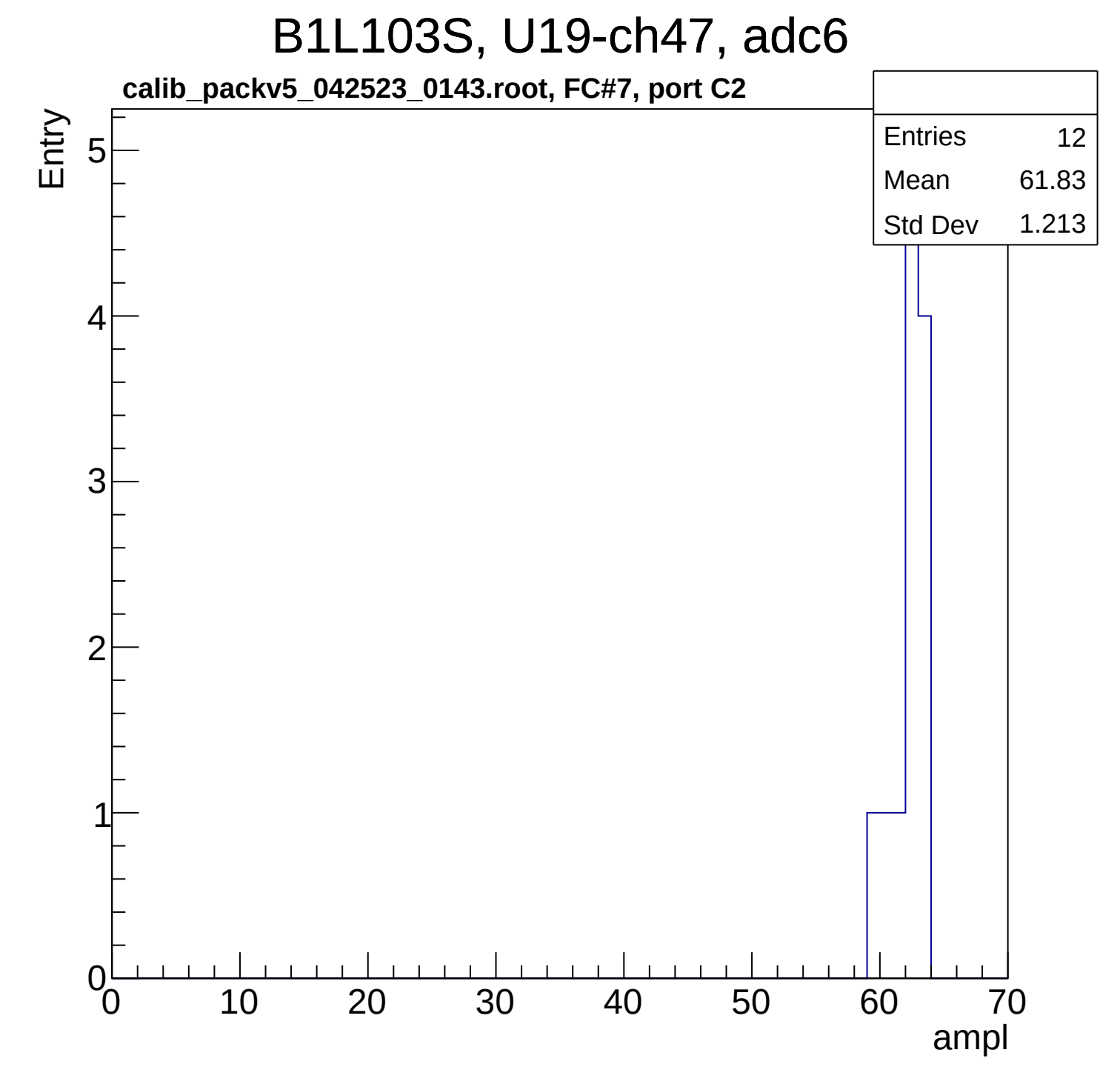

B1L103S, U19-ch47, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 36.25
Std Dev 3.227
Gaus mean : 36.7218
Gaus Width: 3.5208
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

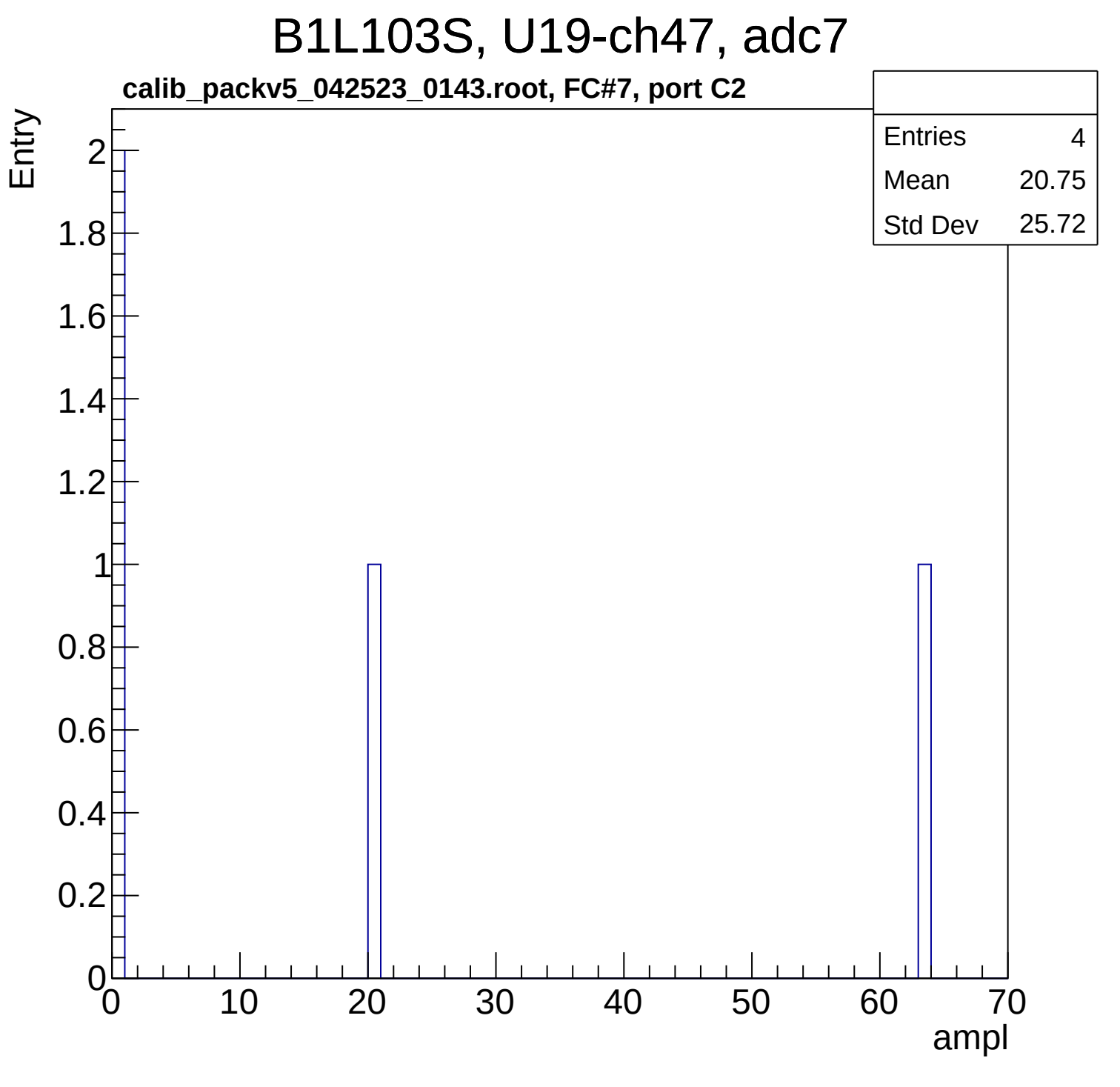

B1L103S, U19-ch47, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.49
Std Dev 3.912
Gaus mean : 43.4774
Gaus Width: 3.6512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

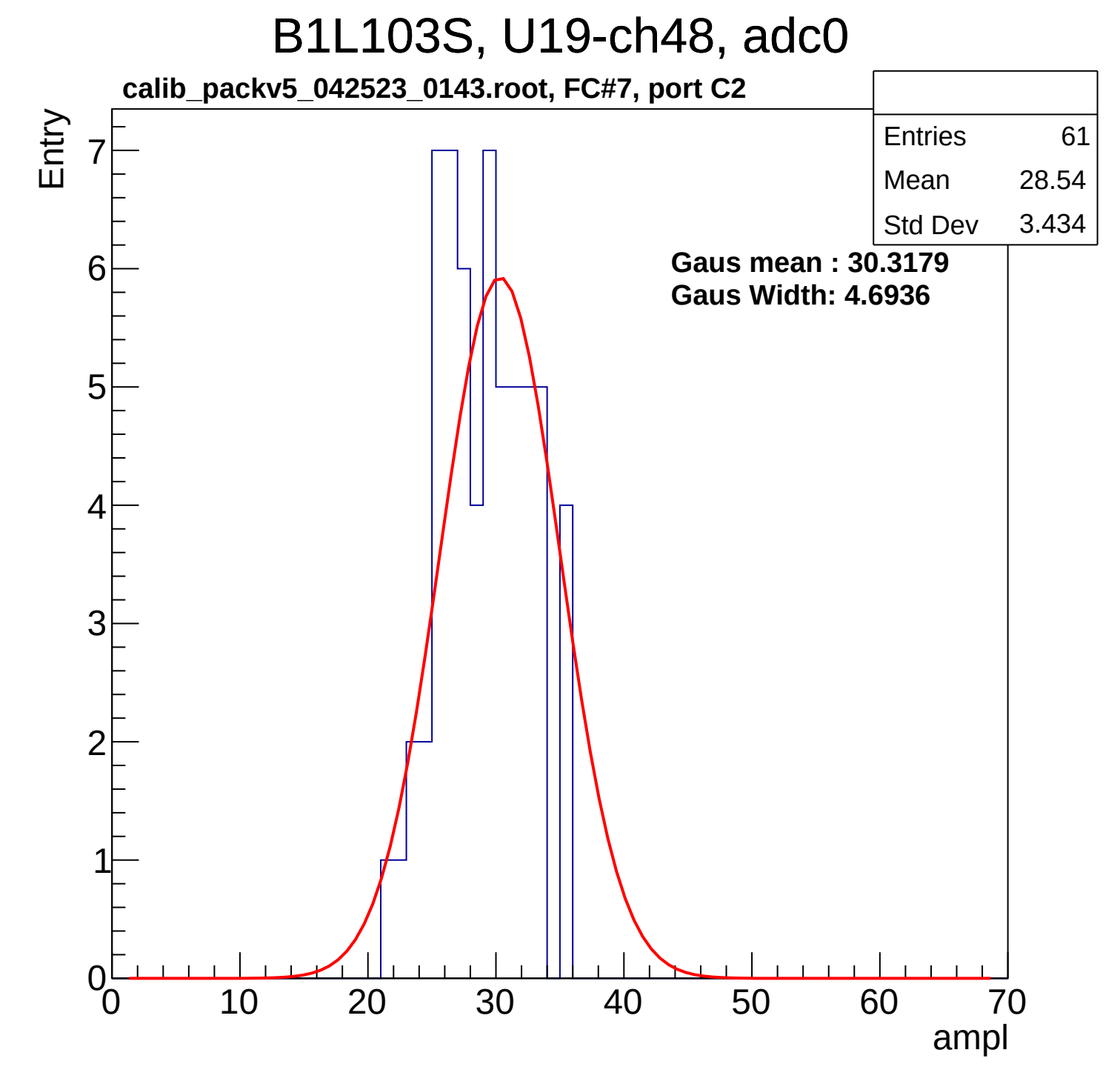

B1L103S, U19-ch47, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 49.79
Std Dev 3.716
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

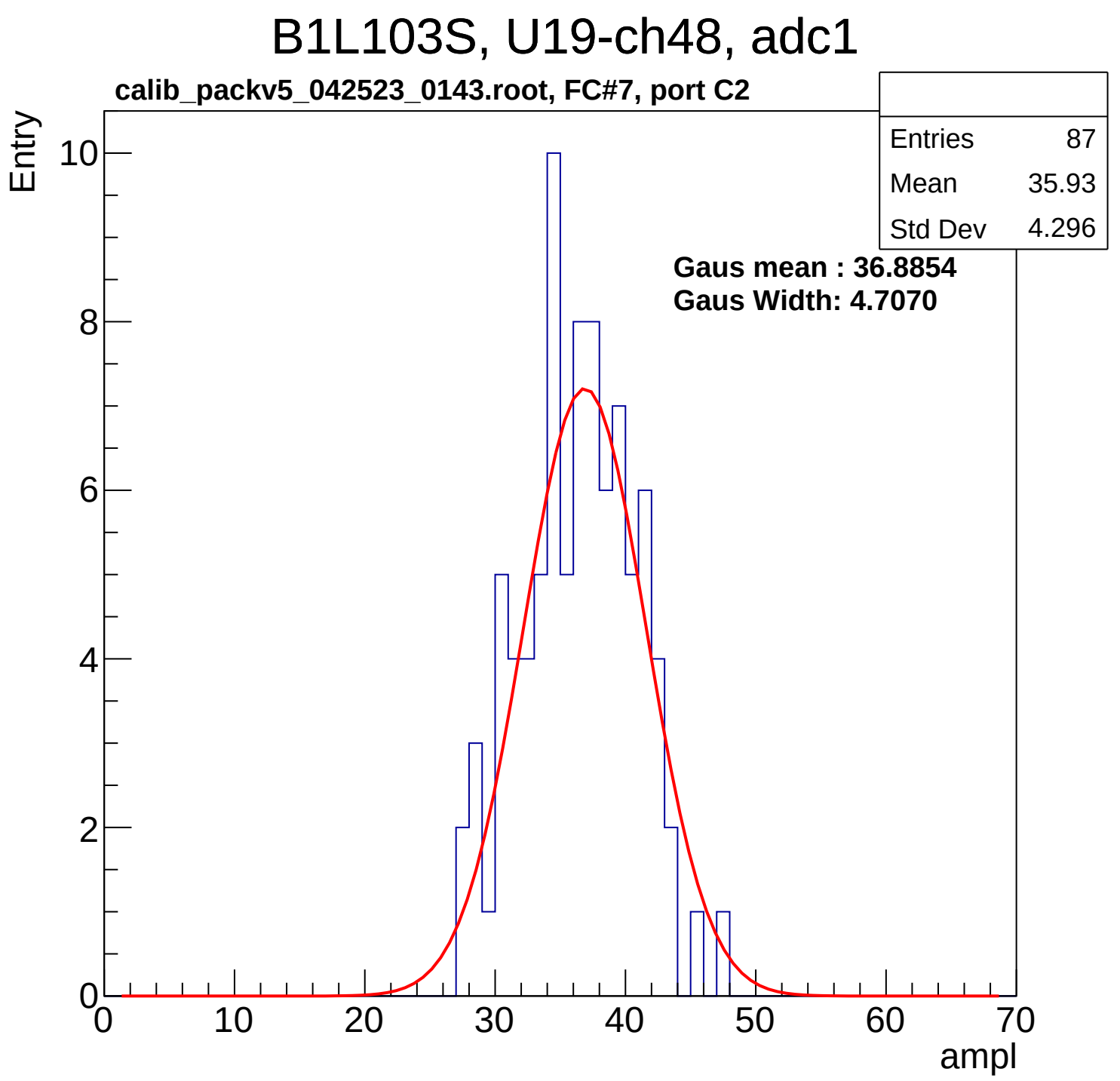

B1L103S, U19-ch47, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 54.5
Std Dev 8.222
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

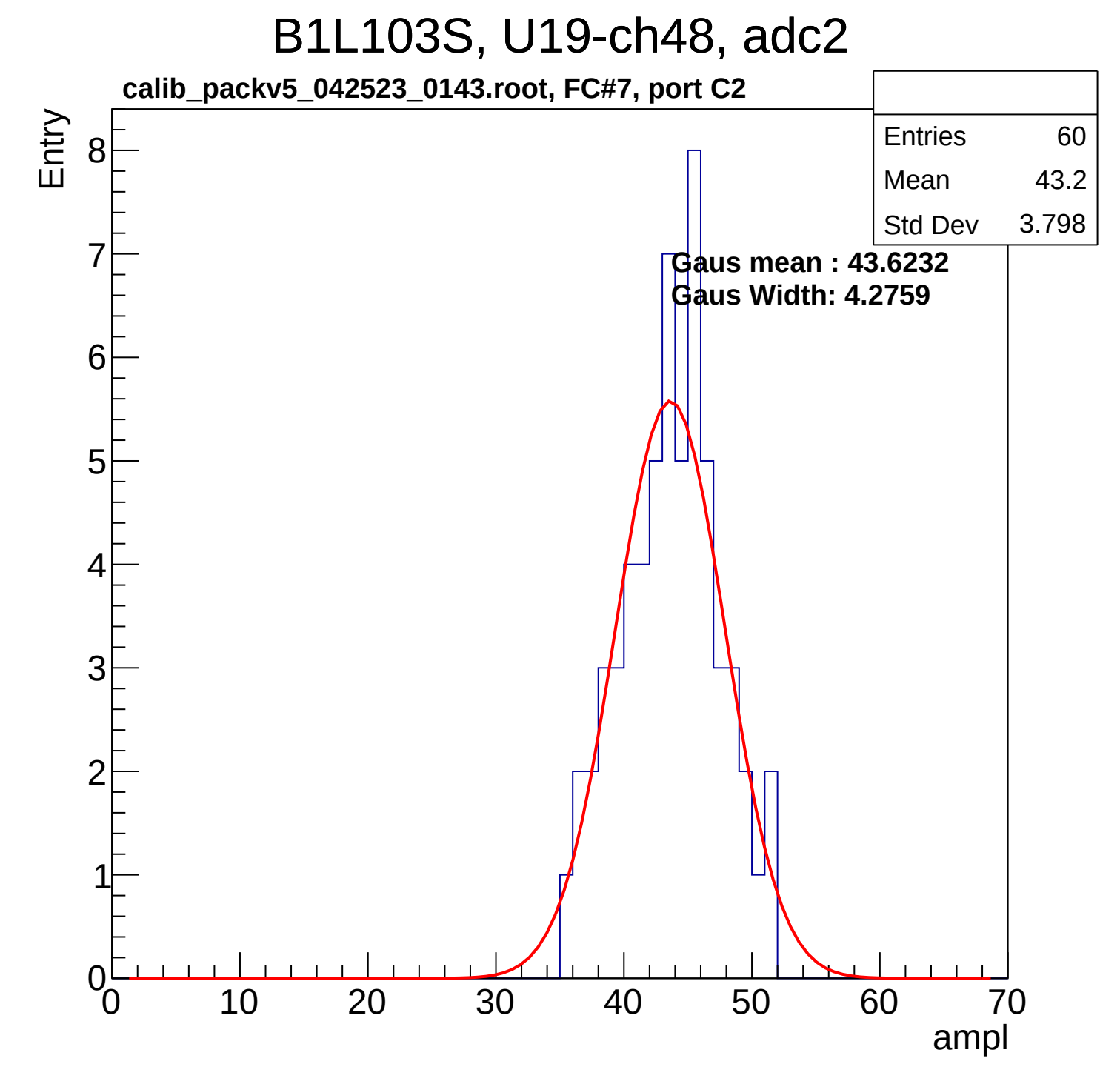

B1L103S, U19-ch47, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 59.92
Std Dev 2.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

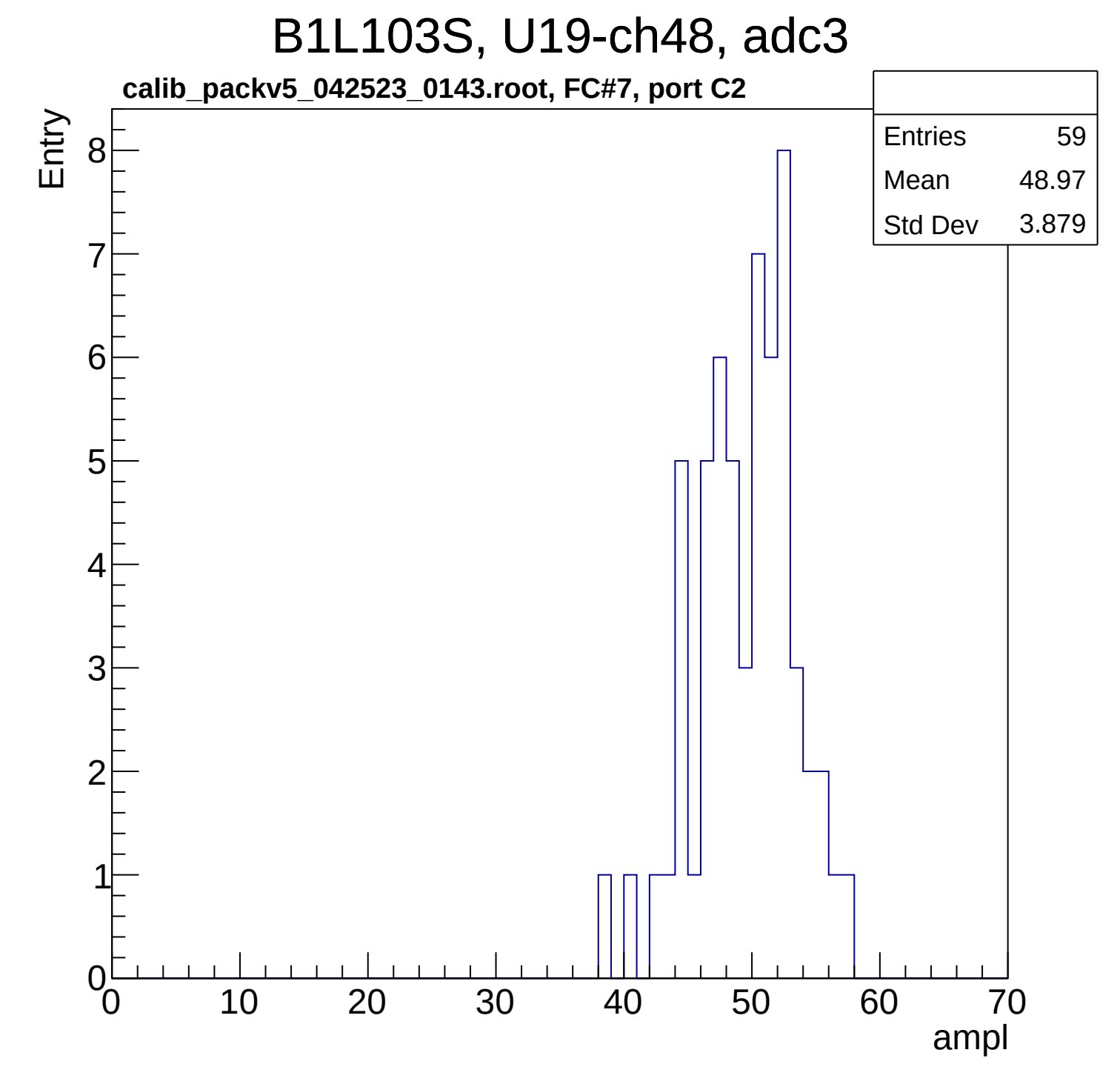

B1L103S, U19-ch47, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.83
Std Dev 1.213
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

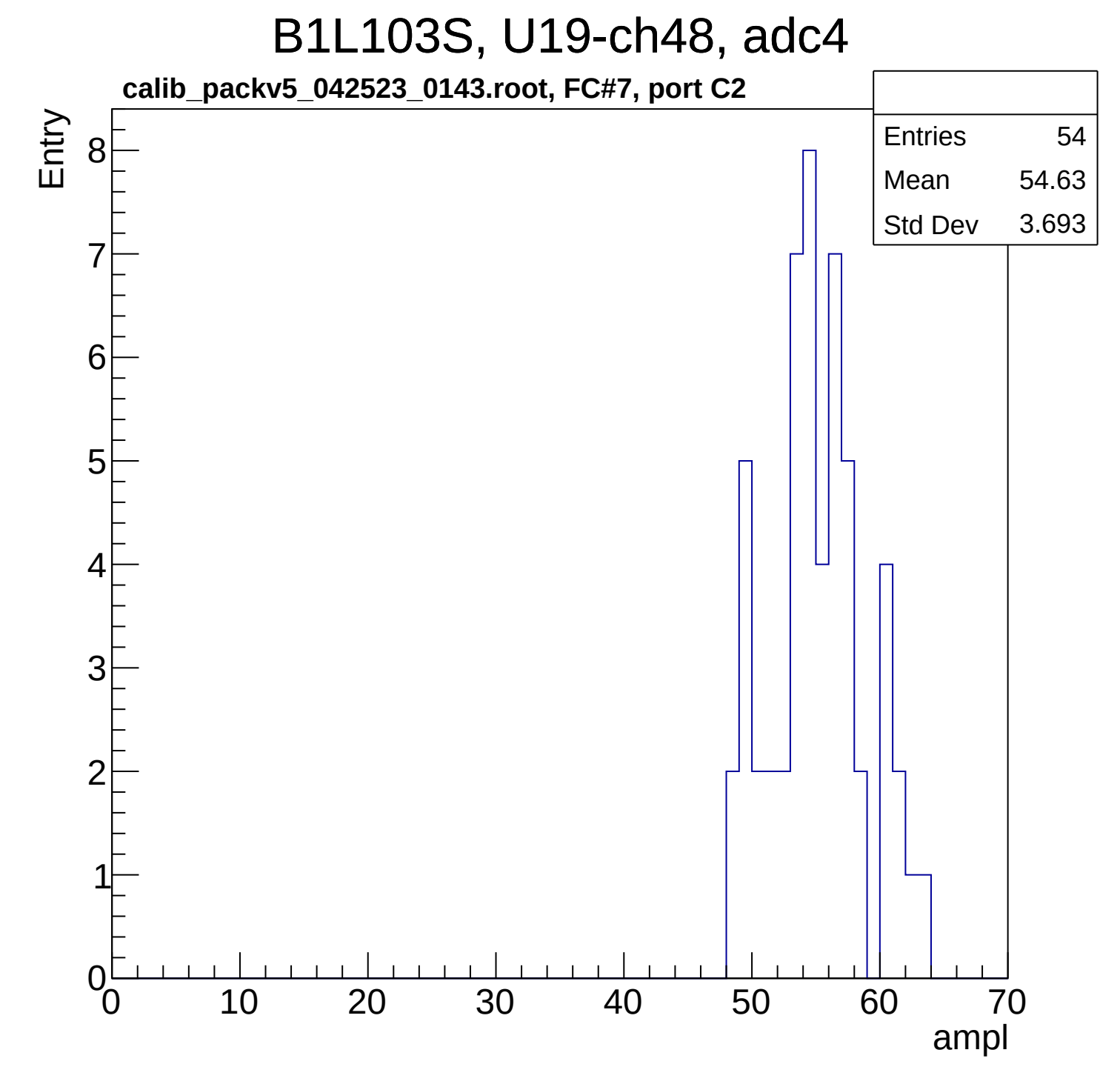

B1L103S, U19-ch47, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 28.54
Std Dev 3.434
Gaus mean : 30.3179
Gaus Width: 4.6936
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 35.93
Std Dev 4.296
Gaus mean : 36.8854
Gaus Width: 4.7070
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.2
Std Dev 3.798
Gaus mean : 43.6232
Gaus Width: 4.2759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 48.97
Std Dev 3.879
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 54.63
Std Dev 3.693
Entry
ampl
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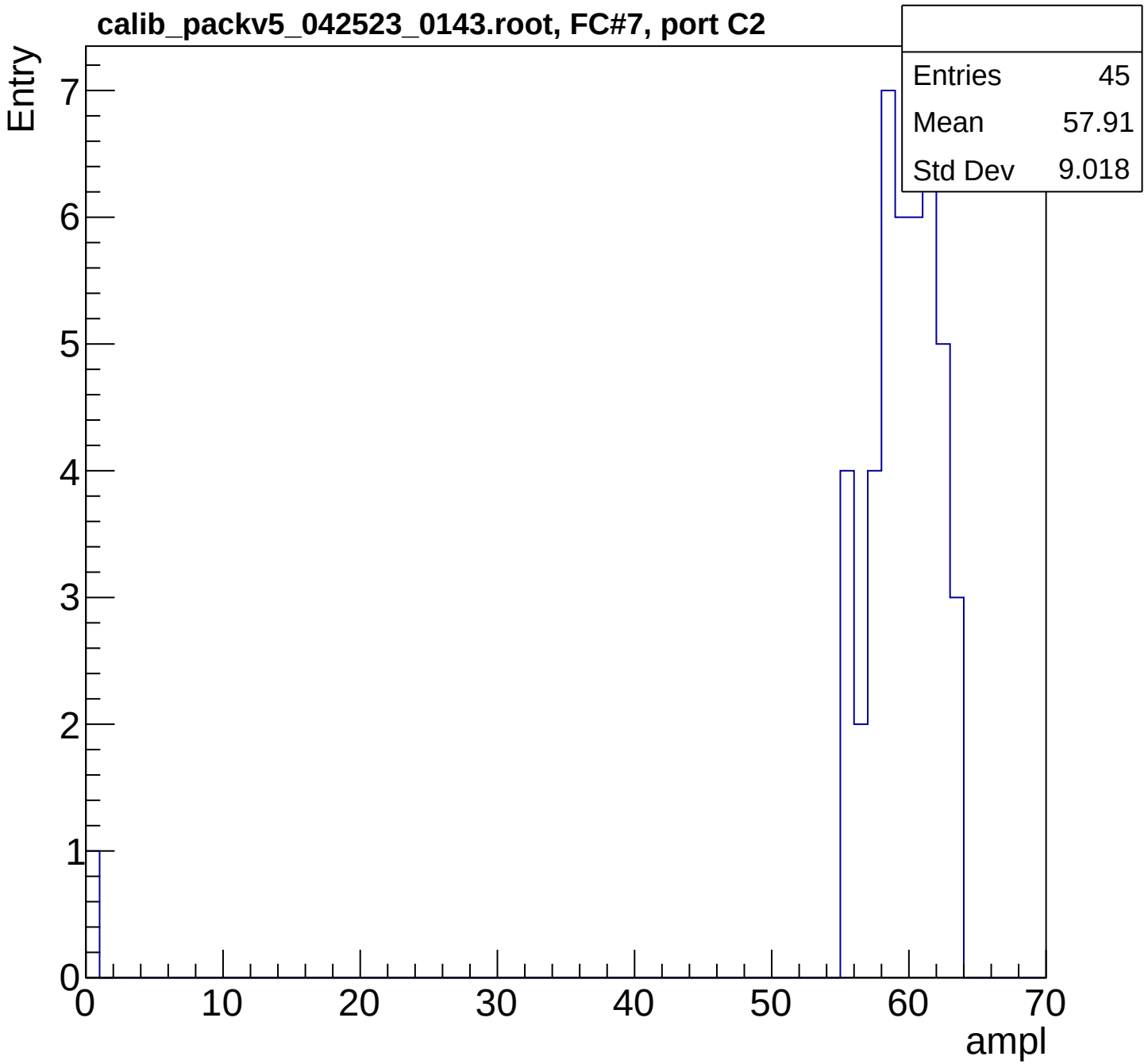
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch48, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 57.91
Std Dev 9.018
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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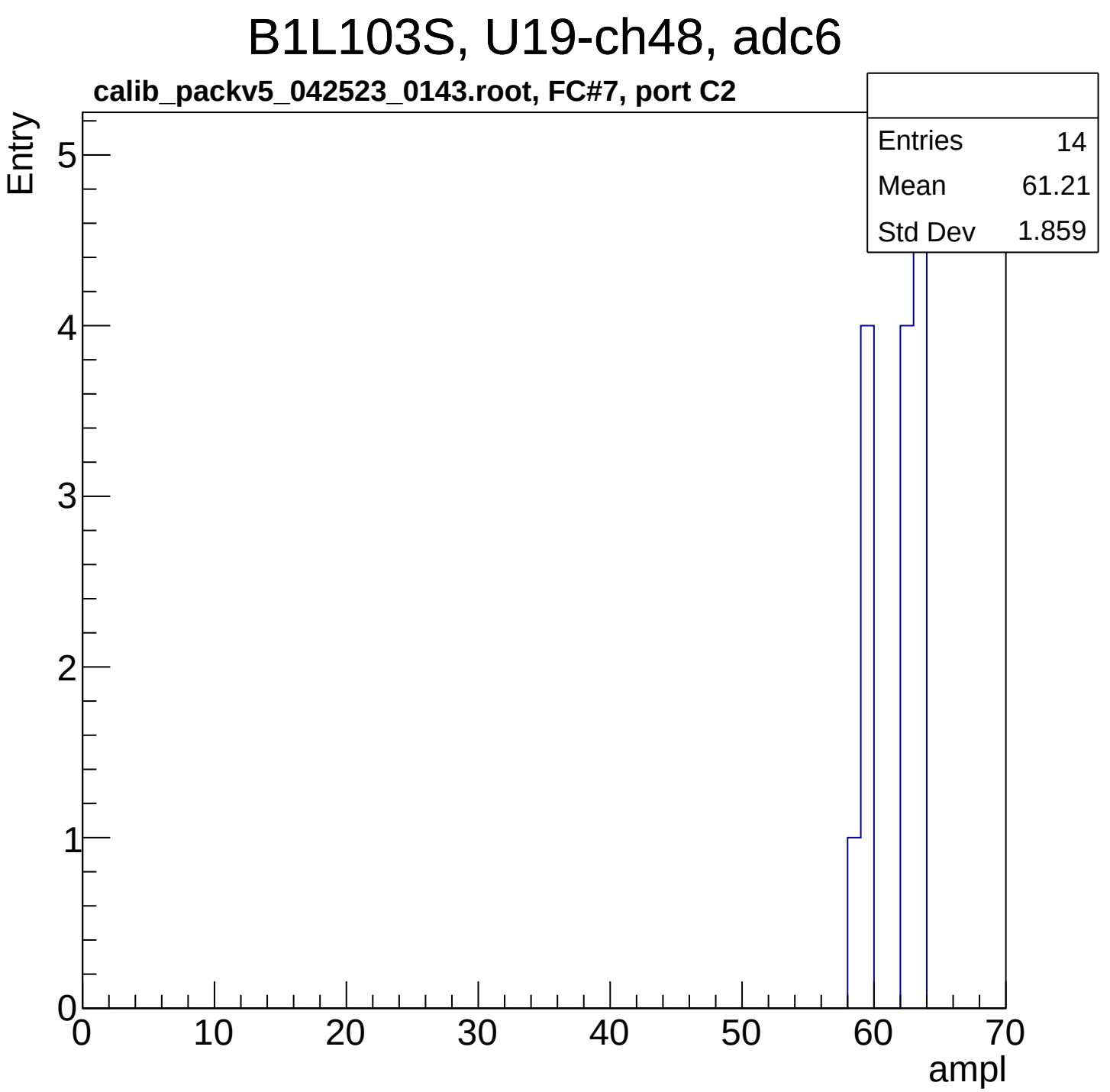
40
50
60
70


B1L103S, U19-ch48, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 61.21
Std Dev 1.859
Entry
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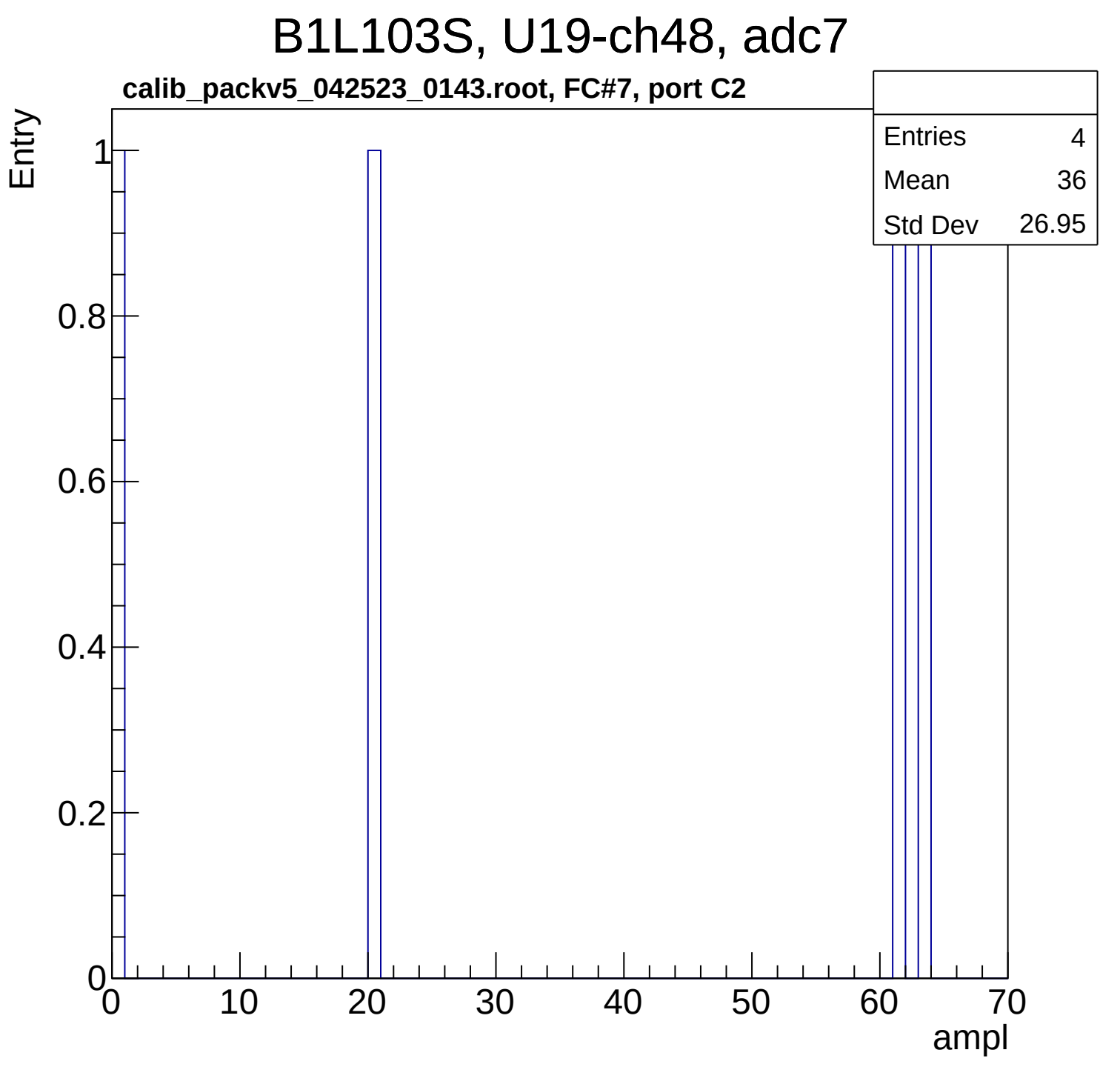
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch48, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 36
Std Dev 26.95
Entry
ampl
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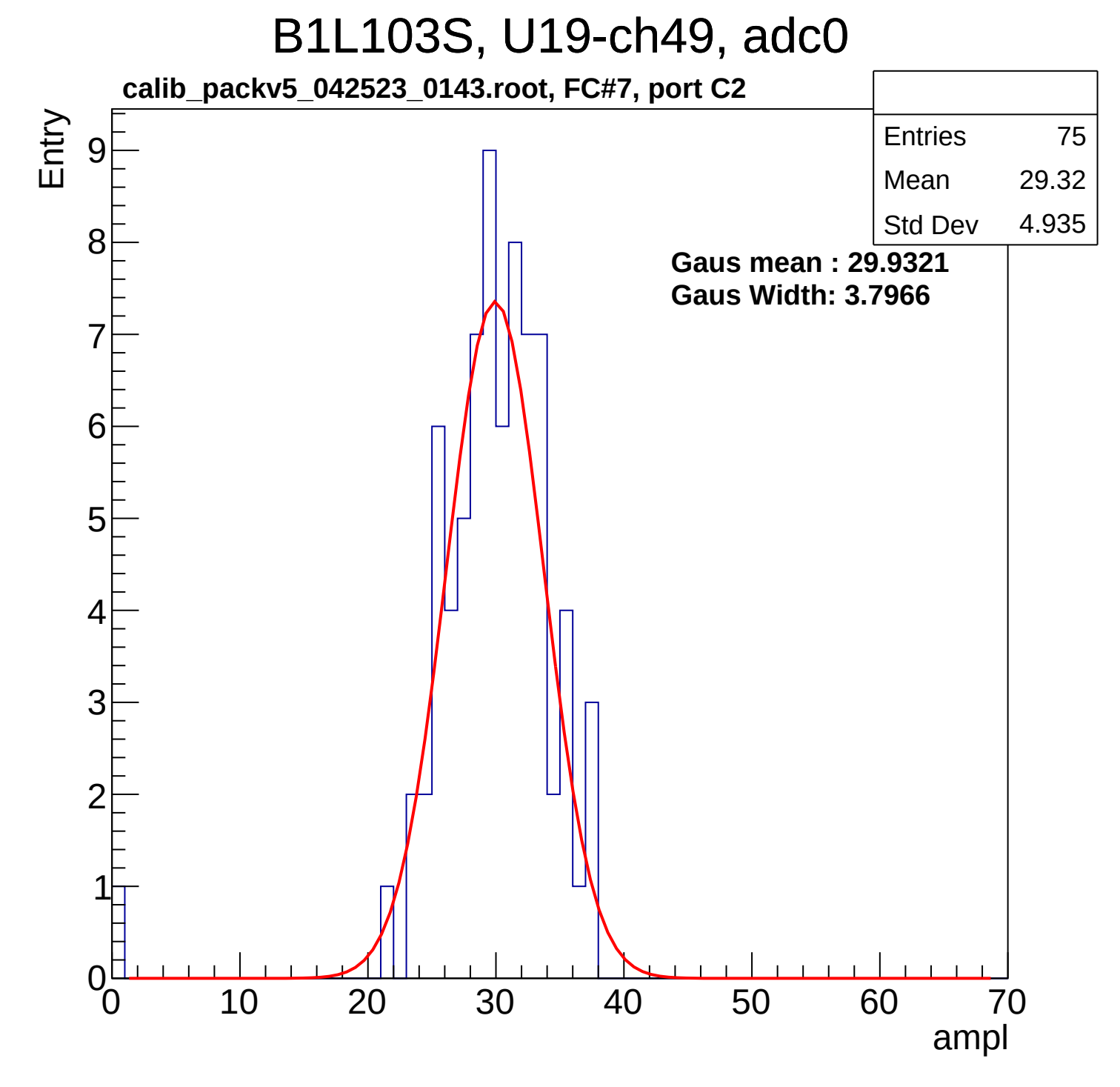
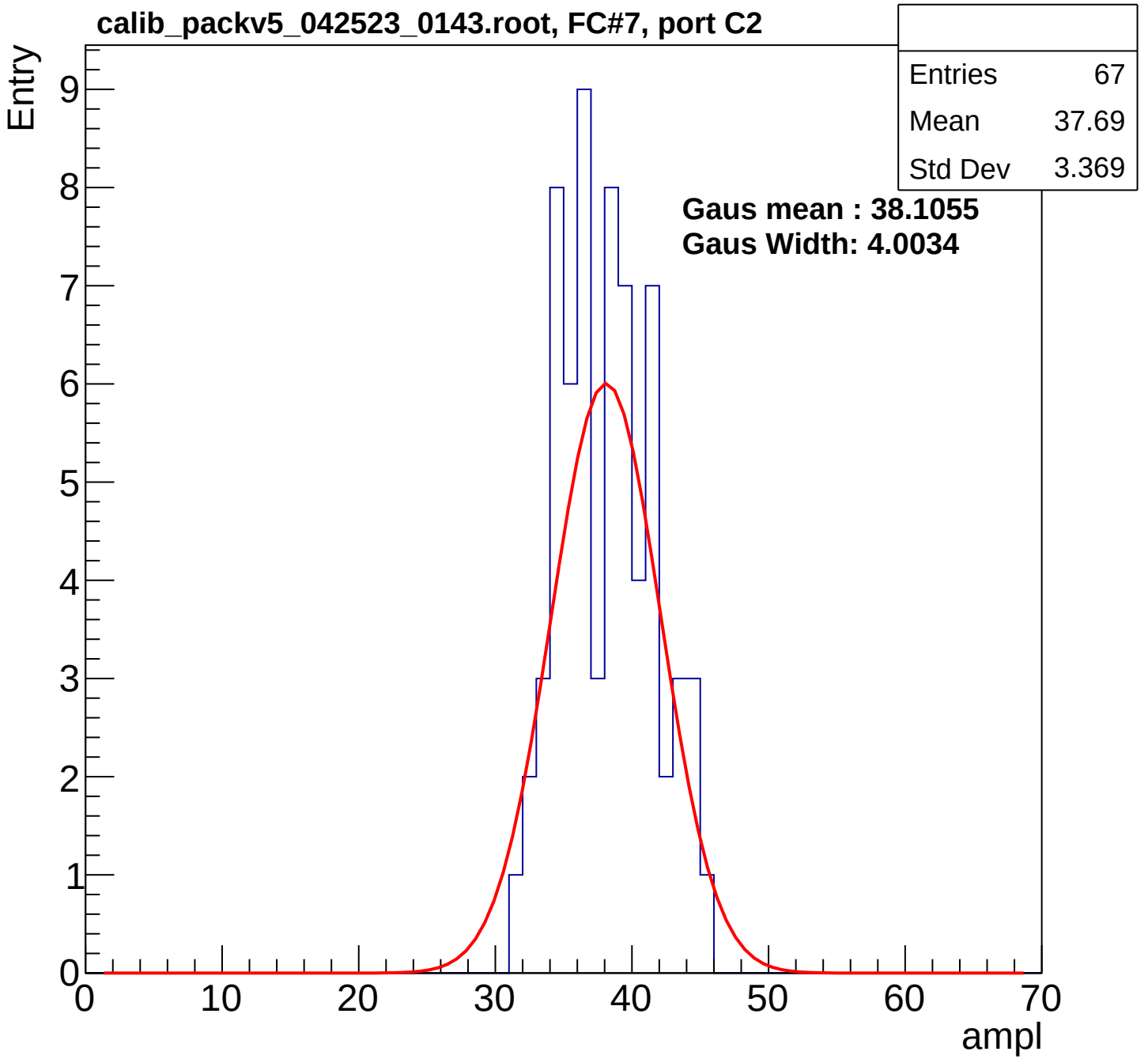
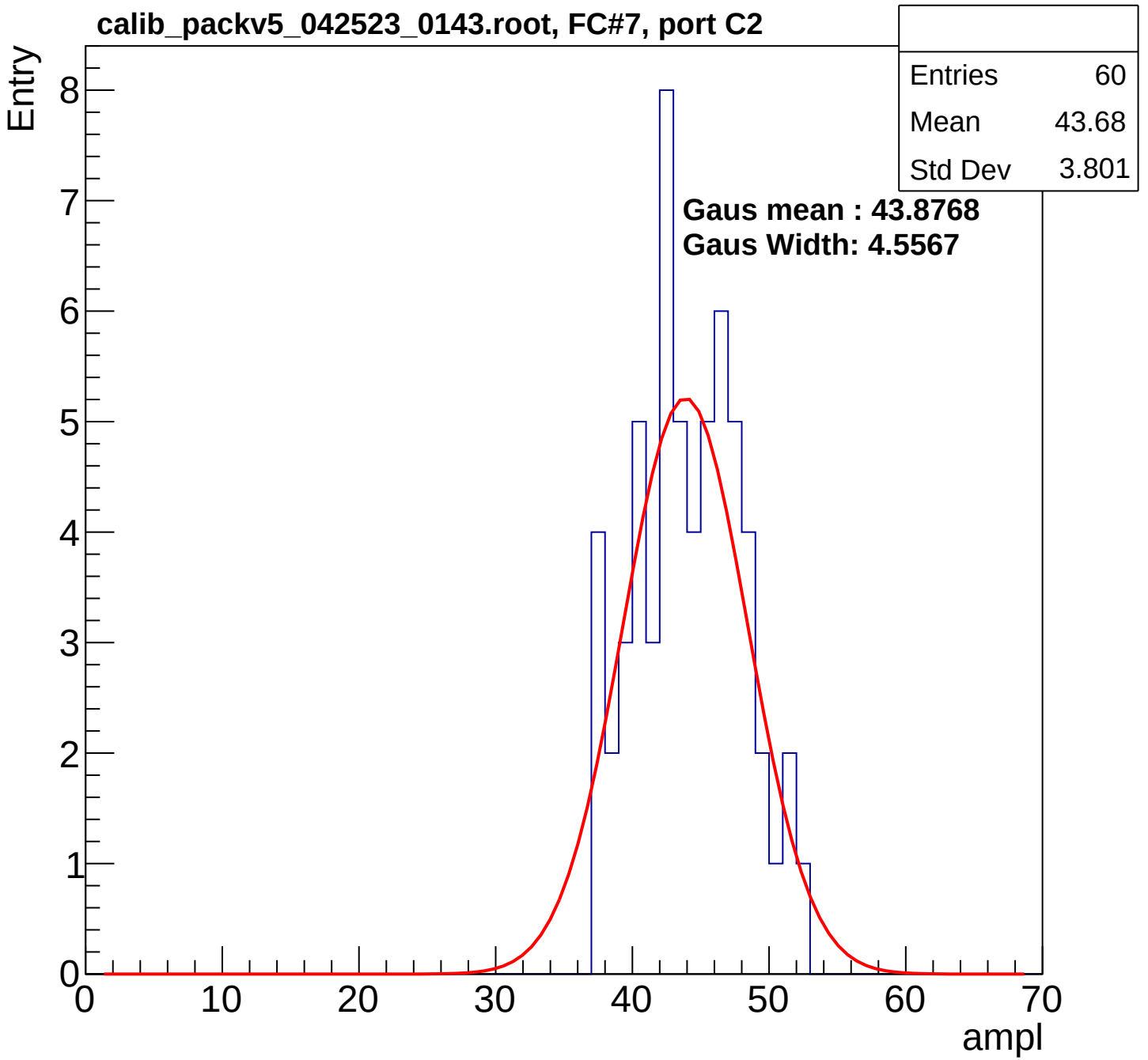
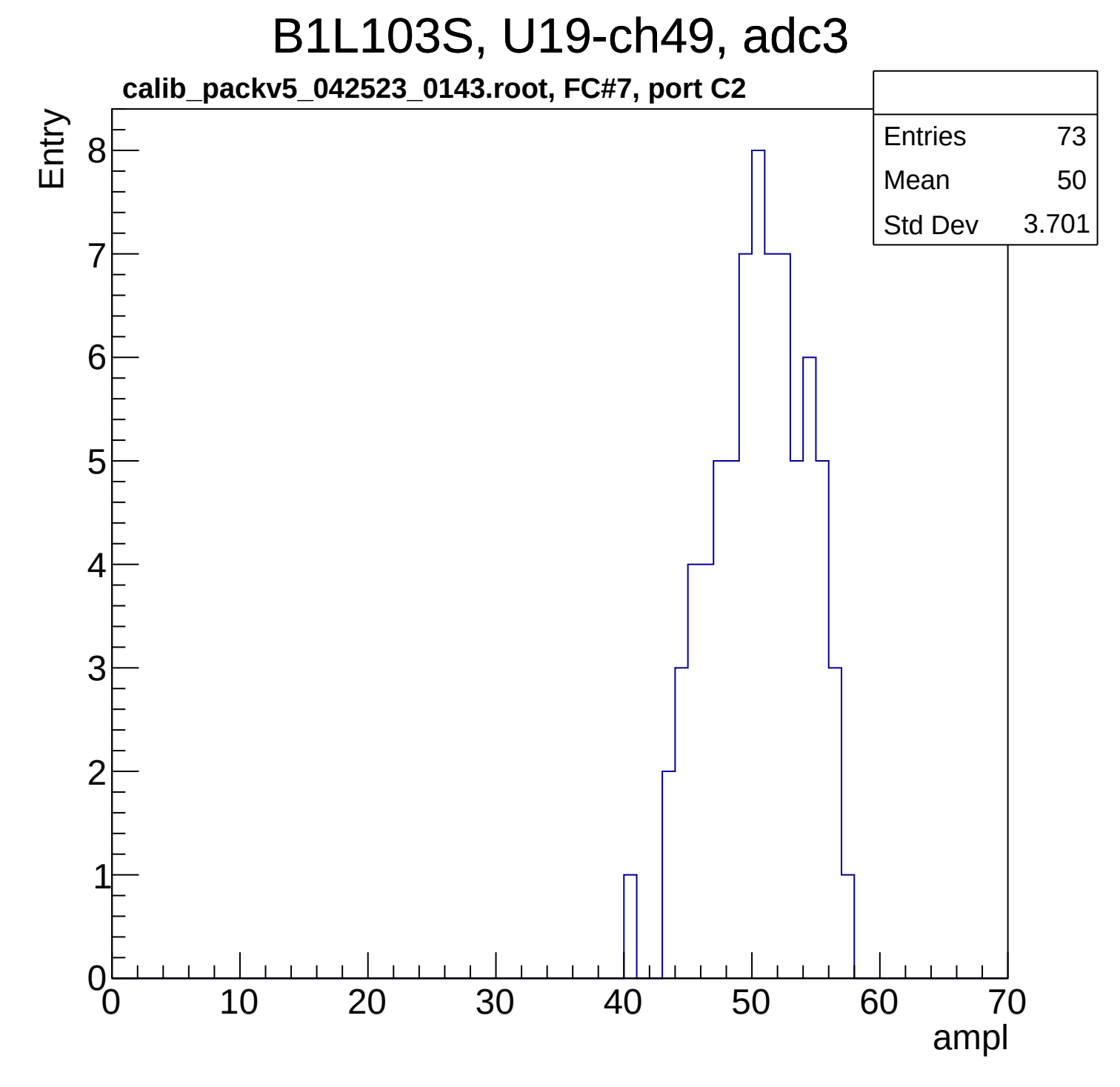
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.32
Std Dev 4.935
Gaus mean : 29.9321
Gaus Width: 3.7966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.69
Std Dev 3.369
Gaus mean : 38.1055
Gaus Width: 4.0034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.68
Std Dev 3.801
Gaus mean : 43.8768
Gaus Width: 4.5567
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50
Std Dev 3.701
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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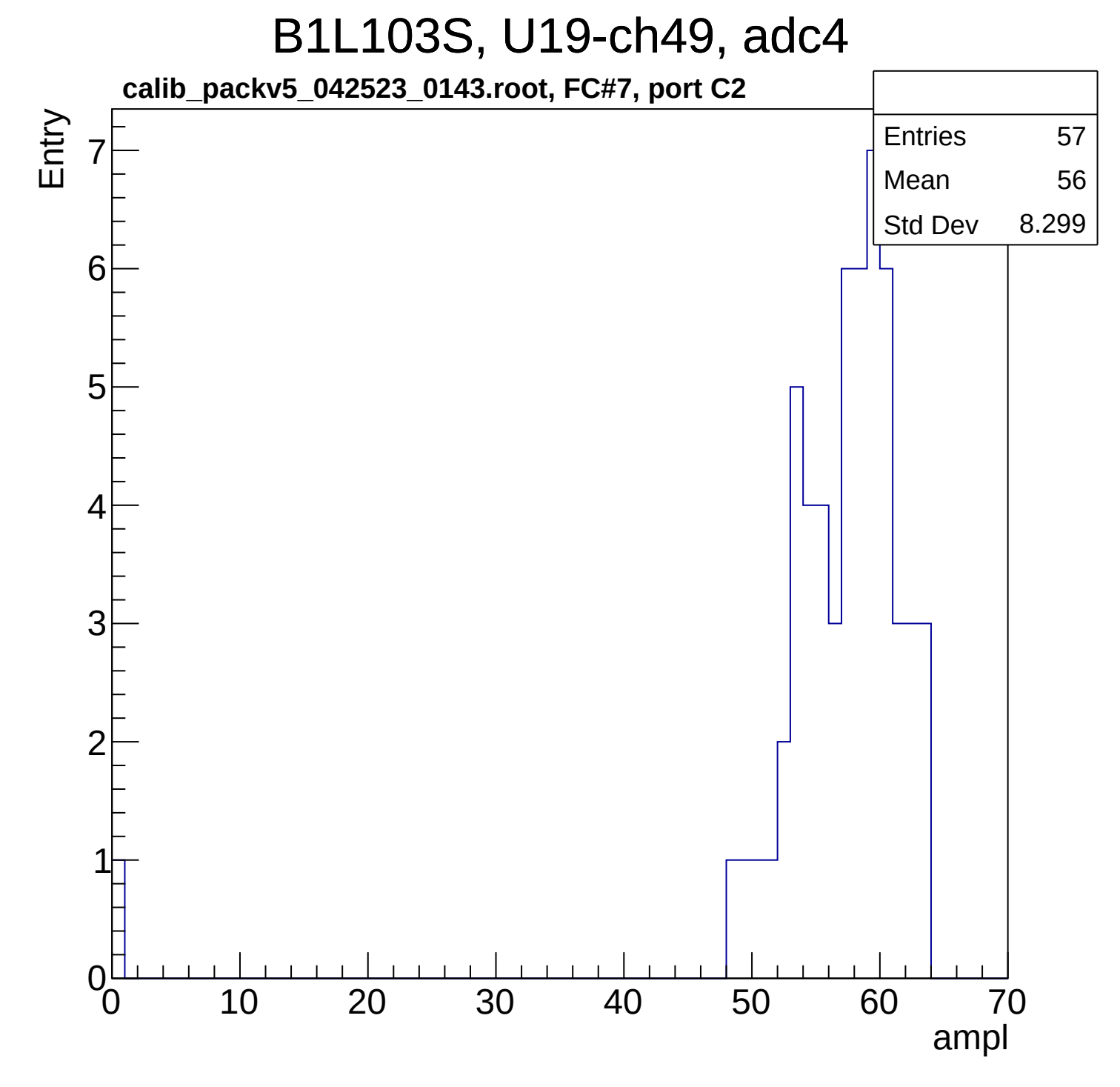
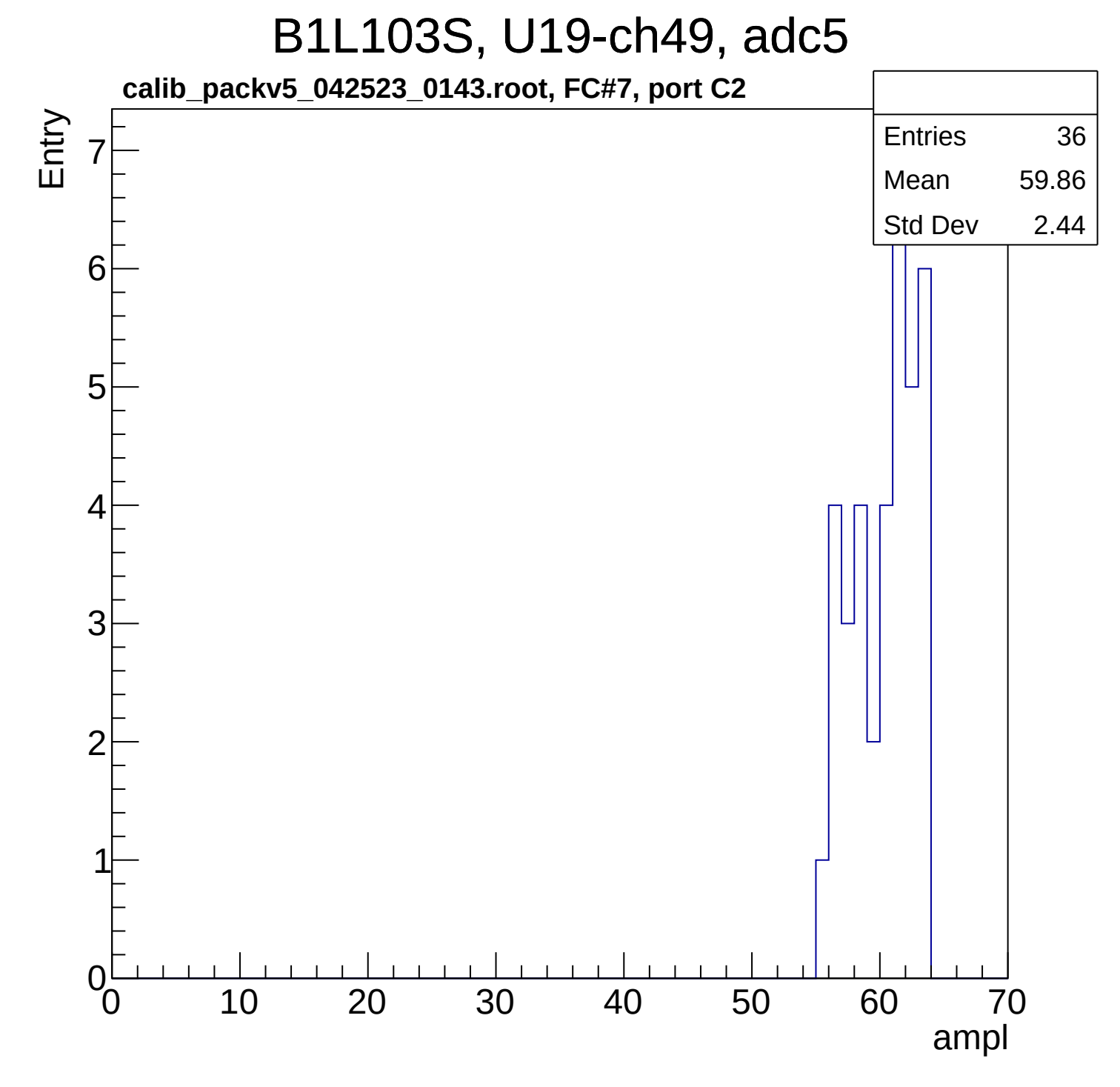
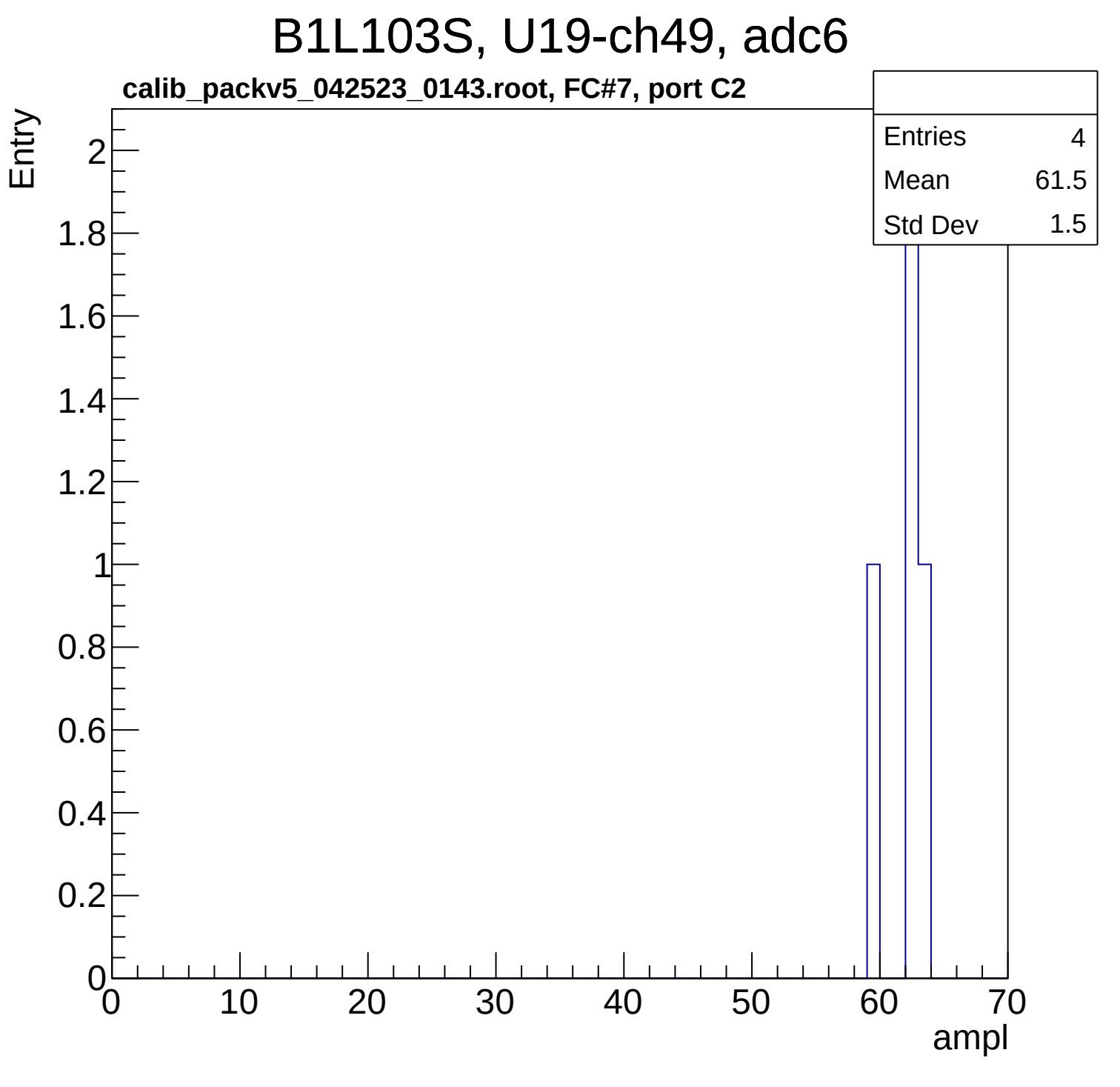
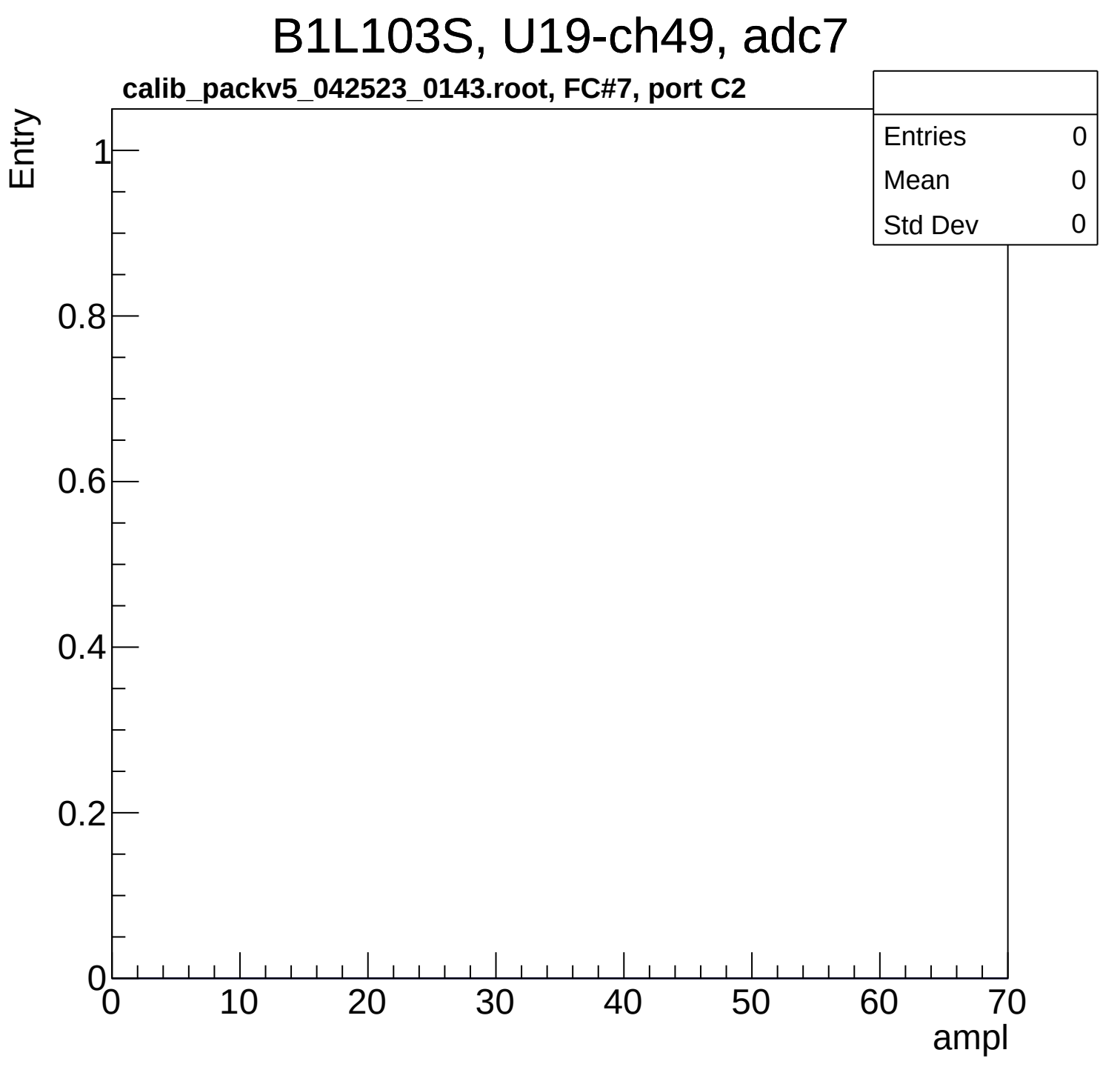
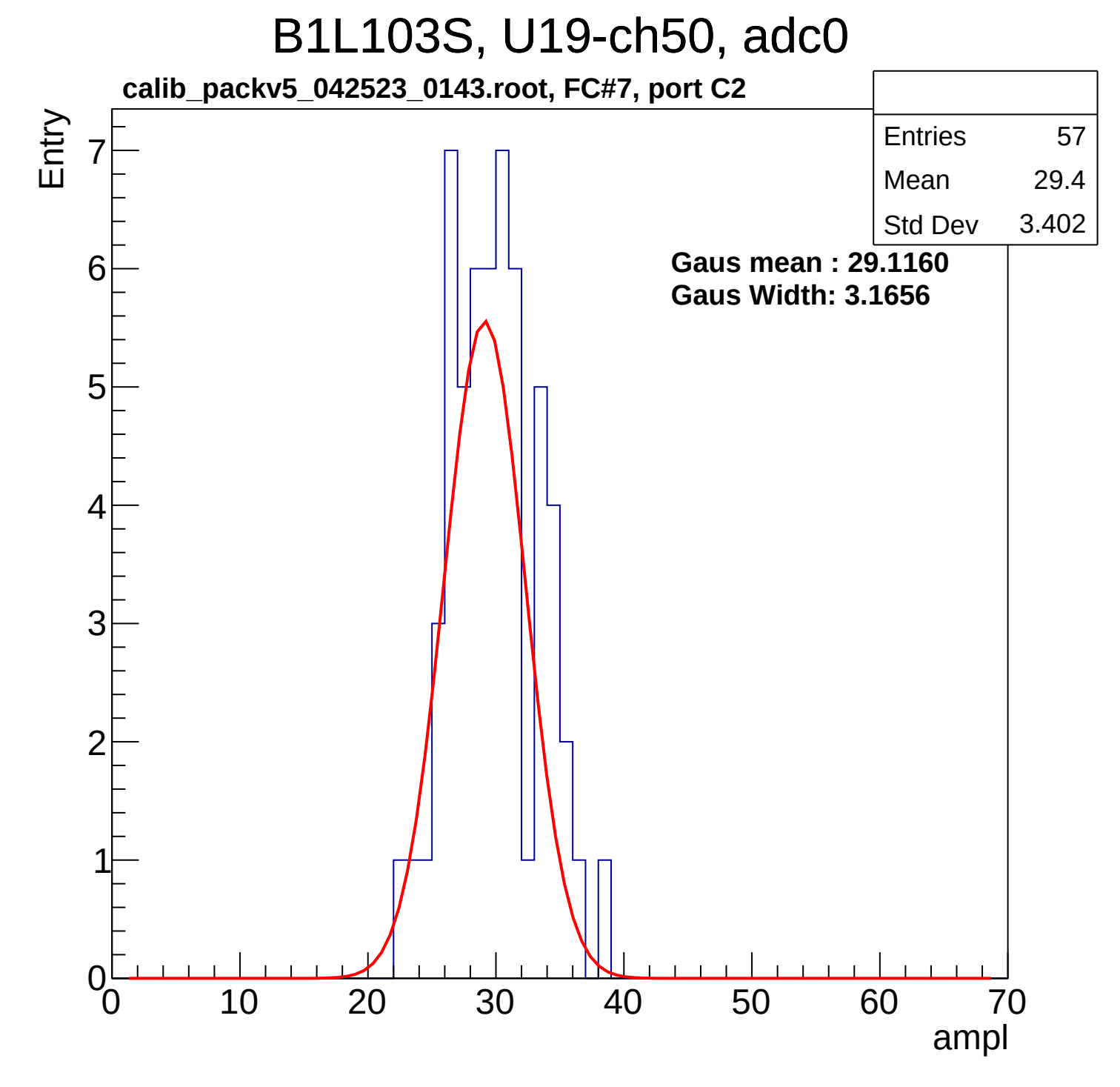
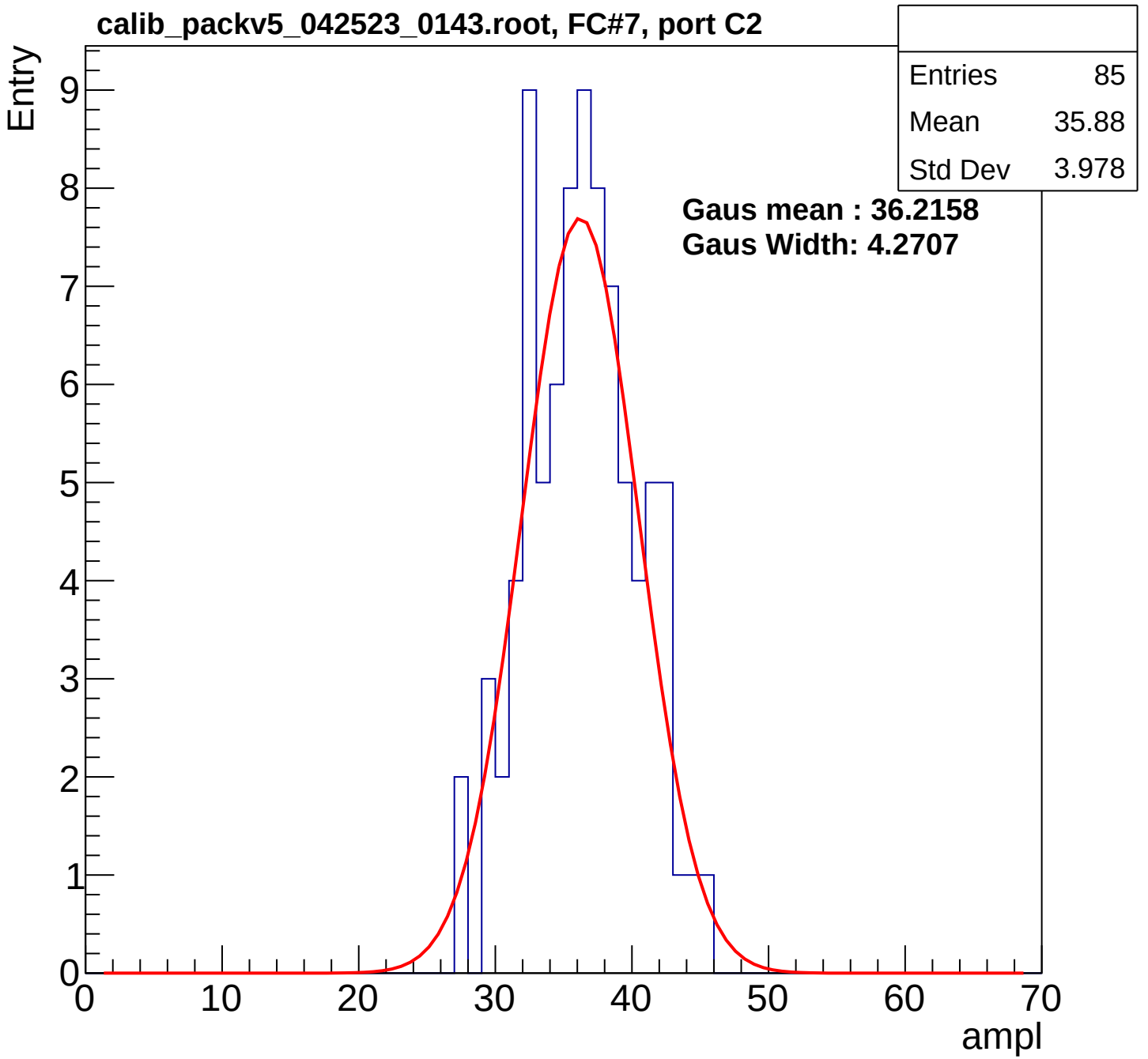

B1L103S, U19-ch49, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 56
Std Dev 8.299
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 59.86
Std Dev 2.44
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch49, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch50, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 29.4
Std Dev 3.402
Gaus mean : 29.1160
Gaus Width: 3.1656
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch50, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 35.88
Std Dev 3.978
Gaus mean : 36.2158
Gaus Width: 4.2707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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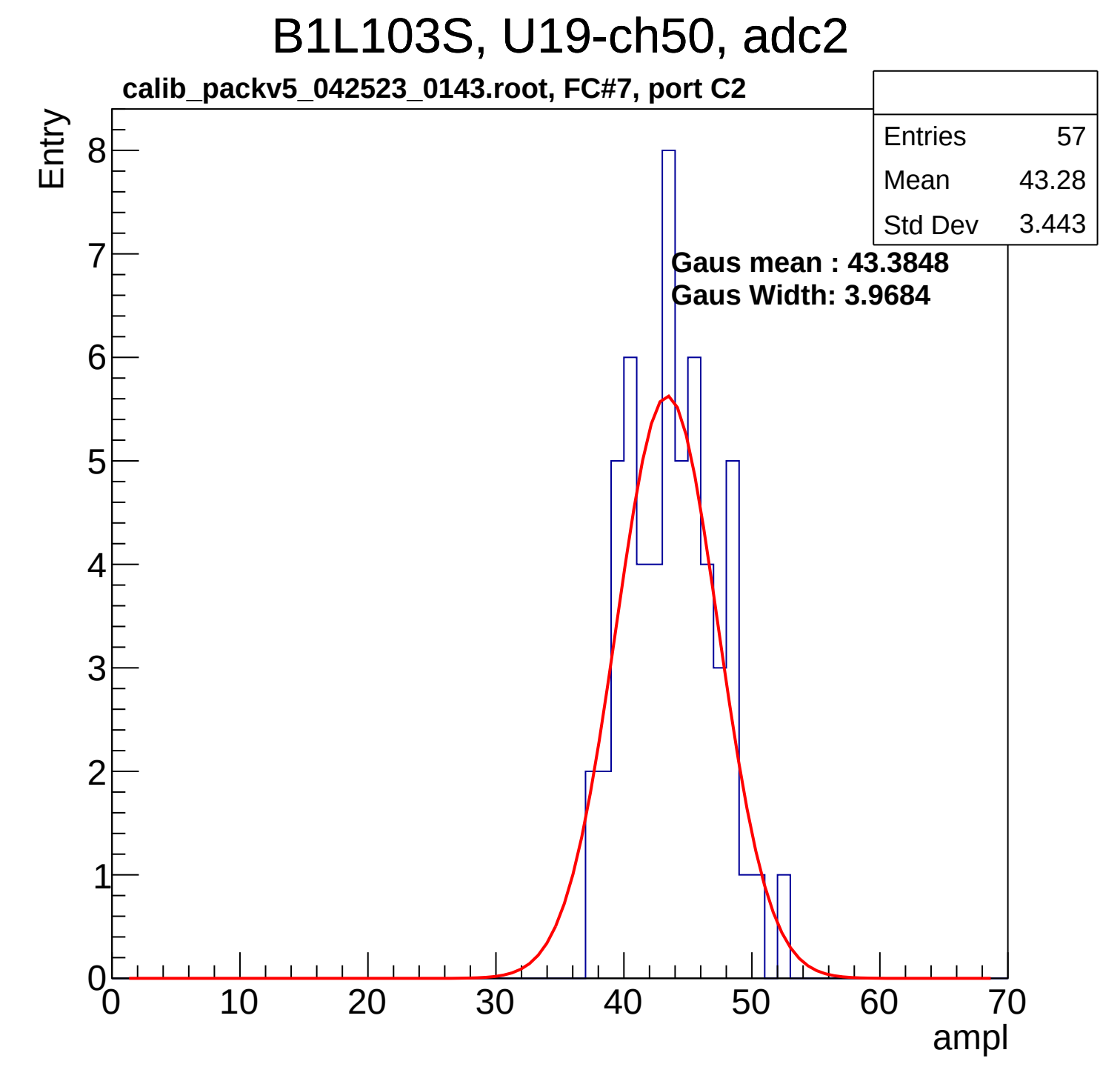
B1L103S, U19-ch50, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.28
Std Dev 3.443
Gaus mean : 43.3848
Gaus Width: 3.9684
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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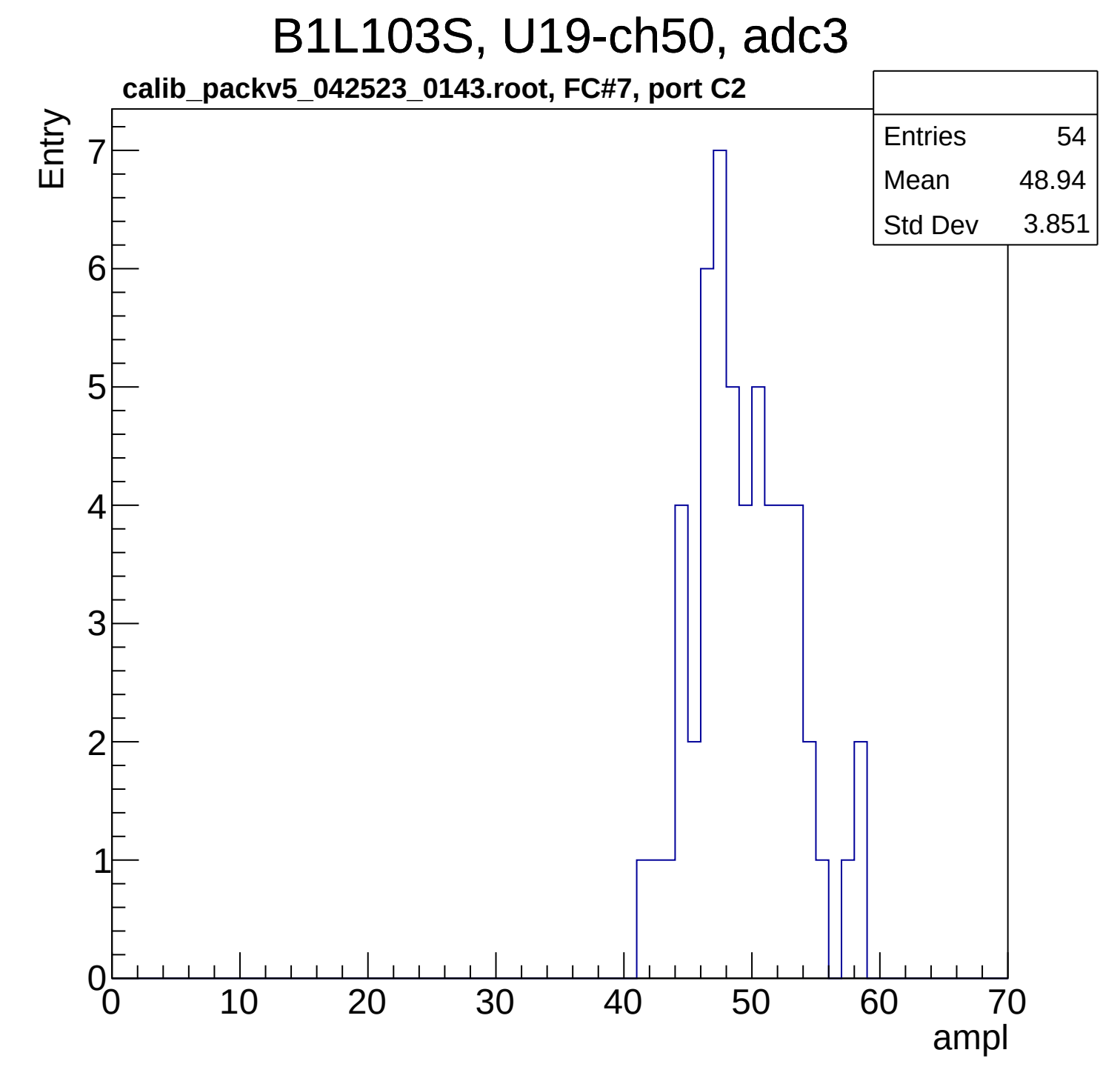
B1L103S, U19-ch50, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 48.94
Std Dev 3.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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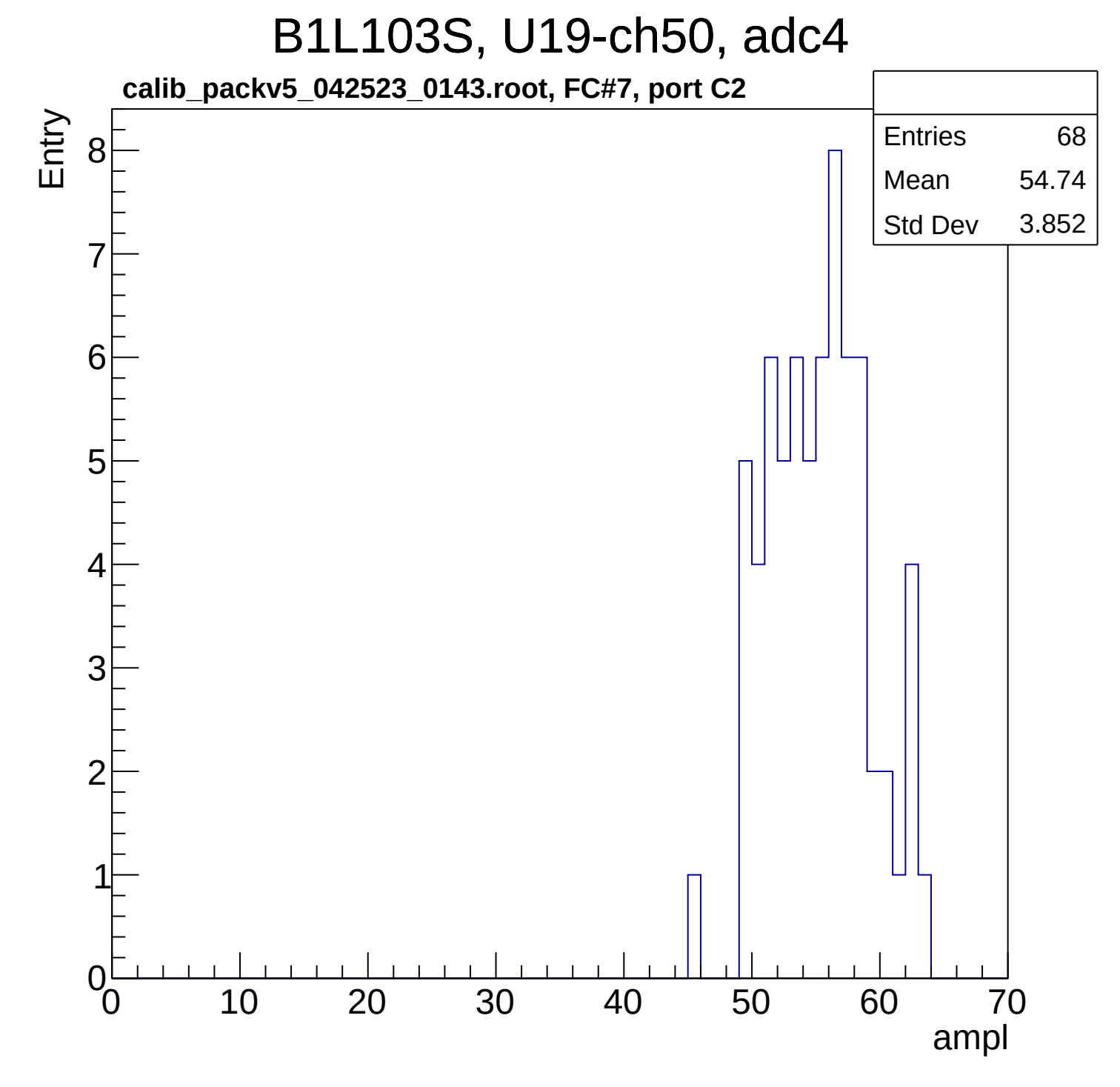
B1L103S, U19-ch50, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 54.74
Std Dev 3.852
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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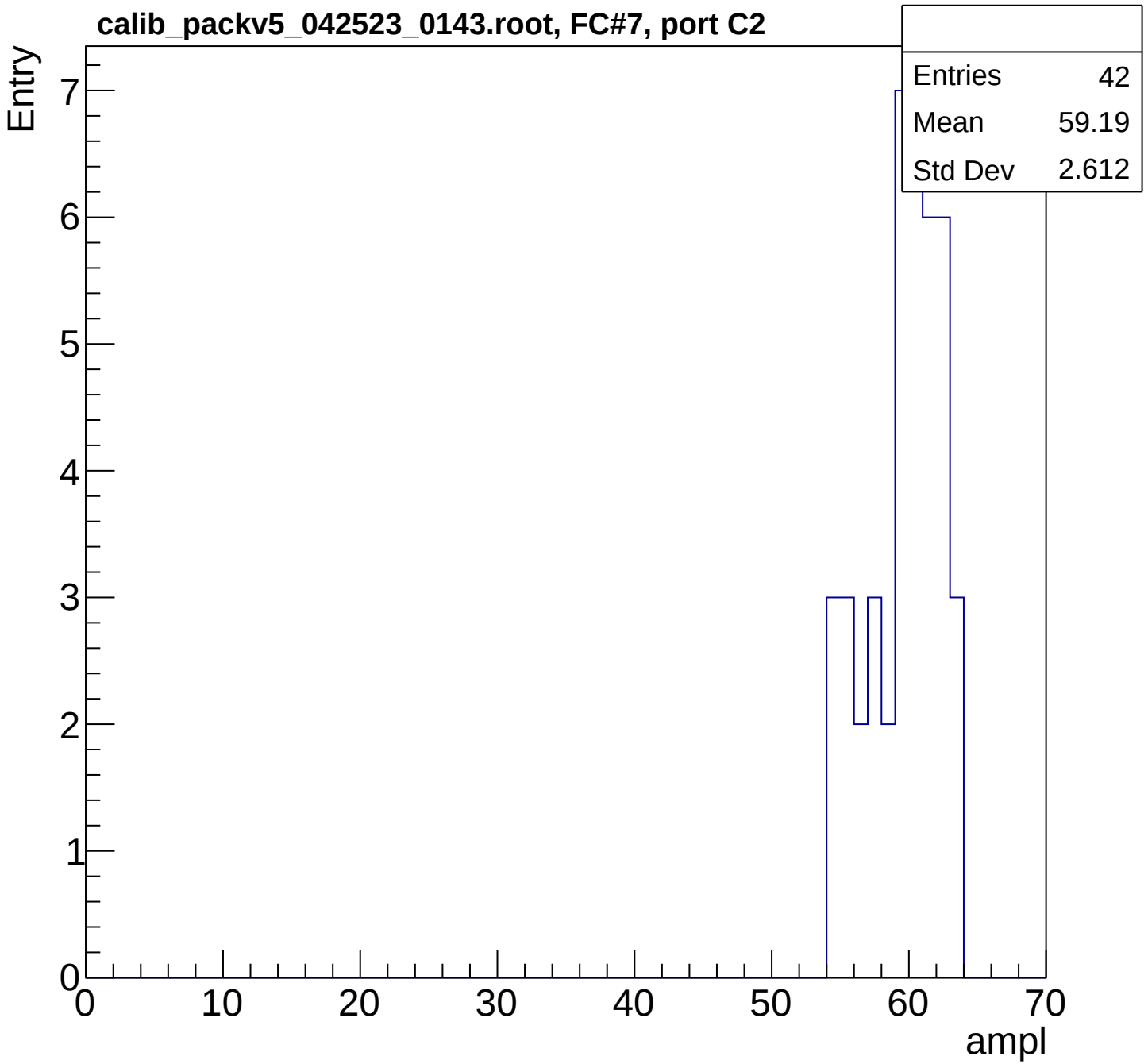
B1L103S, U19-ch50, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 59.19
Std Dev 2.612
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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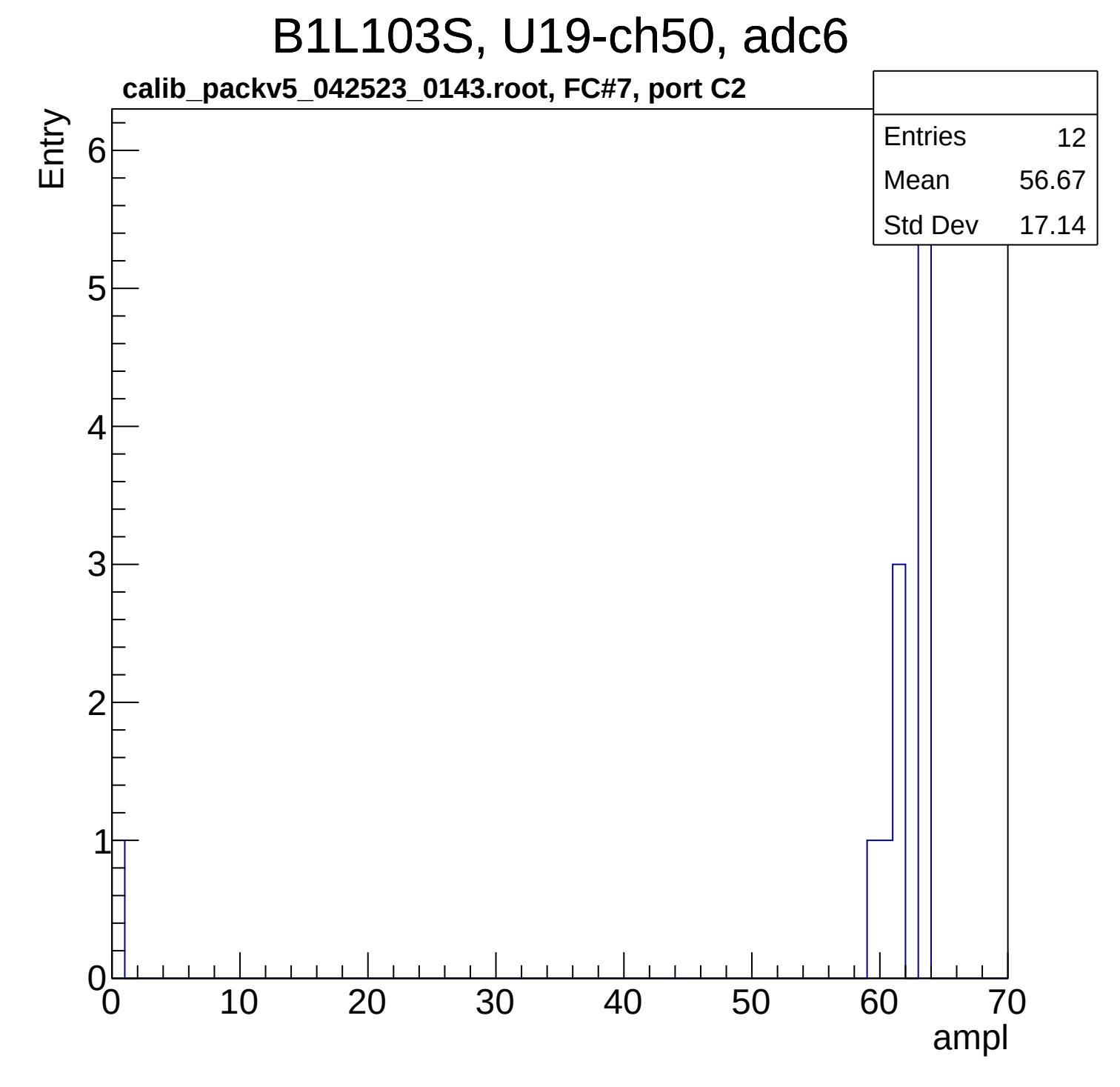
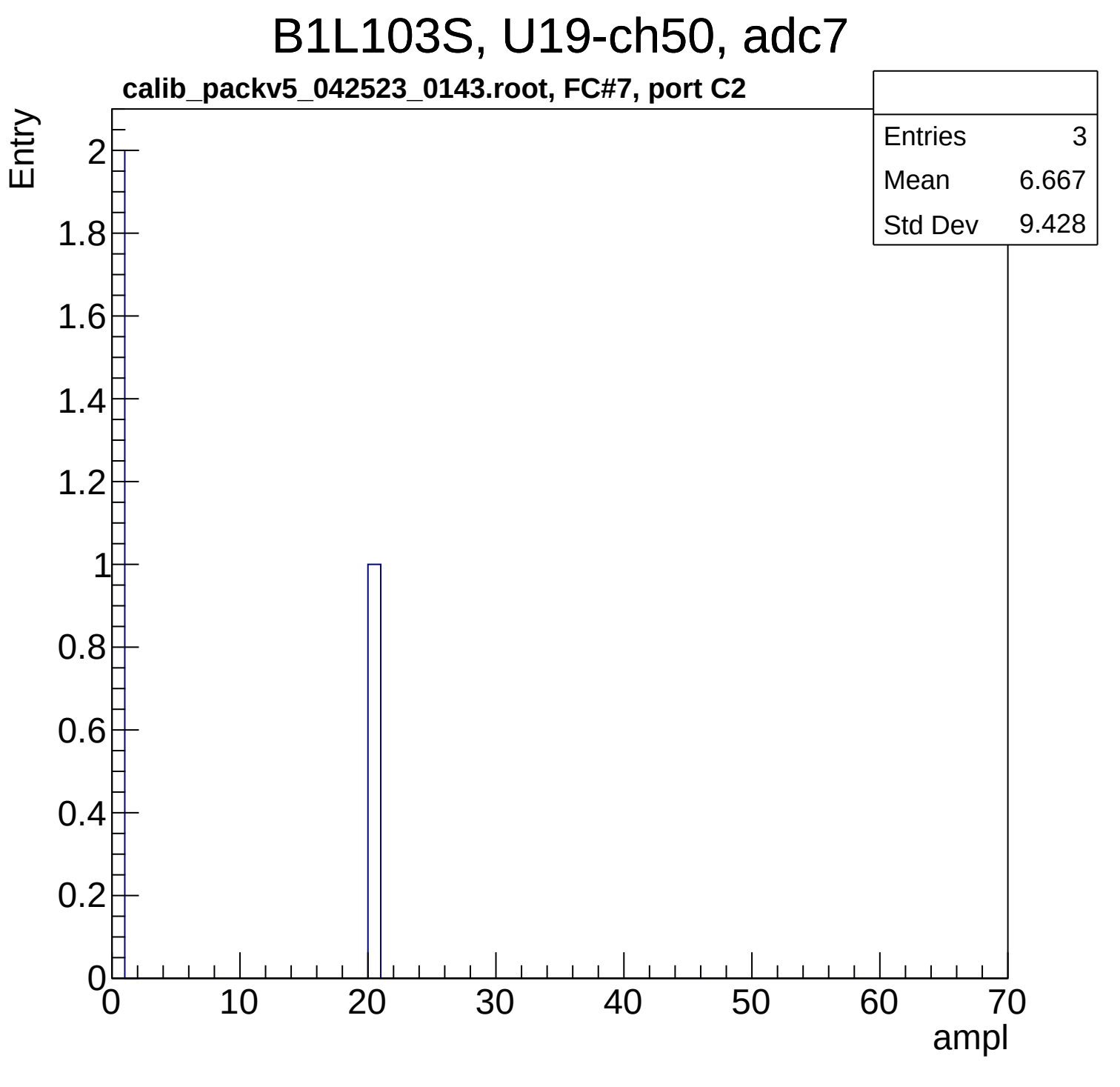

B1L103S, U19-ch50, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.67
Std Dev 17.14
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch50, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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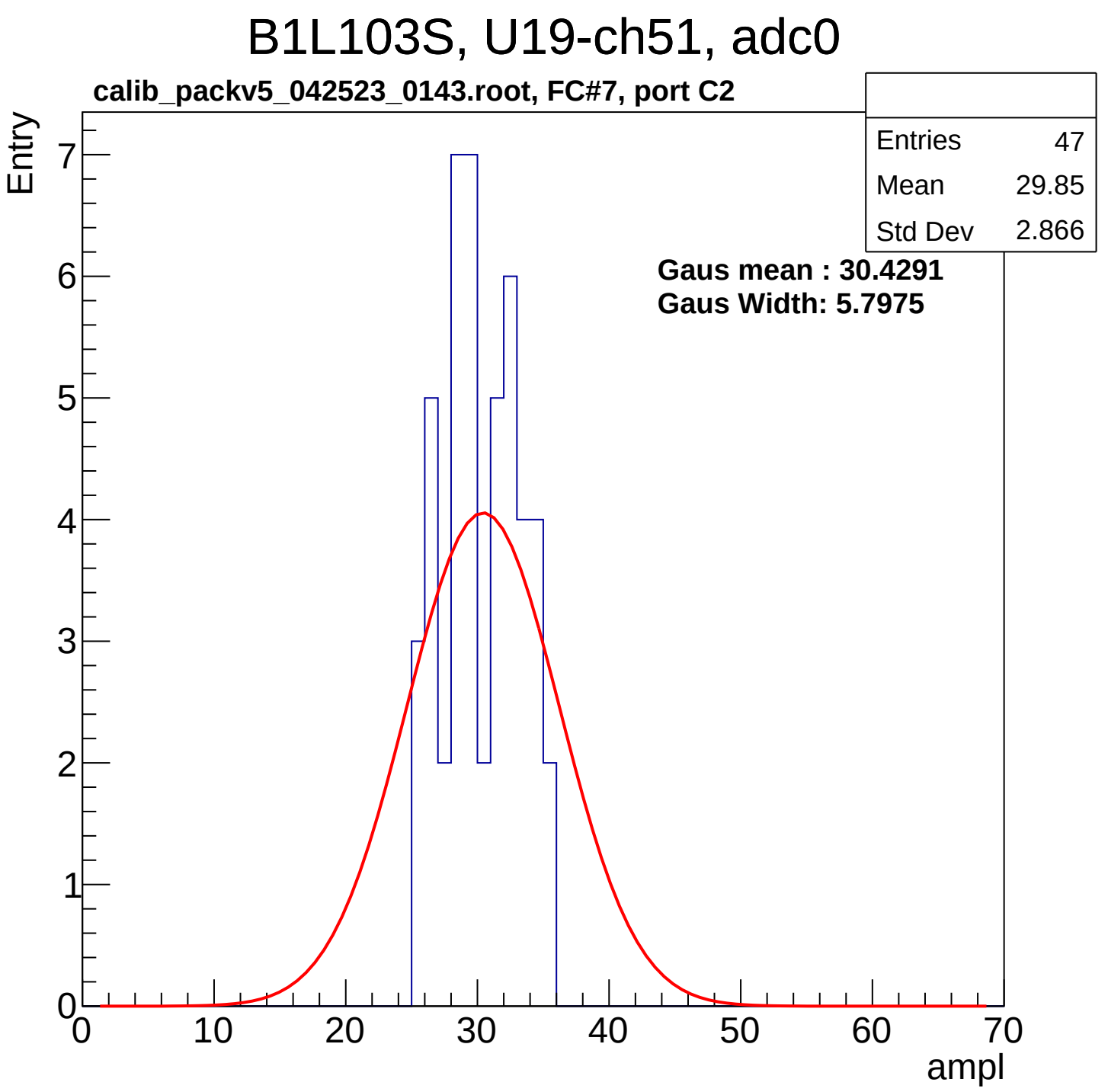
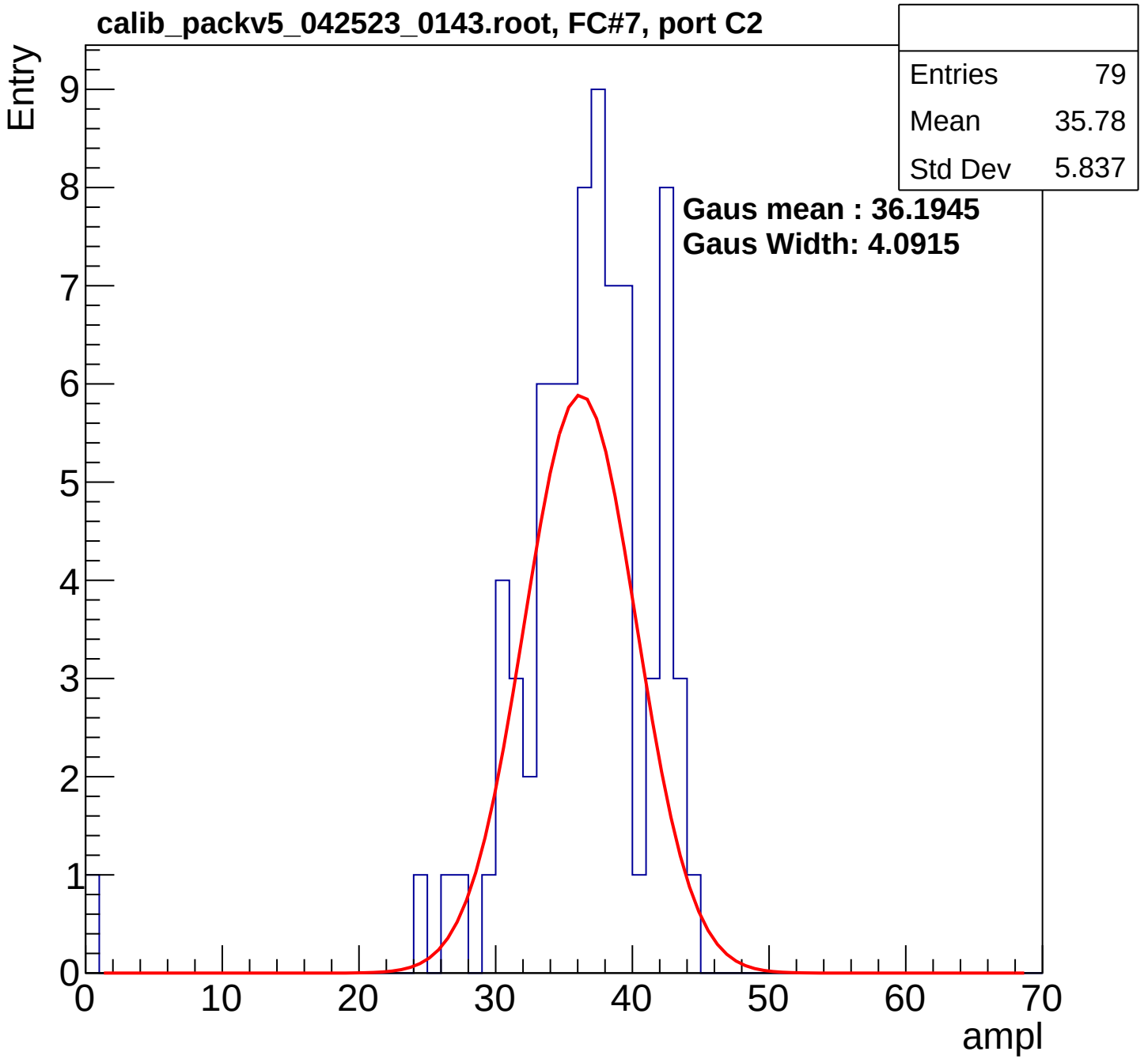
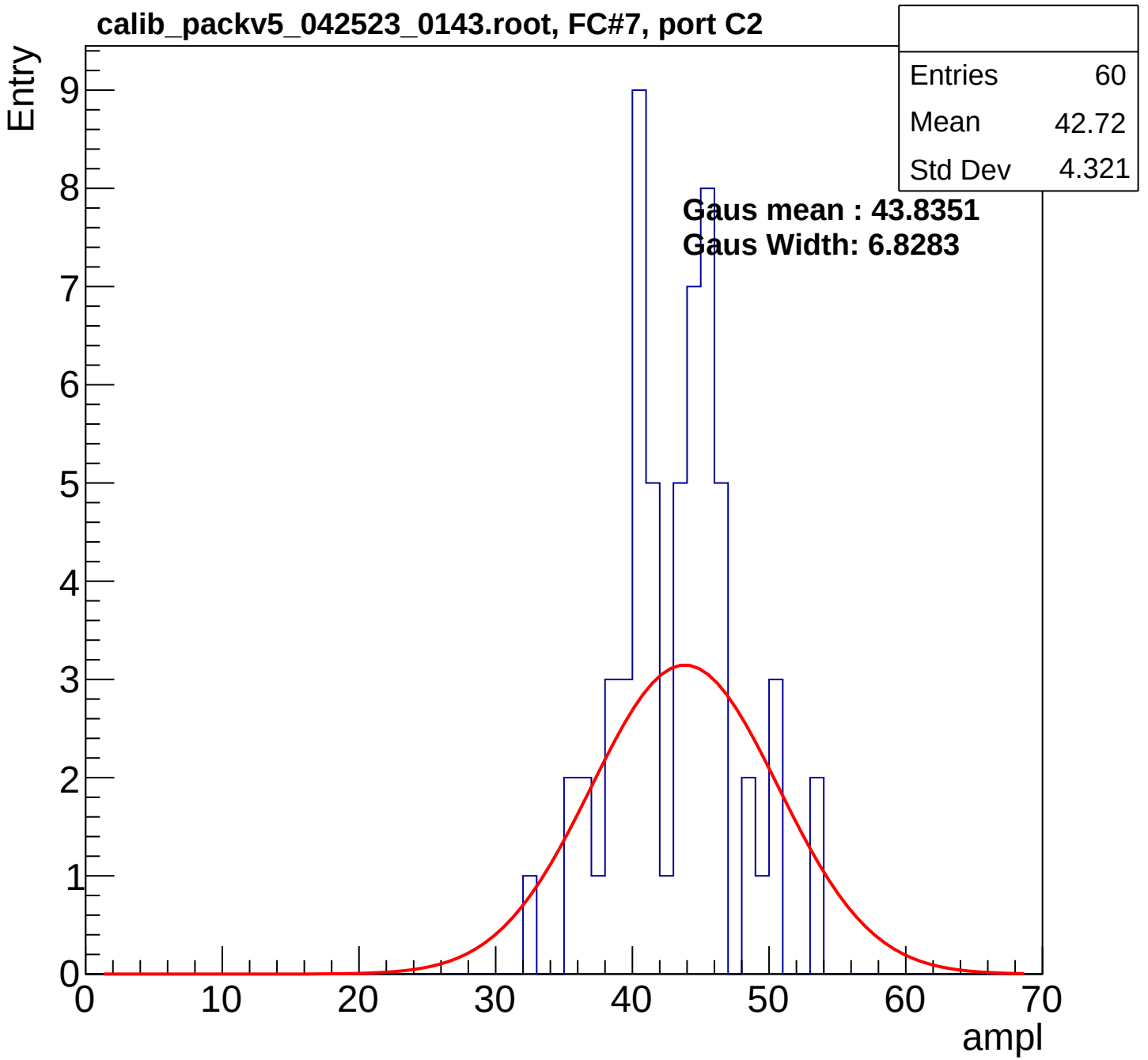
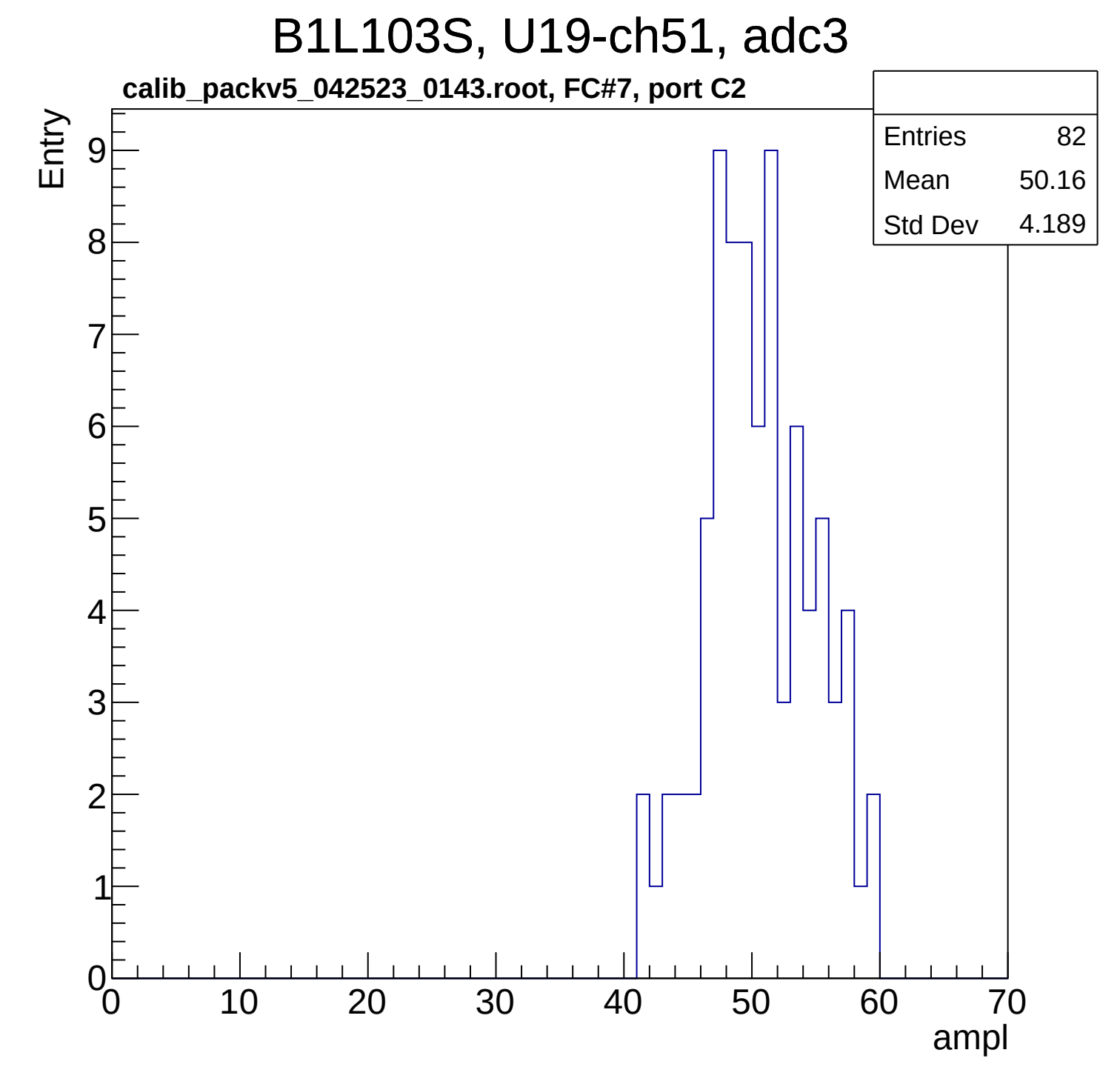

B1L103S, U19-ch51, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 29.85
Std Dev 2.866
Gaus mean : 30.4291
Gaus Width: 5.7975
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch51, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 35.78
Std Dev 5.837
Gaus mean : 36.1945
Gaus Width: 4.0915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch51, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 42.72
Std Dev 4.321
Gaus mean : 43.8351
Gaus Width: 6.8283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch51, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 50.16
Std Dev 4.189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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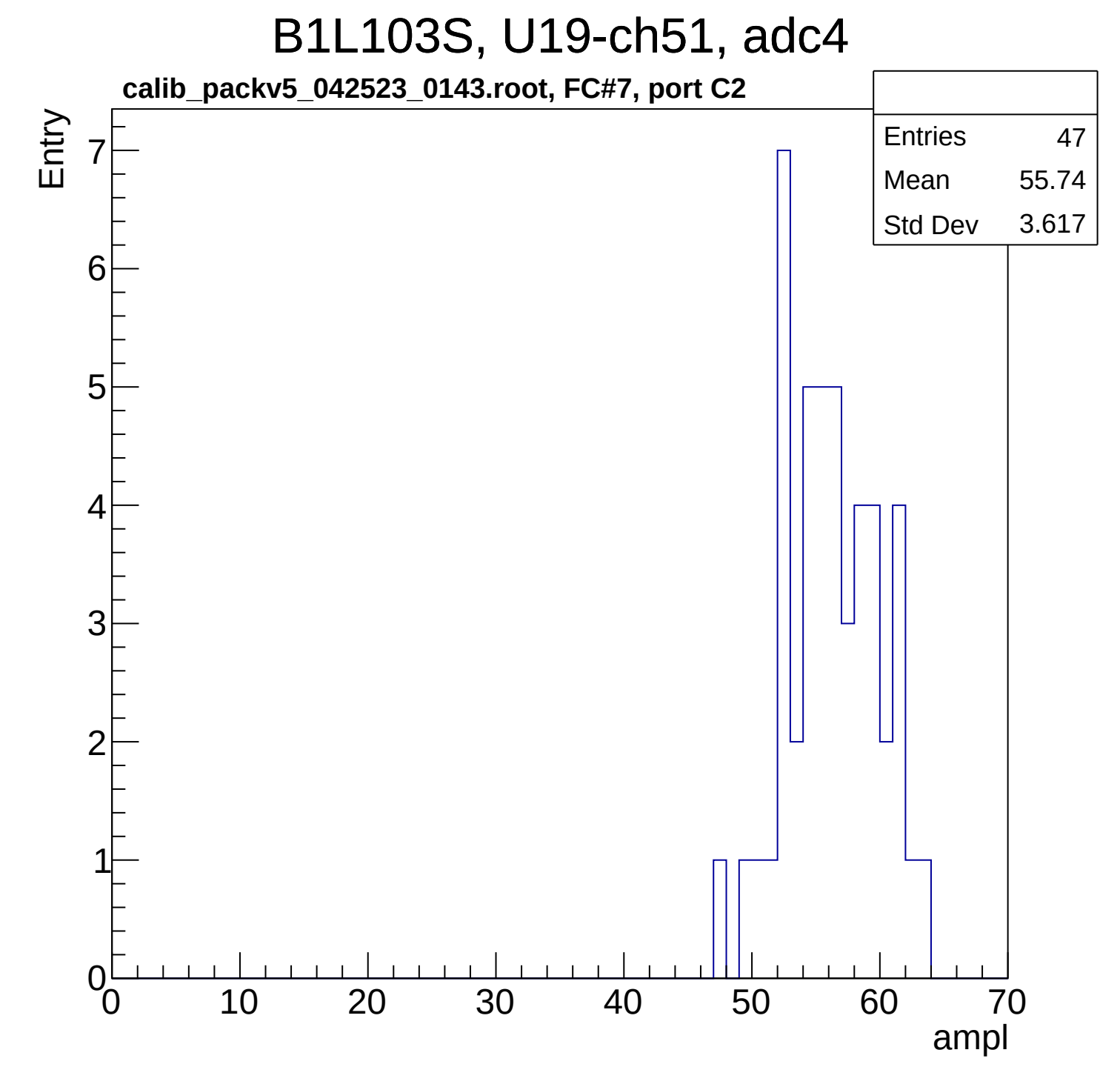
B1L103S, U19-ch51, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 55.74
Std Dev 3.617
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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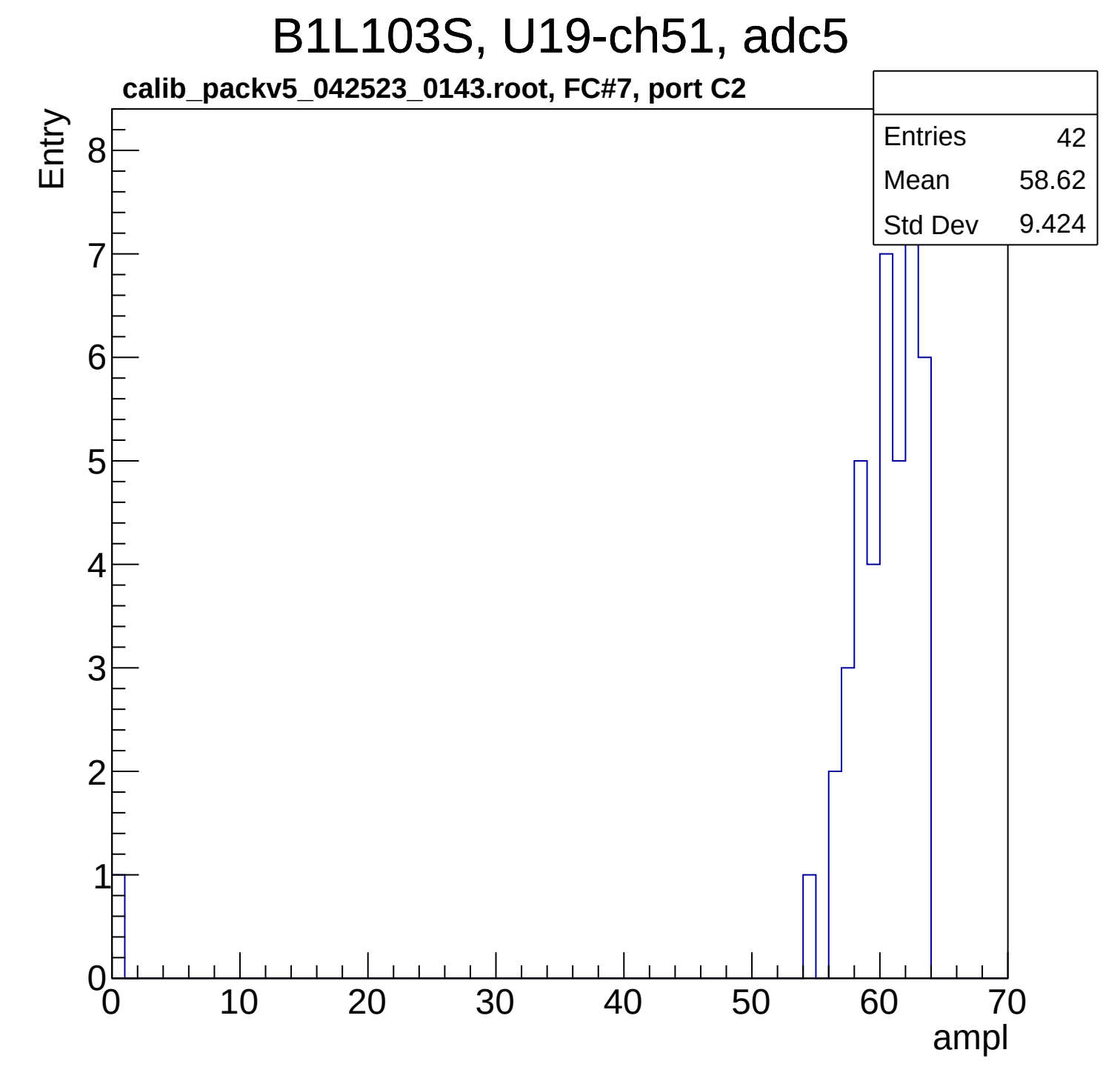
B1L103S, U19-ch51, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.62
Std Dev 9.424
Entry
ampl
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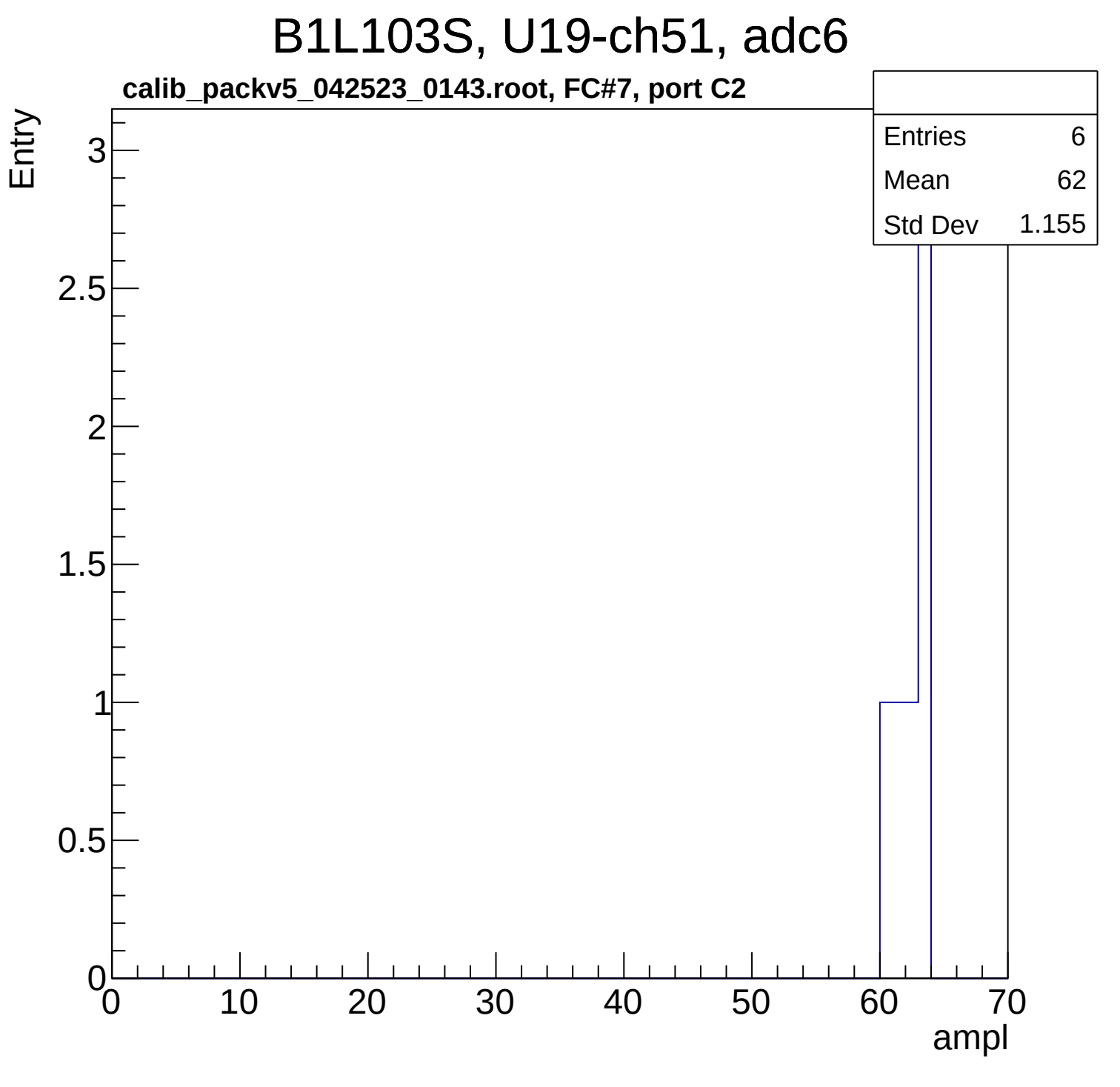
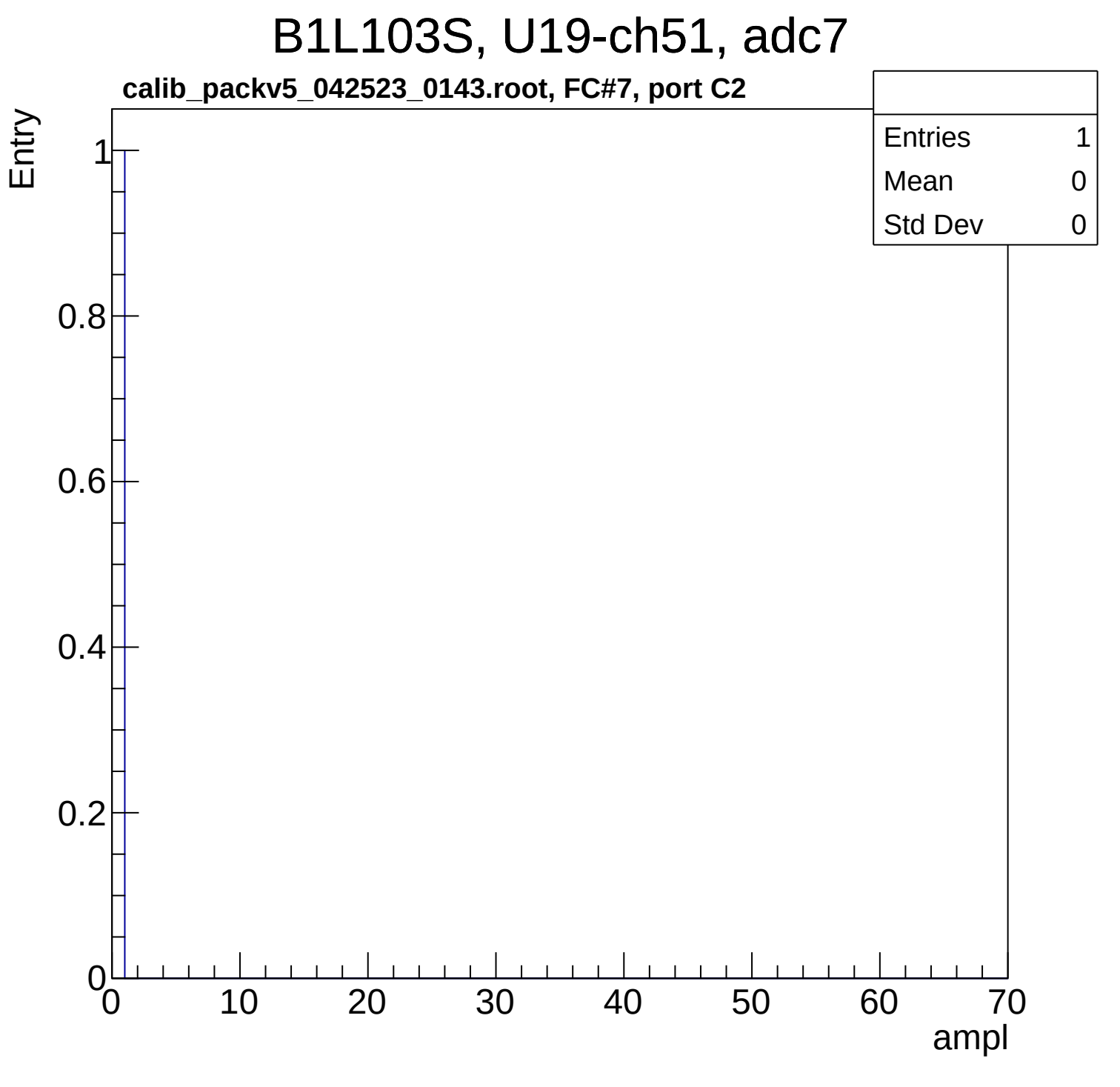
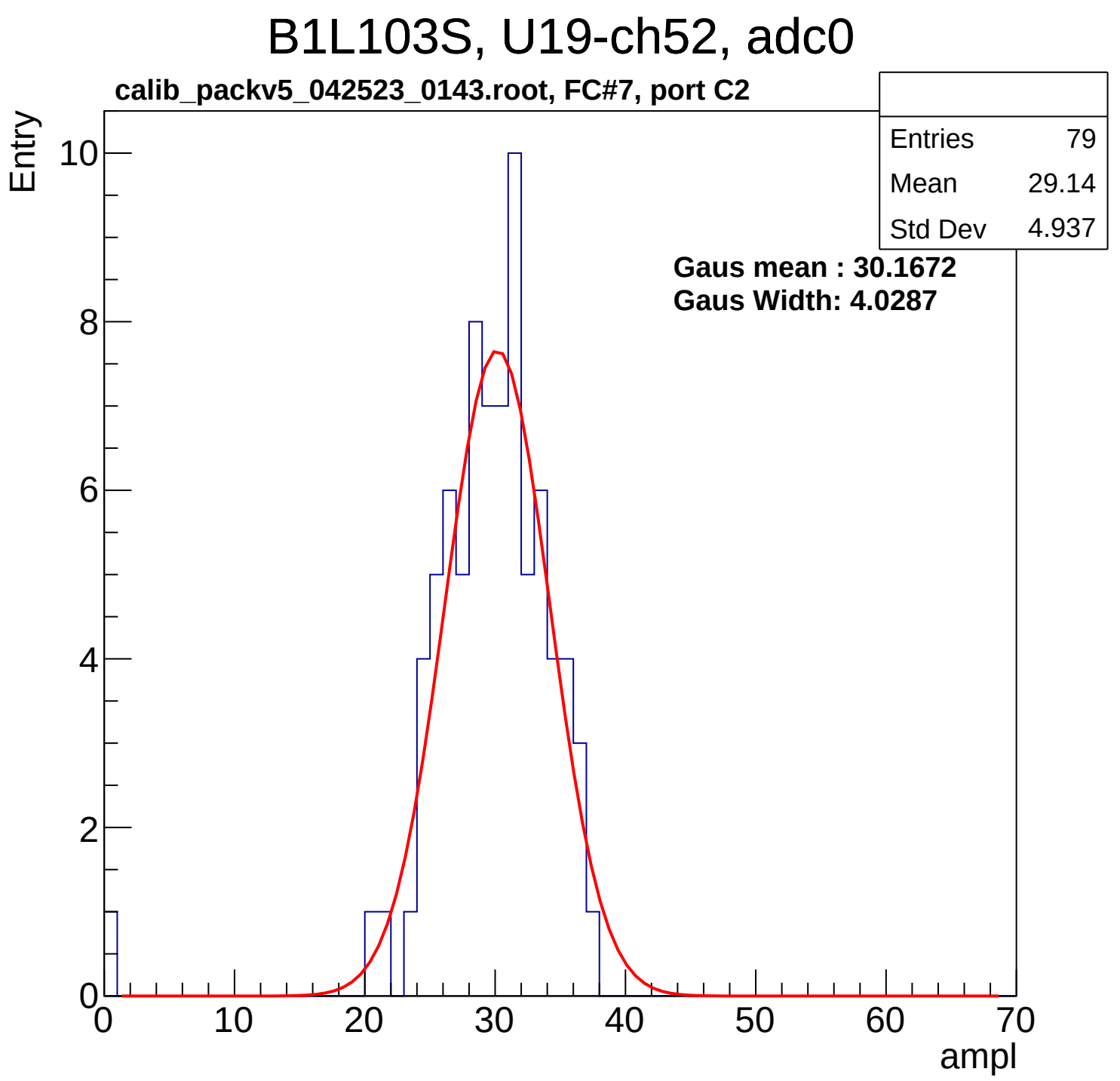
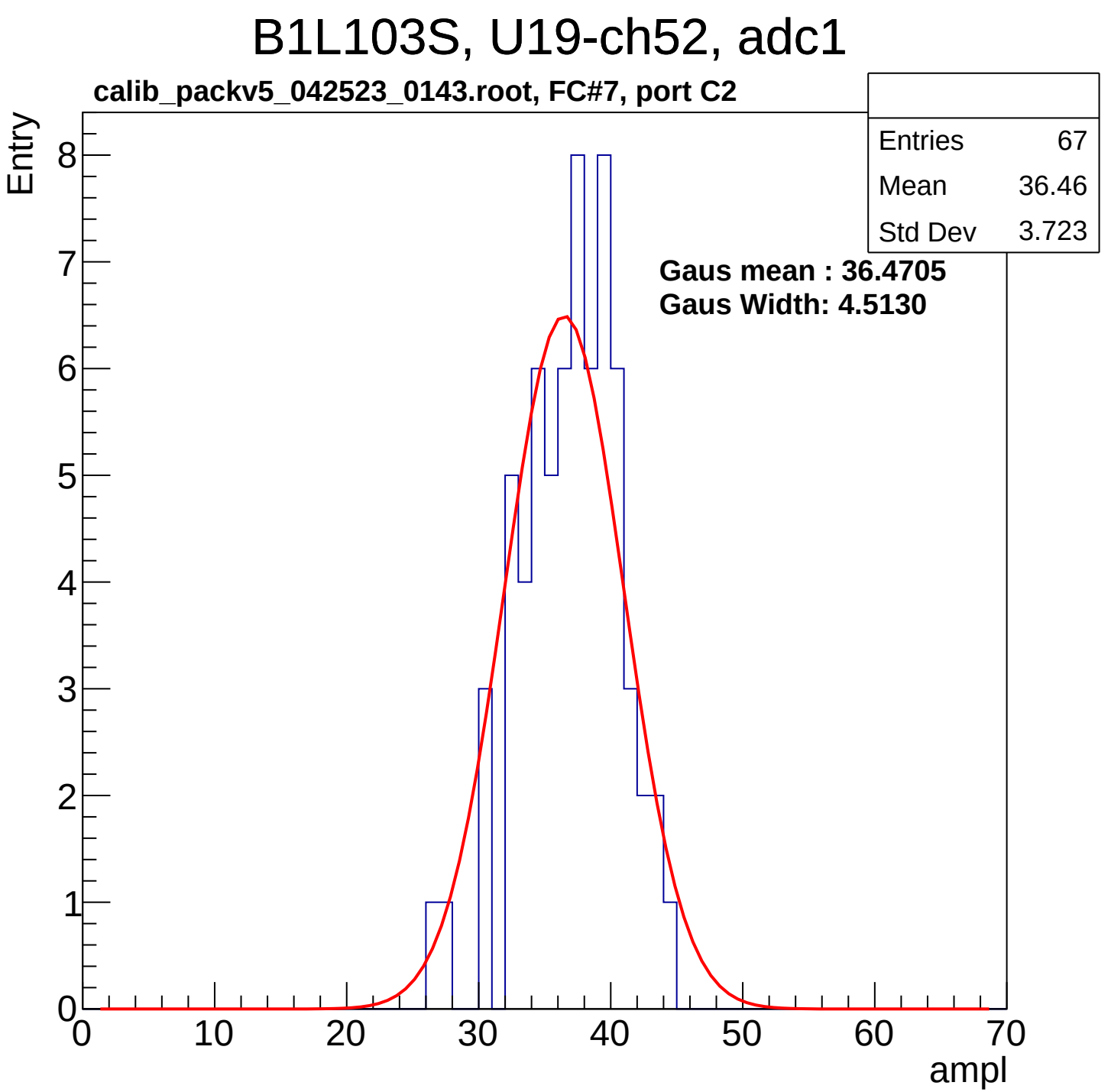
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch51, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch51, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch52, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.14
Std Dev 4.937
Gaus mean : 30.1672
Gaus Width: 4.0287
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch52, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.46
Std Dev 3.723
Gaus mean : 36.4705
Gaus Width: 4.5130
Entry
ampl
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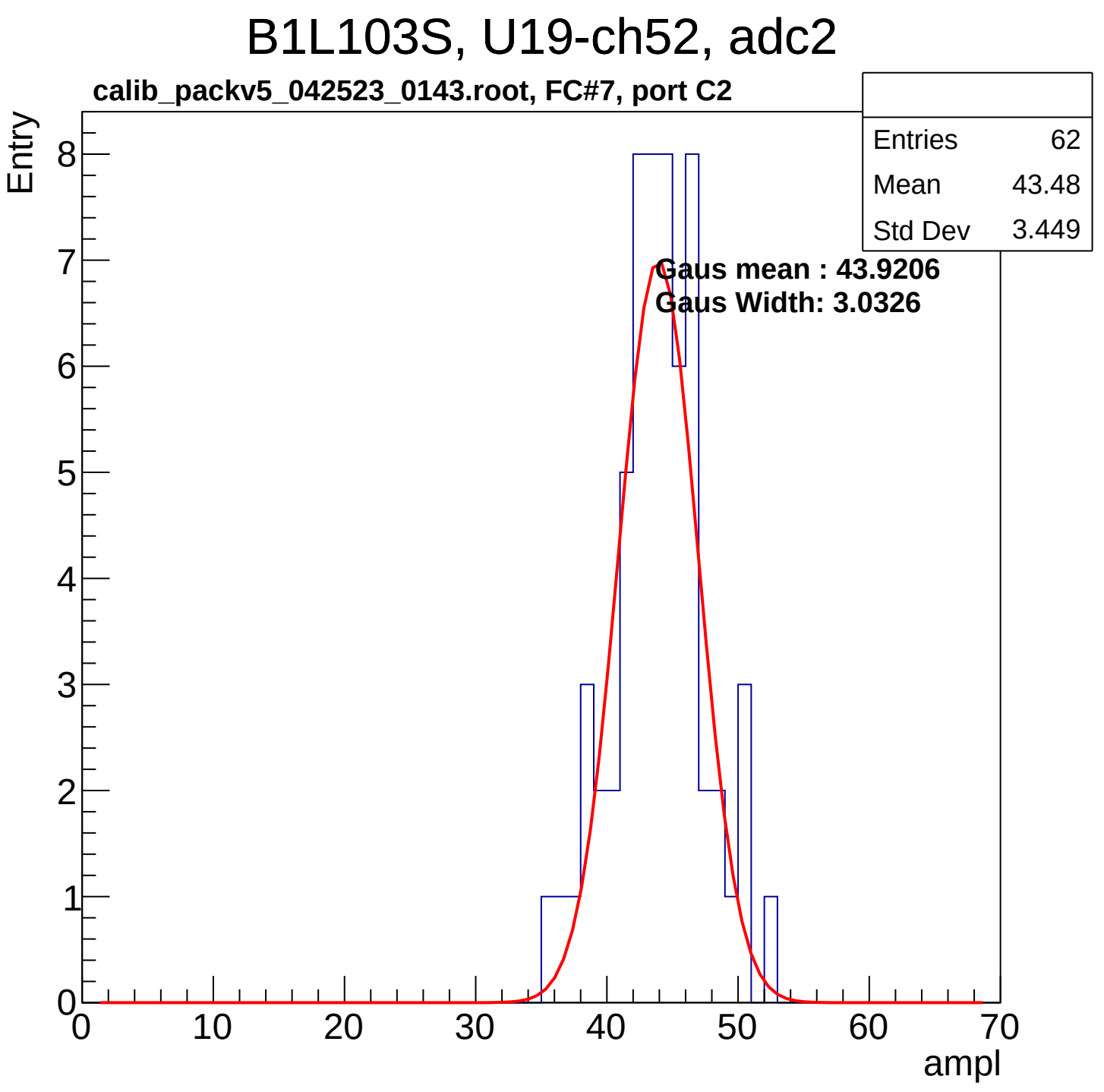
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch52, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 43.48
Std Dev 3.449
Gaus mean : 43.9206
Gaus Width: 3.0326
Entry
ampl
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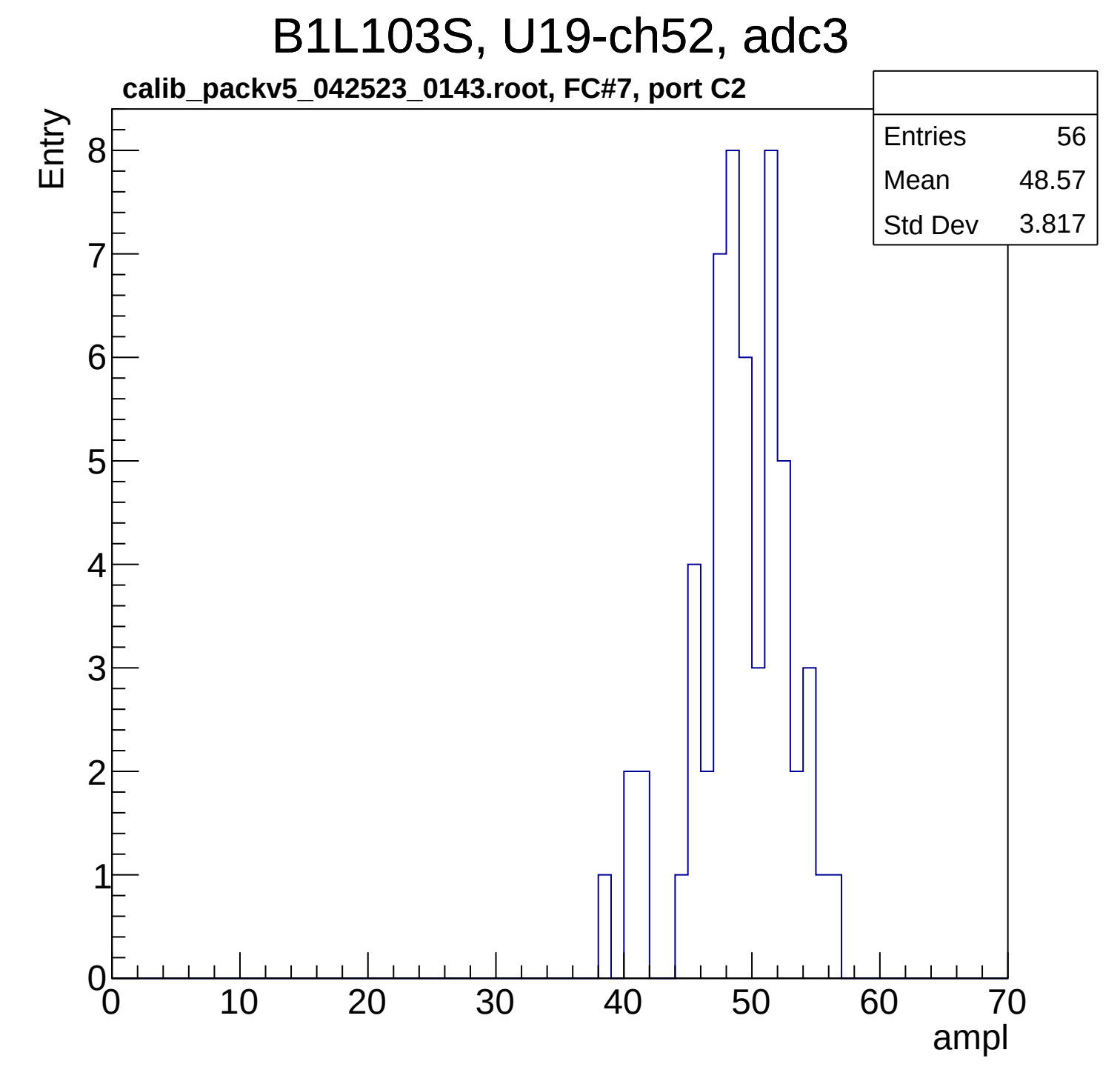
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch52, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 48.57
Std Dev 3.817
Entry
ampl
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2
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4
5
6
7
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0
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60
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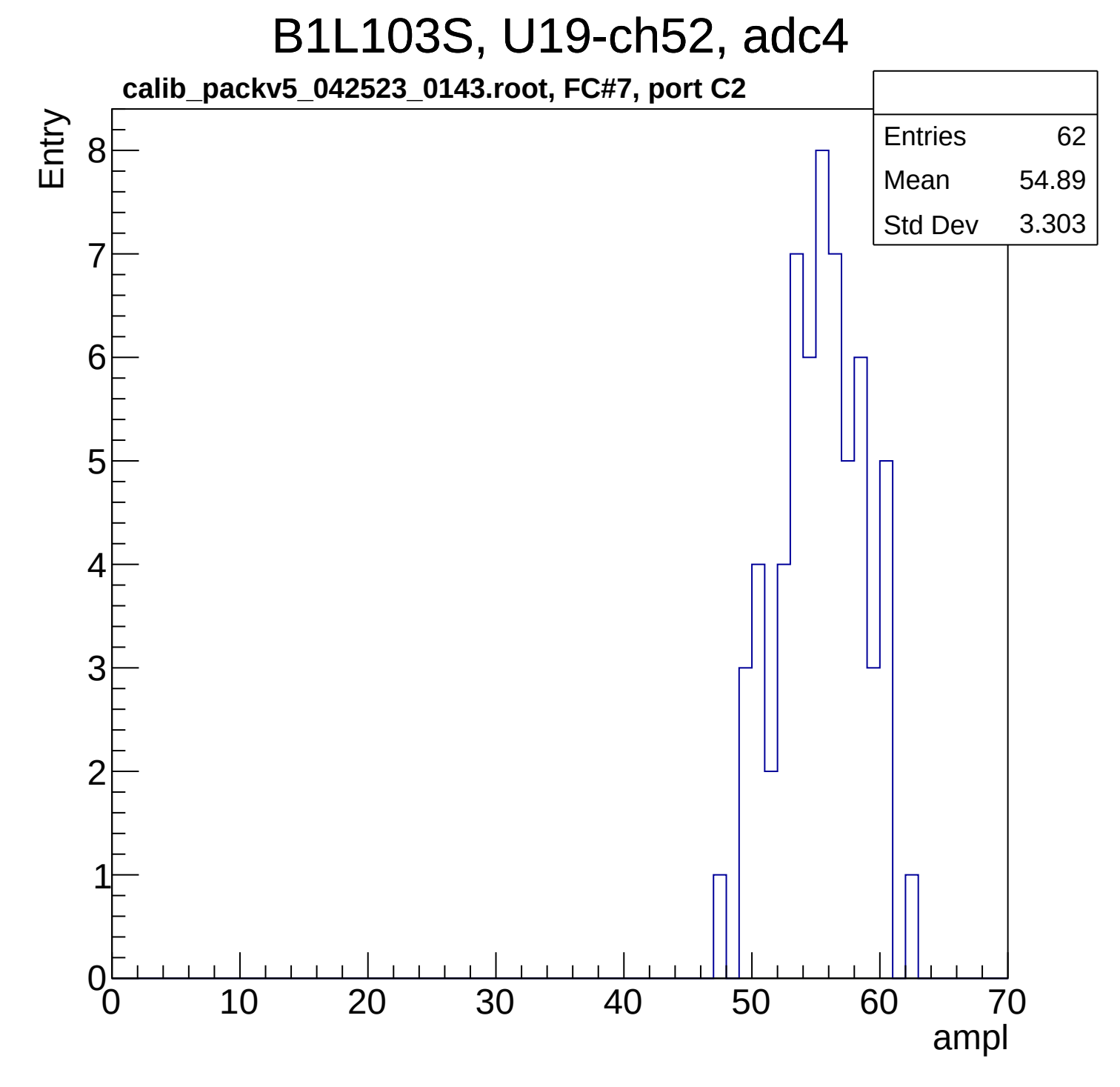
B1L103S, U19-ch52, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 54.89
Std Dev 3.303
Entry
ampl
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1
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3
4
5
6
7
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0
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60
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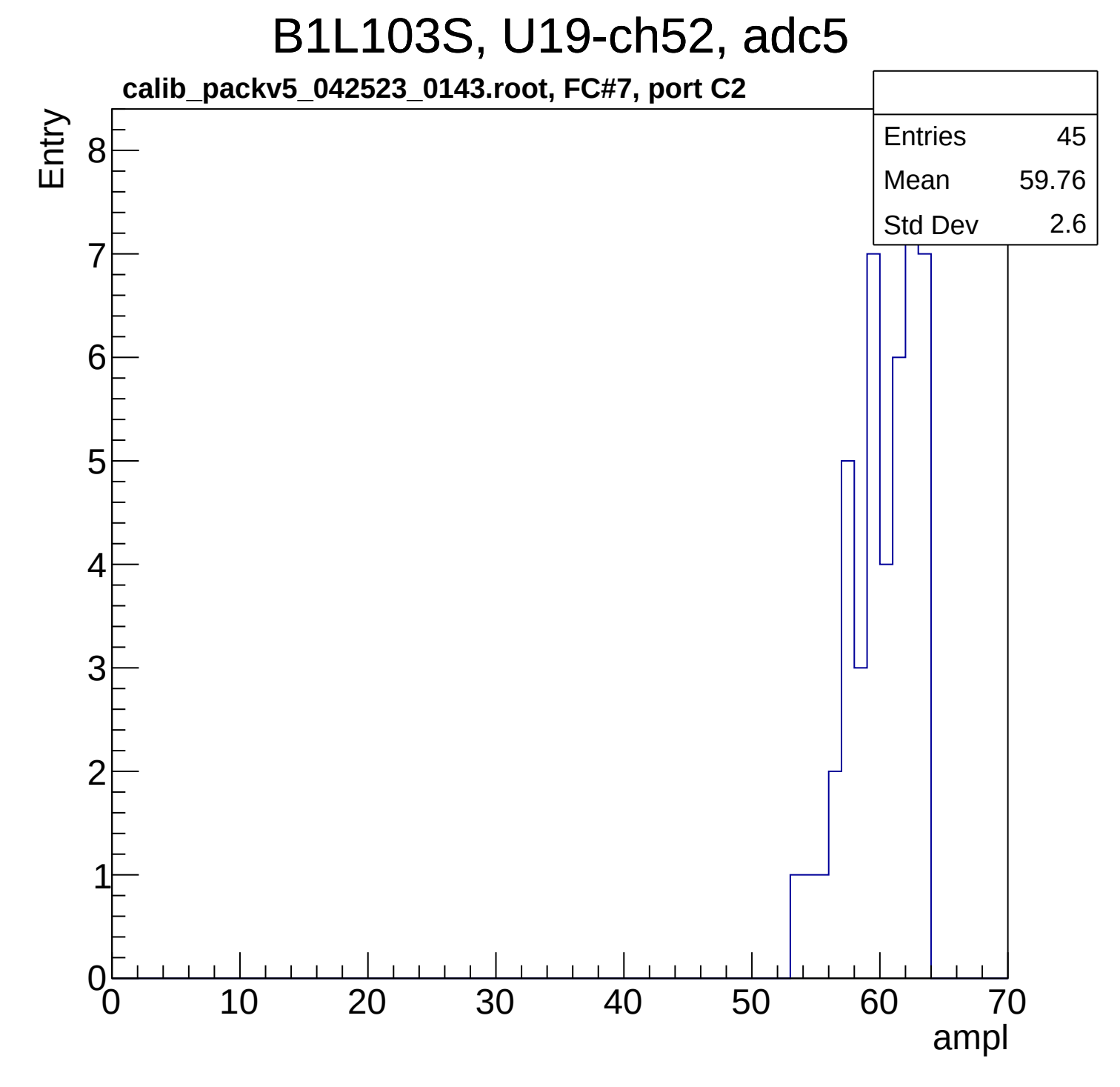
B1L103S, U19-ch52, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.76
Std Dev 2.6
Entry
ampl
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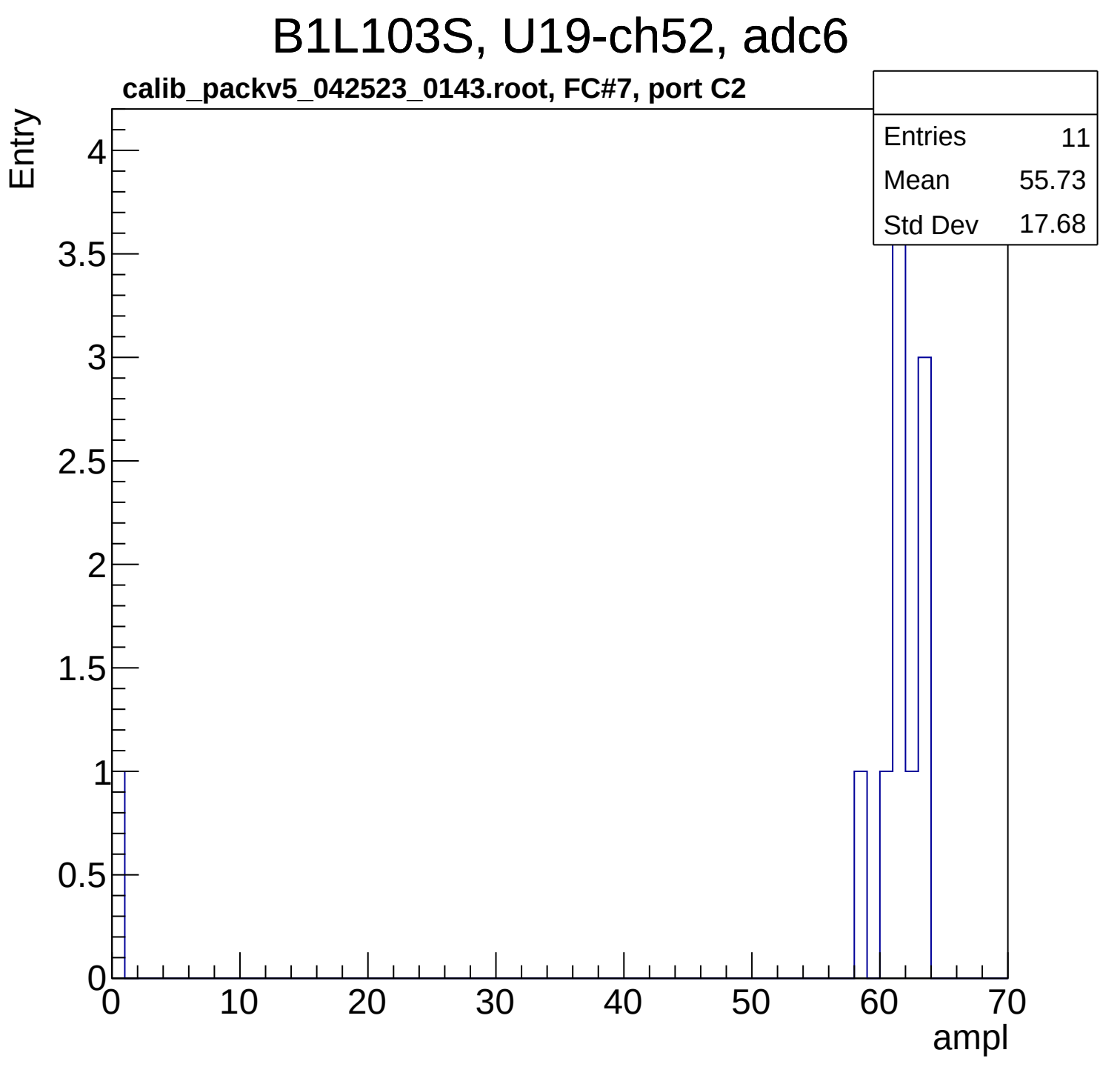
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch52, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 55.73
Std Dev 17.68
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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50
60
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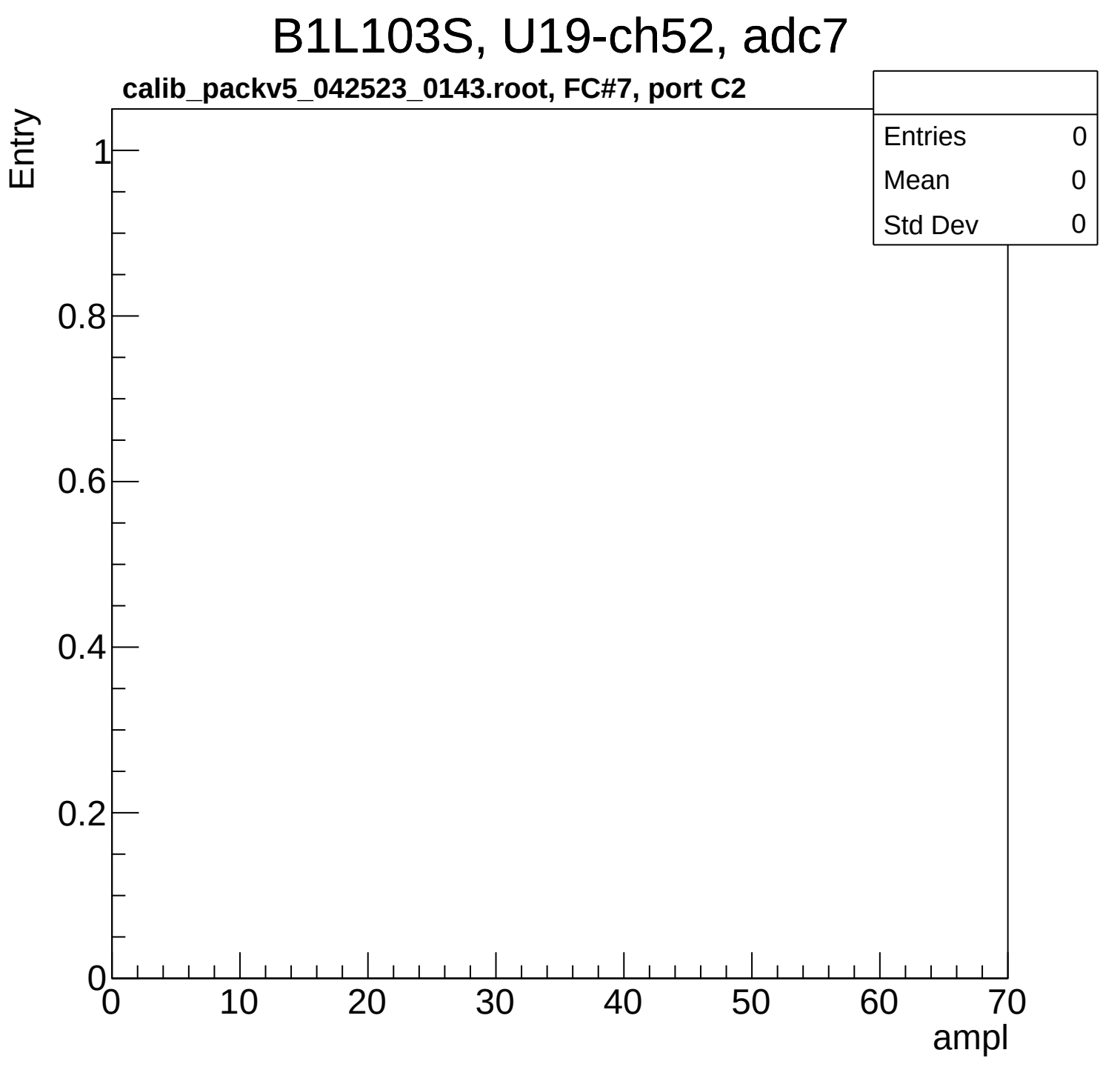
B1L103S, U19-ch52, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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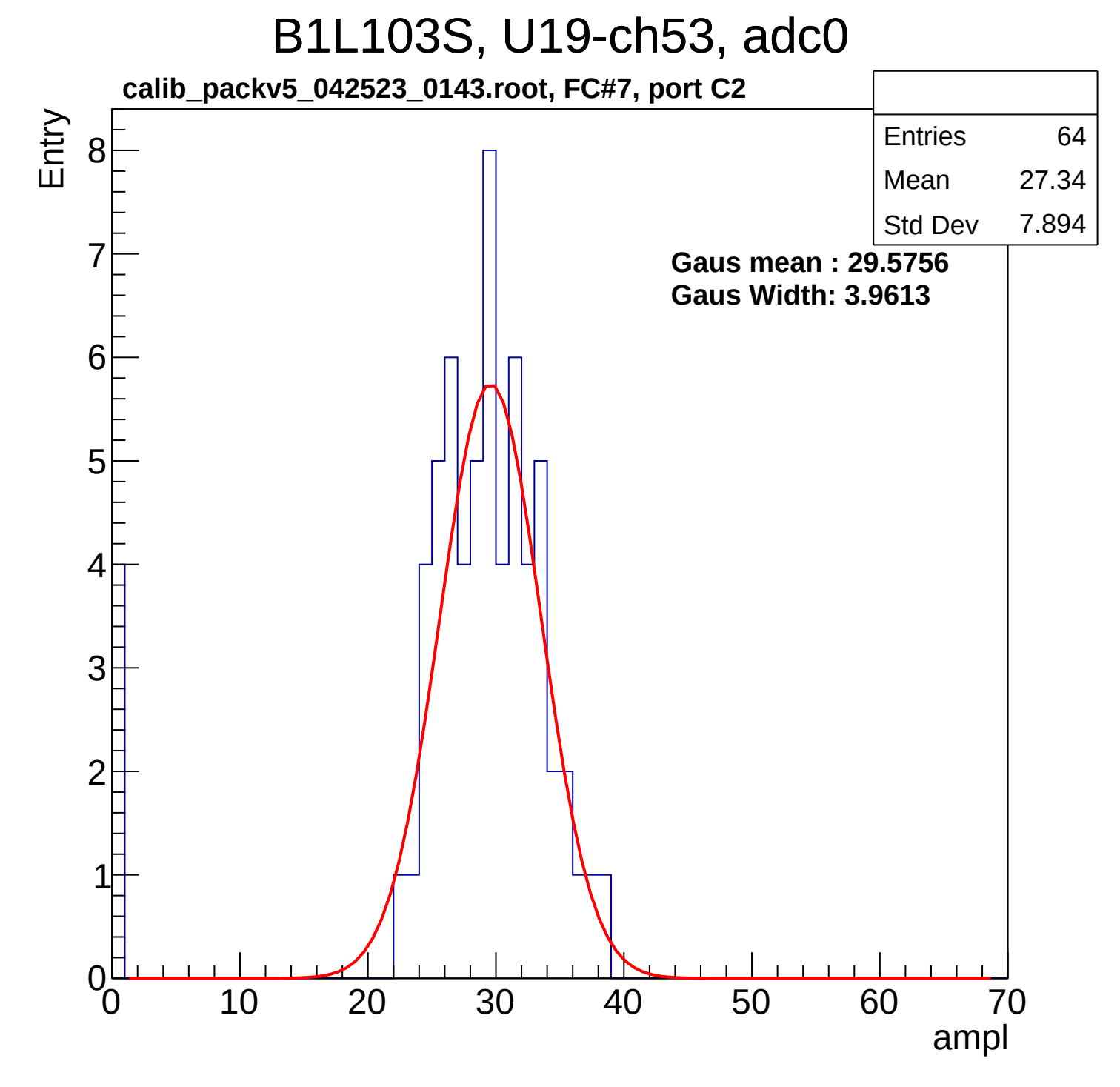
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch53, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 27.34
Std Dev 7.894
Gaus mean : 29.5756
Gaus Width: 3.9613
Entry
ampl
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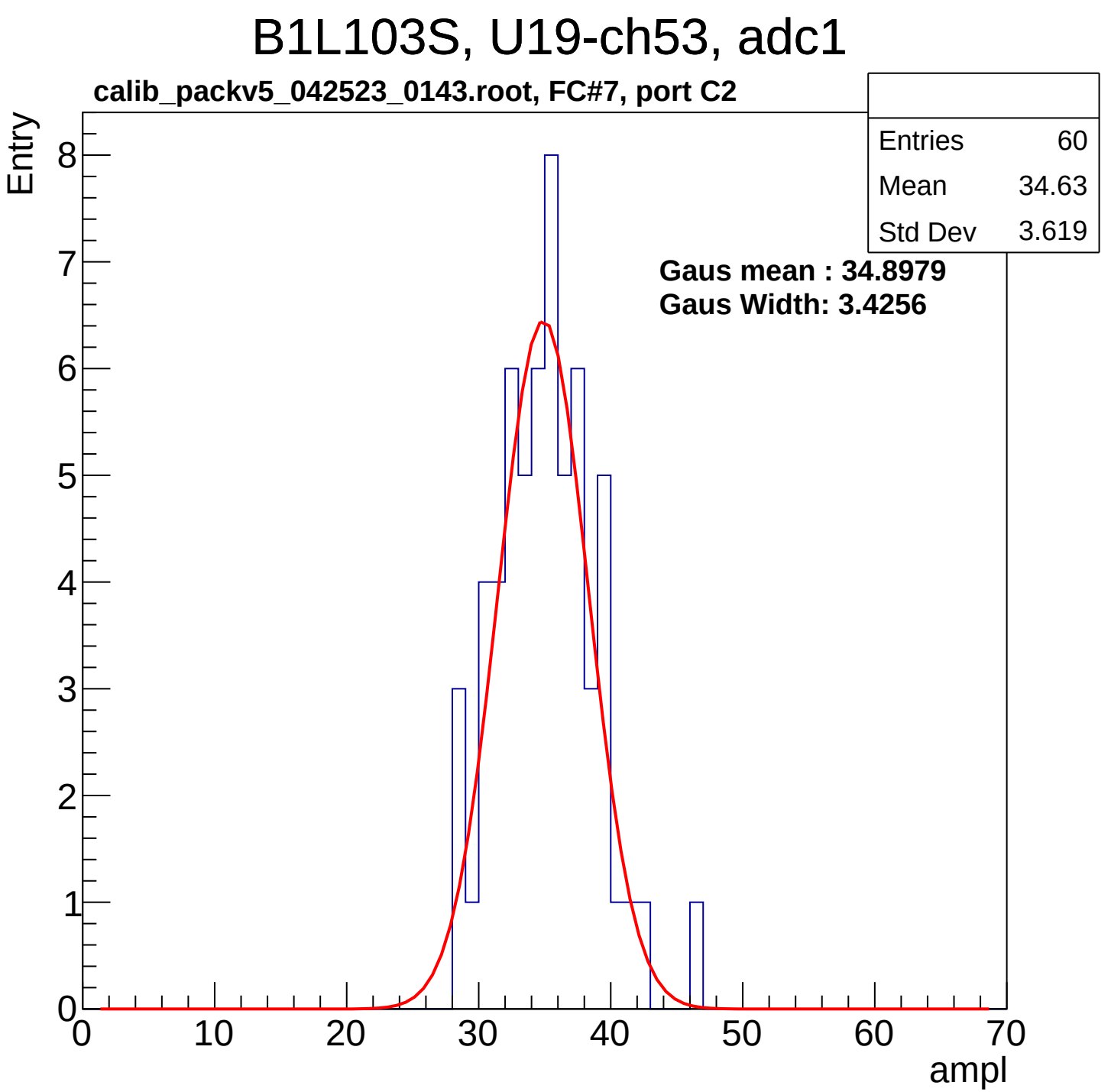
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch53, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 34.63
Std Dev 3.619
Gaus mean : 34.8979
Gaus Width: 3.4256
Entry
ampl
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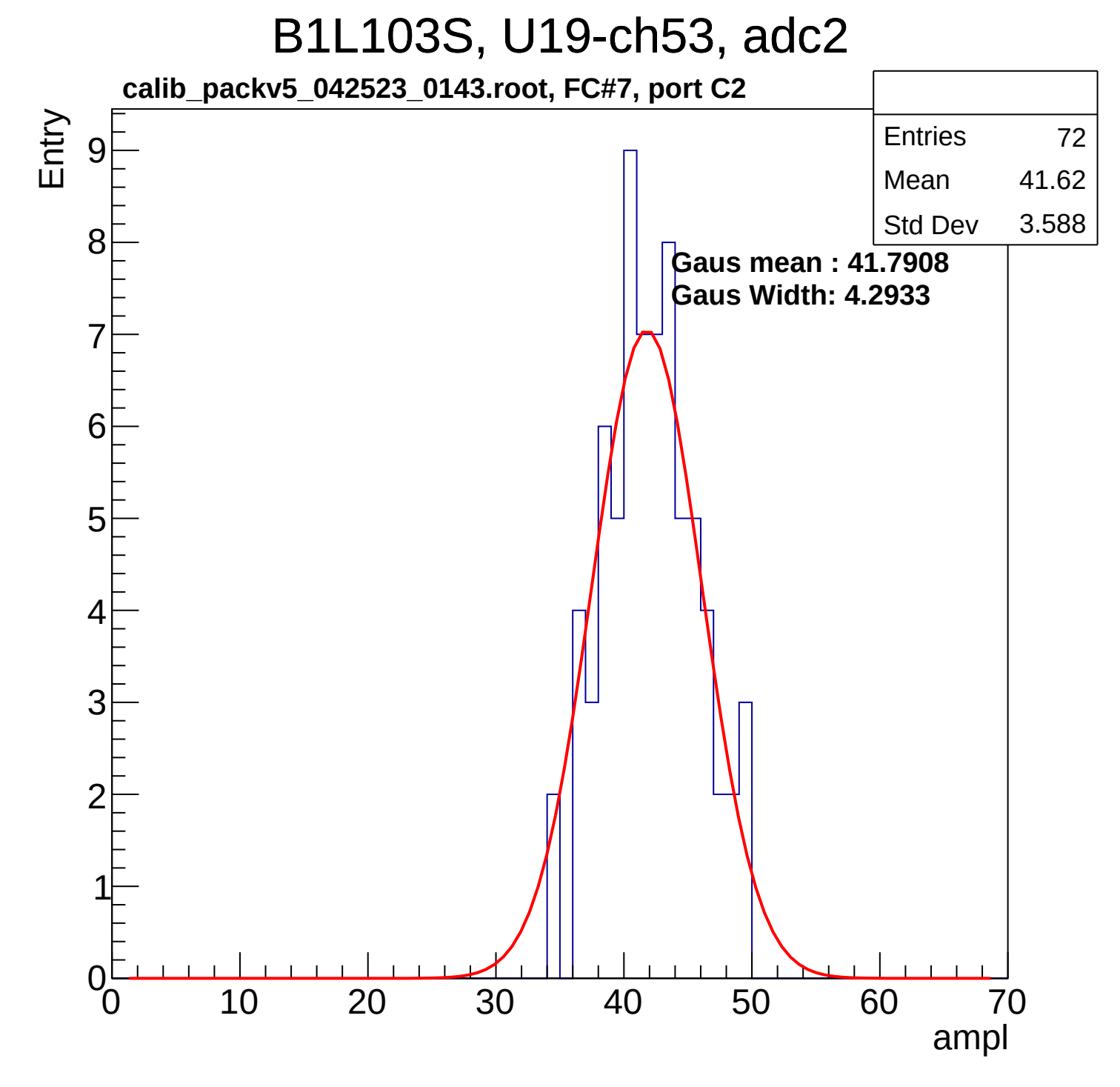
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch53, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 41.62
Std Dev 3.588
Gaus mean : 41.7908
Gaus Width: 4.2933
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
50
60
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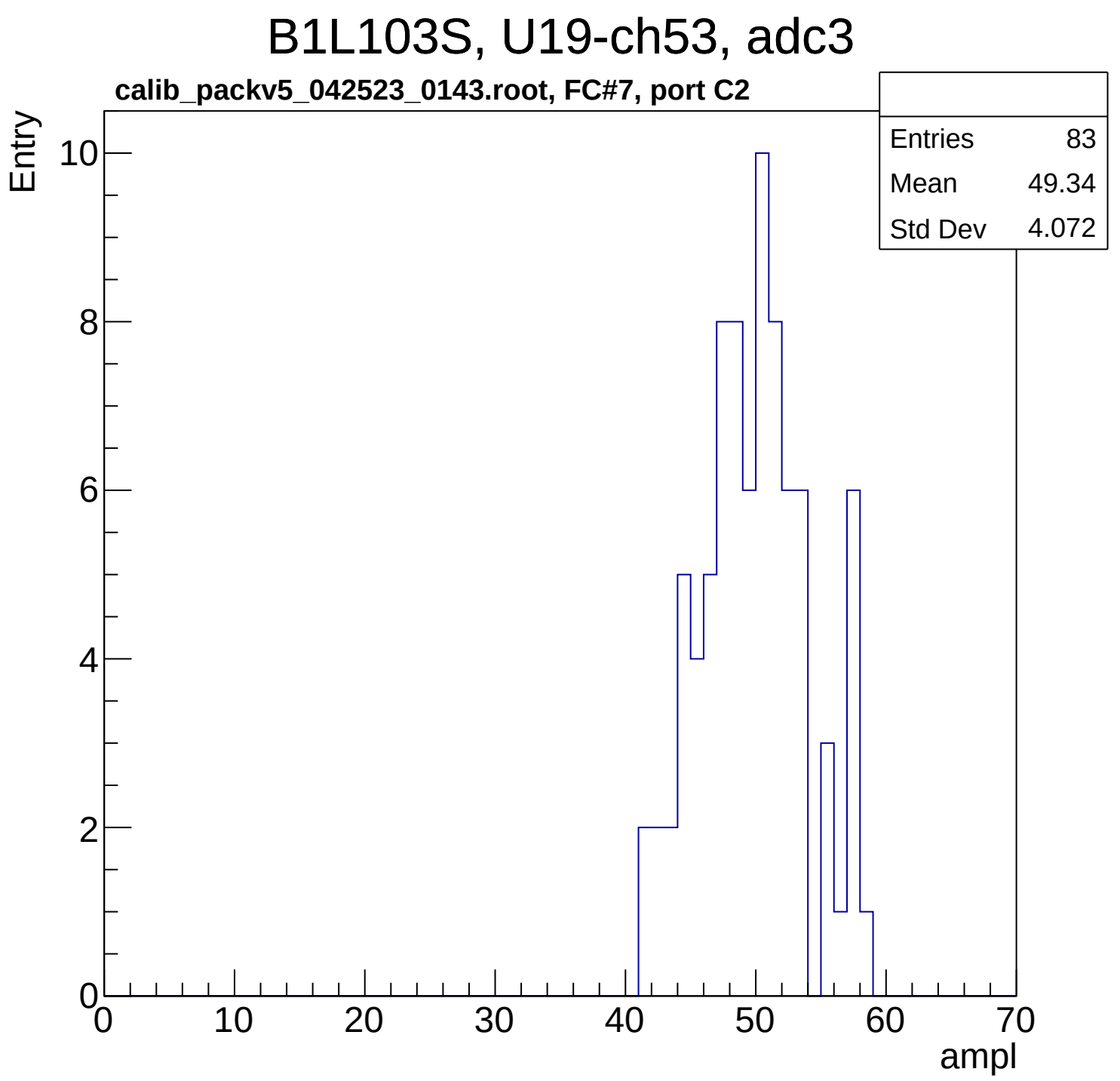
B1L103S, U19-ch53, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 49.34
Std Dev 4.072
Entry
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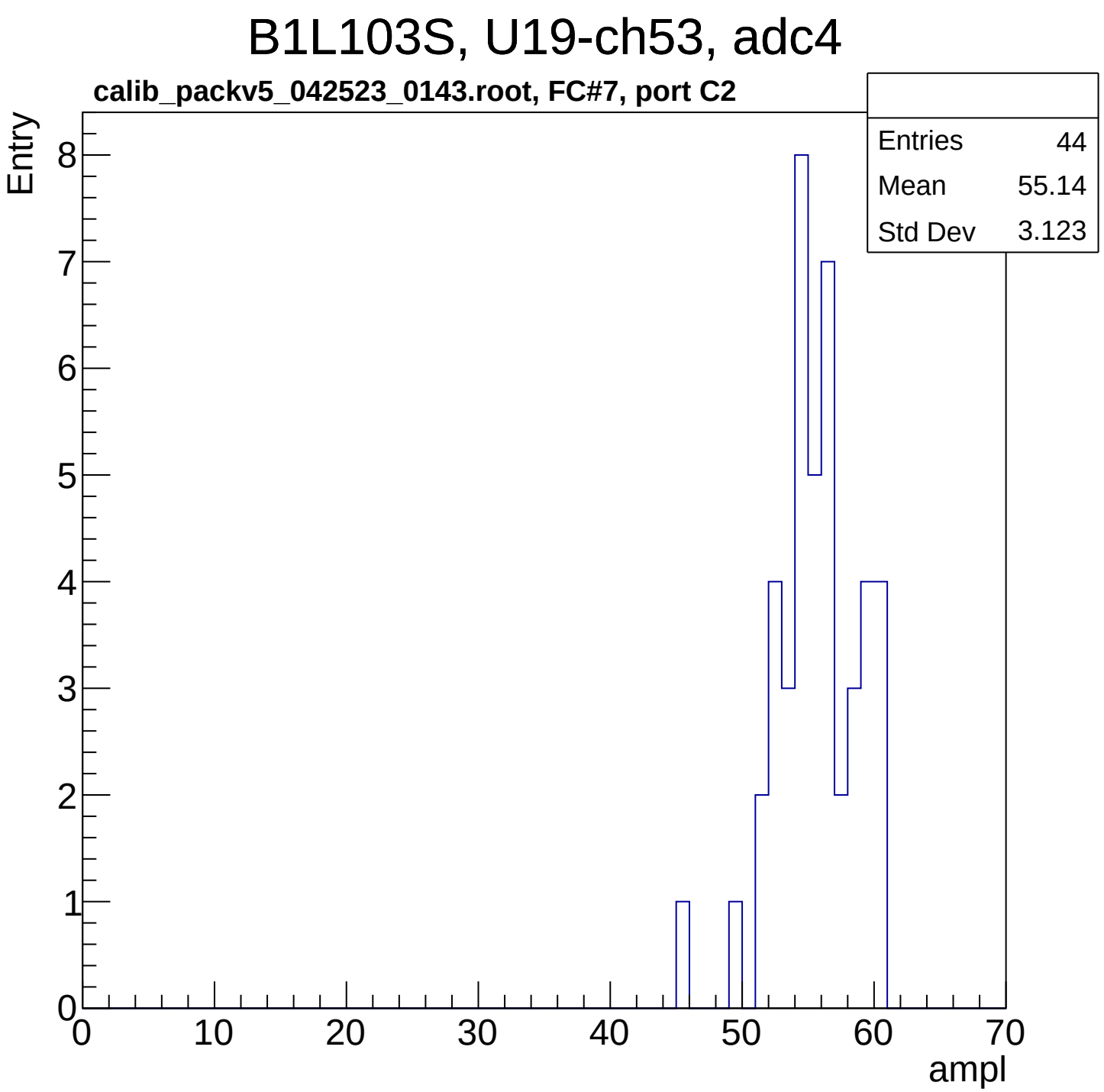
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch53, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 55.14
Std Dev 3.123
Entry
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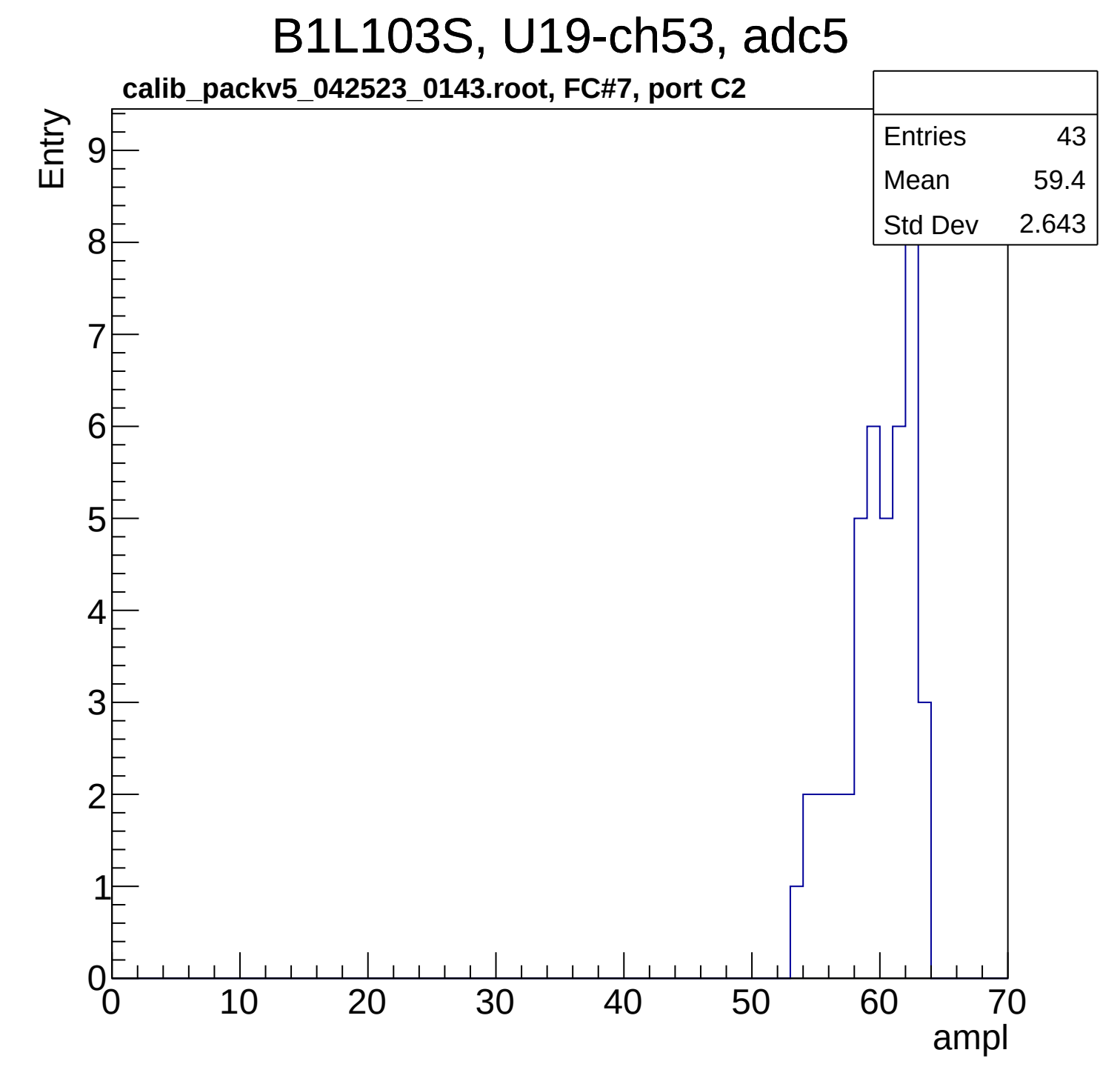
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch53, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.4
Std Dev 2.643
Entry
ampl
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2
3
4
5
6
7
8
9
0
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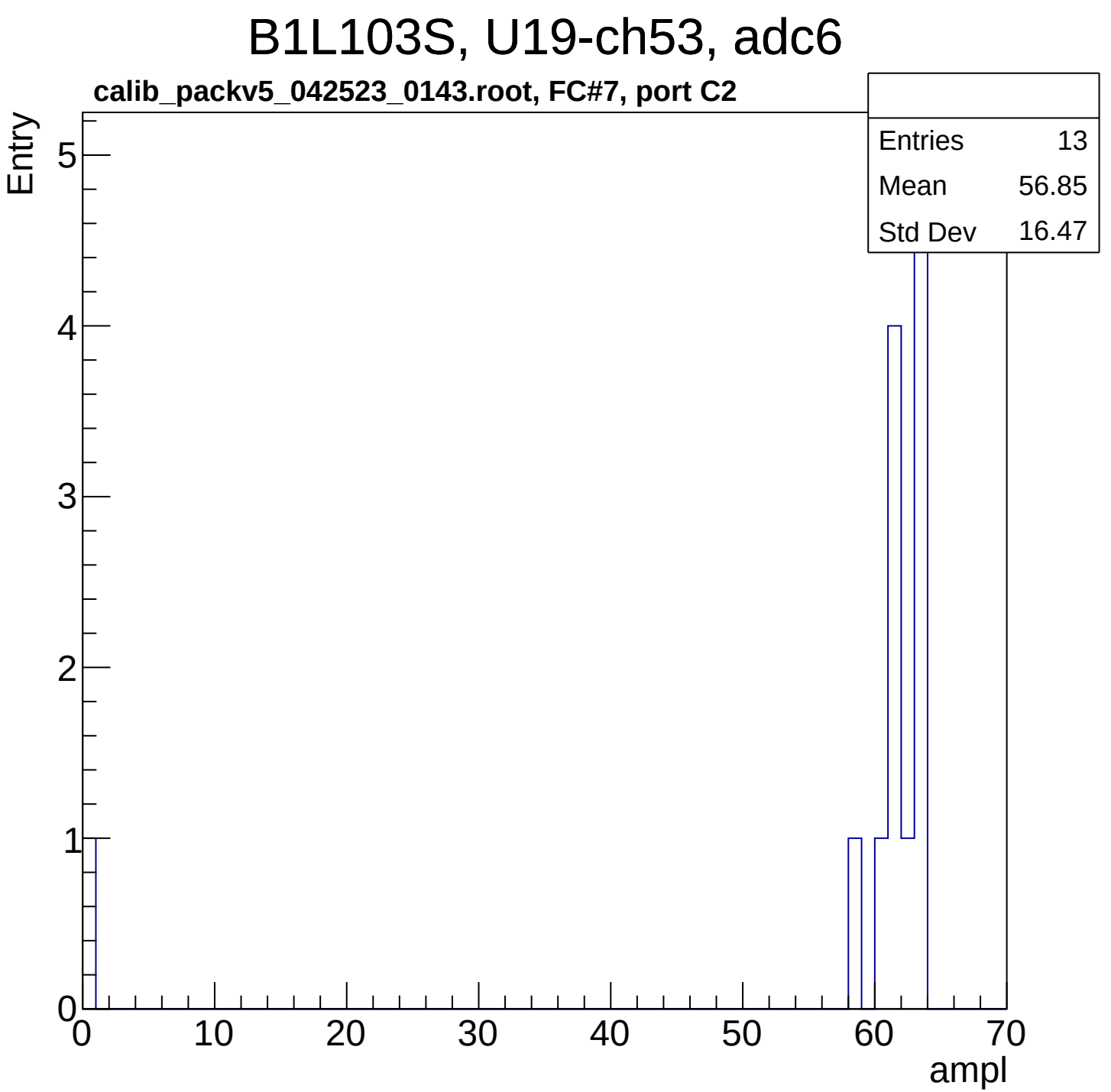
B1L103S, U19-ch53, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.85
Std Dev 16.47
Entry
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2
3
4
5
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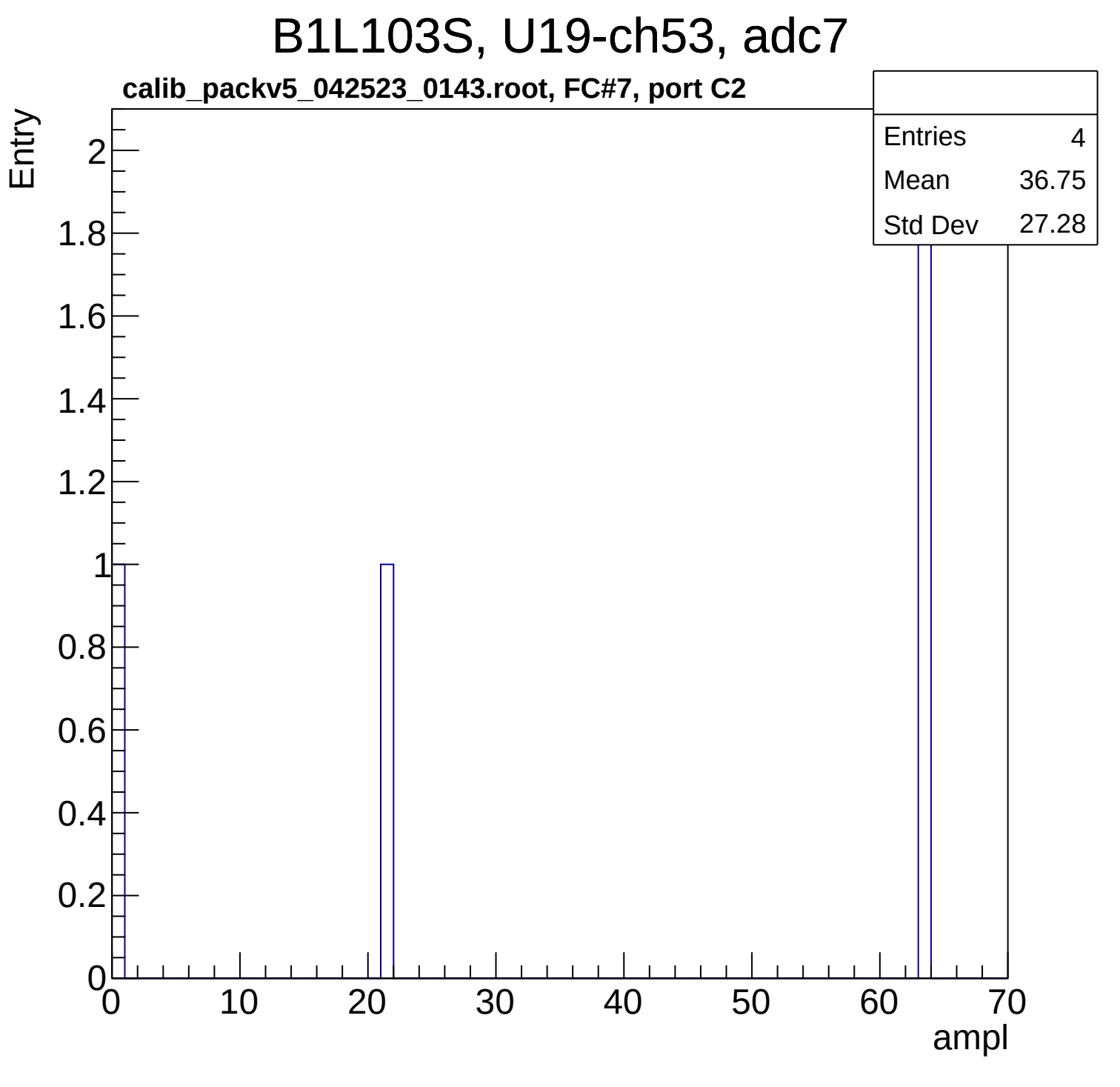
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch53, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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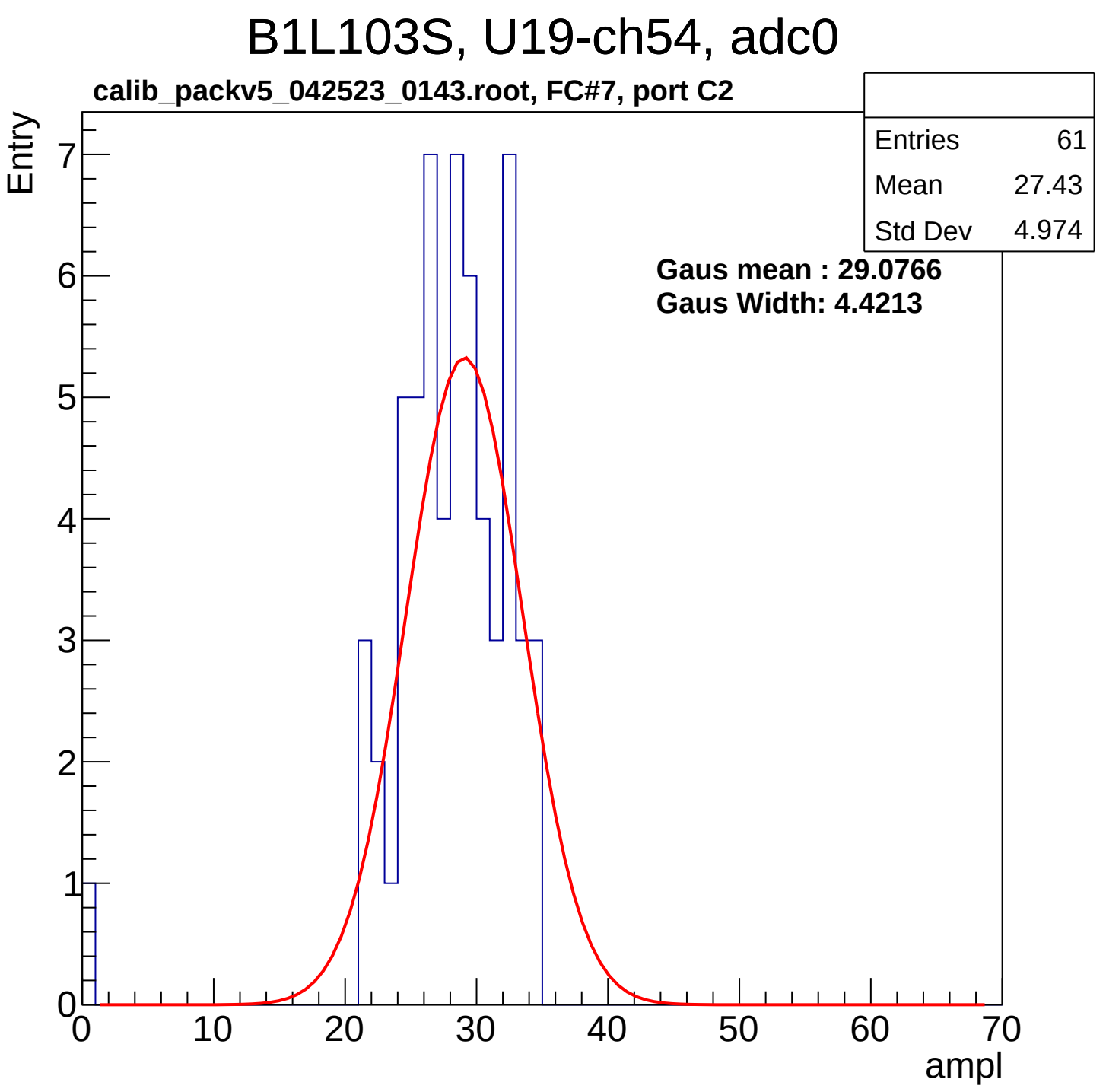
B1L103S, U19-ch54, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 27.43
Std Dev 4.974
Gaus mean : 29.0766
Gaus Width: 4.4213
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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60
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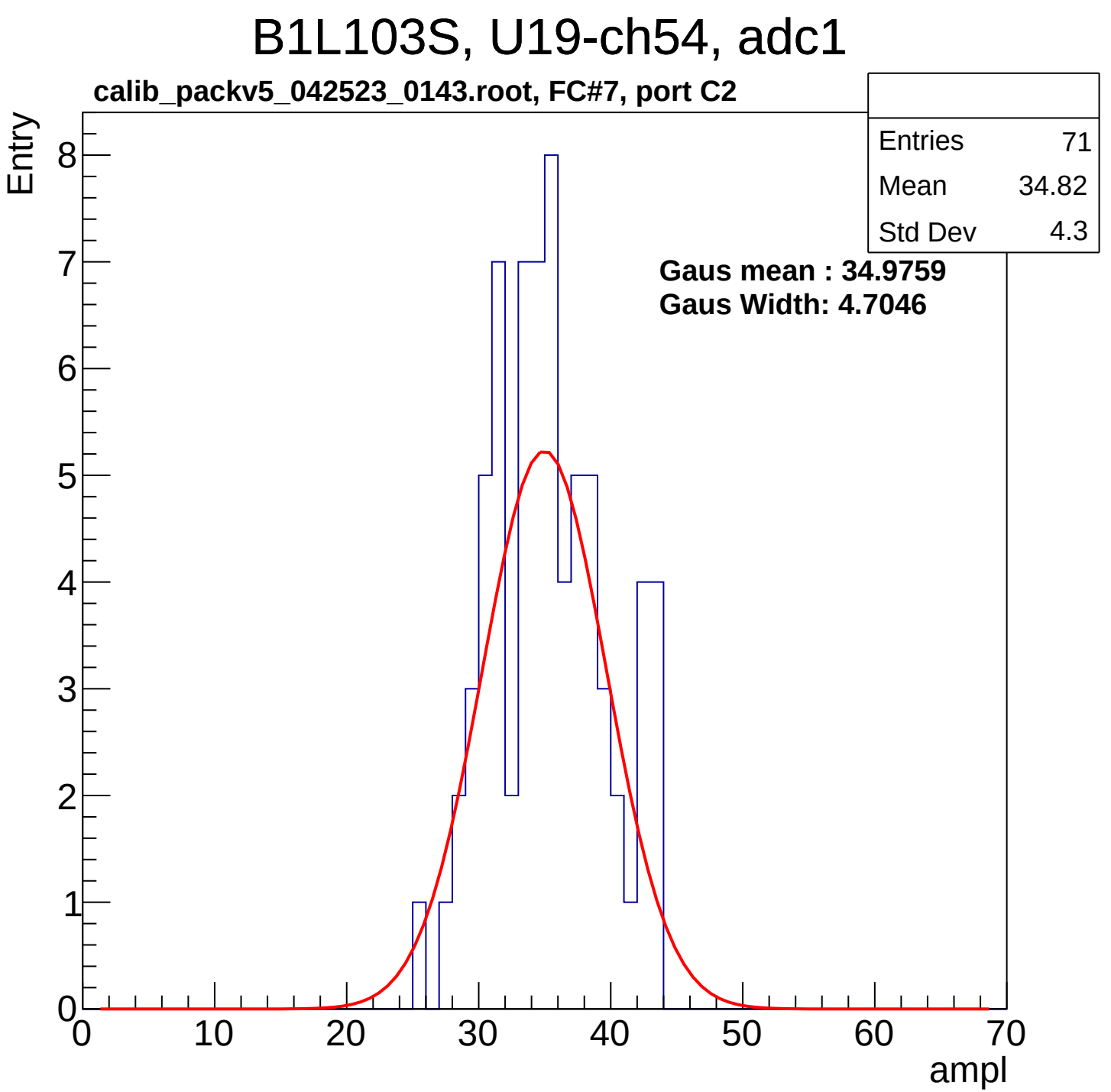
B1L103S, U19-ch54, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 34.82
Std Dev 4.3
Gaus mean : 34.9759
Gaus Width: 4.7046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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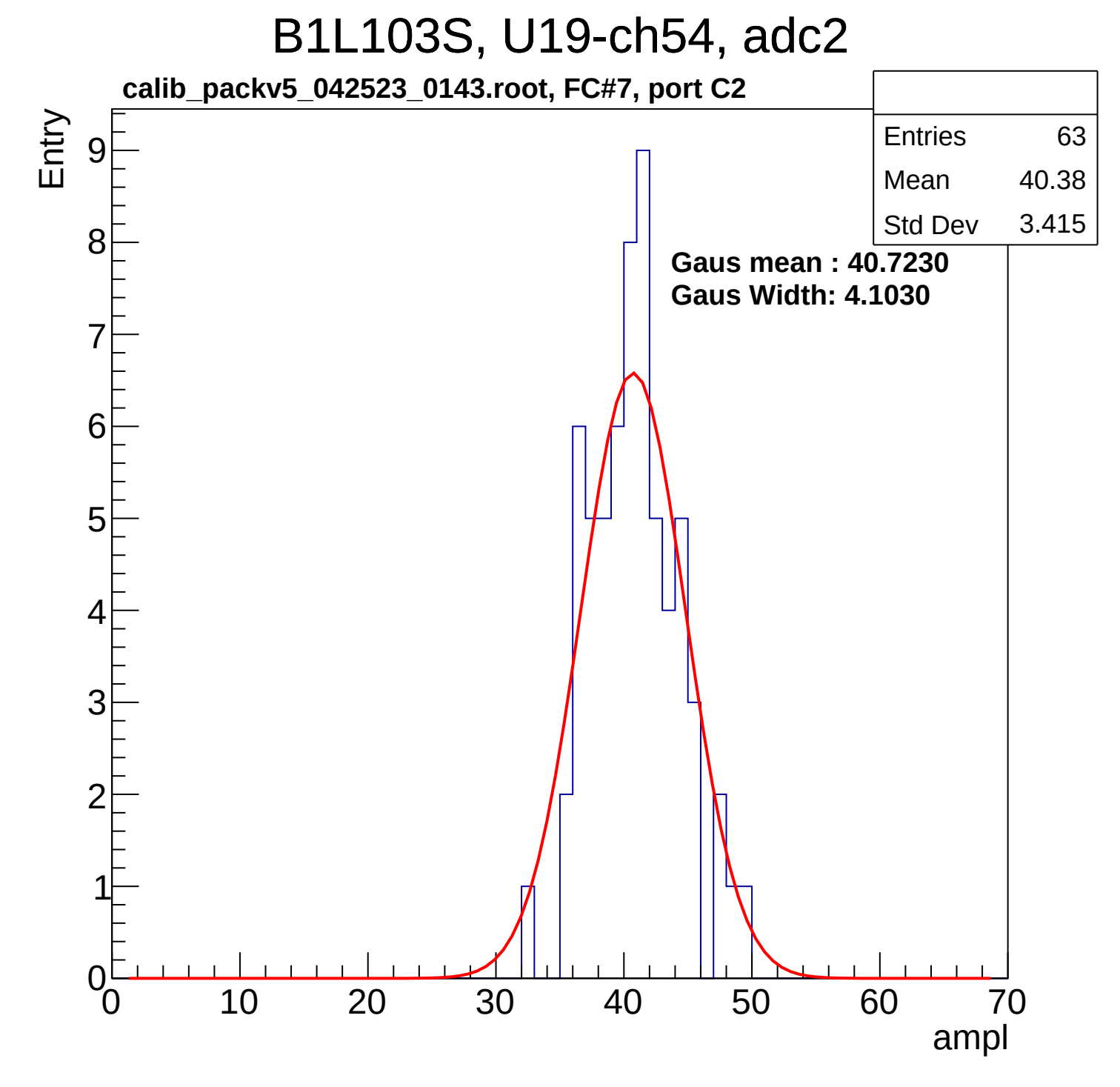
B1L103S, U19-ch54, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 40.38
Std Dev 3.415
Gaus mean : 40.7230
Gaus Width: 4.1030
Entry
ampl
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2
3
4
5
6
7
8
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0
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30
40
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60
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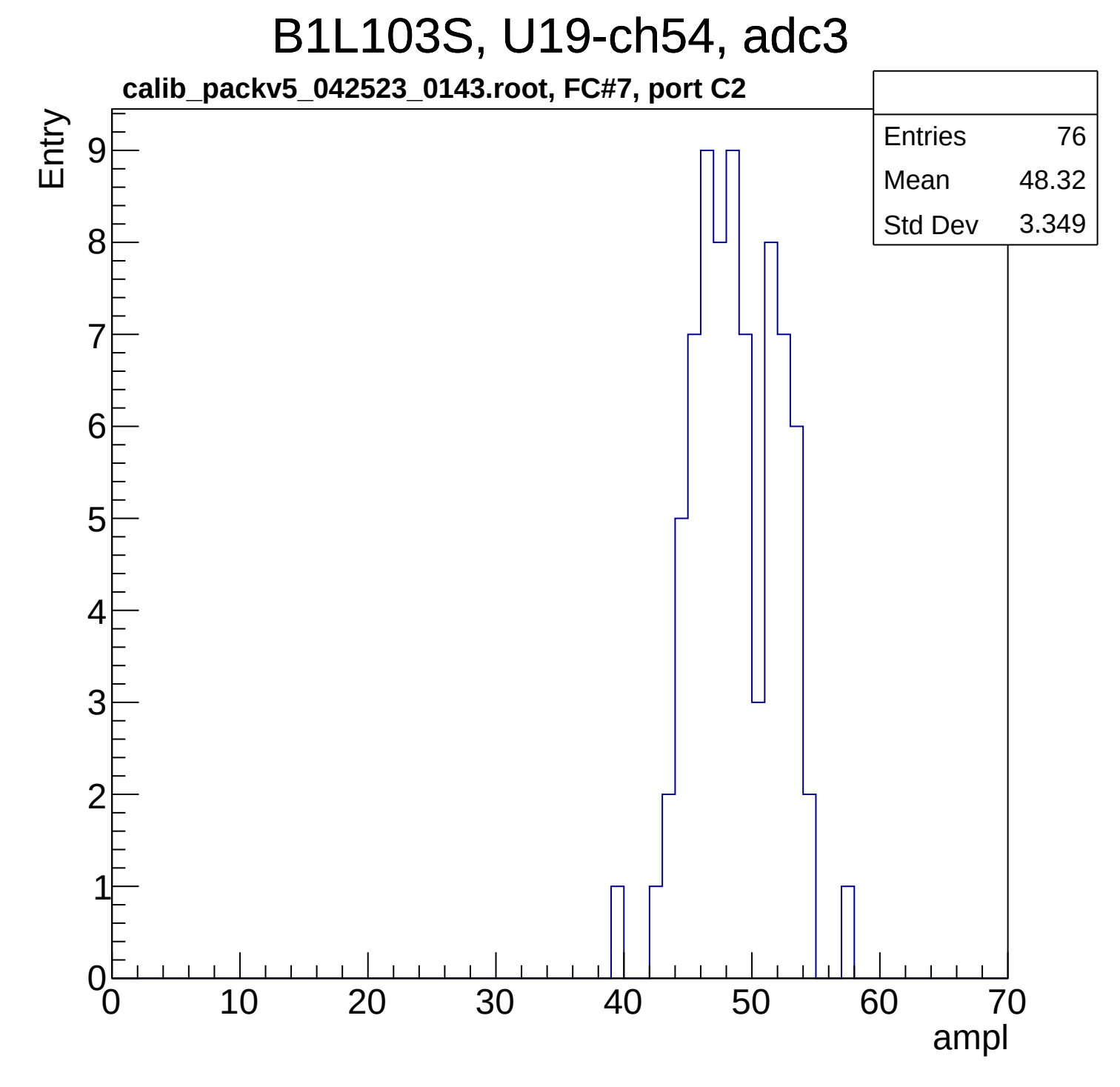
B1L103S, U19-ch54, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 48.32
Std Dev 3.349
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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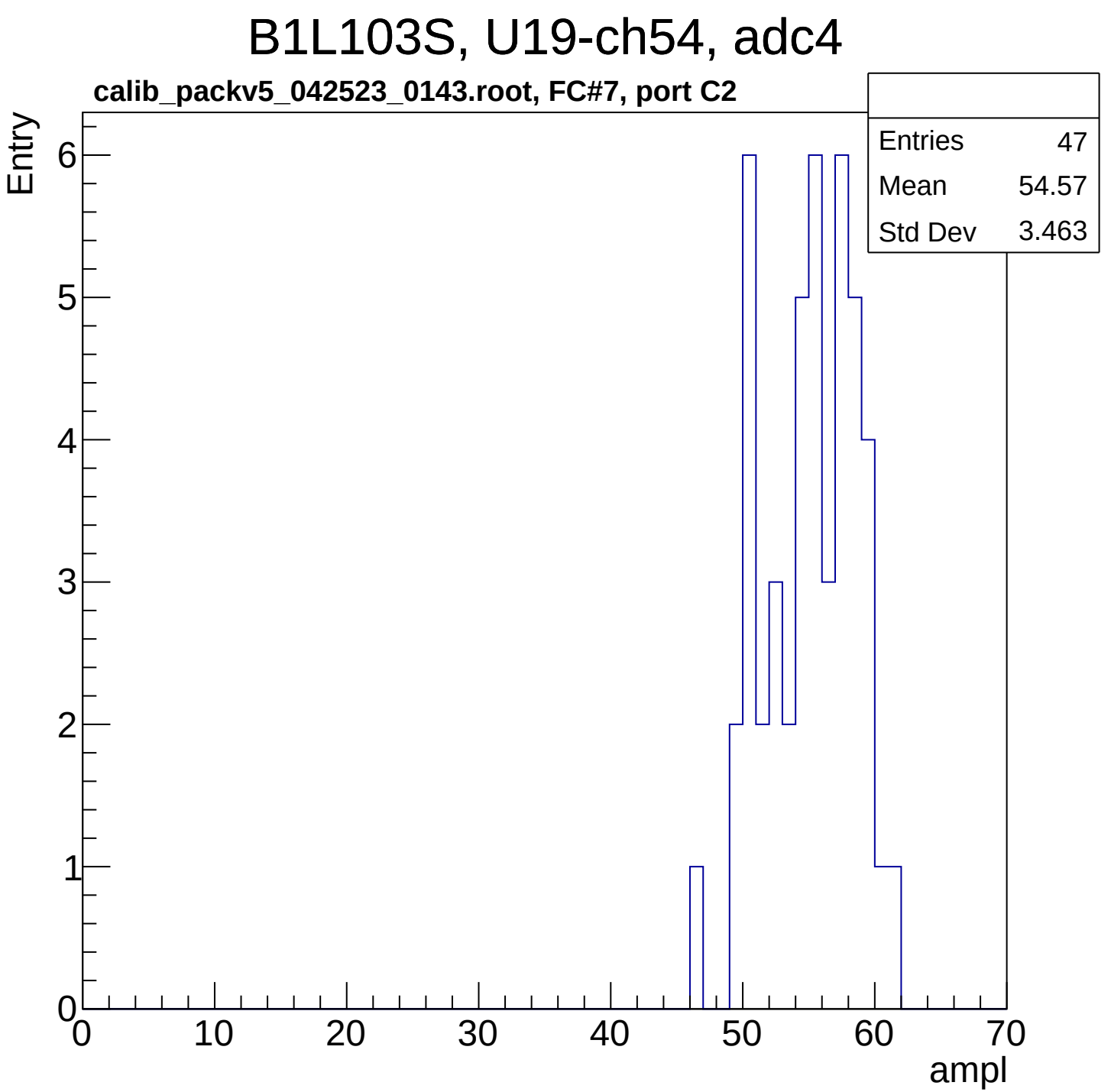
B1L103S, U19-ch54, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 54.57
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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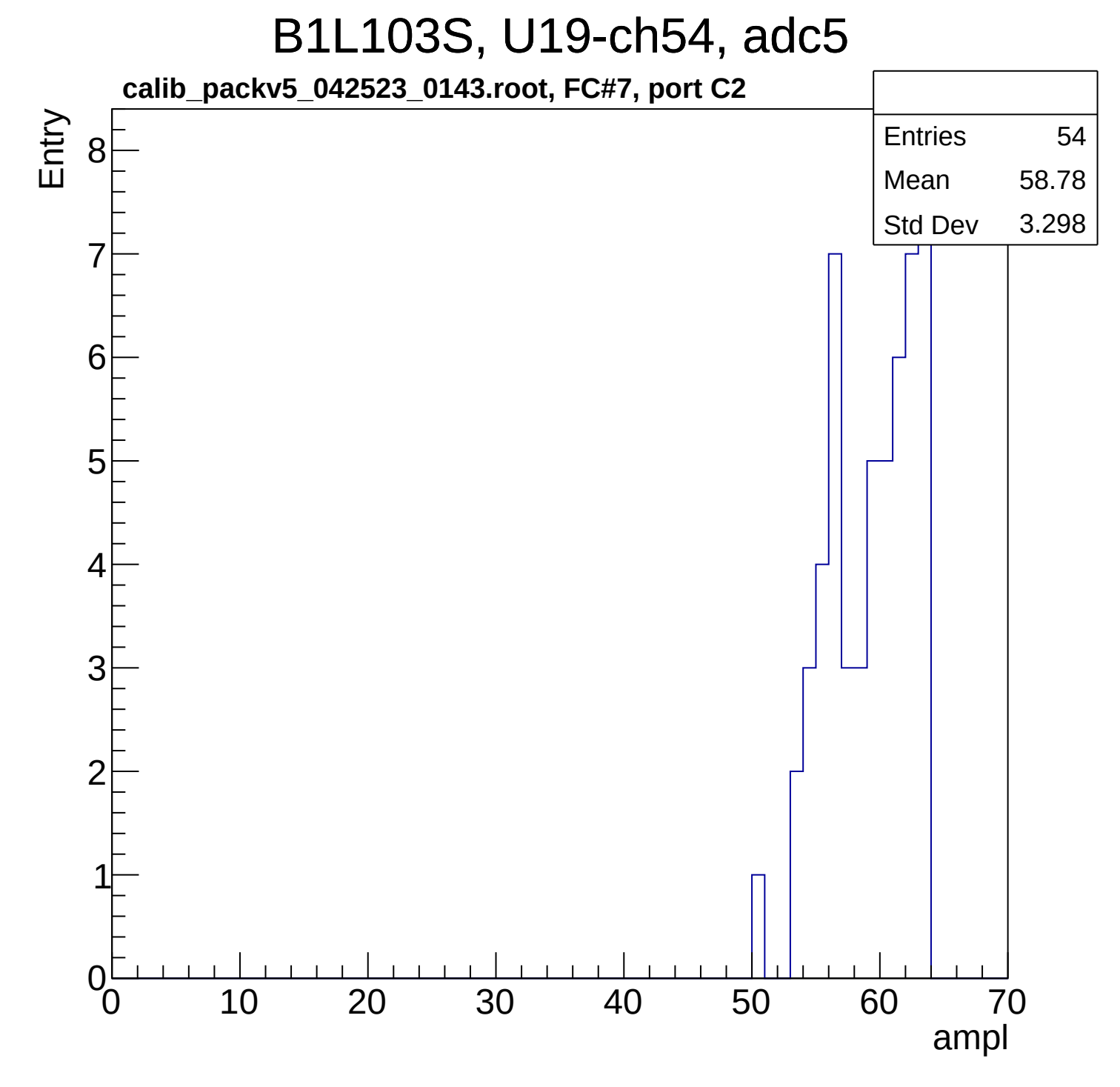
B1L103S, U19-ch54, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.78
Std Dev 3.298
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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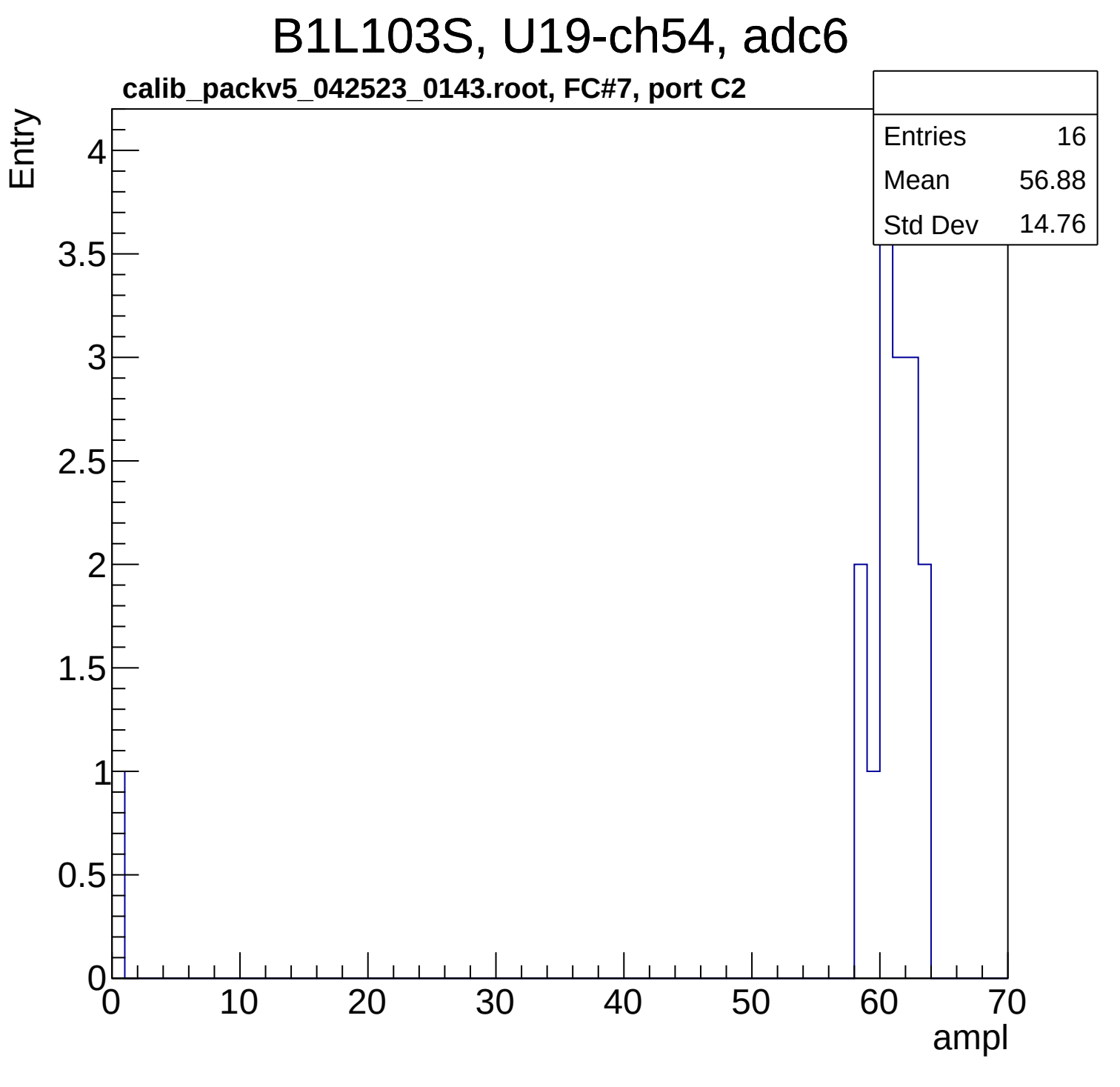
B1L103S, U19-ch54, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 56.88
Std Dev 14.76
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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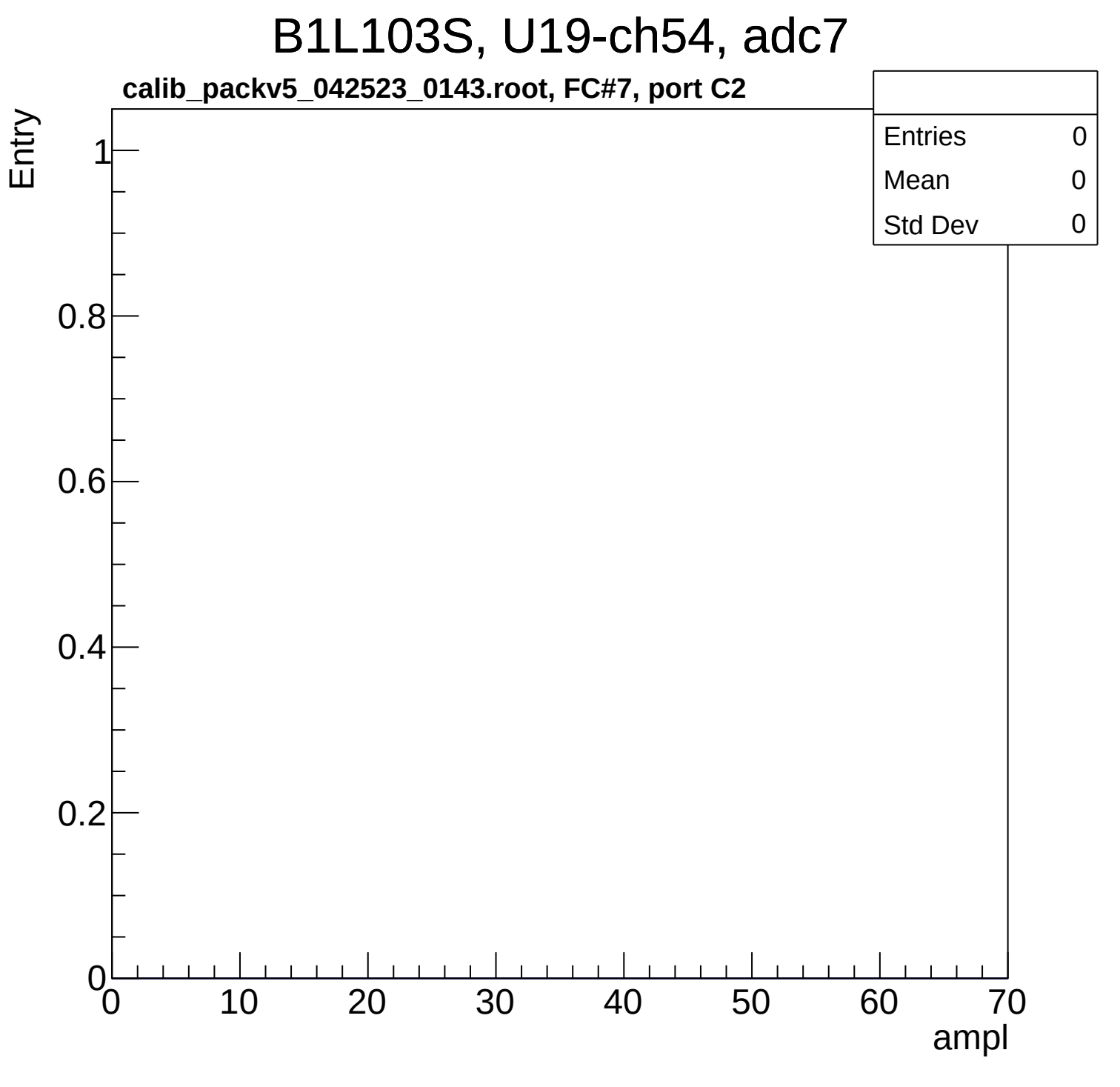
B1L103S, U19-ch54, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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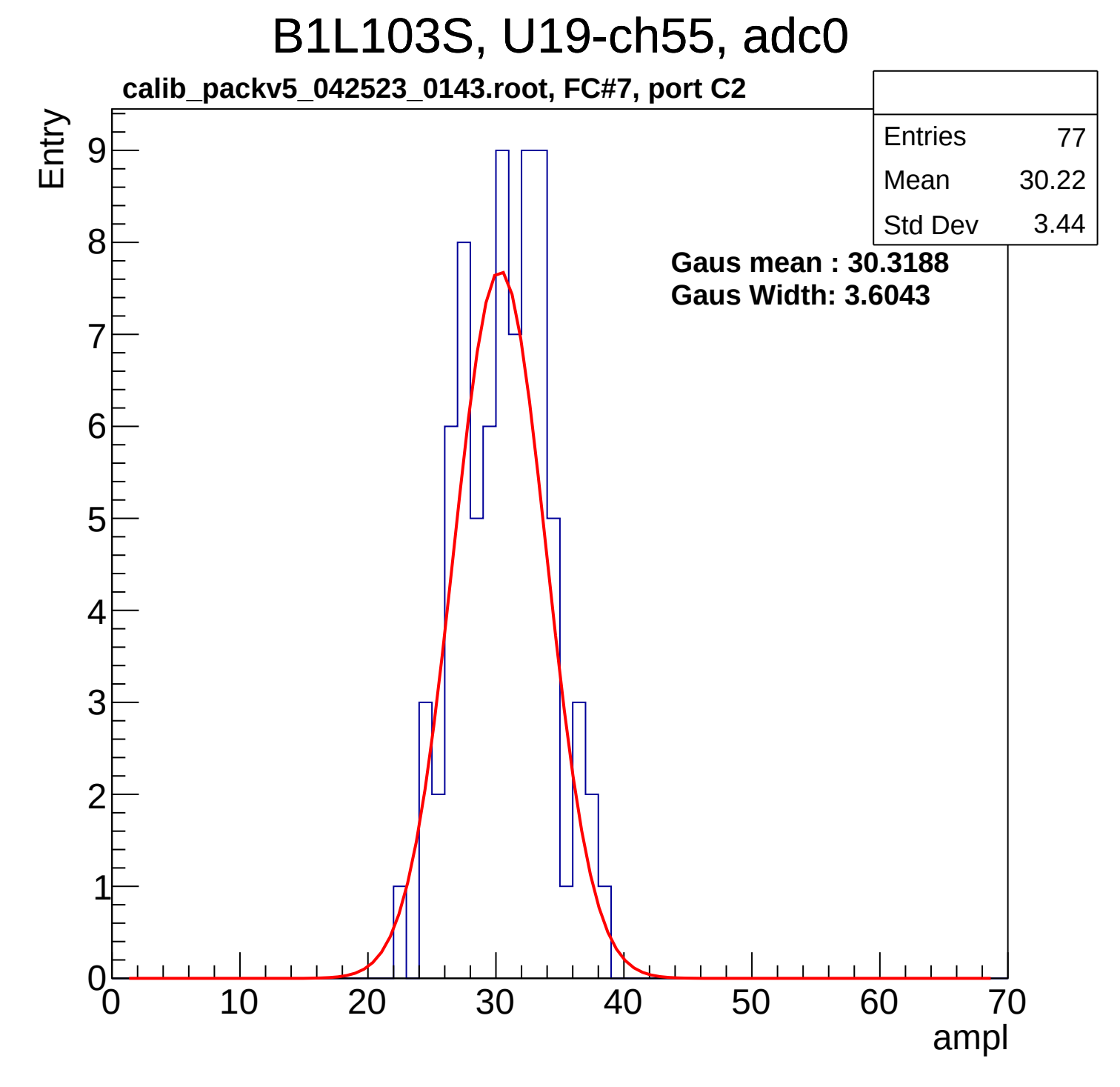
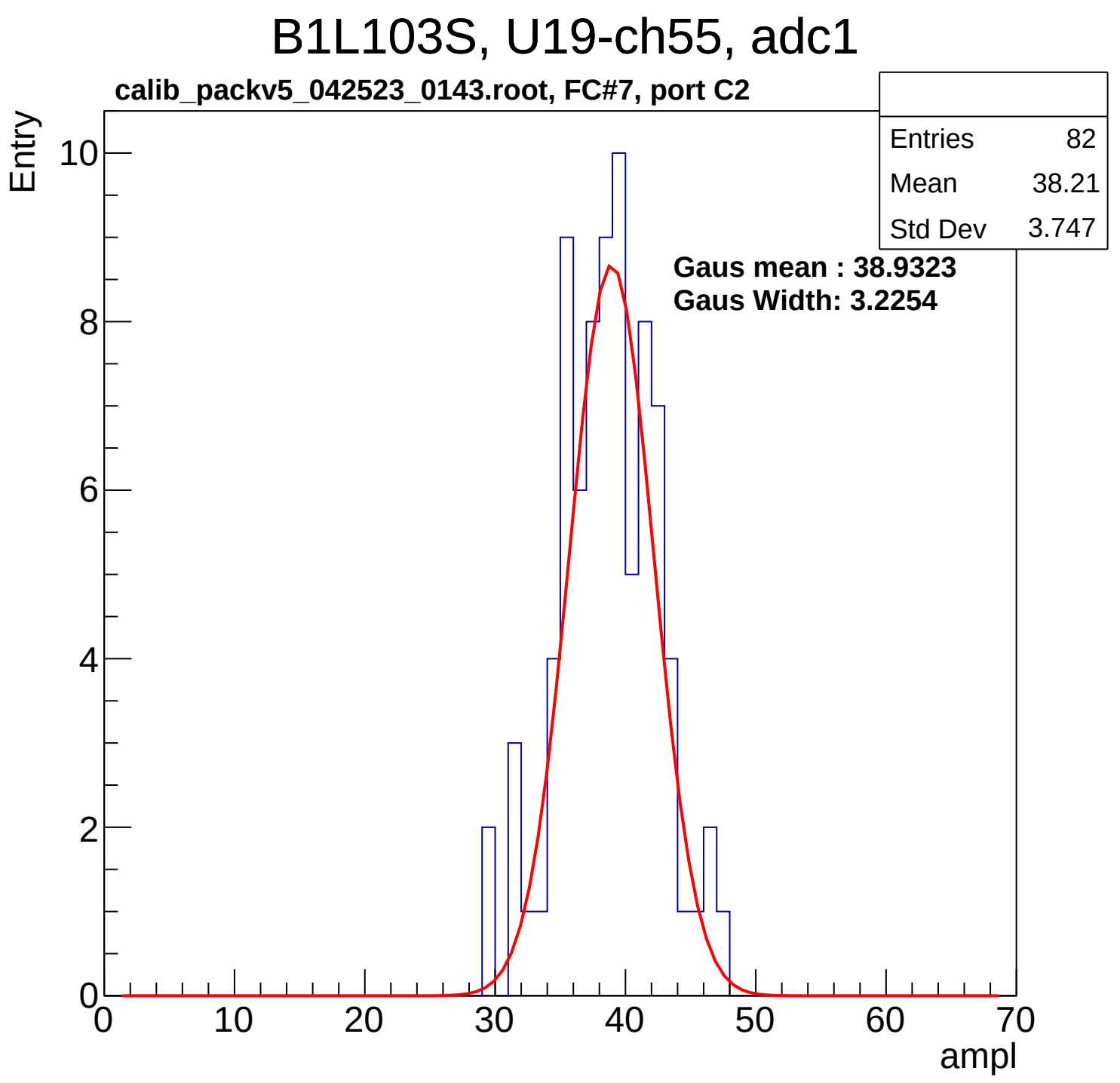
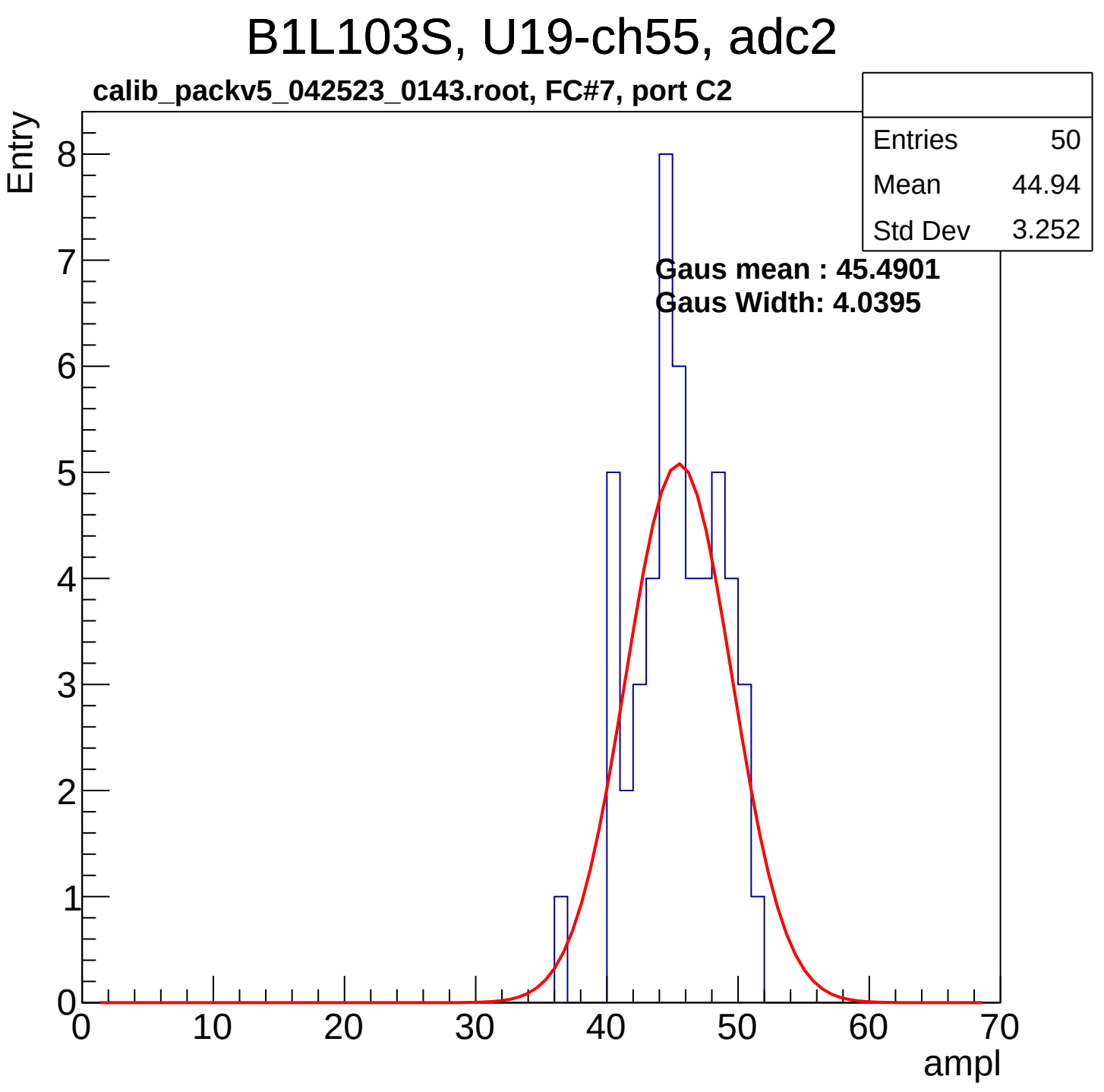
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch55, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.22
Std Dev 3.44
Gaus mean : 30.3188
Gaus Width: 3.6043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch55, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 38.21
Std Dev 3.747
Gaus mean : 38.9323
Gaus Width: 3.2254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch55, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 44.94
Std Dev 3.252
Gaus mean : 45.4901
Gaus Width: 4.0395
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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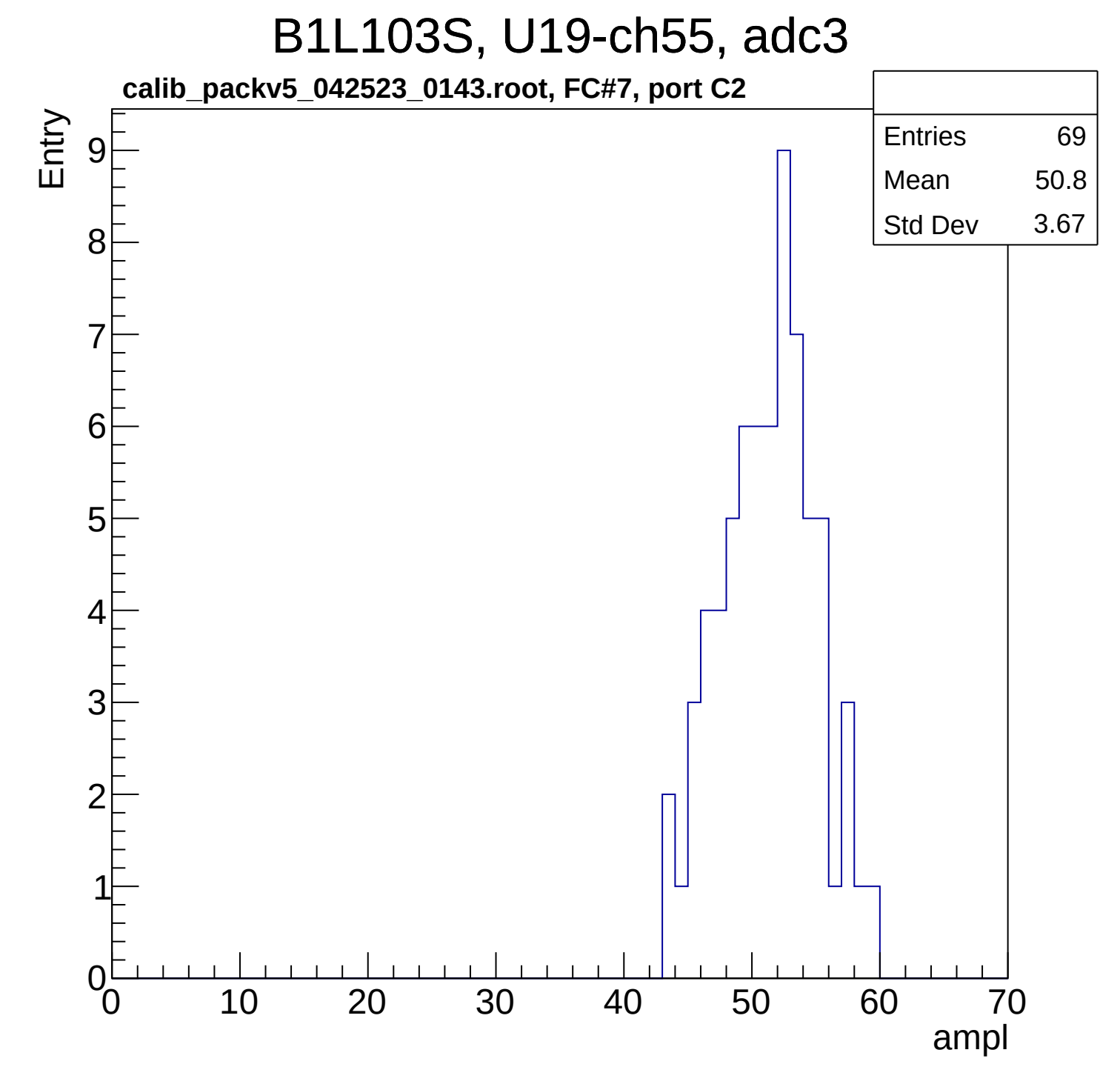
B1L103S, U19-ch55, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.8
Std Dev 3.67
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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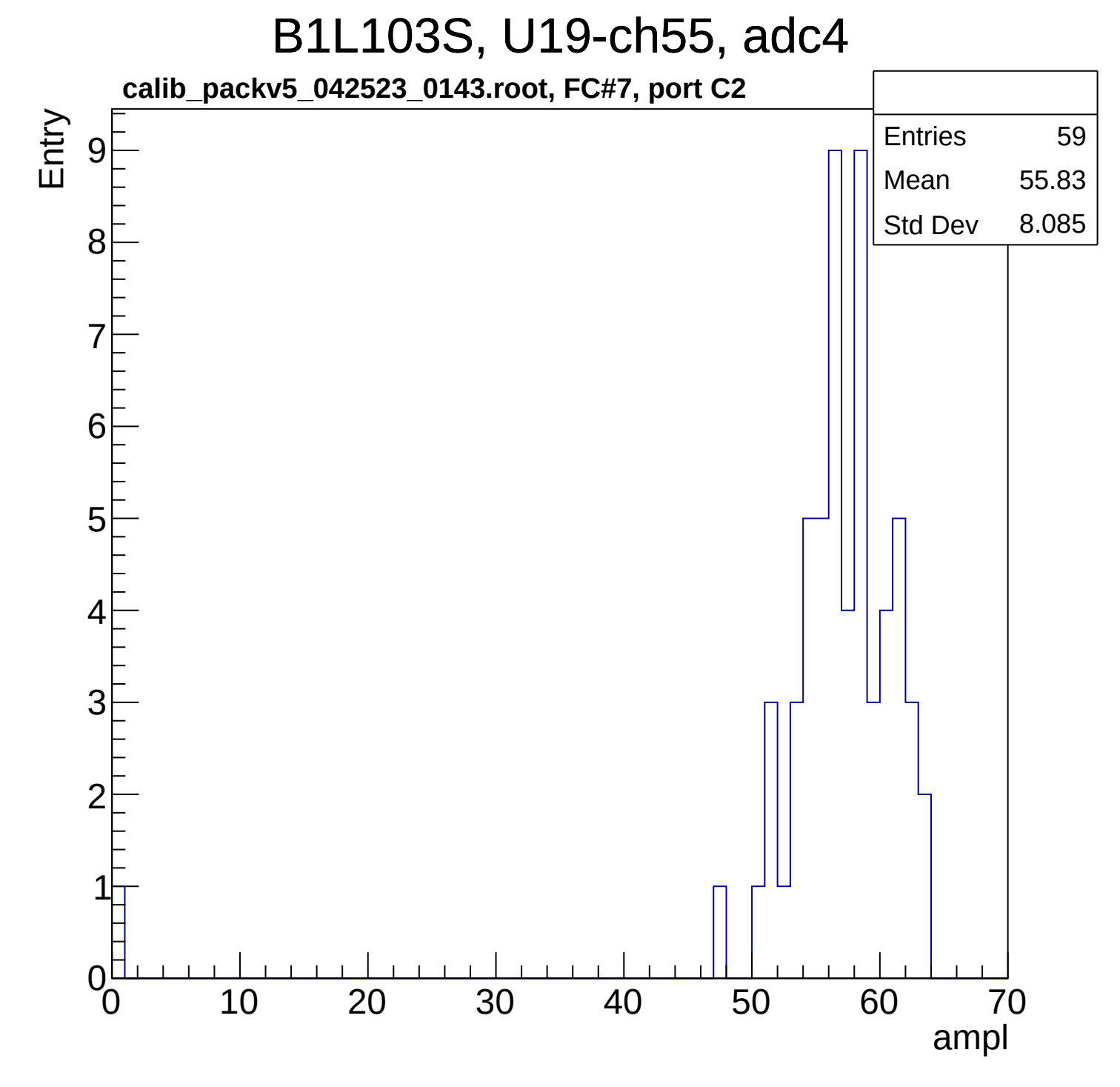
B1L103S, U19-ch55, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.83
Std Dev 8.085
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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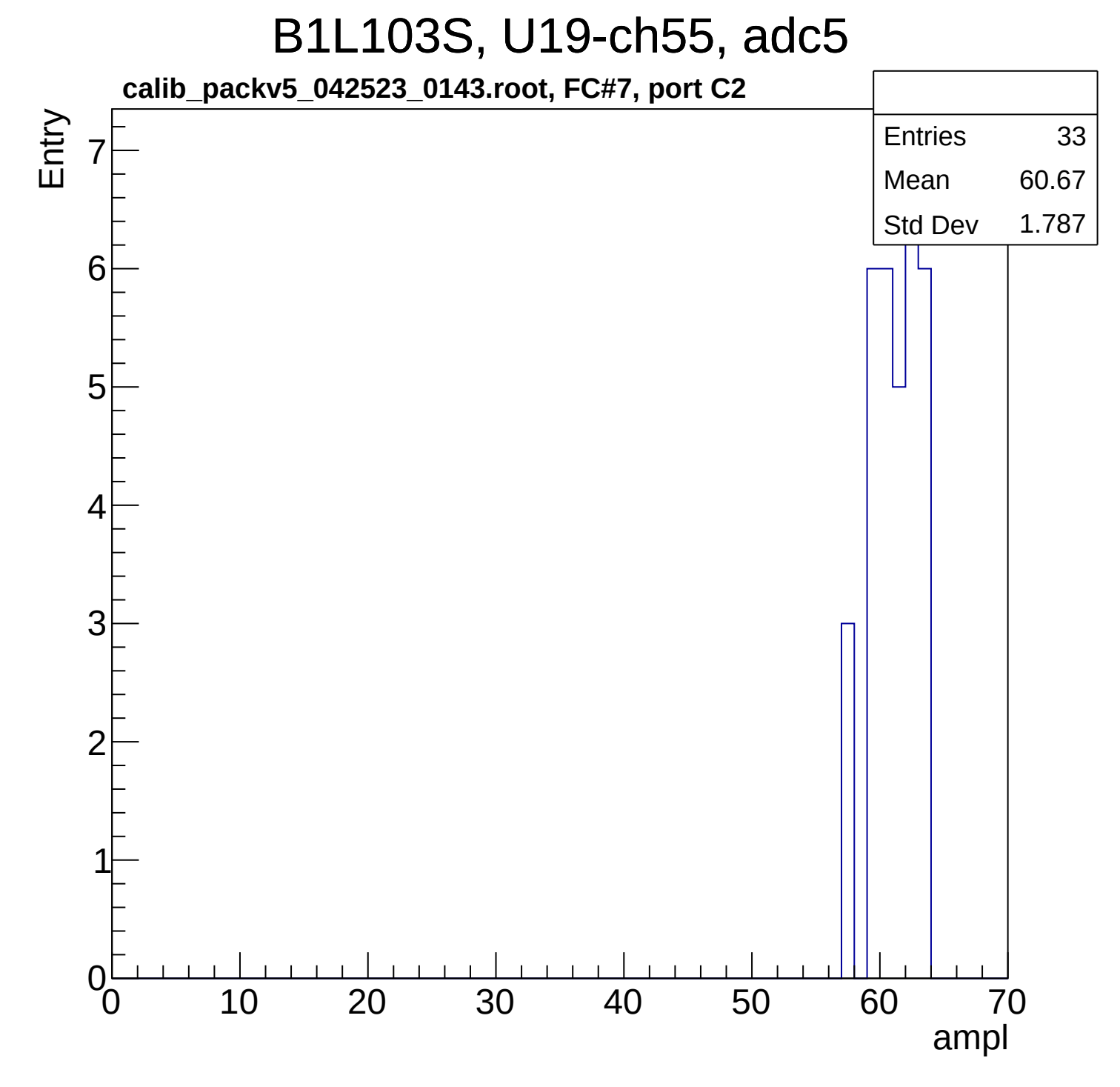
B1L103S, U19-ch55, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 60.67
Std Dev 1.787
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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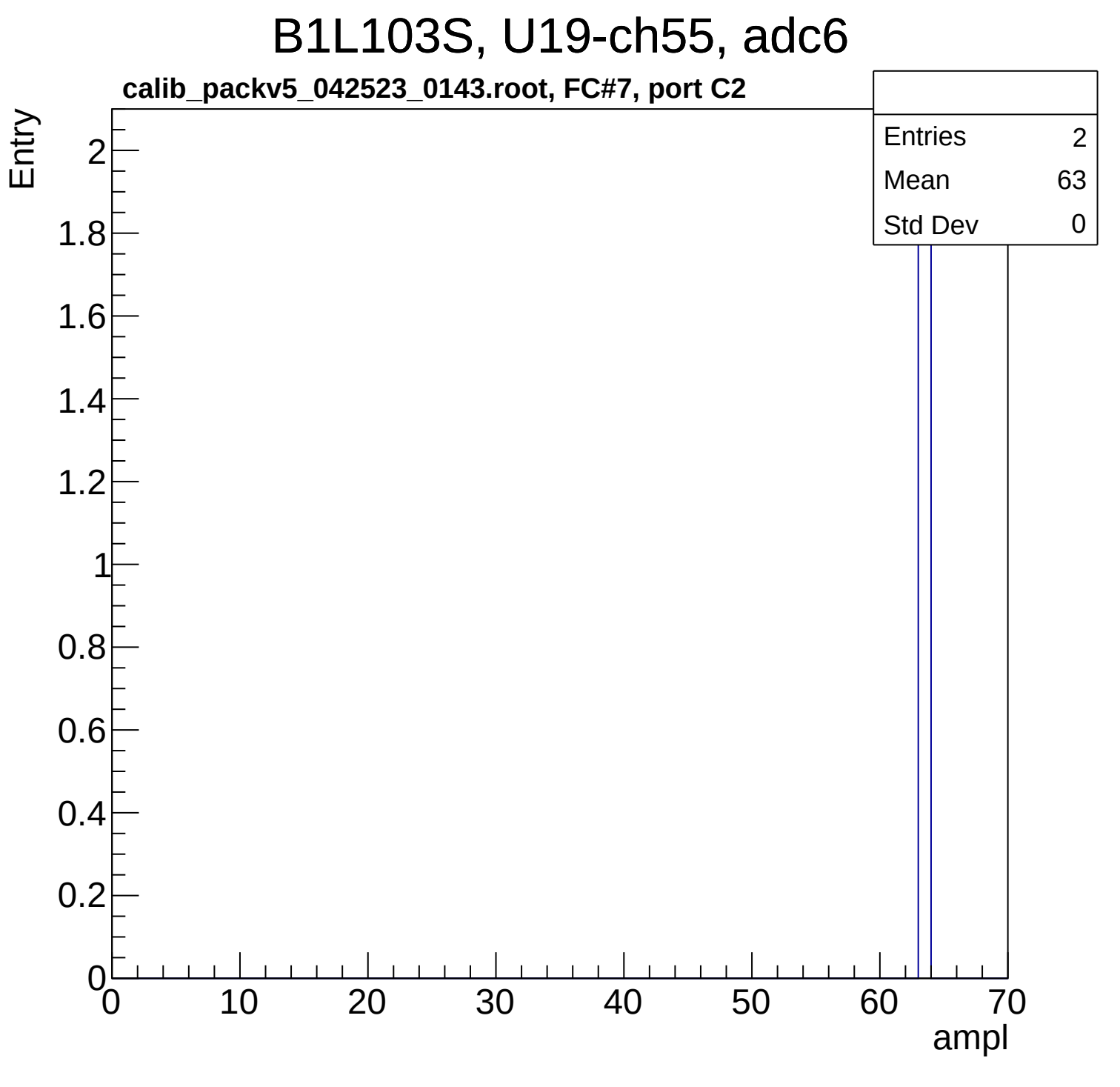
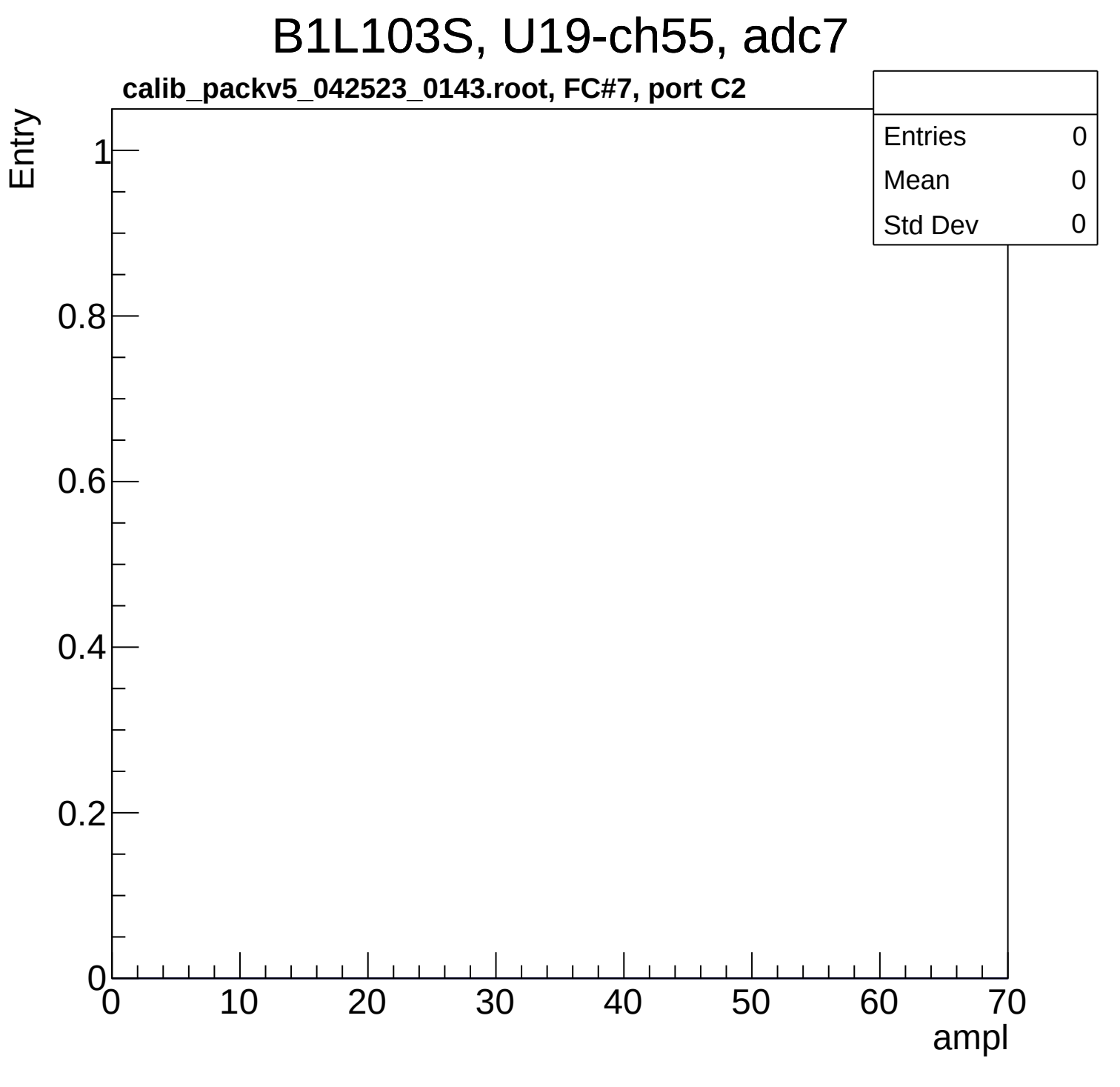
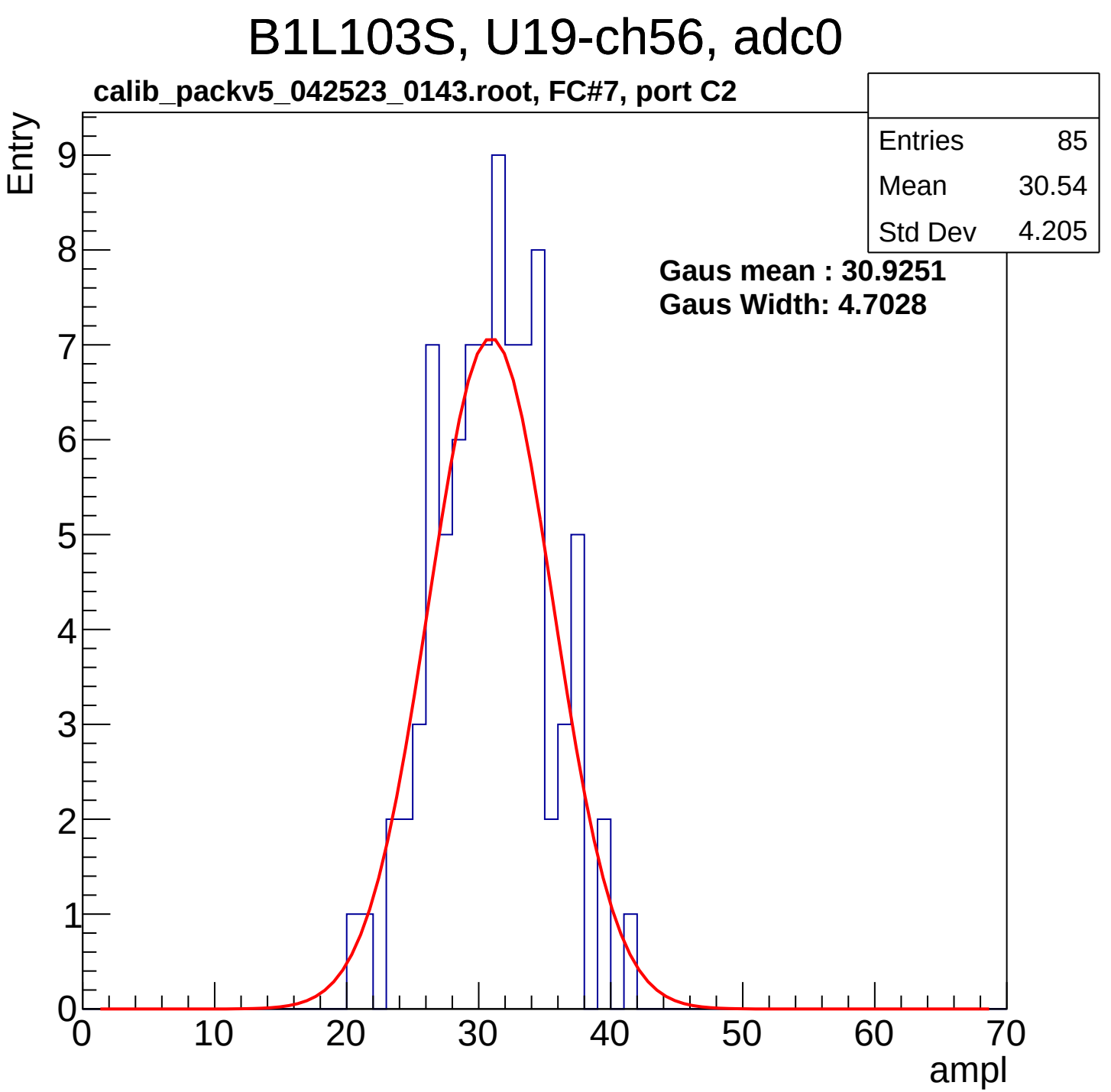

B1L103S, U19-ch55, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch55, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch56, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 30.54
Std Dev 4.205
Gaus mean : 30.9251
Gaus Width: 4.7028
Entry
ampl
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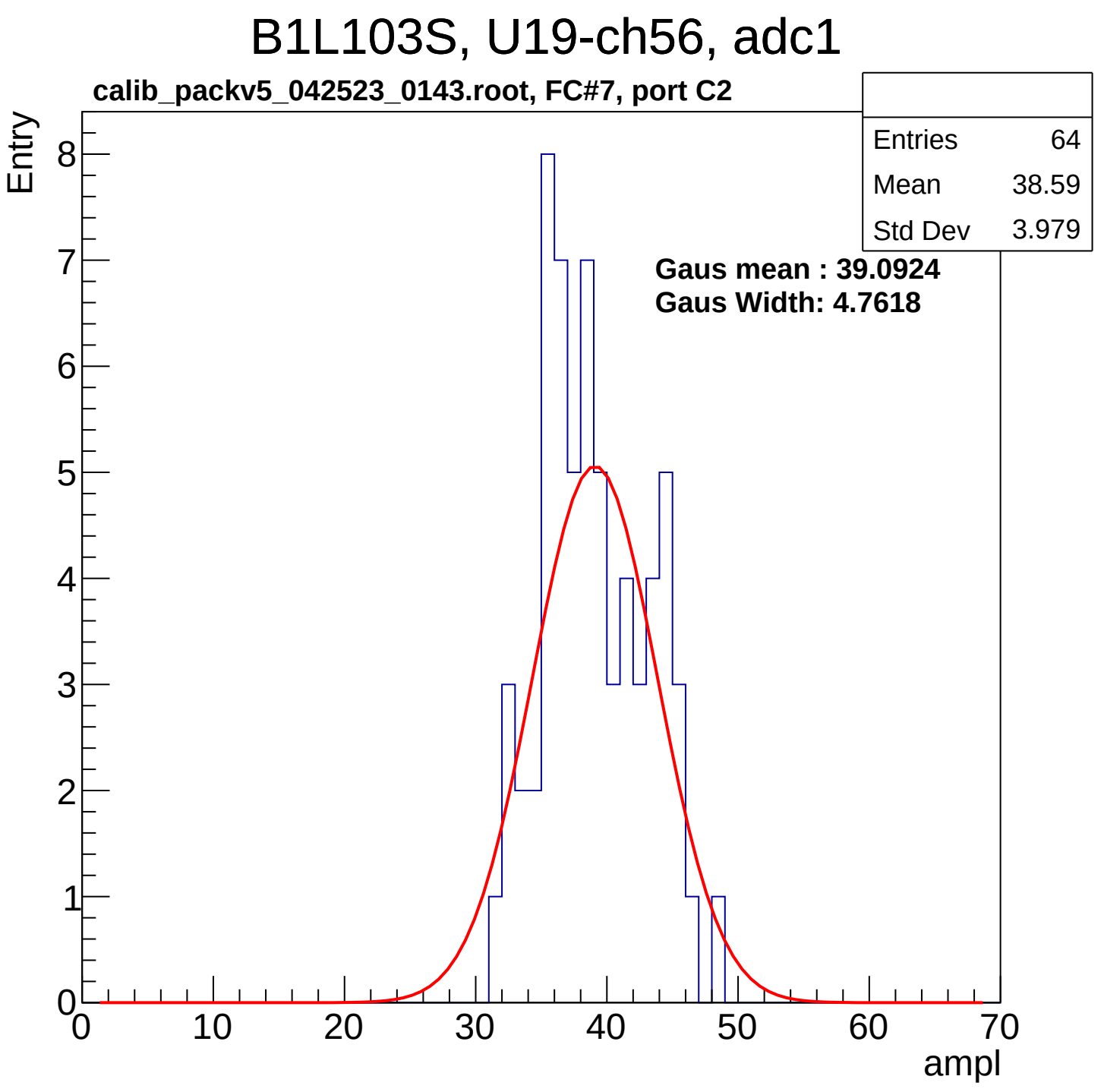
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch56, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 38.59
Std Dev 3.979
Gaus mean : 39.0924
Gaus Width: 4.7618
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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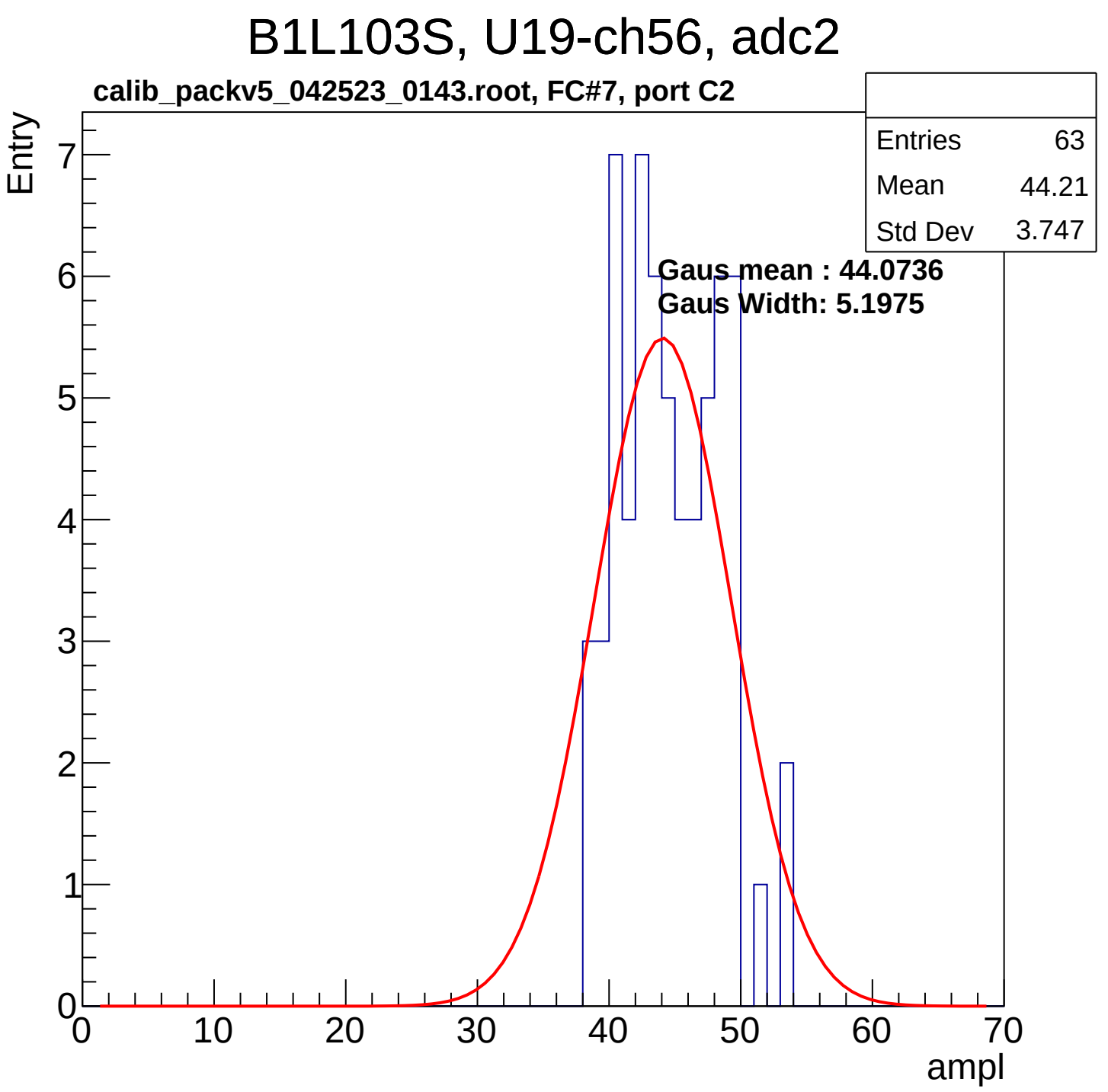
40
50
60
70


B1L103S, U19-ch56, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 44.21
Std Dev 3.747
Gaus mean : 44.0736
Gaus Width: 5.1975
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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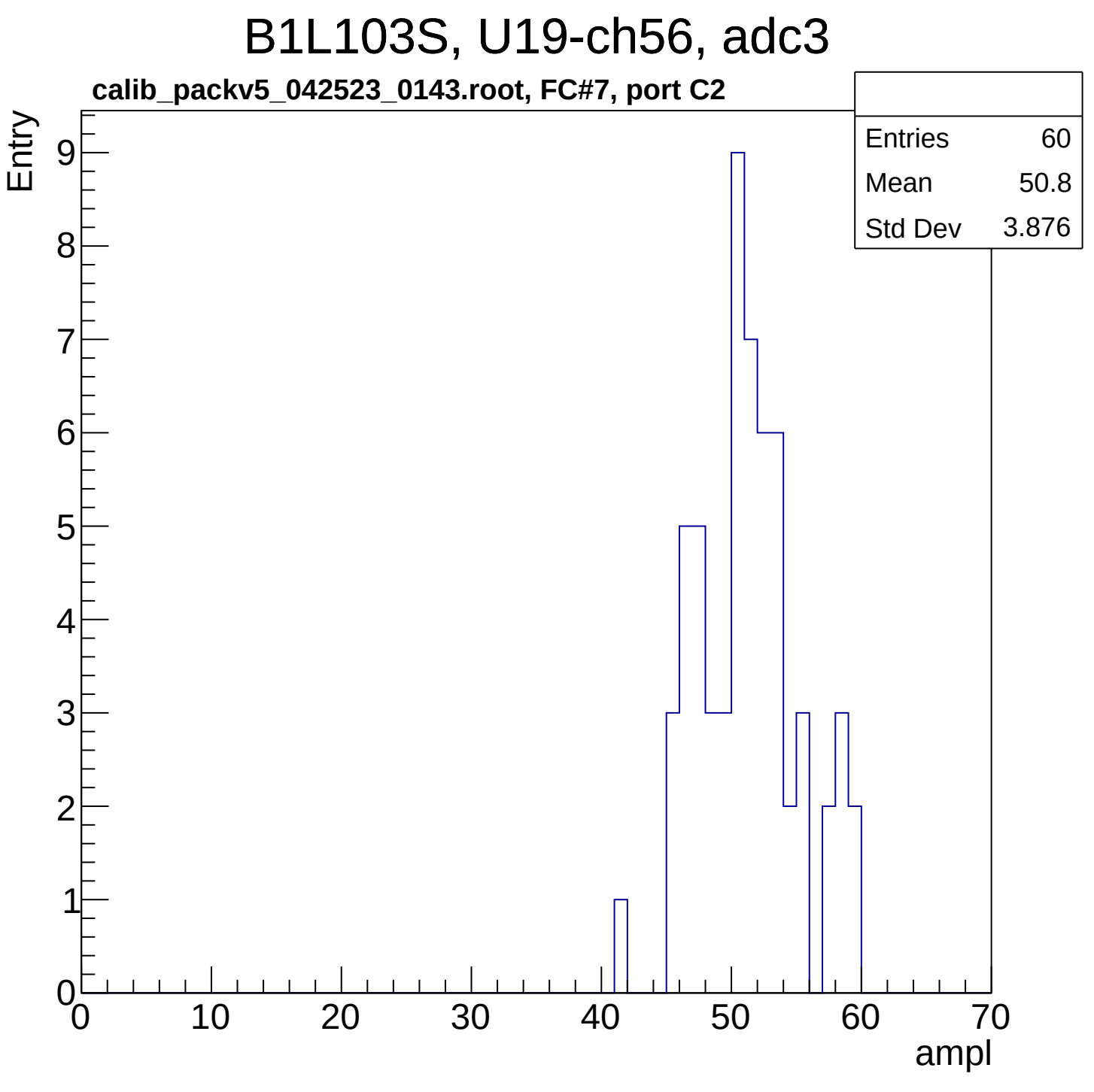
B1L103S, U19-ch56, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.8
Std Dev 3.876
Entry
ampl
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0
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60
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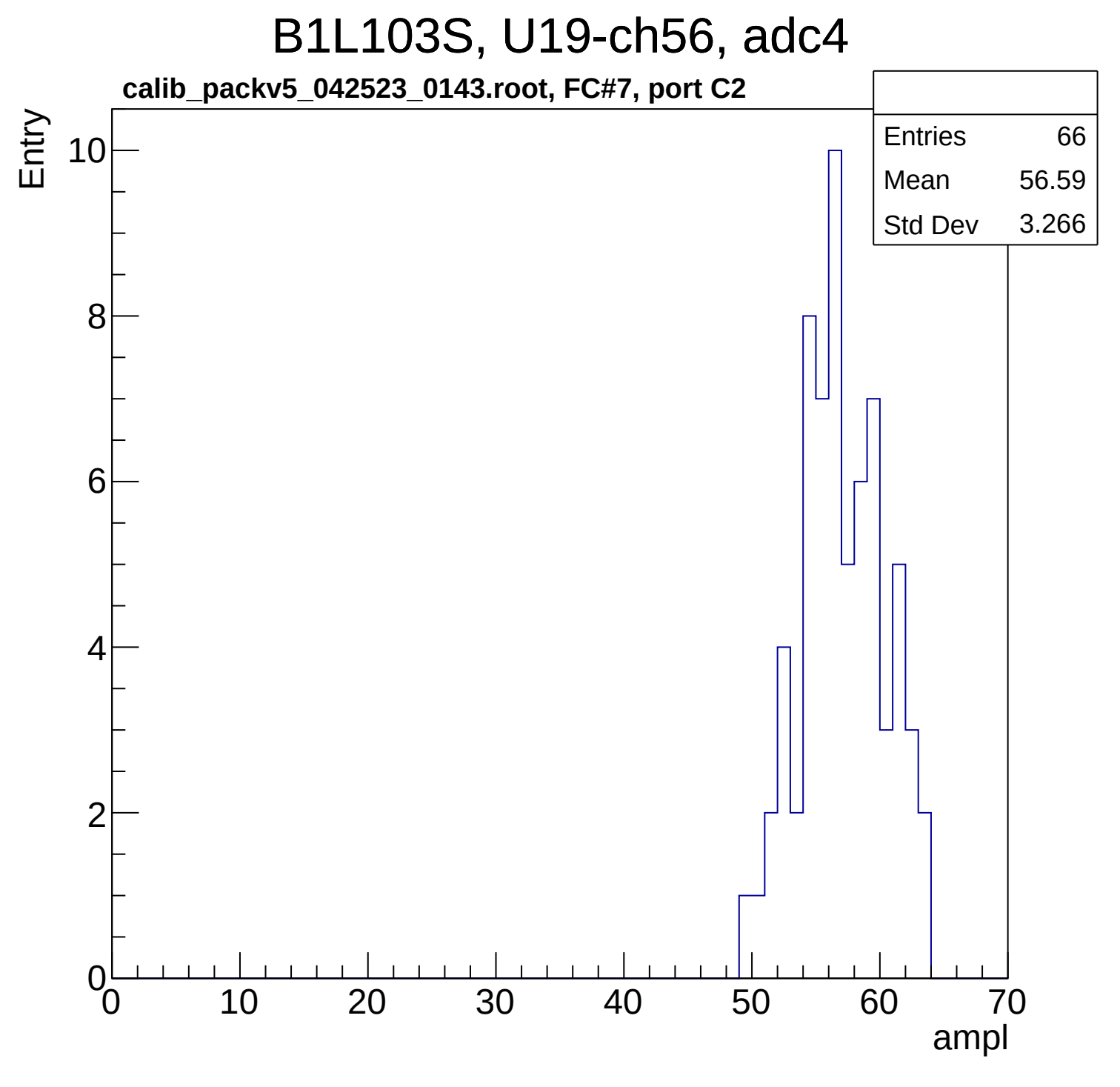
B1L103S, U19-ch56, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 56.59
Std Dev 3.266
Entry
ampl
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2
4
6
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0
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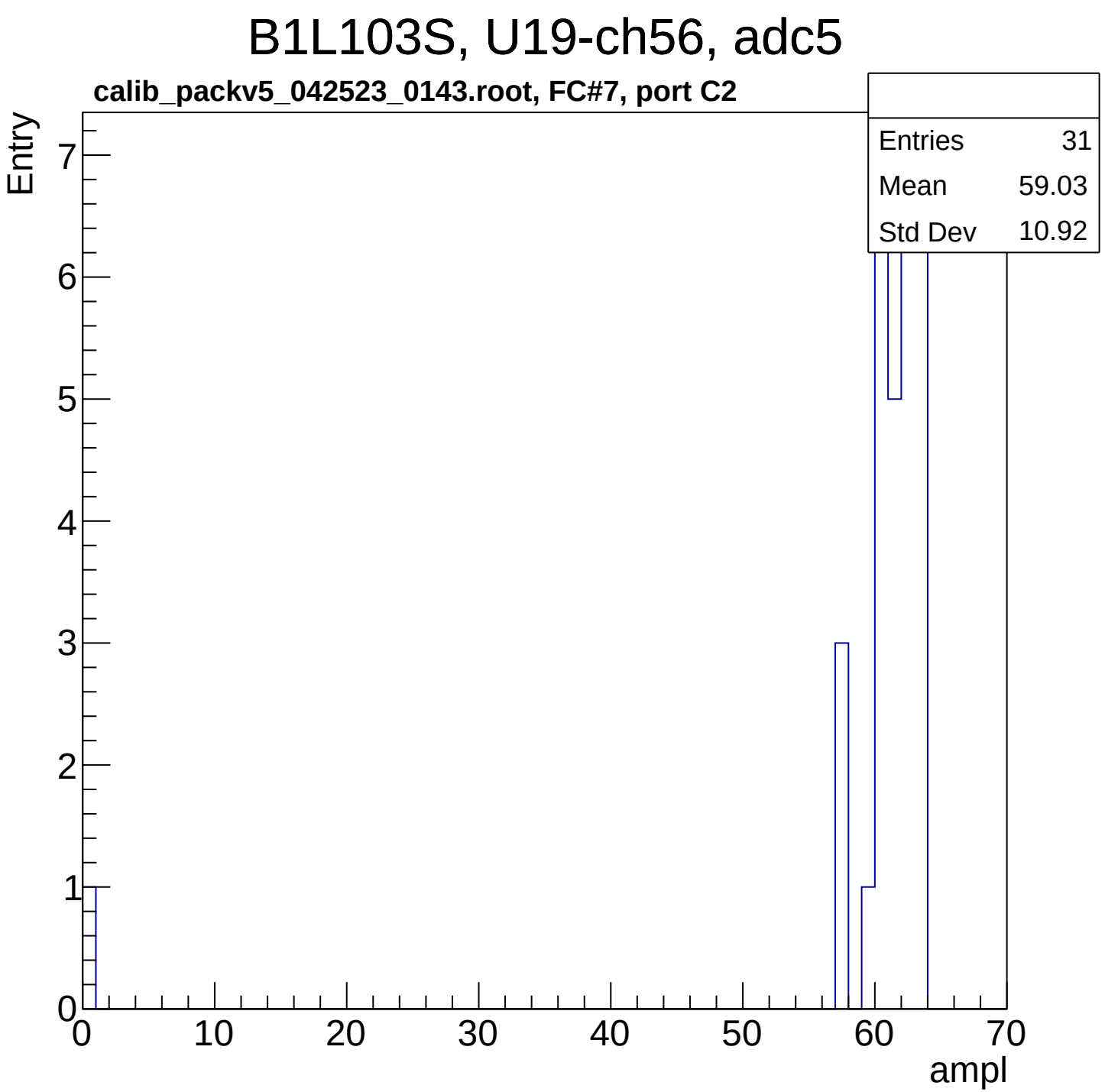
40
50
60
70


B1L103S, U19-ch56, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 59.03
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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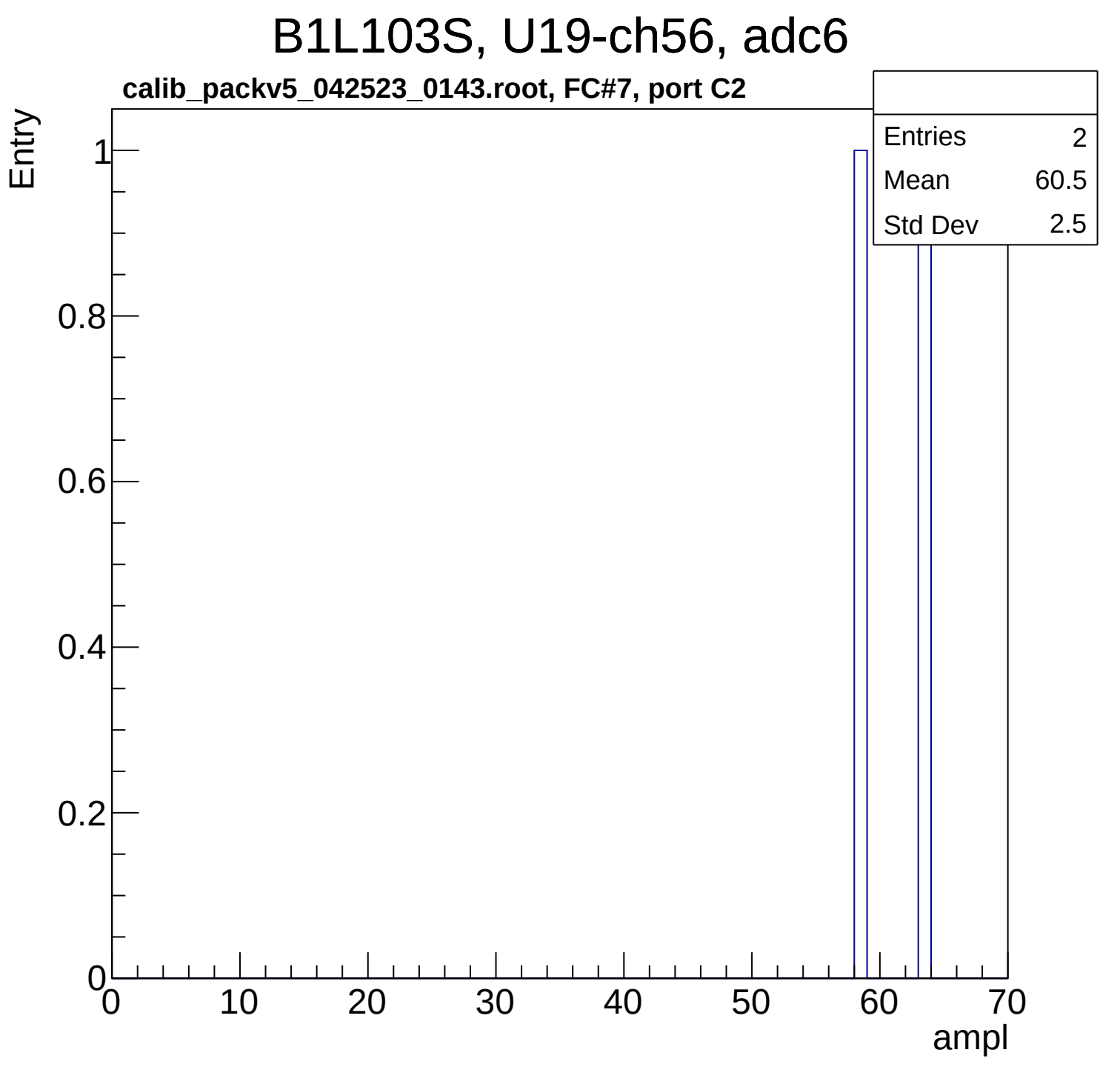
B1L103S, U19-ch56, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 60.5
Std Dev 2.5
Entry
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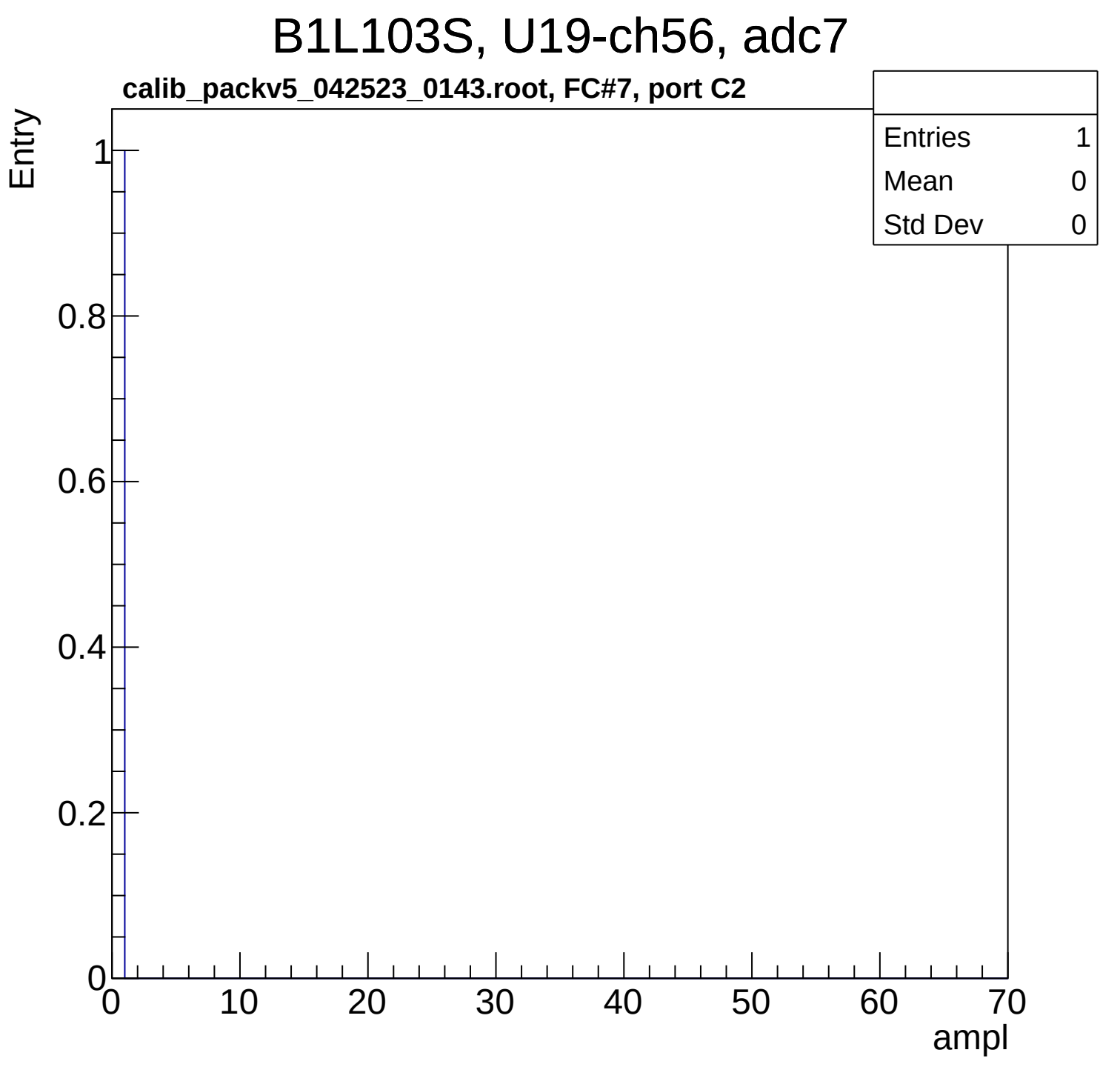
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch56, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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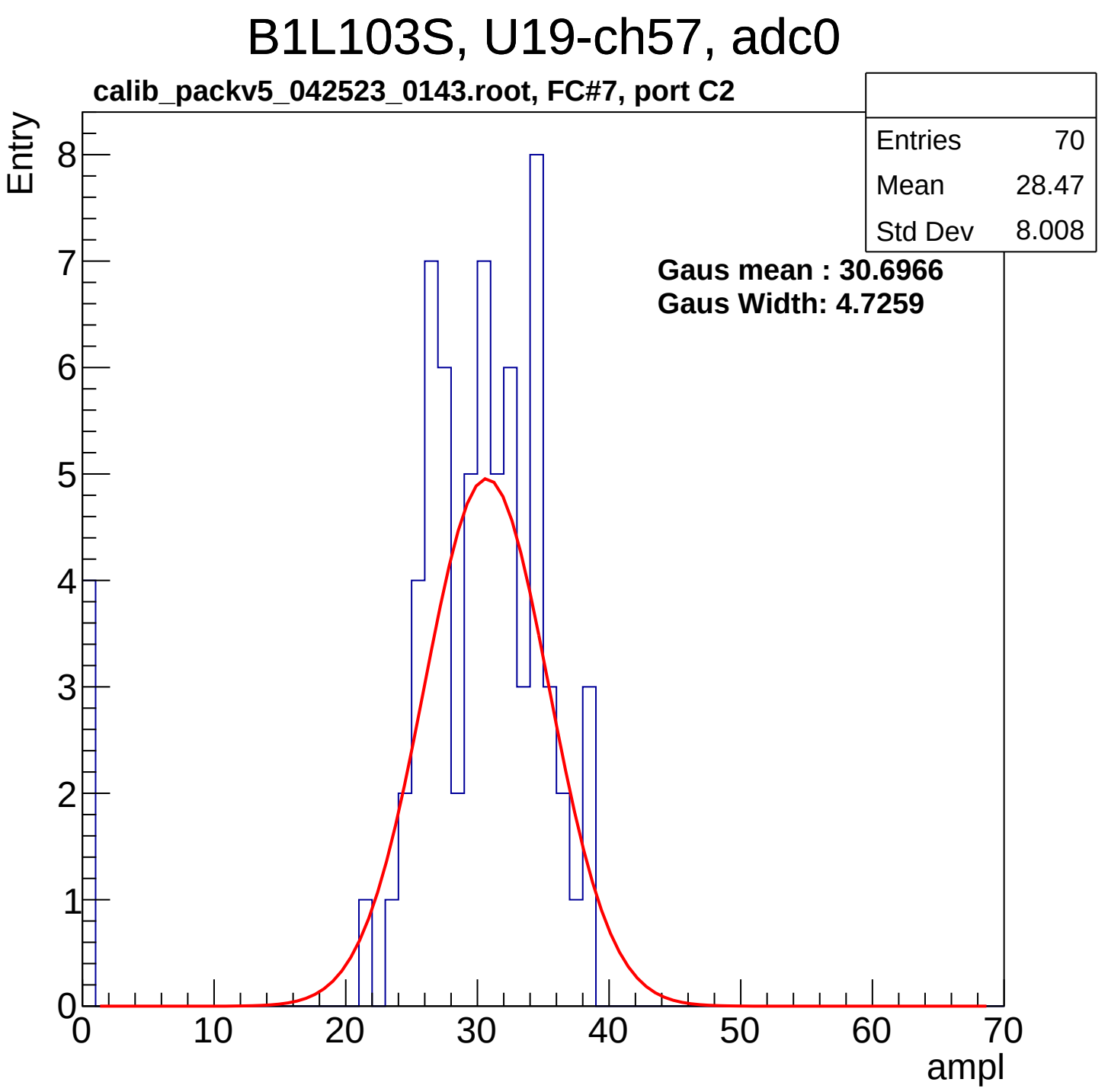
50
60
70
ampl


B1L103S, U19-ch57, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 28.47
Std Dev 8.008
Gaus mean : 30.6966
Gaus Width: 4.7259
Entry
ampl
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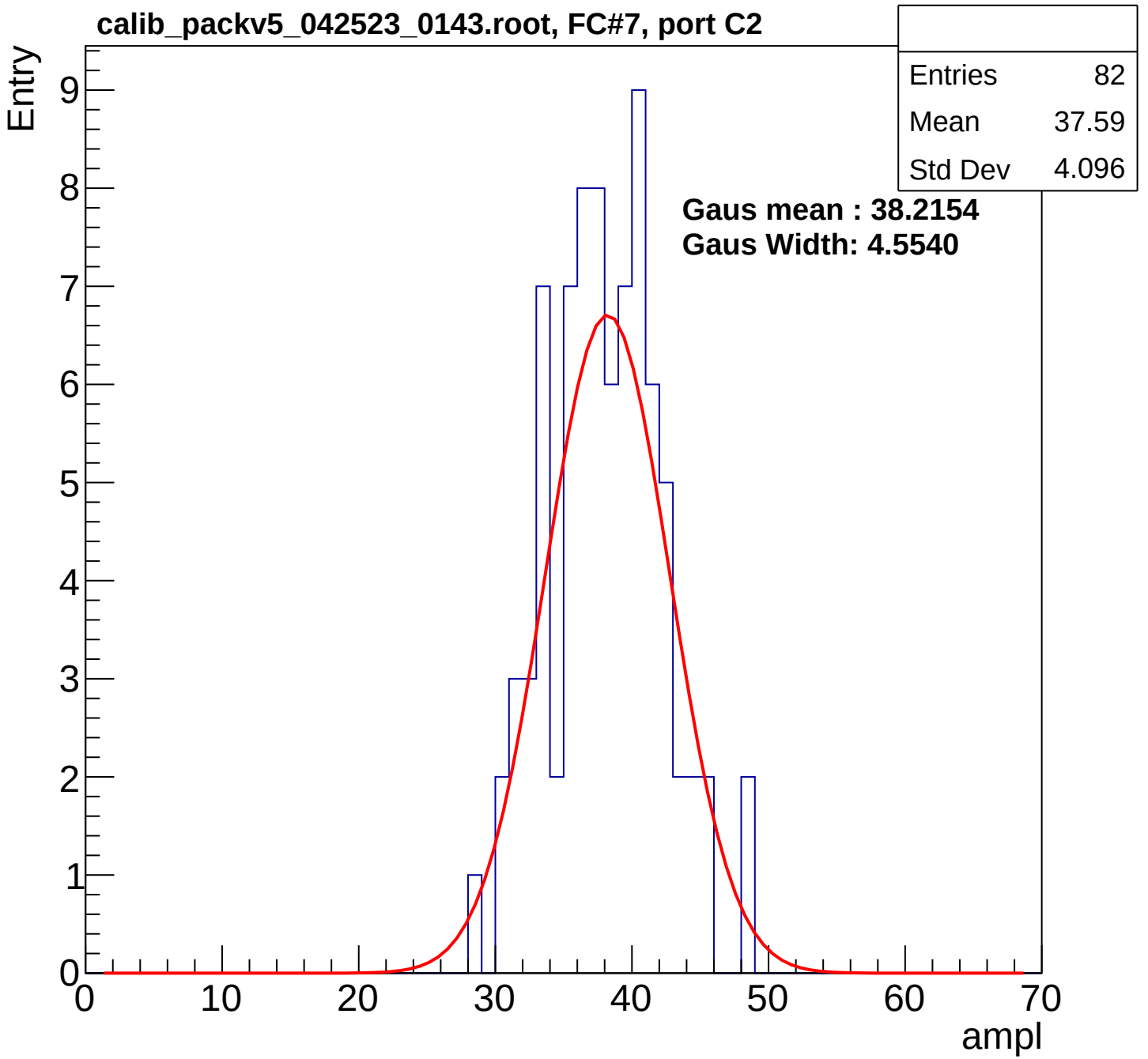
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch57, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 37.59
Std Dev 4.096
Gaus mean : 38.2154
Gaus Width: 4.5540
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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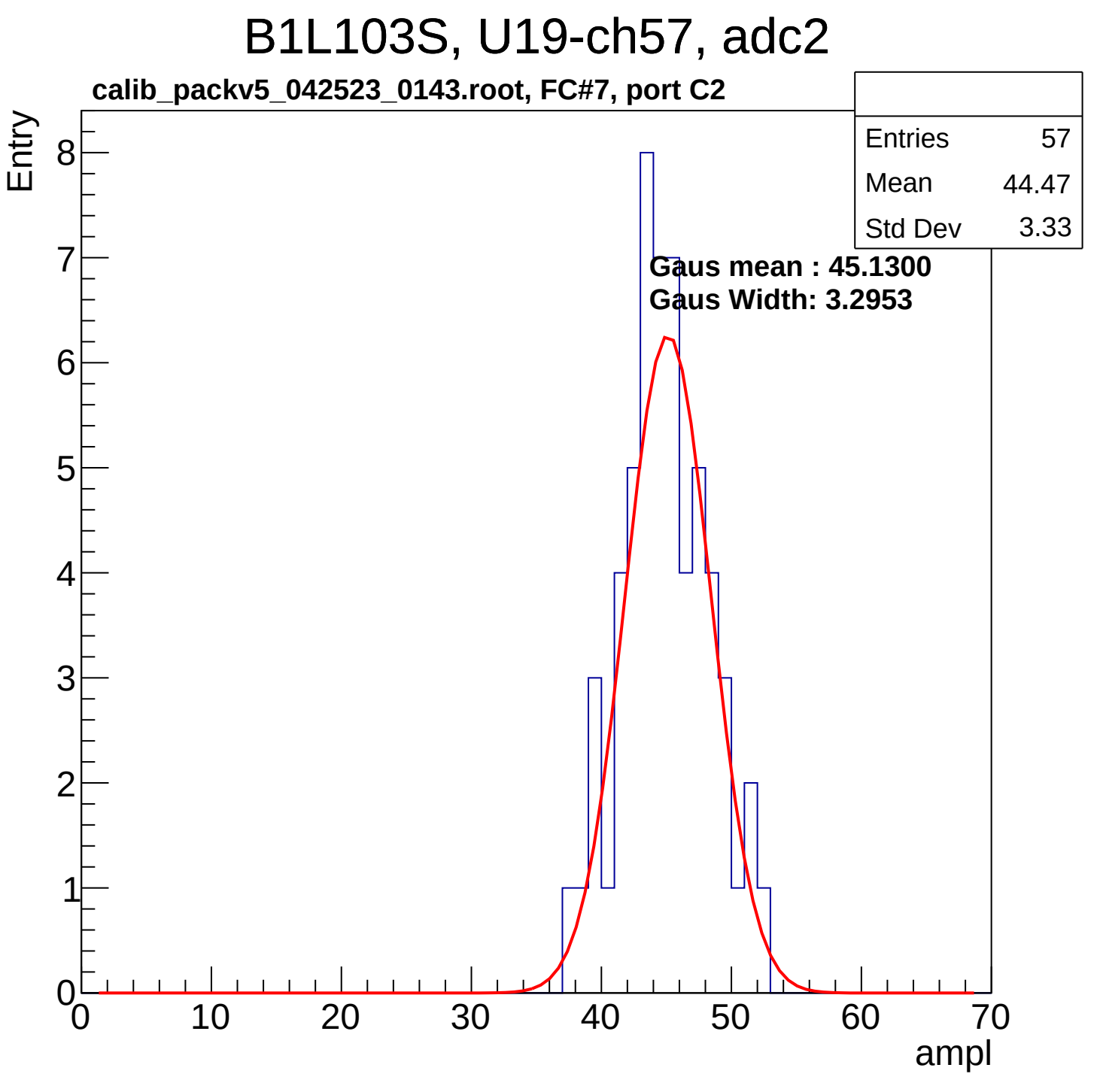
B1L103S, U19-ch57, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 44.47
Std Dev 3.33
Gaus mean : 45.1300
Gaus Width: 3.2953
Entry
ampl
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2
3
4
5
6
7
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0
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60
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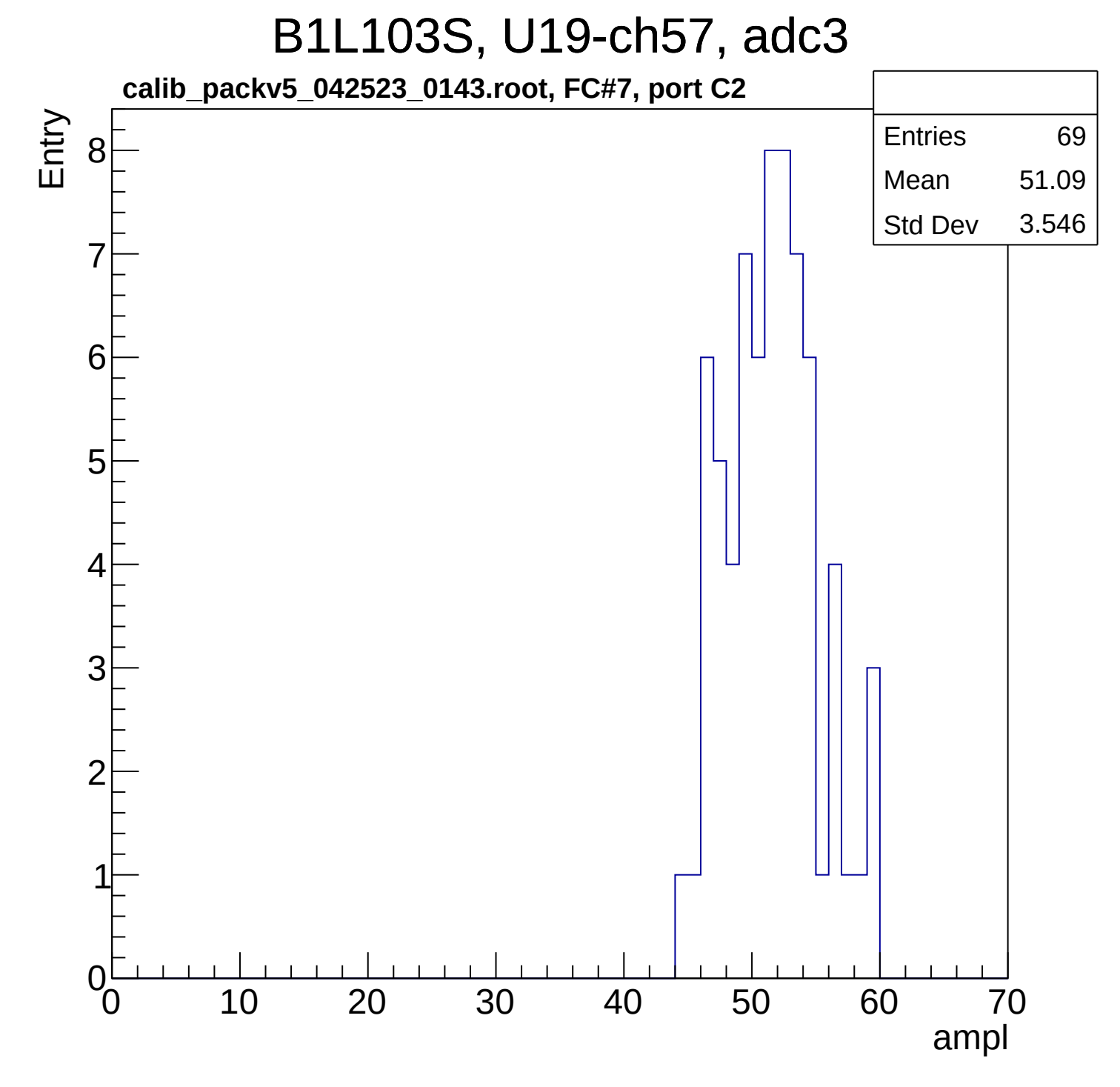
B1L103S, U19-ch57, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 51.09
Std Dev 3.546
Entry
ampl
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2
3
4
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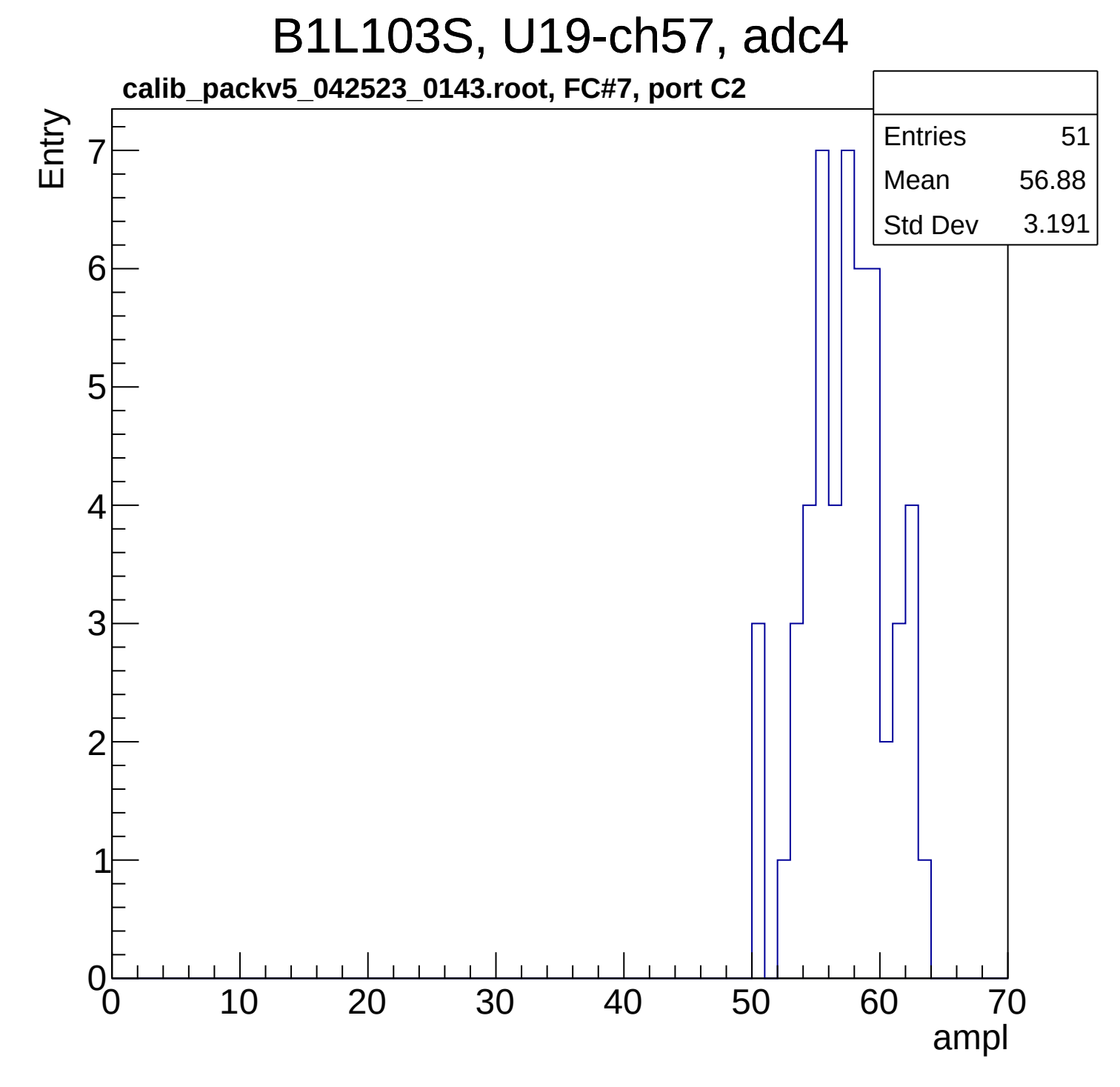
B1L103S, U19-ch57, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 56.88
Std Dev 3.191
Entry
ampl
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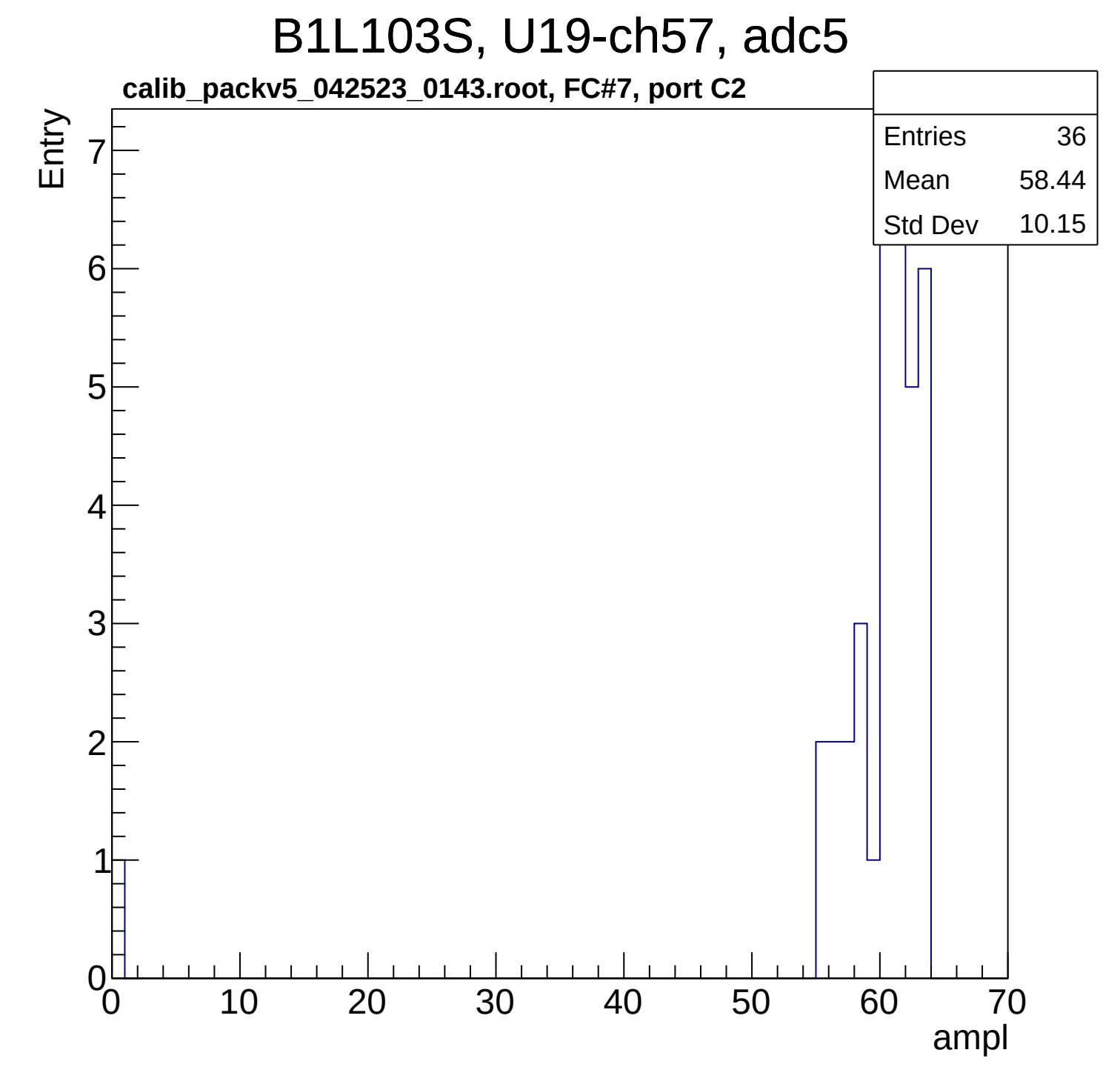
B1L103S, U19-ch57, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.44
Std Dev 10.15
Entry
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3
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5
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10
20
30
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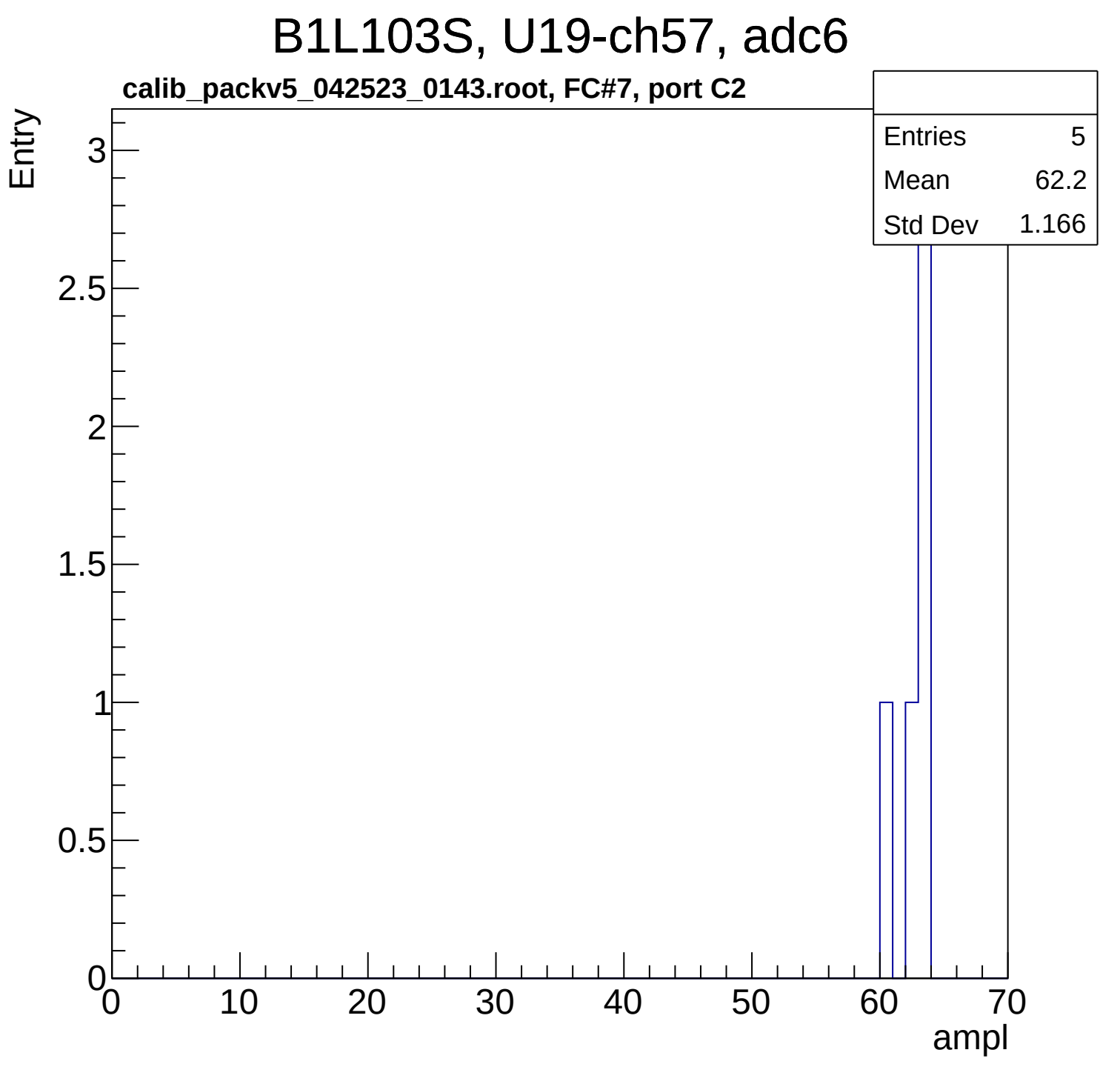
50
60
70
ampl


B1L103S, U19-ch57, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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60
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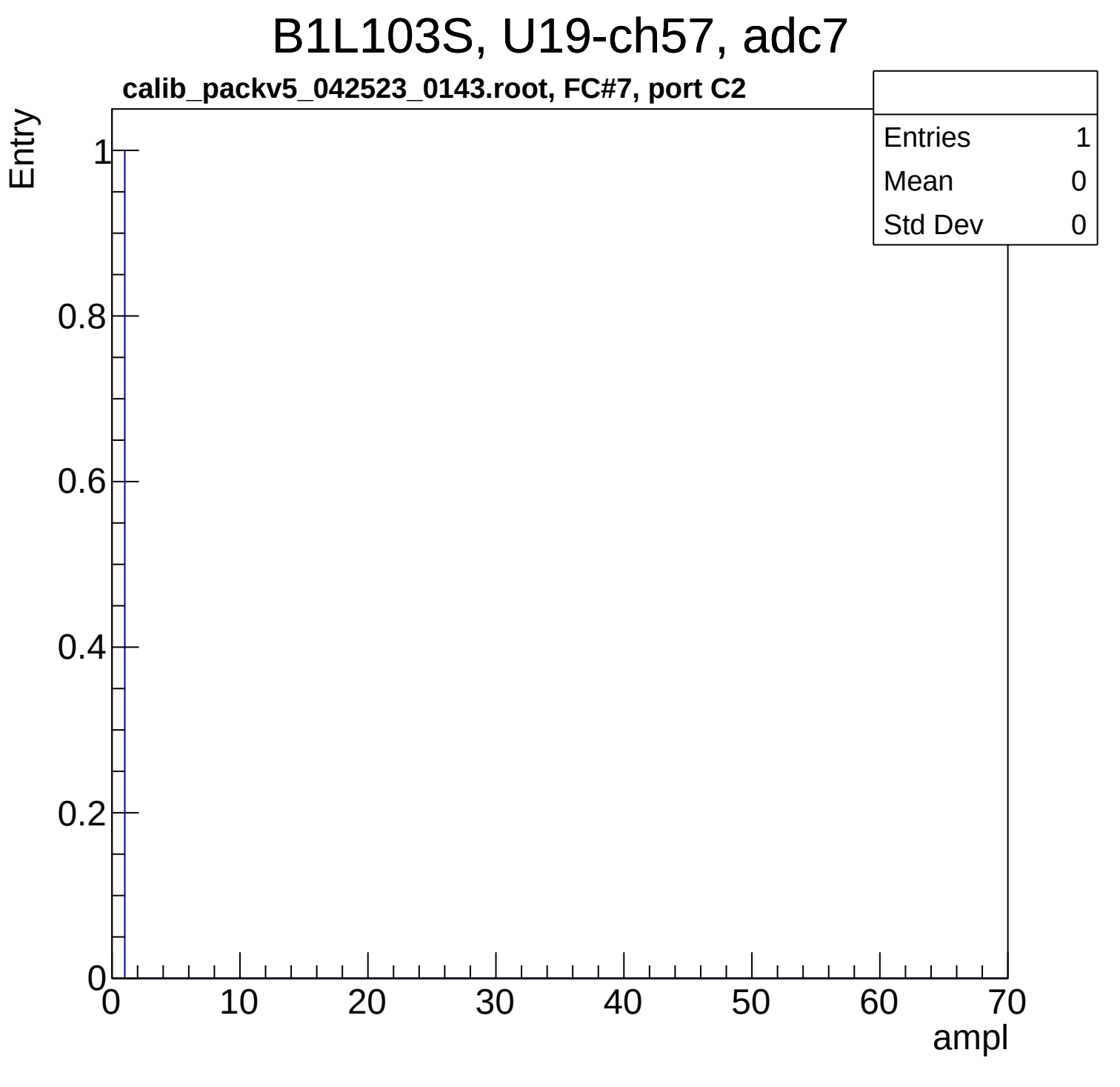
B1L103S, U19-ch57, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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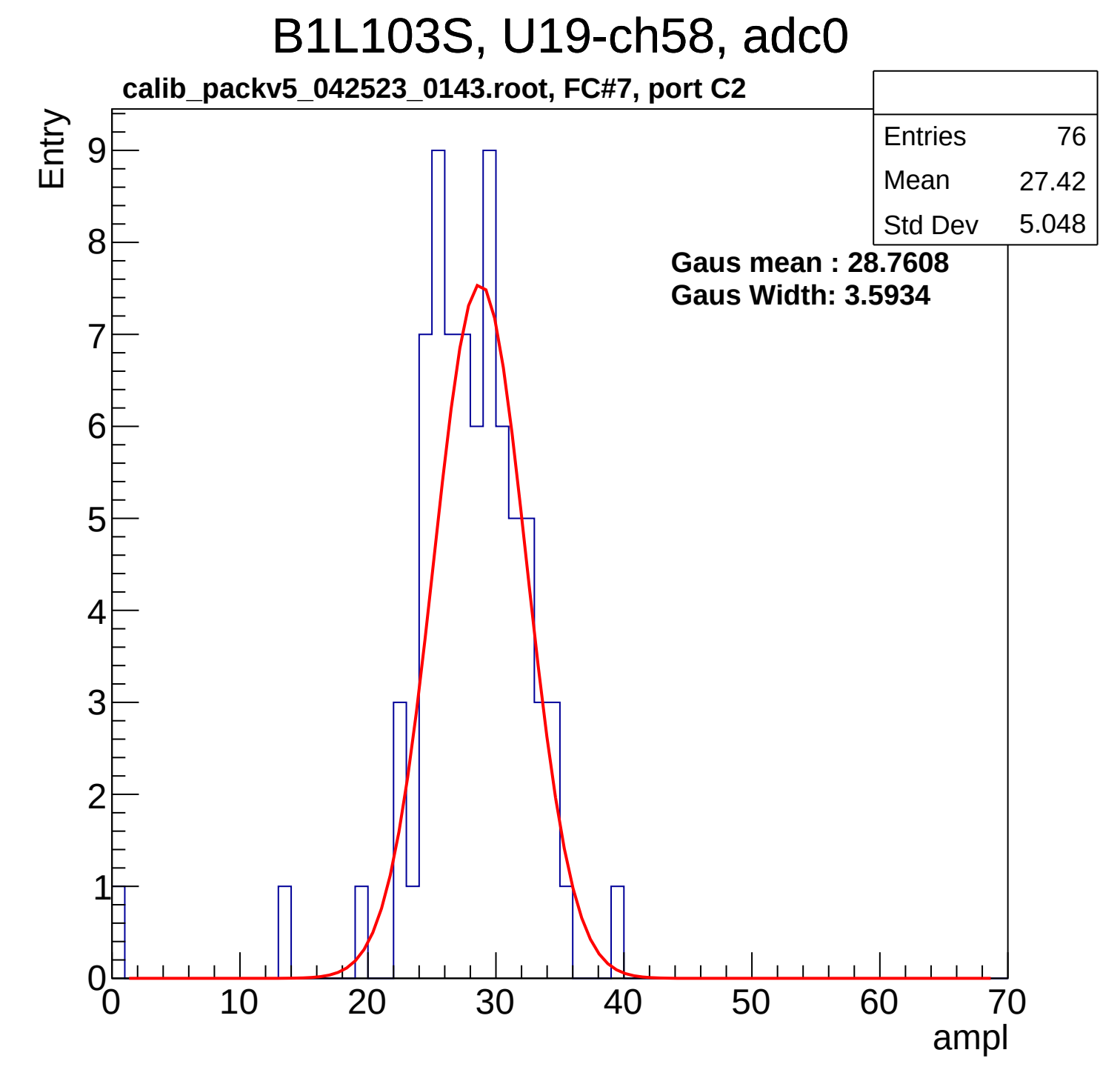
40
50
60
70
ampl


B1L103S, U19-ch58, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 27.42
Std Dev 5.048
Gaus mean : 28.7608
Gaus Width: 3.5934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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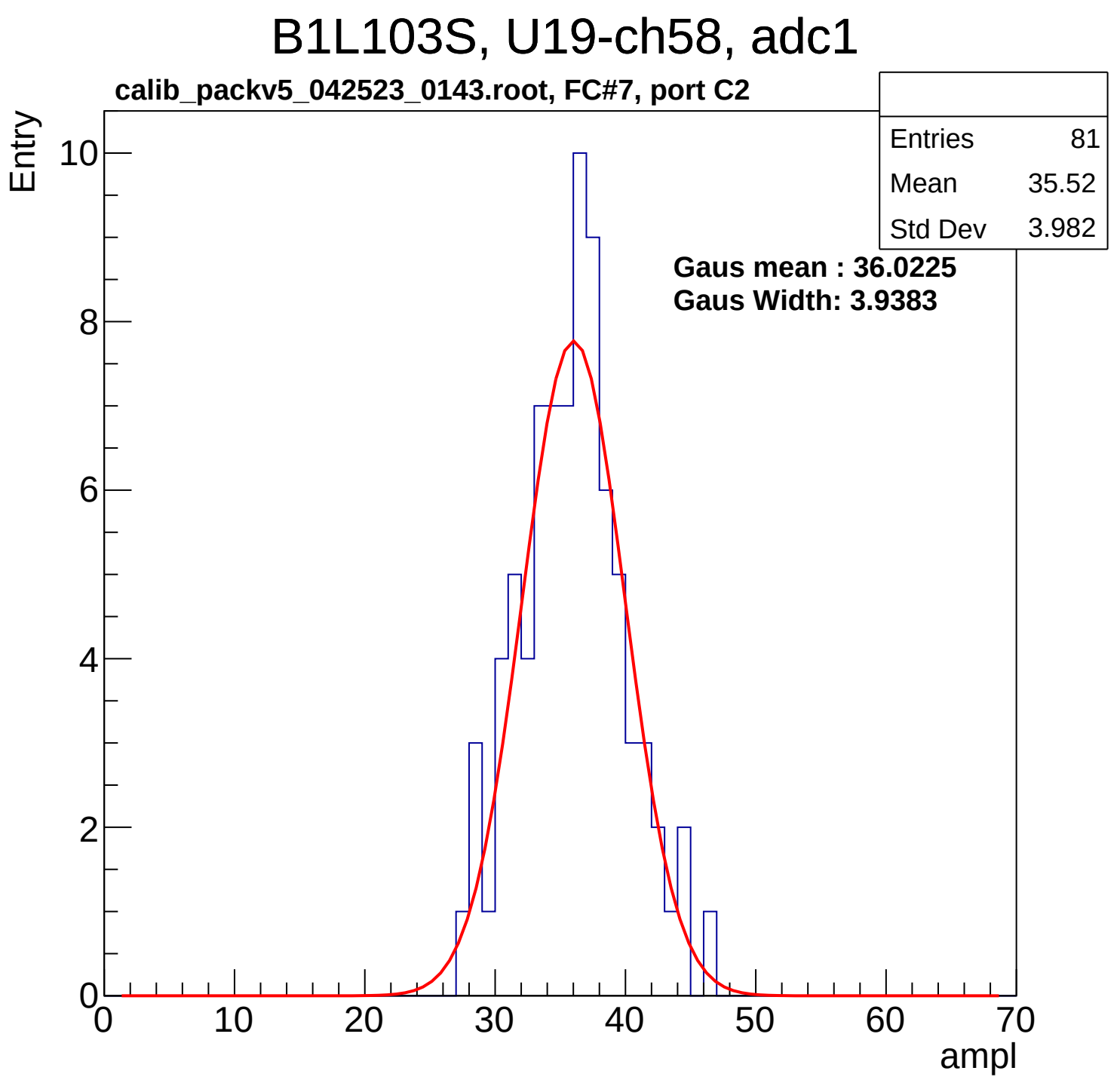
B1L103S, U19-ch58, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 35.52
Std Dev 3.982
Gaus mean : 36.0225
Gaus Width: 3.9383
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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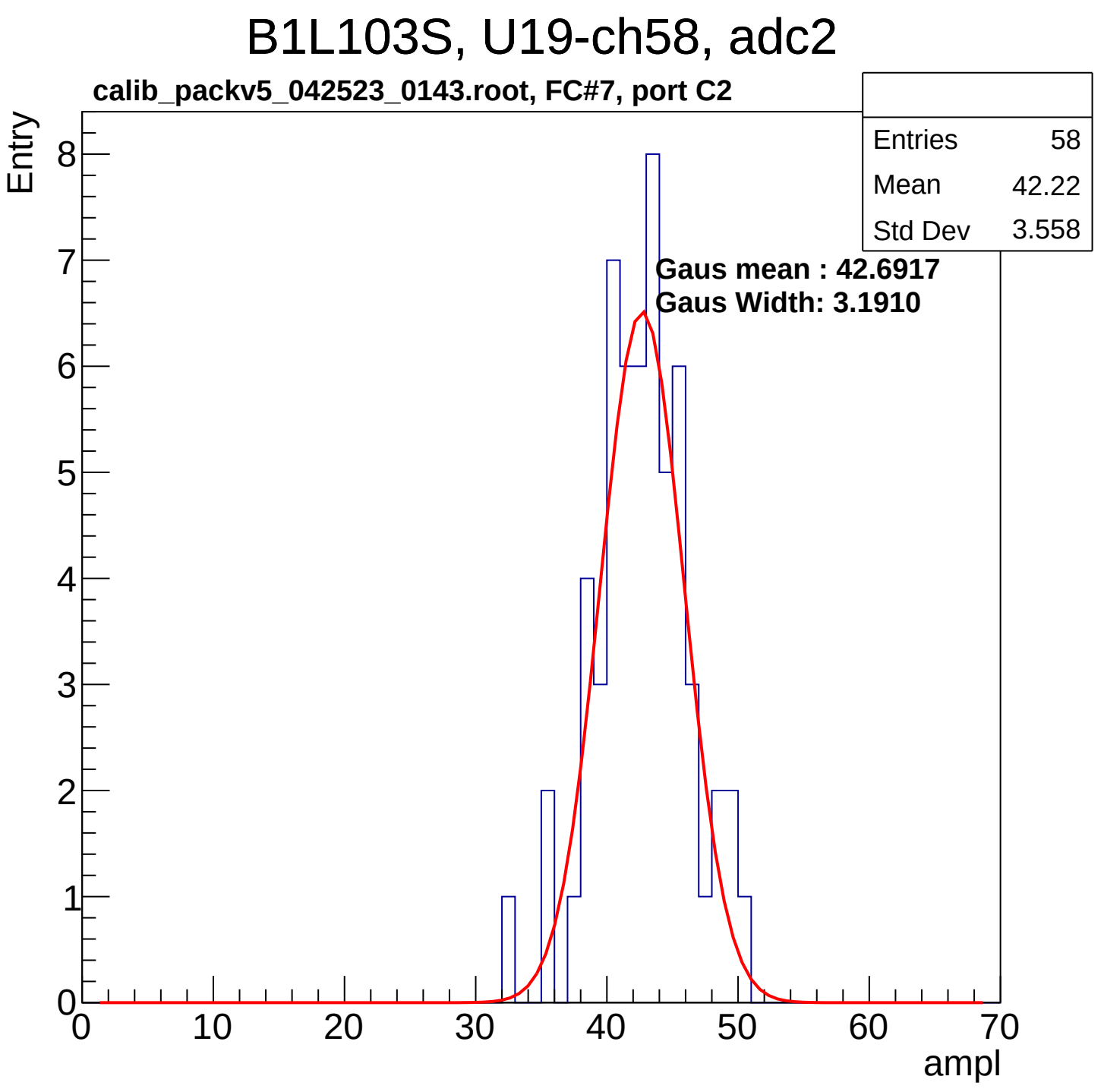
B1L103S, U19-ch58, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 42.22
Std Dev 3.558
Gaus mean : 42.6917
Gaus Width: 3.1910
Entry
ampl
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1
2
3
4
5
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0
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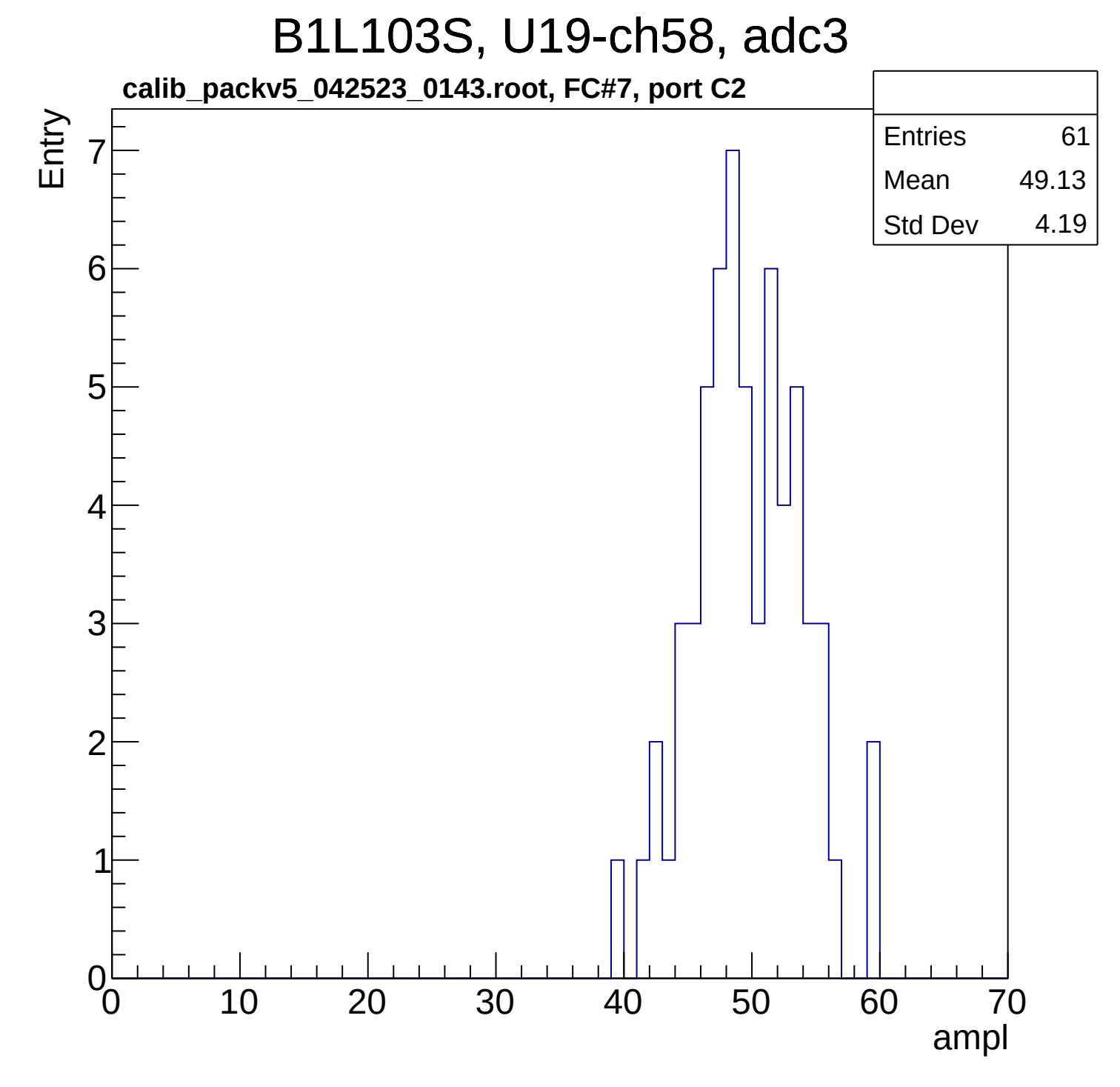
B1L103S, U19-ch58, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 49.13
Std Dev 4.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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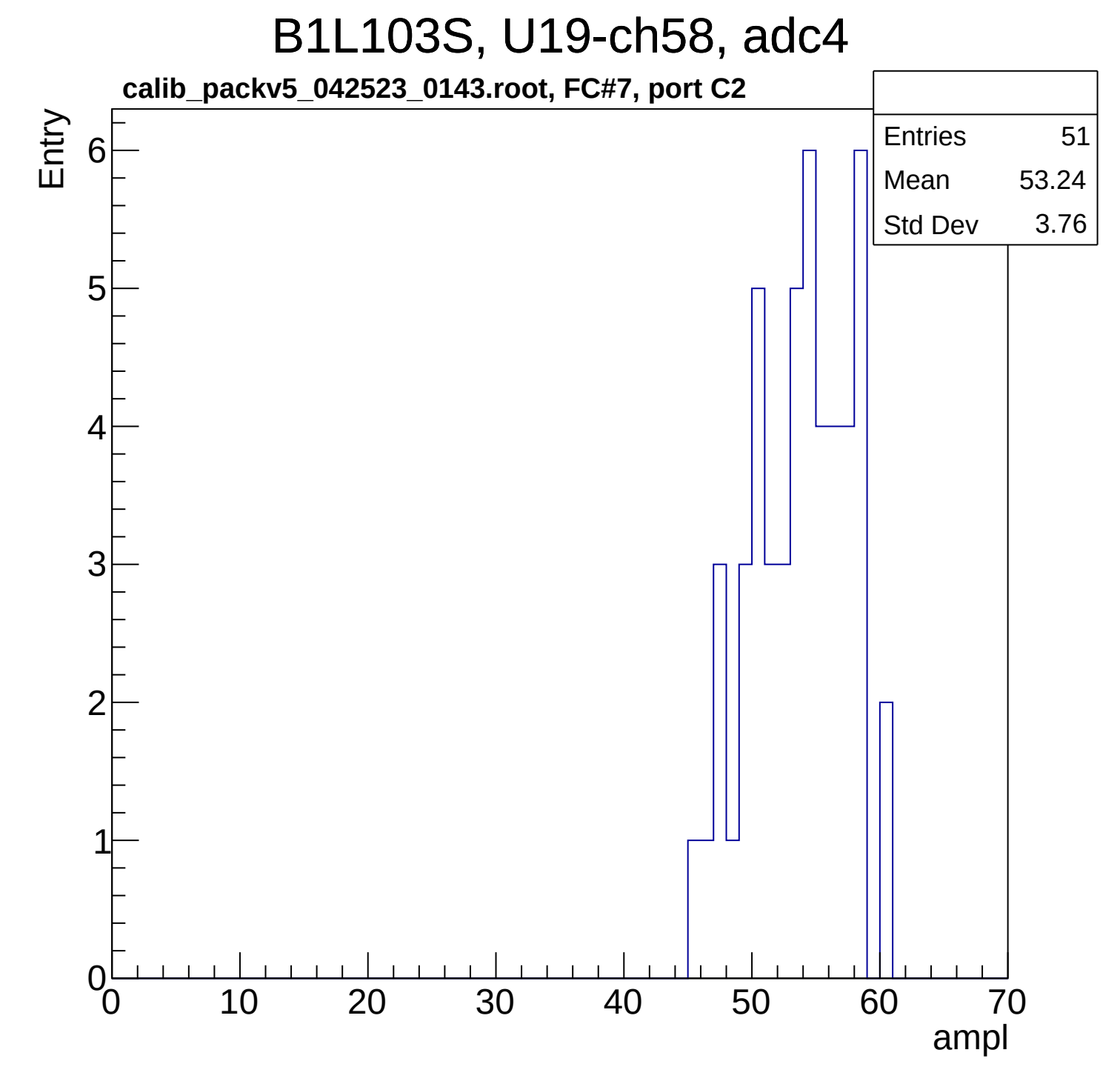
B1L103S, U19-ch58, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 53.24
Std Dev 3.76
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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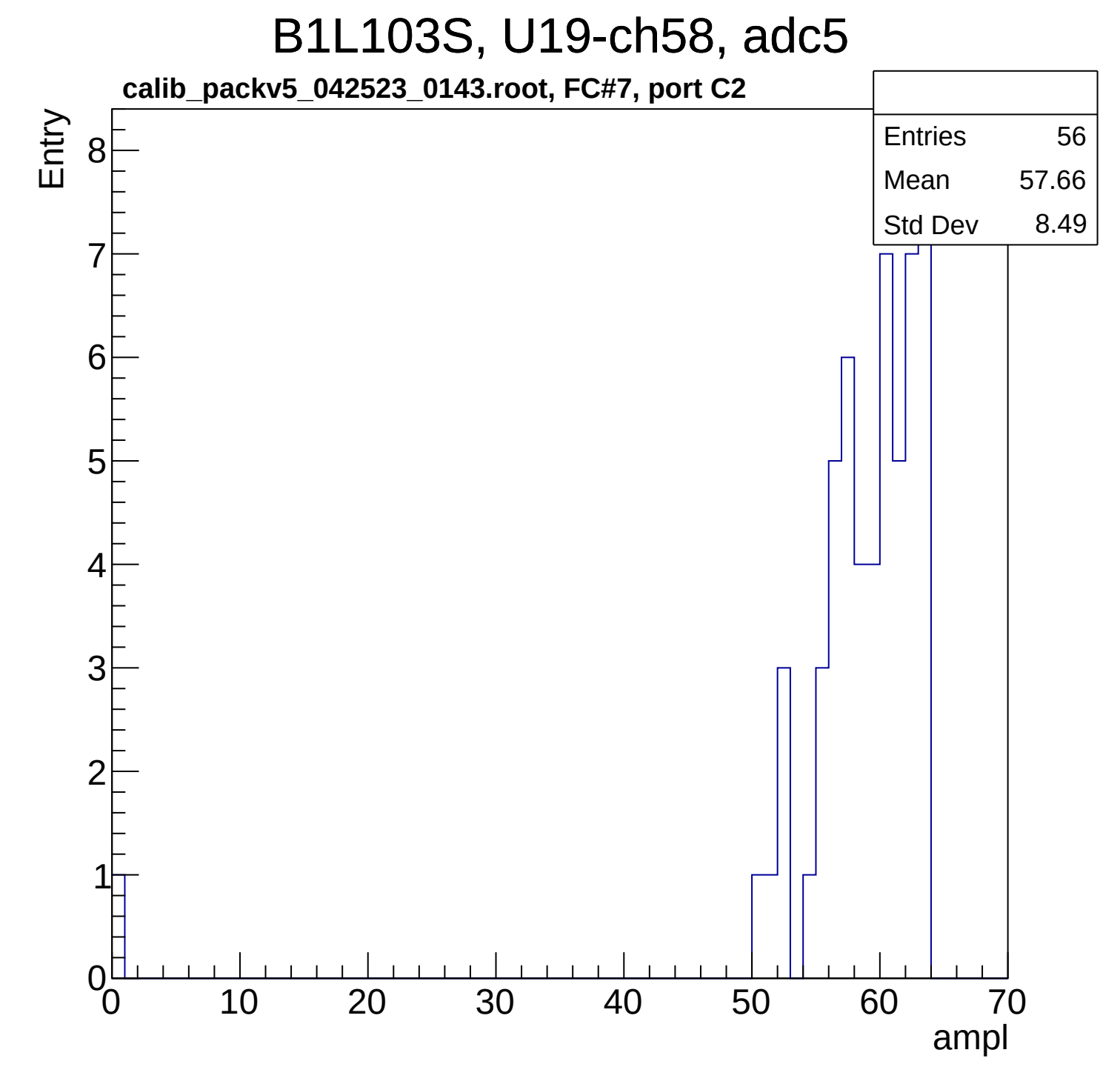
B1L103S, U19-ch58, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.66
Std Dev 8.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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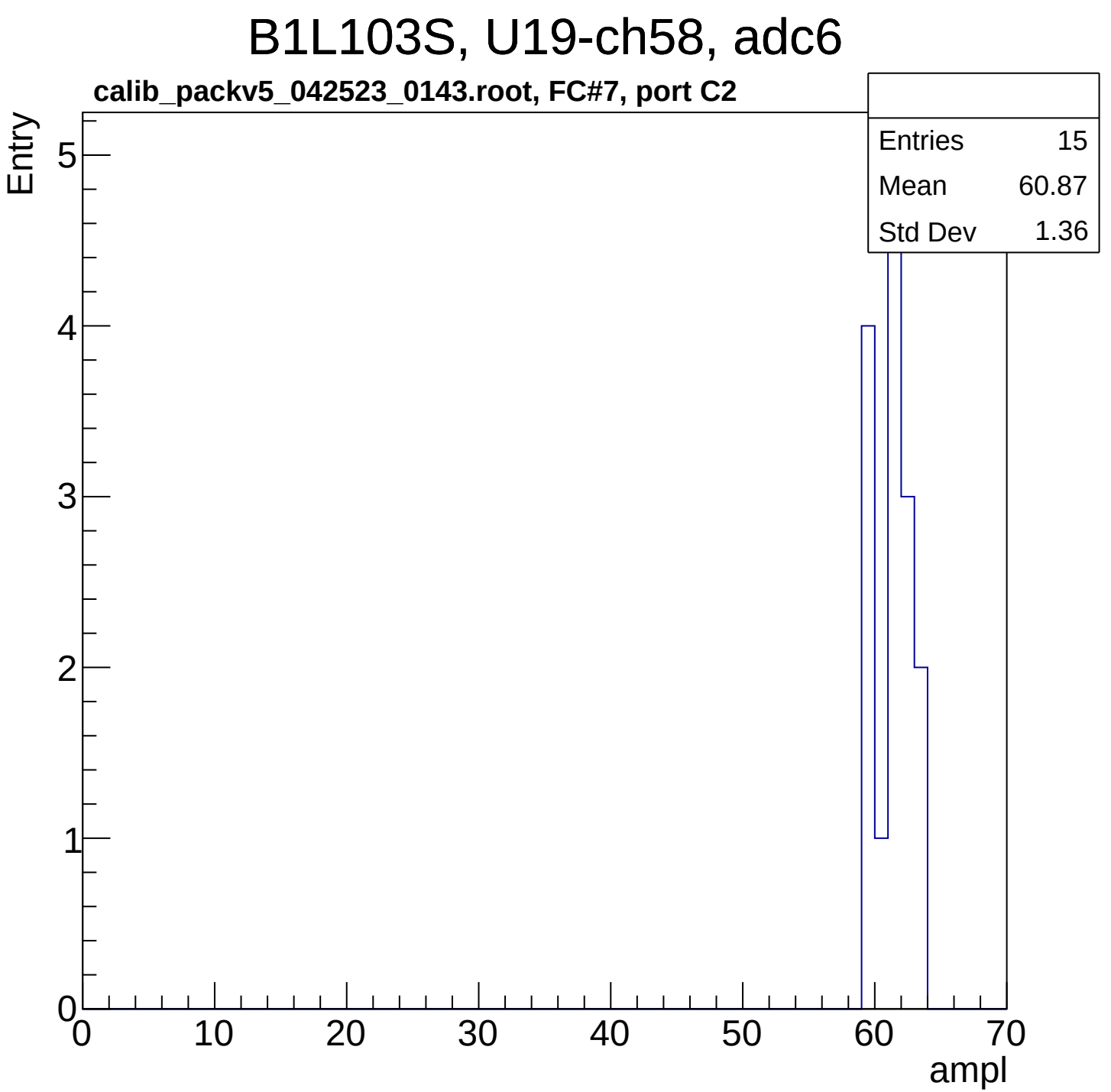
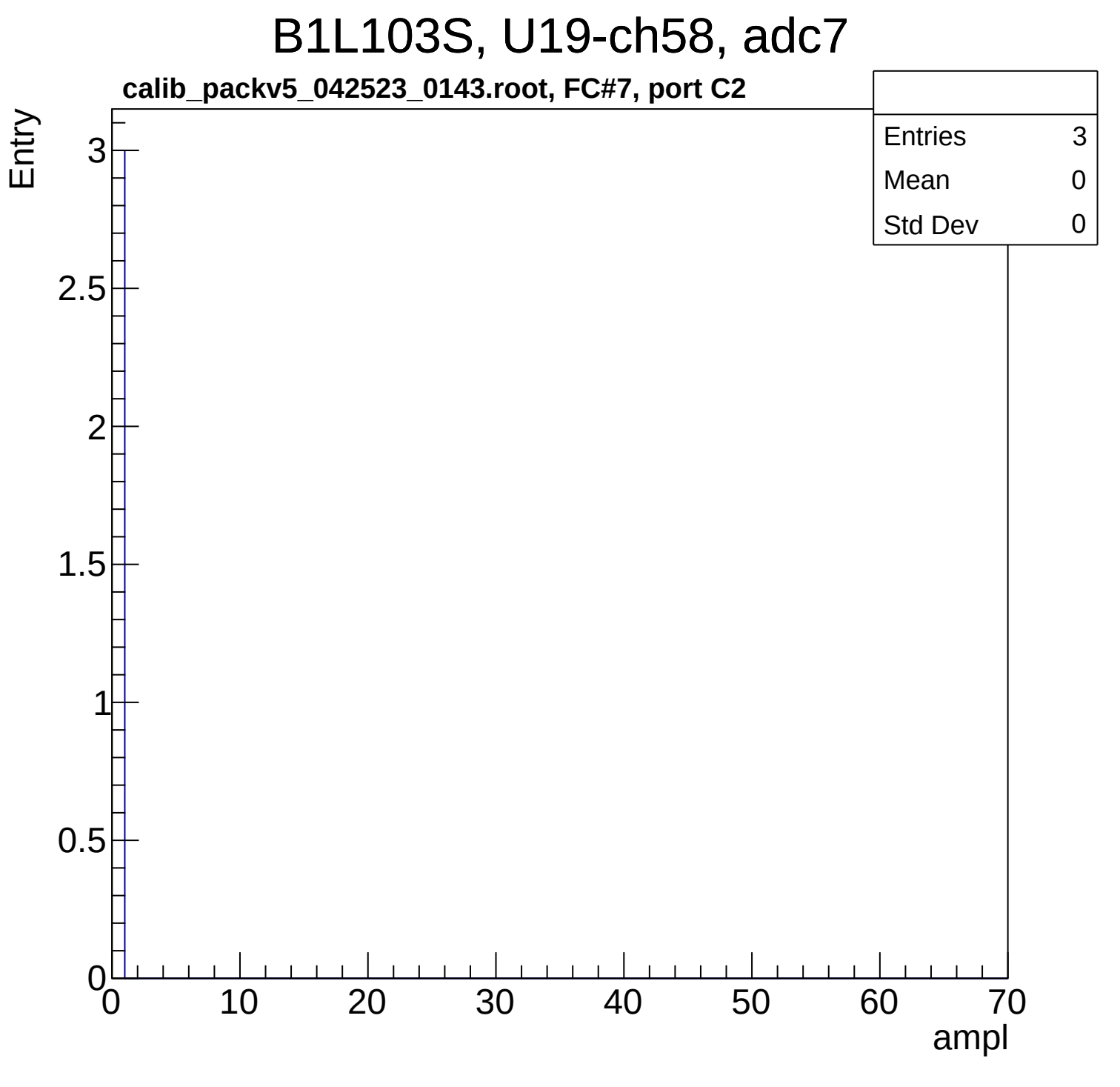
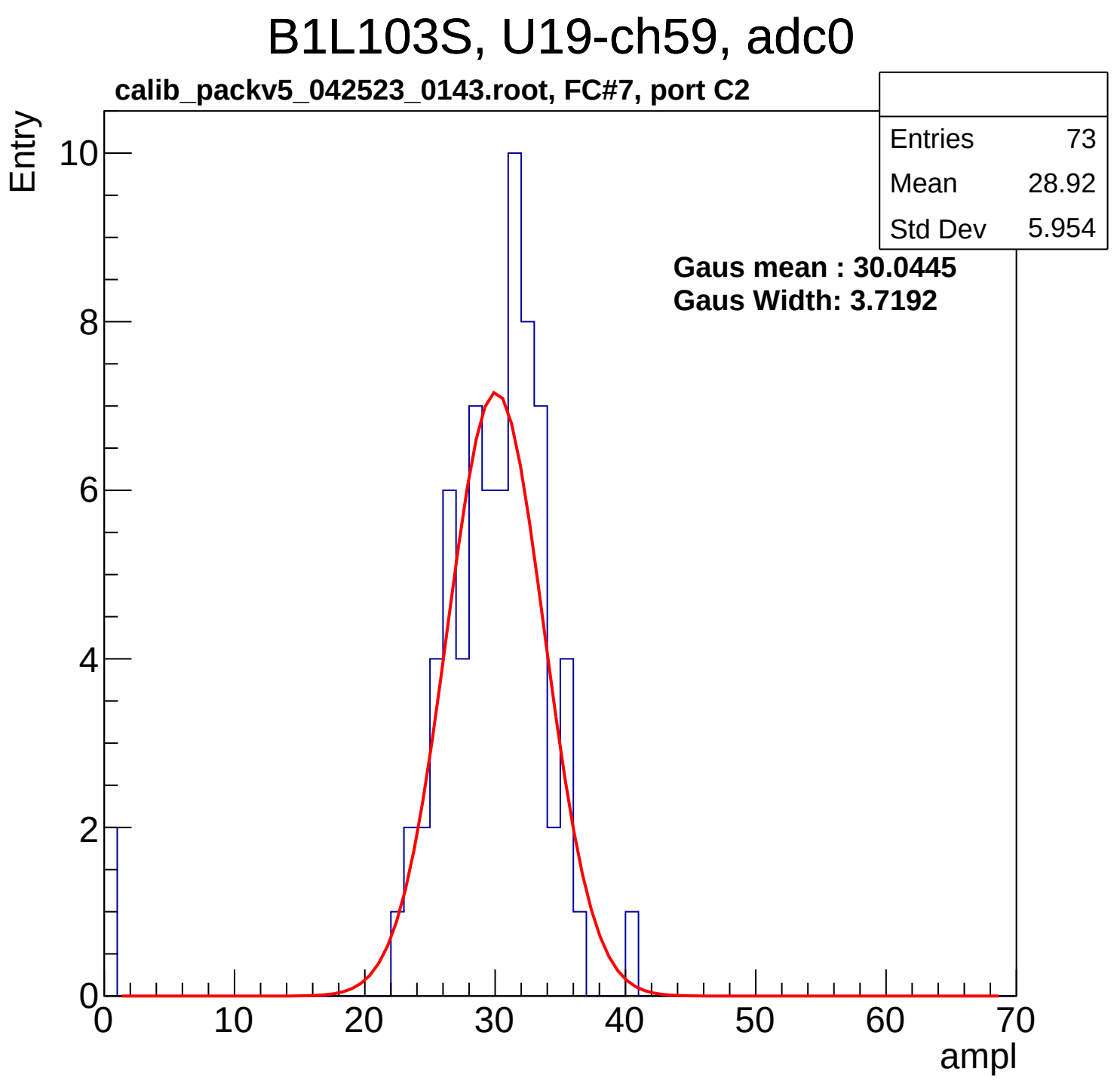
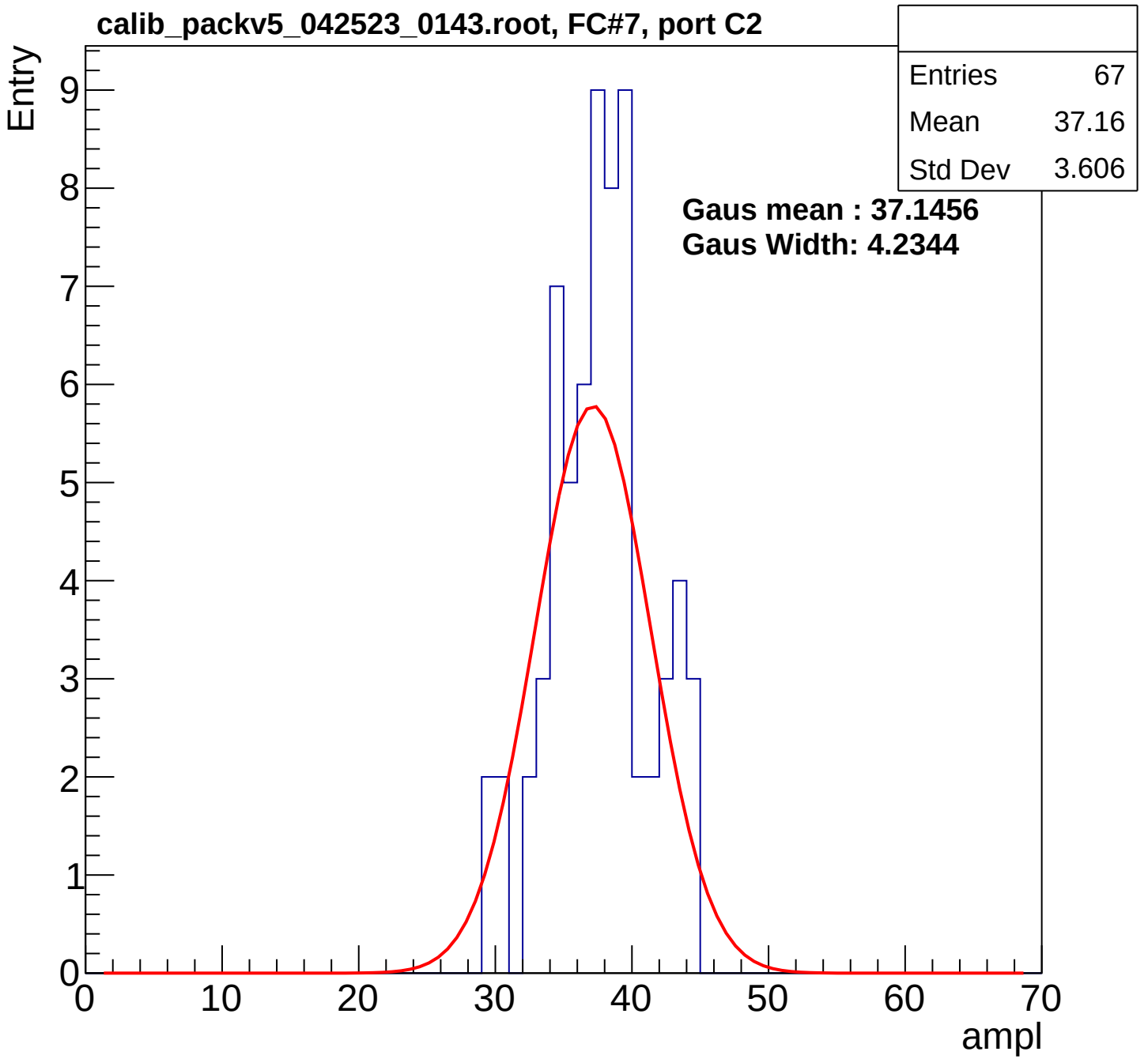

B1L103S, U19-ch58, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 60.87
Std Dev 1.36
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch58, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch59, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 28.92
Std Dev 5.954
Gaus mean : 30.0445
Gaus Width: 3.7192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch59, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.16
Std Dev 3.606
Gaus mean : 37.1456
Gaus Width: 4.2344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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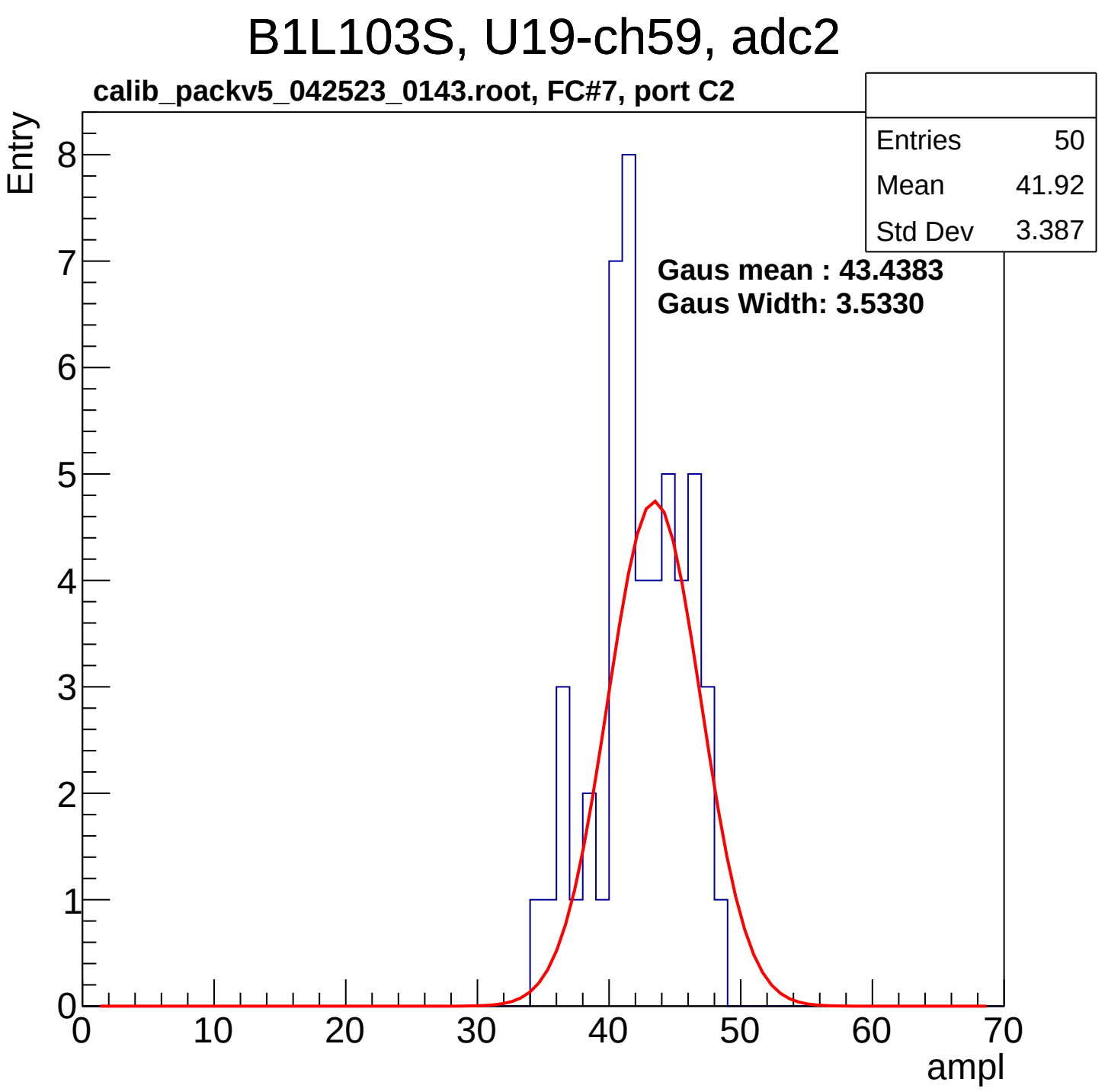
B1L103S, U19-ch59, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 41.92
Std Dev 3.387
Gaus mean : 43.4383
Gaus Width: 3.5330
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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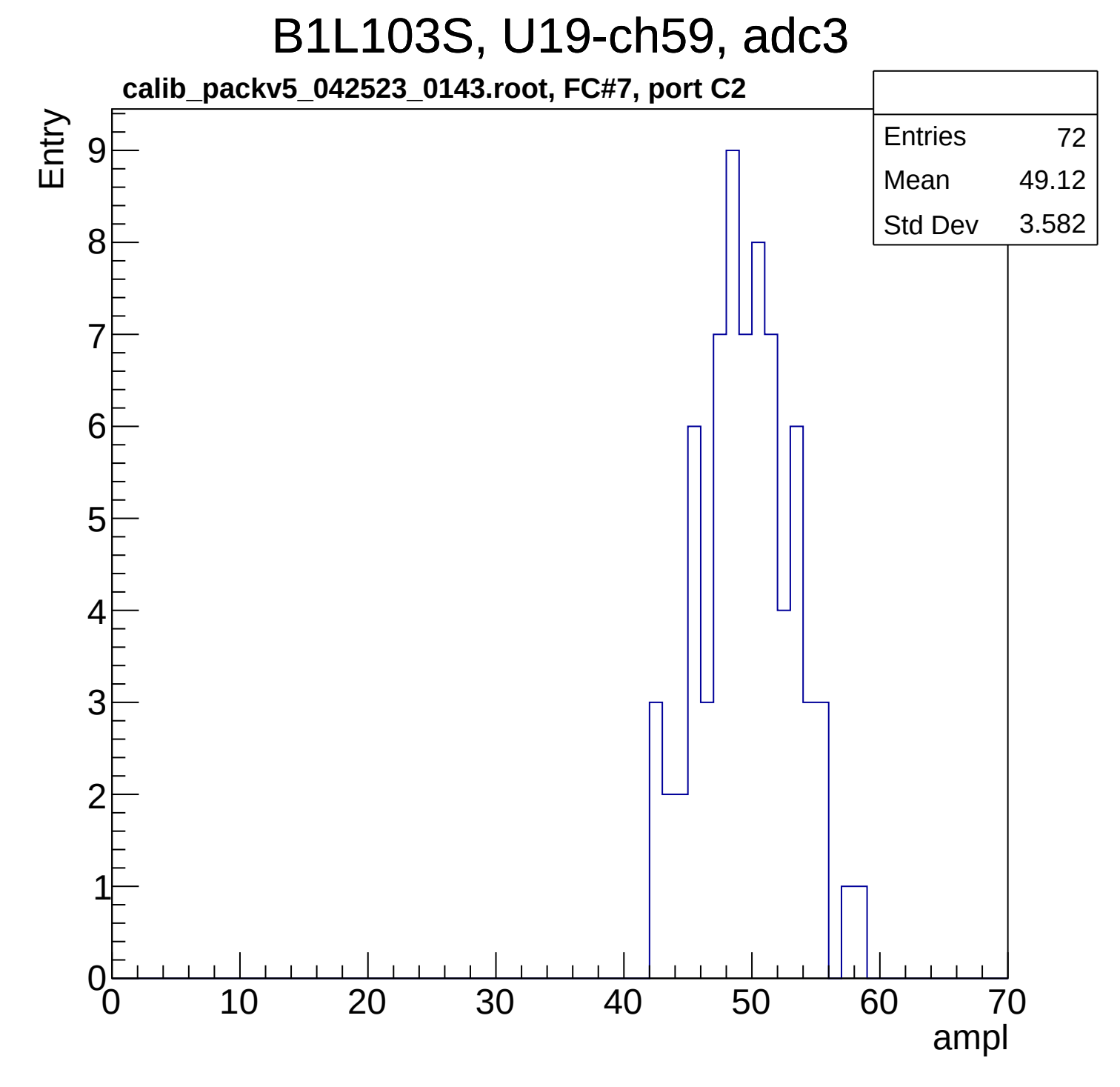
B1L103S, U19-ch59, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.12
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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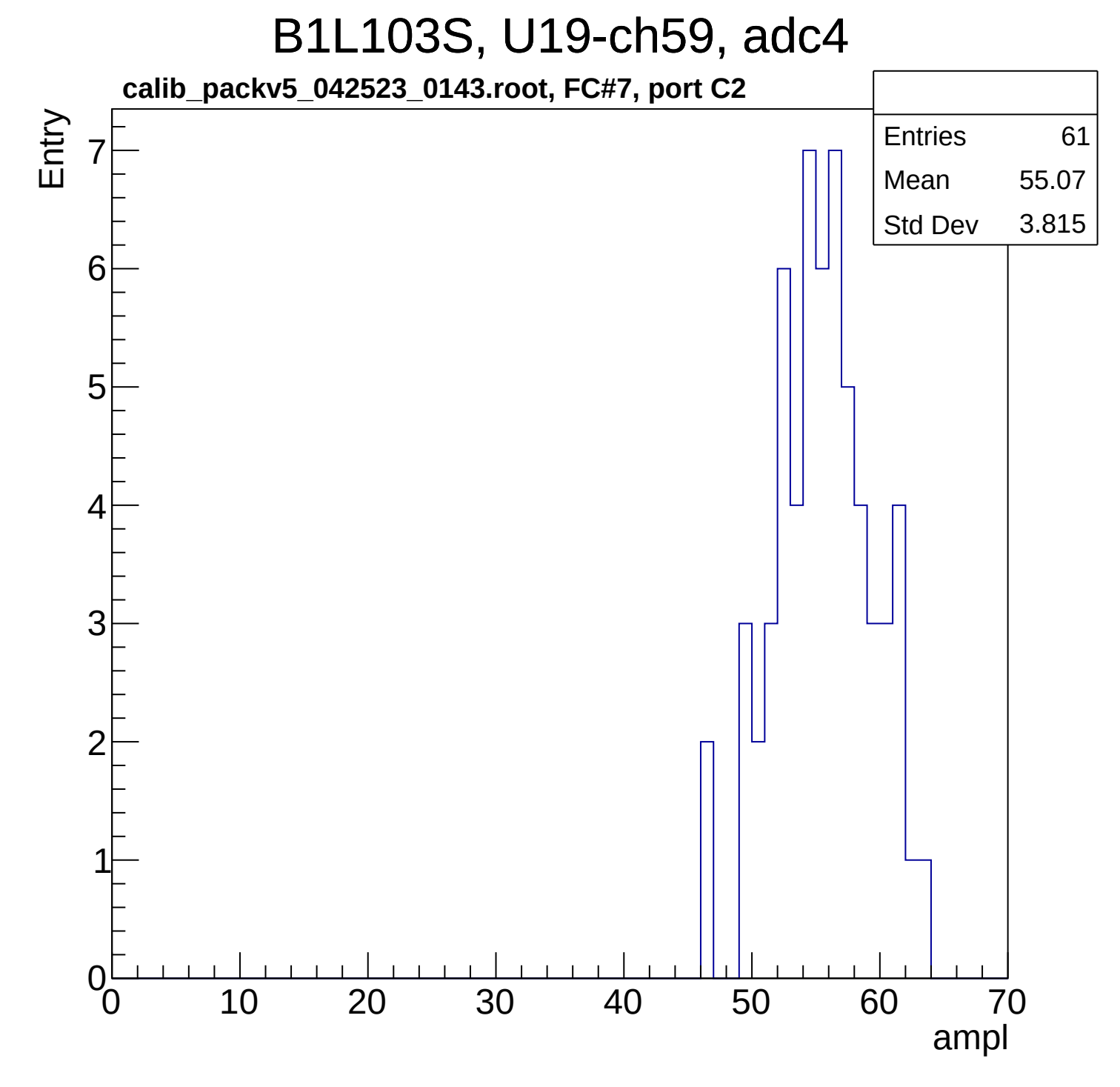
B1L103S, U19-ch59, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 55.07
Std Dev 3.815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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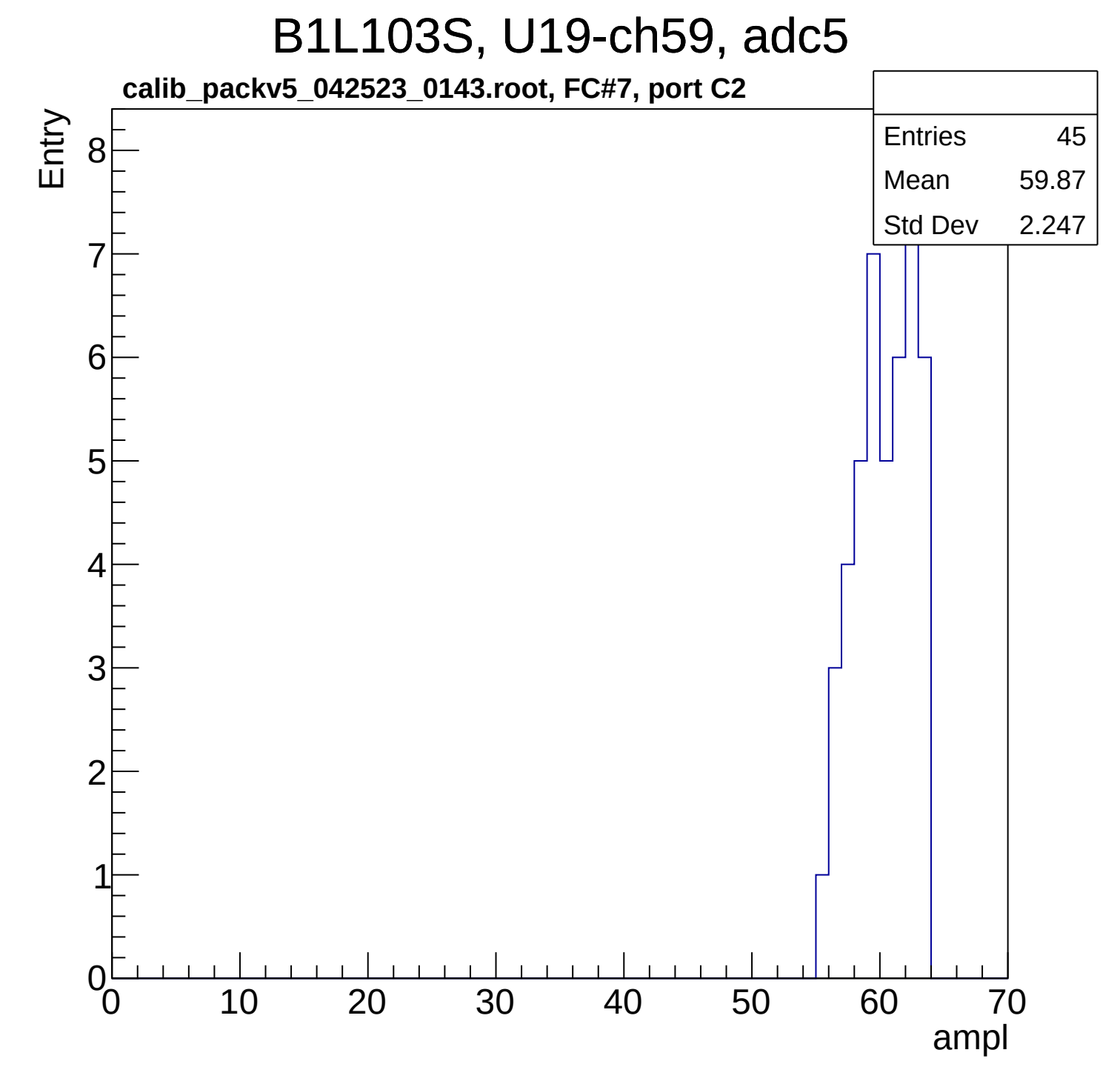
B1L103S, U19-ch59, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.87
Std Dev 2.247
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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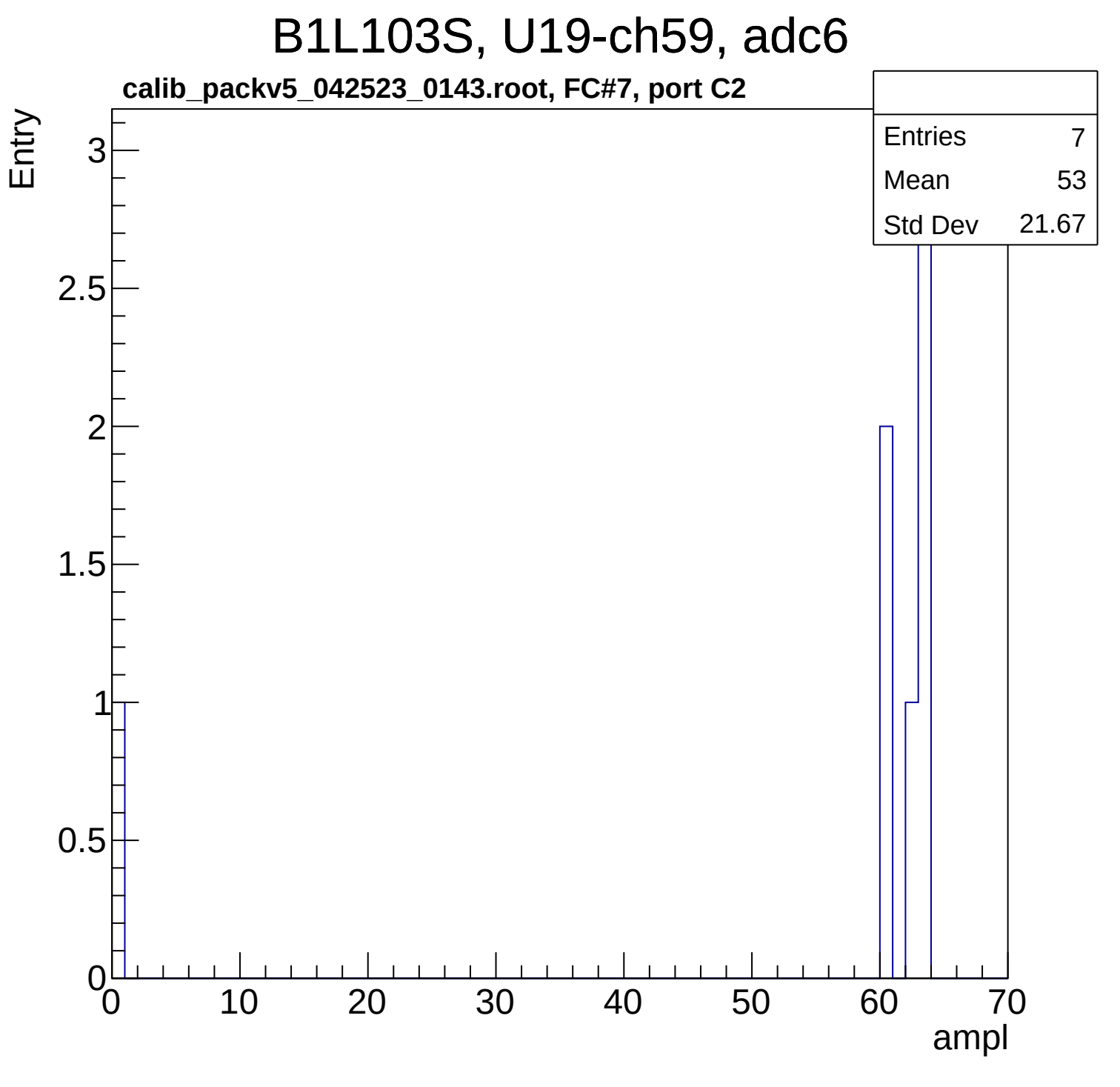
B1L103S, U19-ch59, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 53
Std Dev 21.67
Entry
ampl
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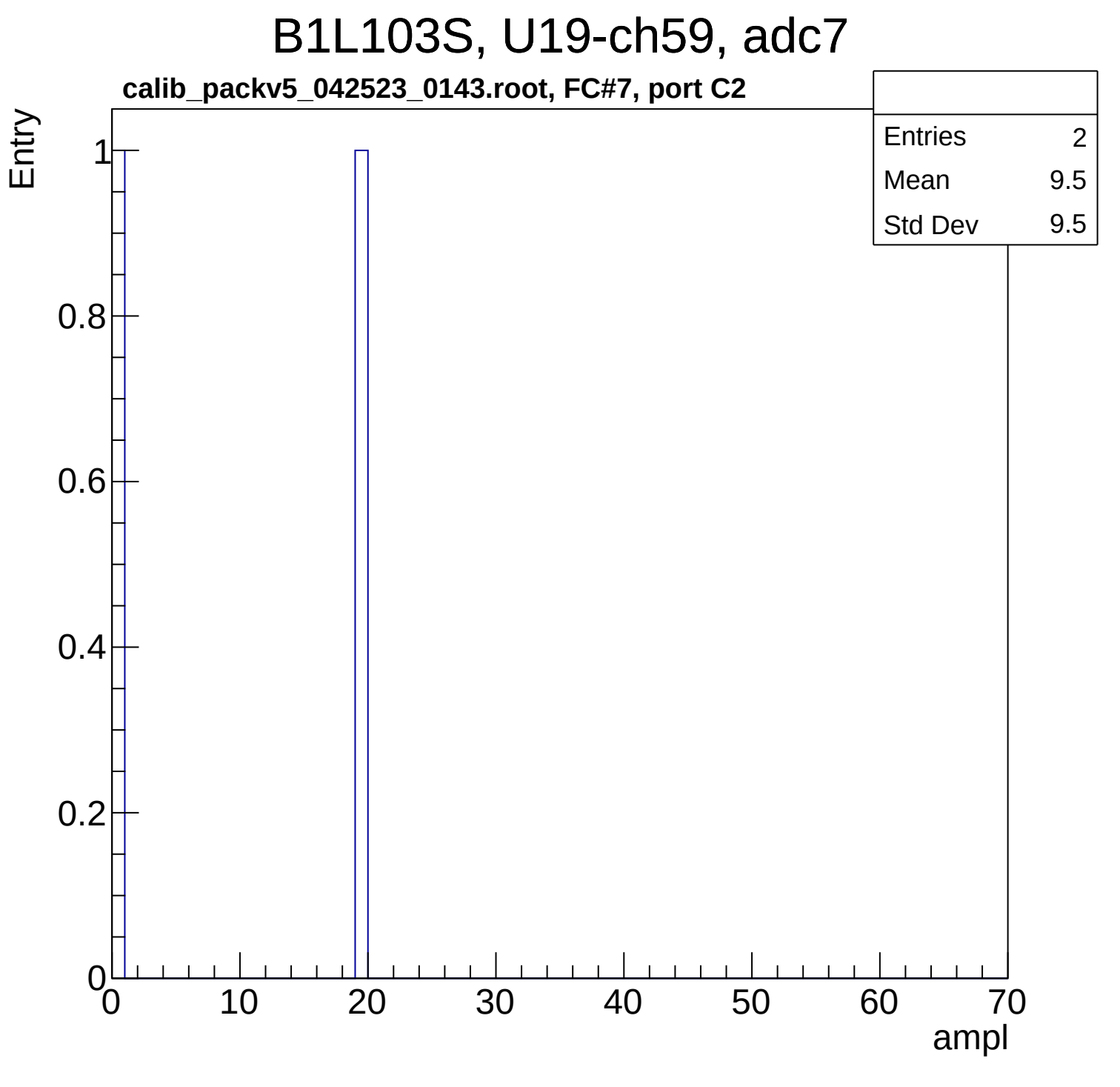
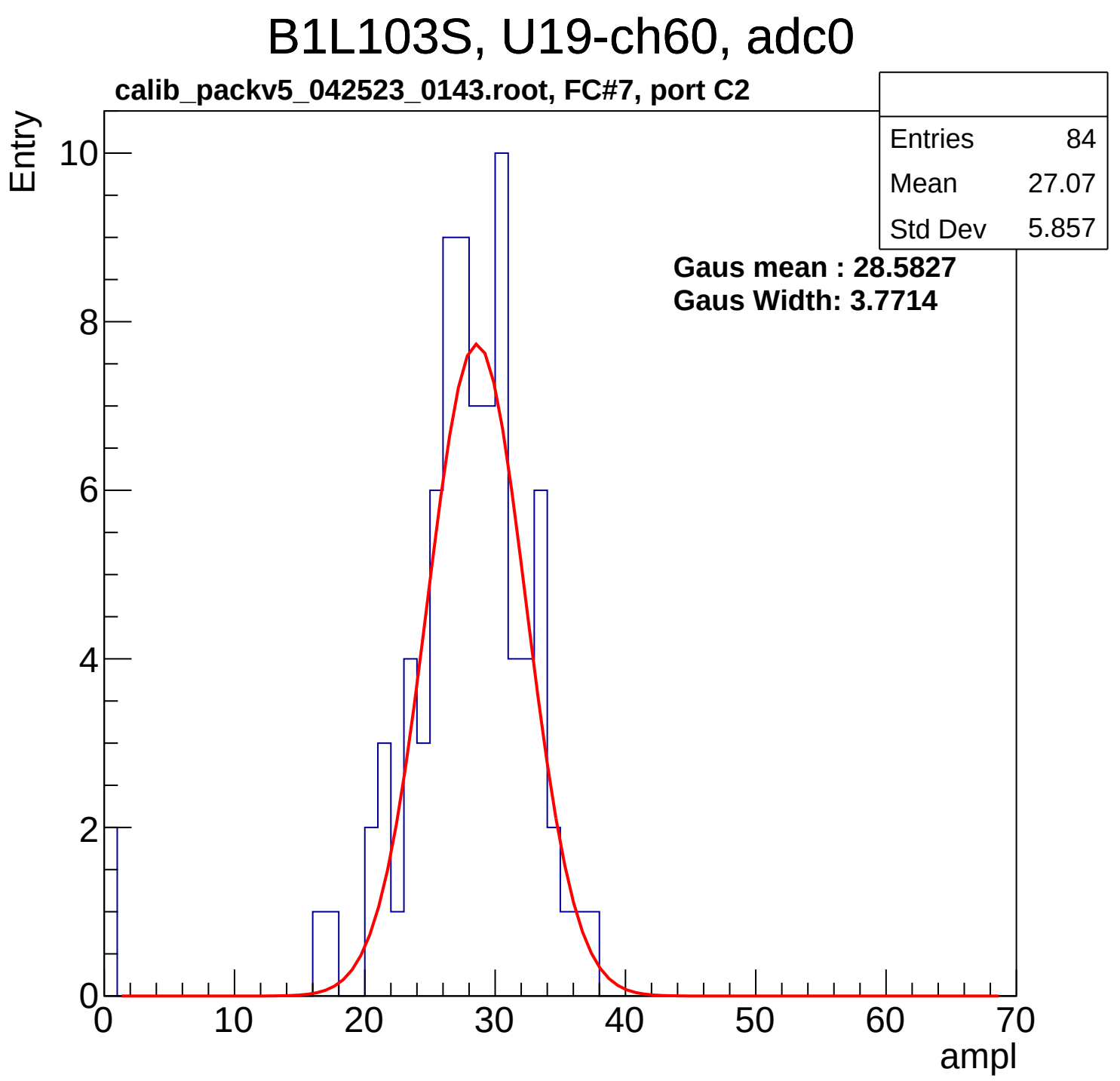
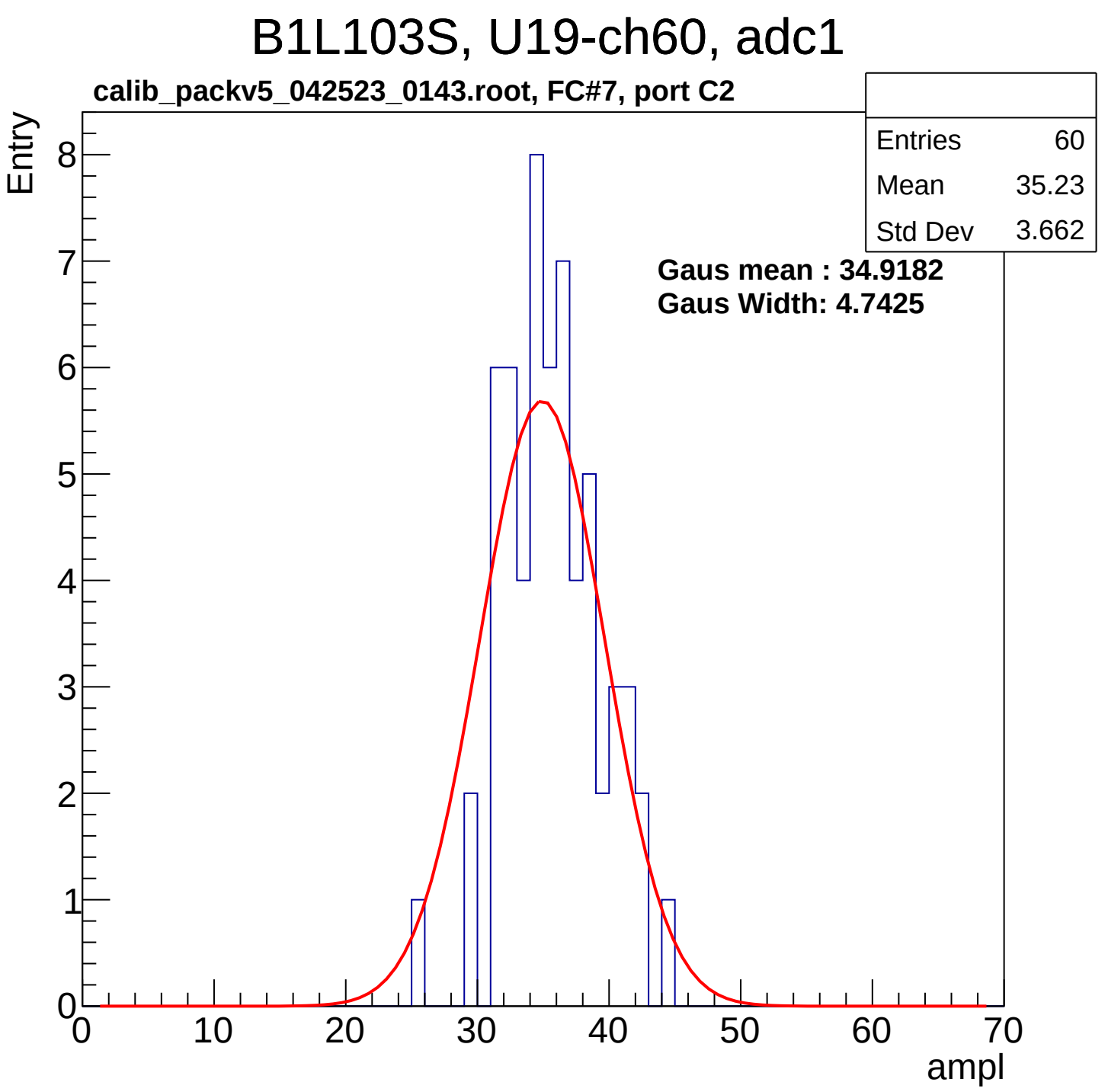
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch59, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch60, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 27.07
Std Dev 5.857
Gaus mean : 28.5827
Gaus Width: 3.7714
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch60, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 35.23
Std Dev 3.662
Gaus mean : 34.9182
Gaus Width: 4.7425
Entry
ampl
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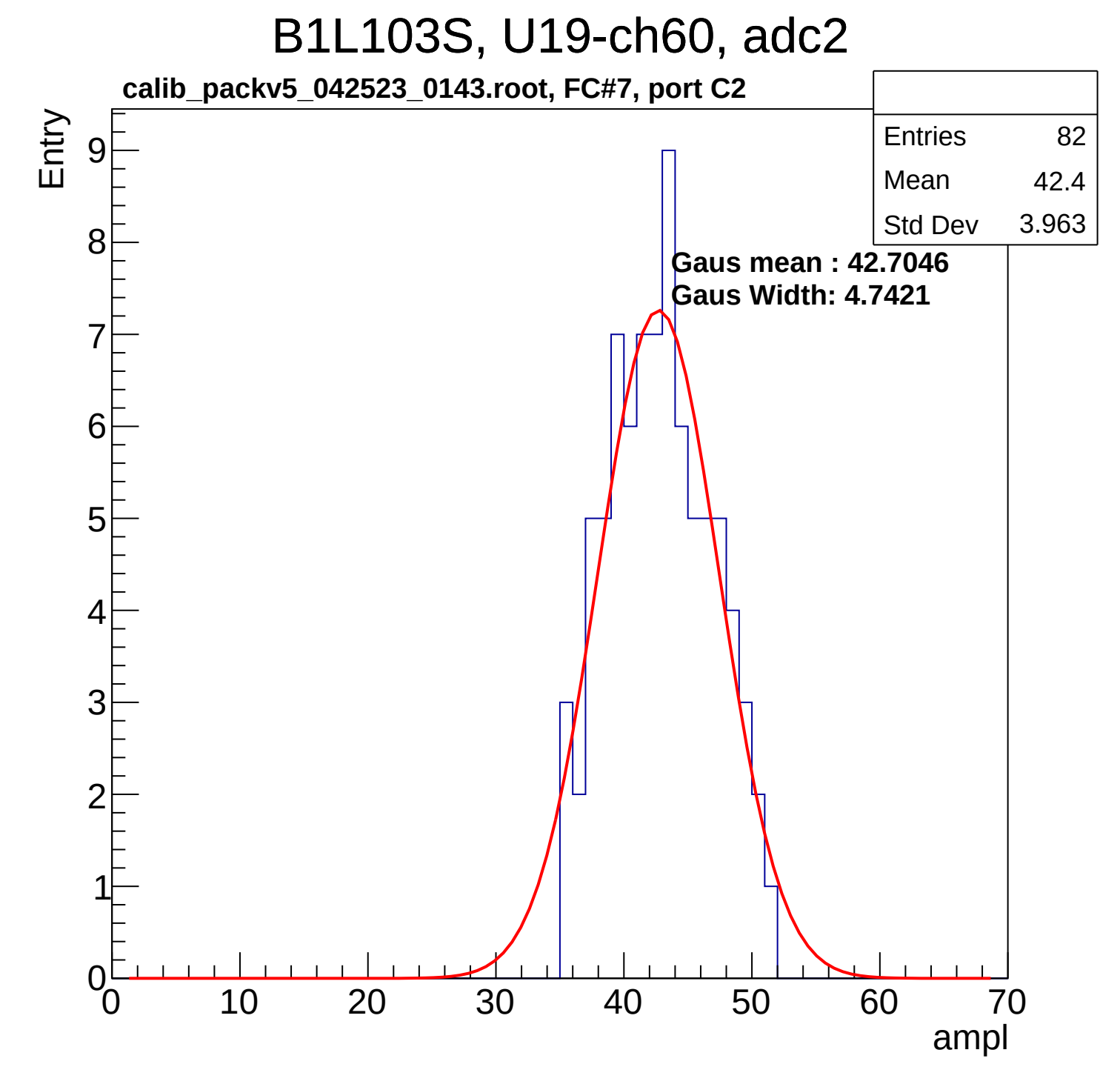
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch60, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 42.4
Std Dev 3.963
Gaus mean : 42.7046
Gaus Width: 4.7421
Entry
ampl
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2
3
4
5
6
7
8
9
0
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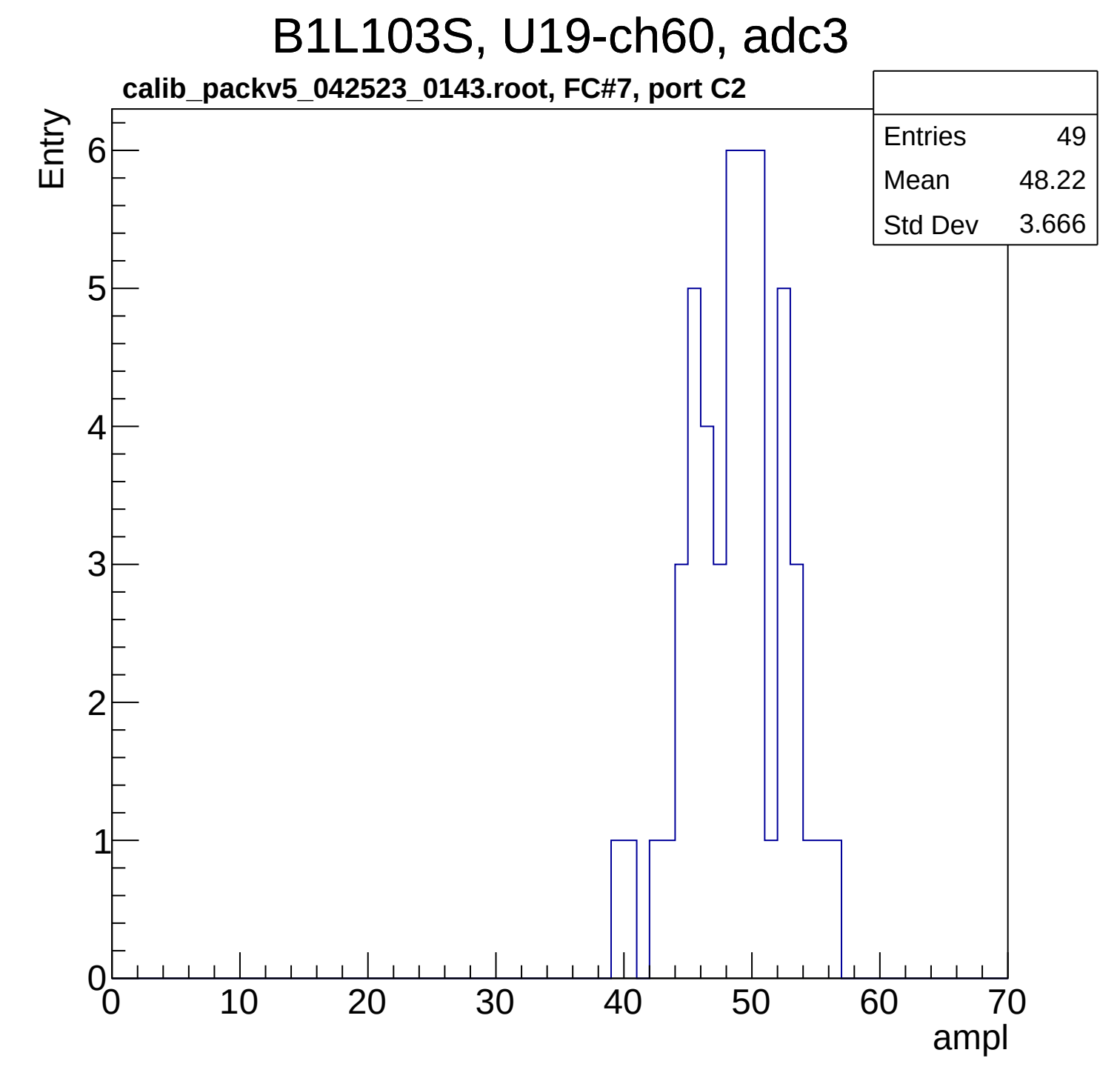
40
50
60
70


B1L103S, U19-ch60, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 48.22
Std Dev 3.666
Entry
ampl
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3
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0
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60
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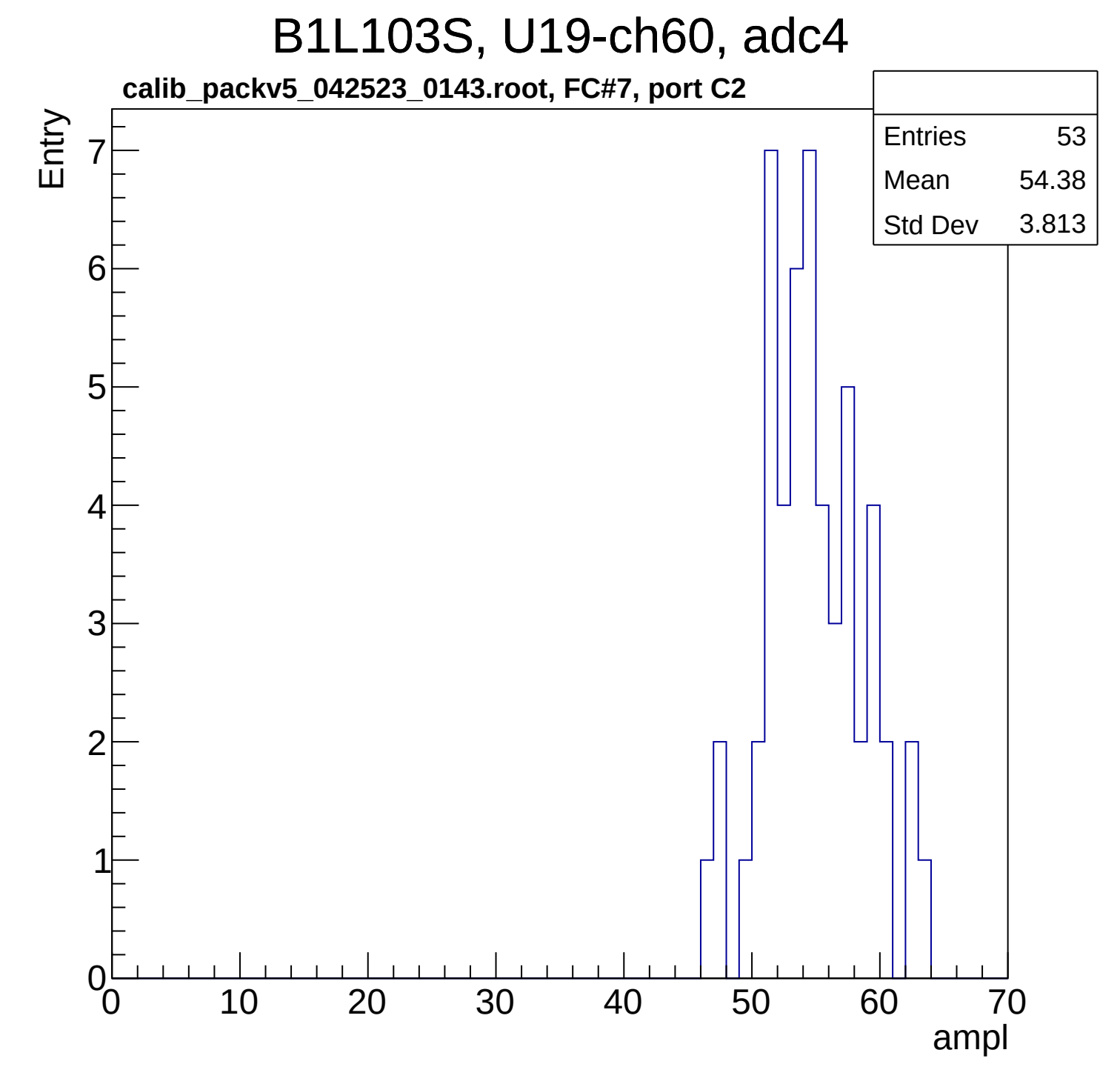
B1L103S, U19-ch60, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 54.38
Std Dev 3.813
Entry
ampl
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3
4
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6
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0
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60
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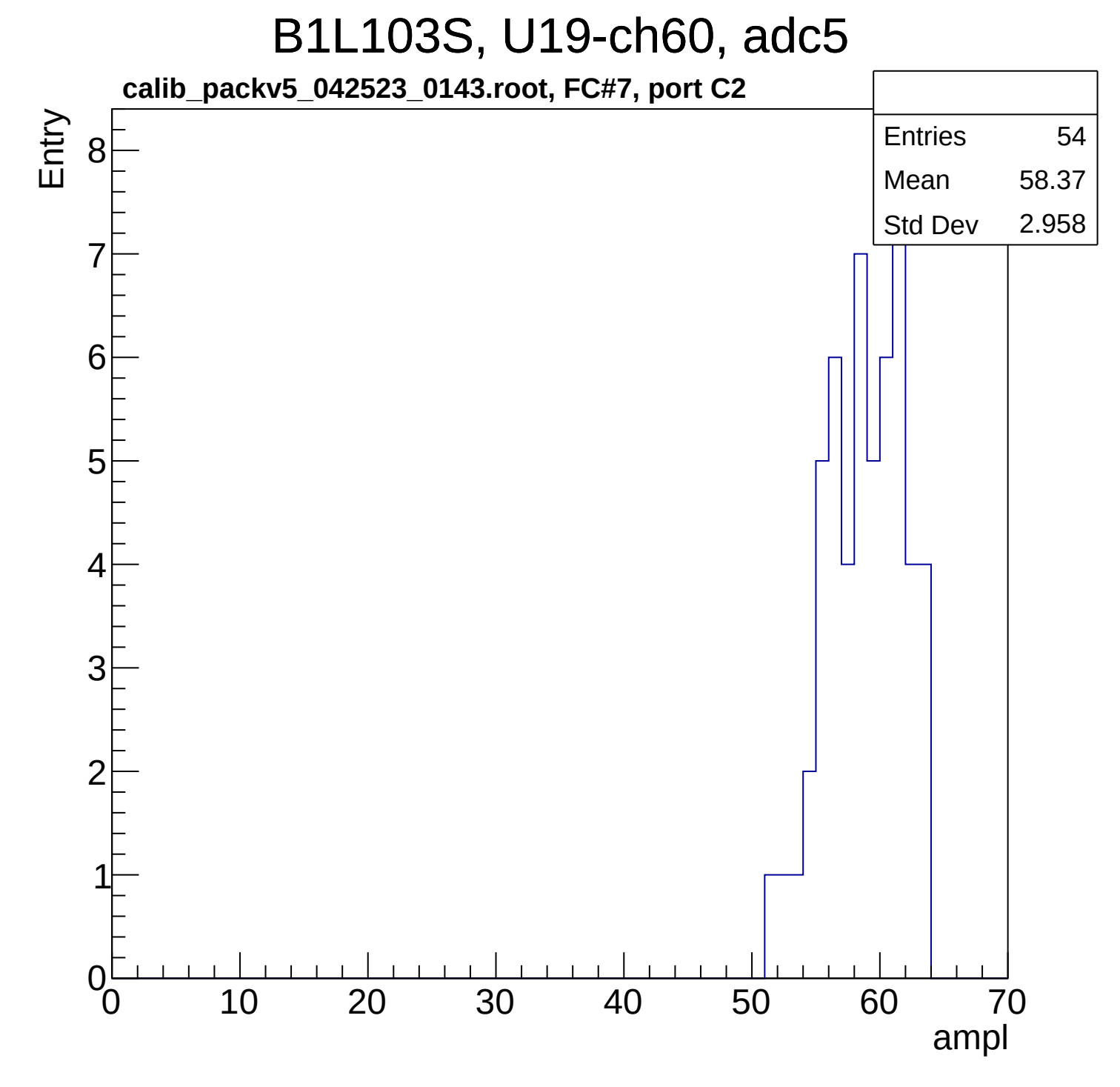
B1L103S, U19-ch60, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.37
Std Dev 2.958
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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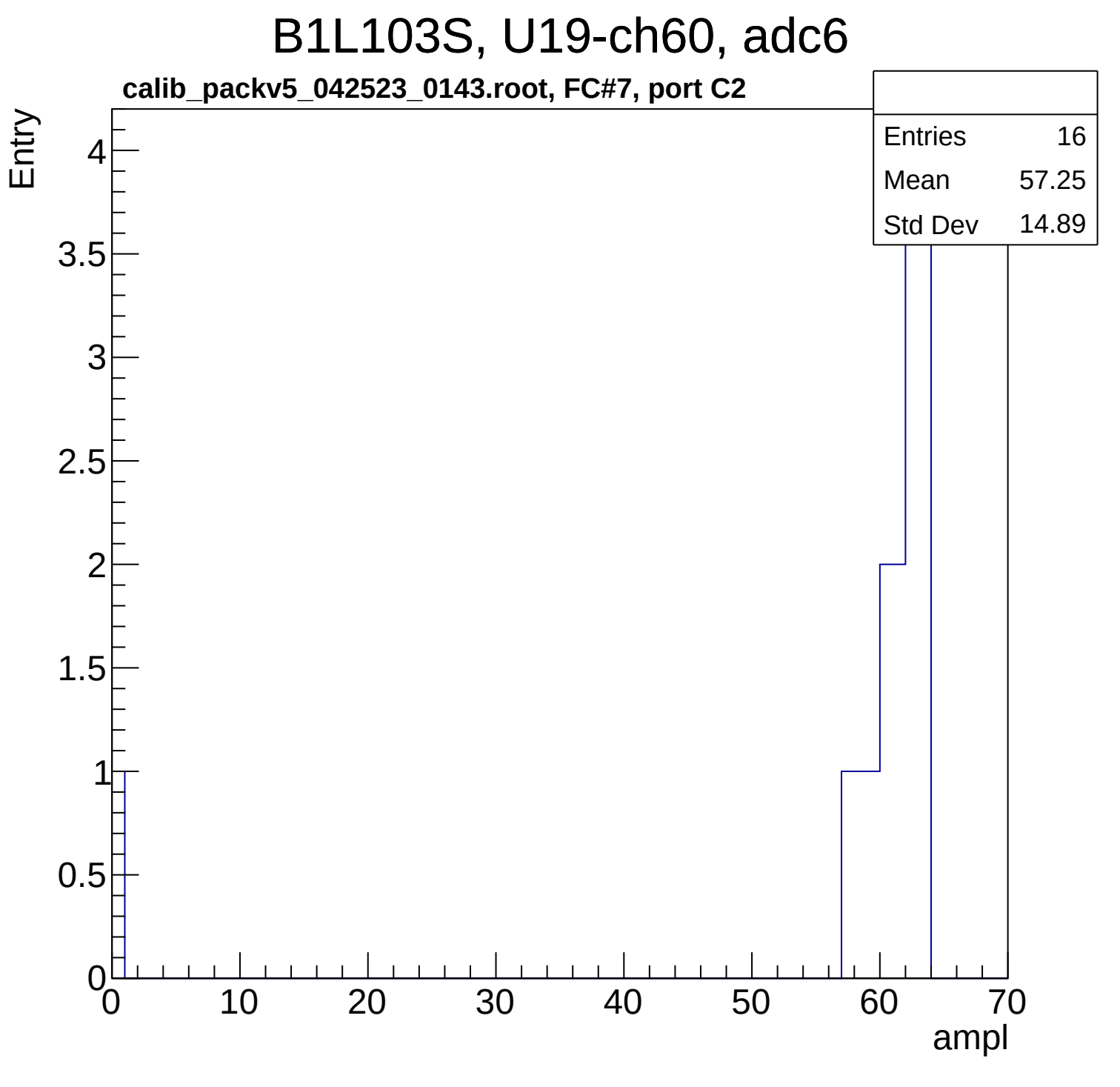
B1L103S, U19-ch60, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 57.25
Std Dev 14.89
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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50
60
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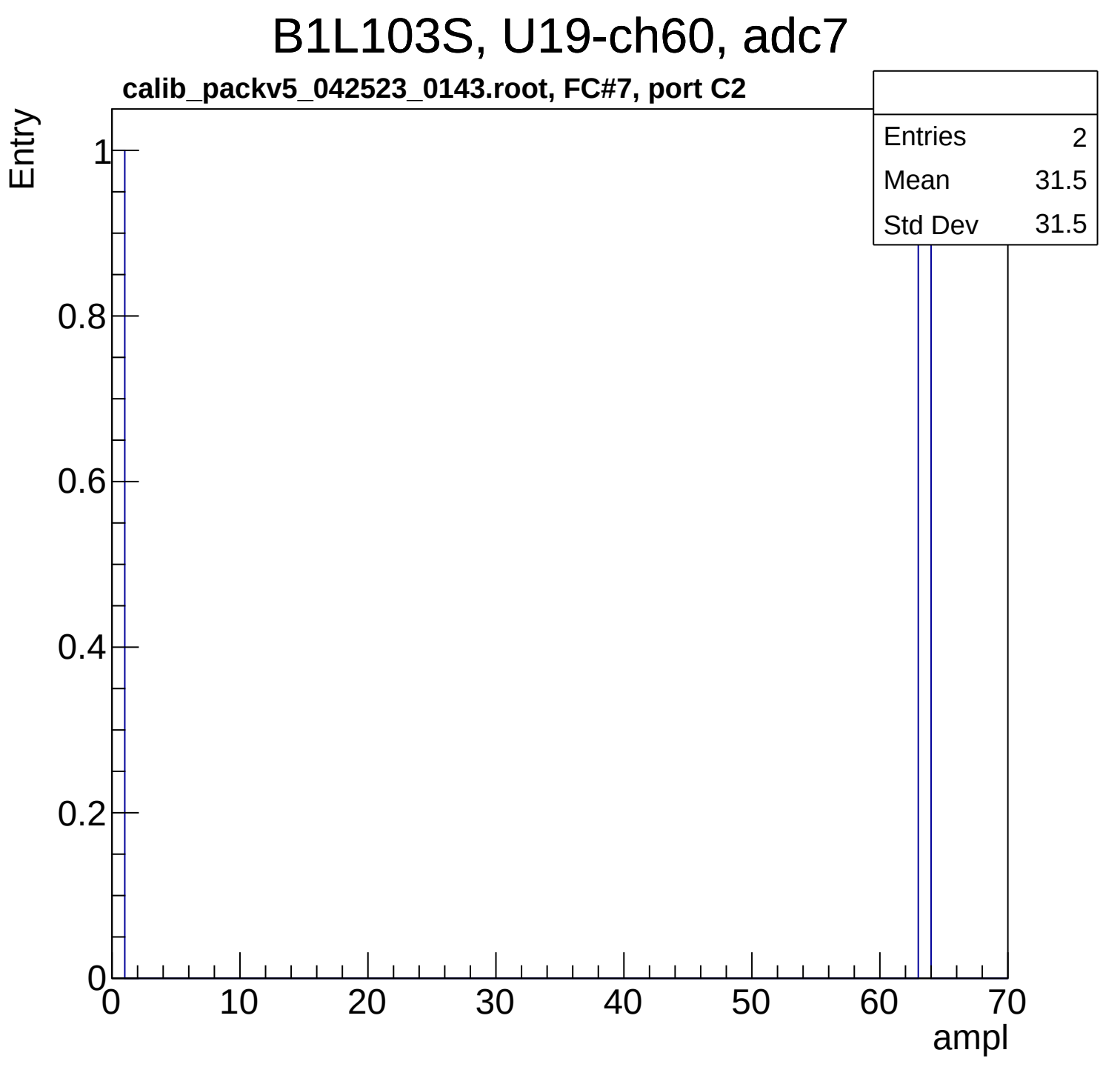
B1L103S, U19-ch60, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.4
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0
10
20
30
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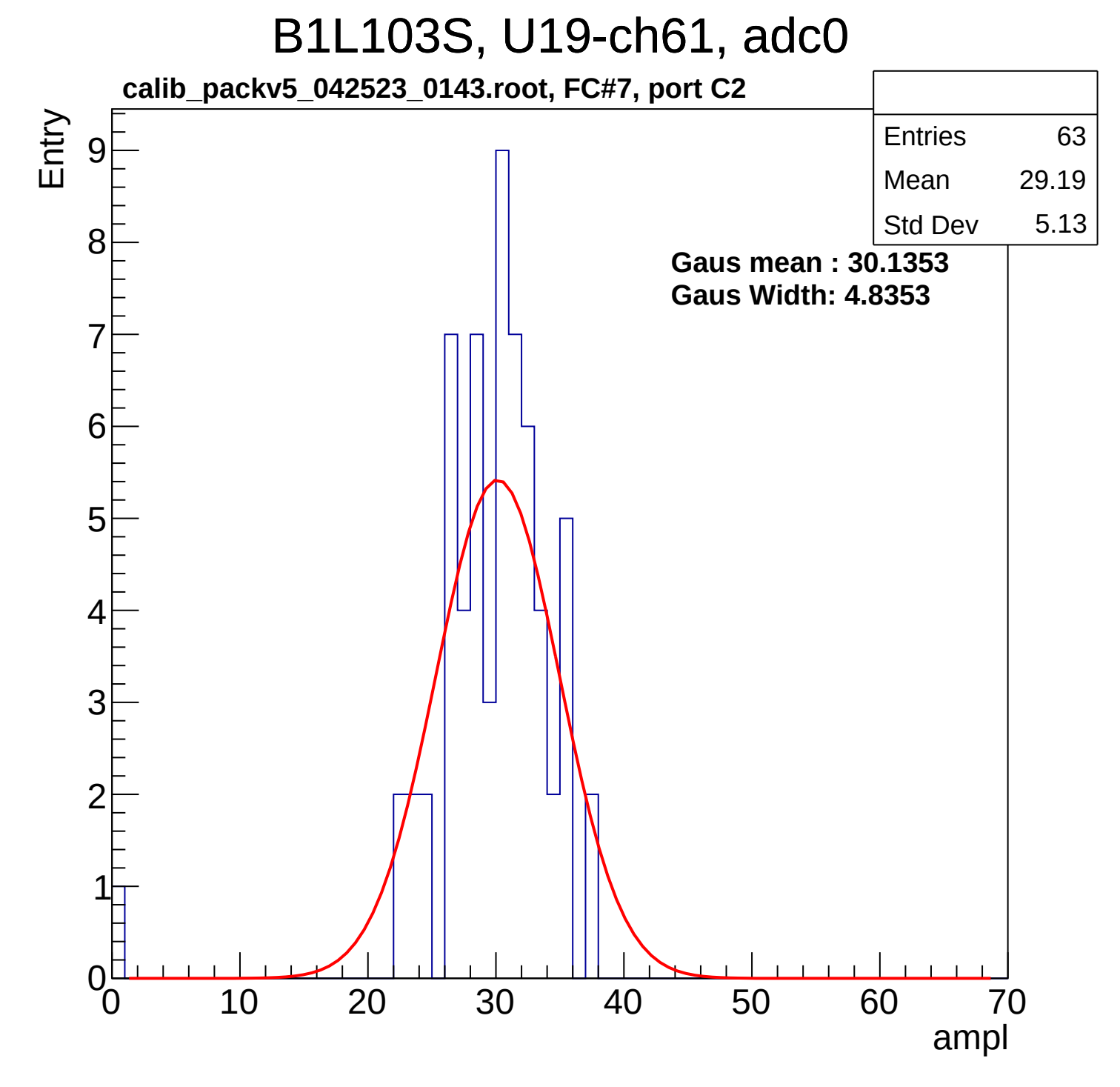
50
60
70
ampl


B1L103S, U19-ch61, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.19
Std Dev 5.13
Gaus mean : 30.1353
Gaus Width: 4.8353
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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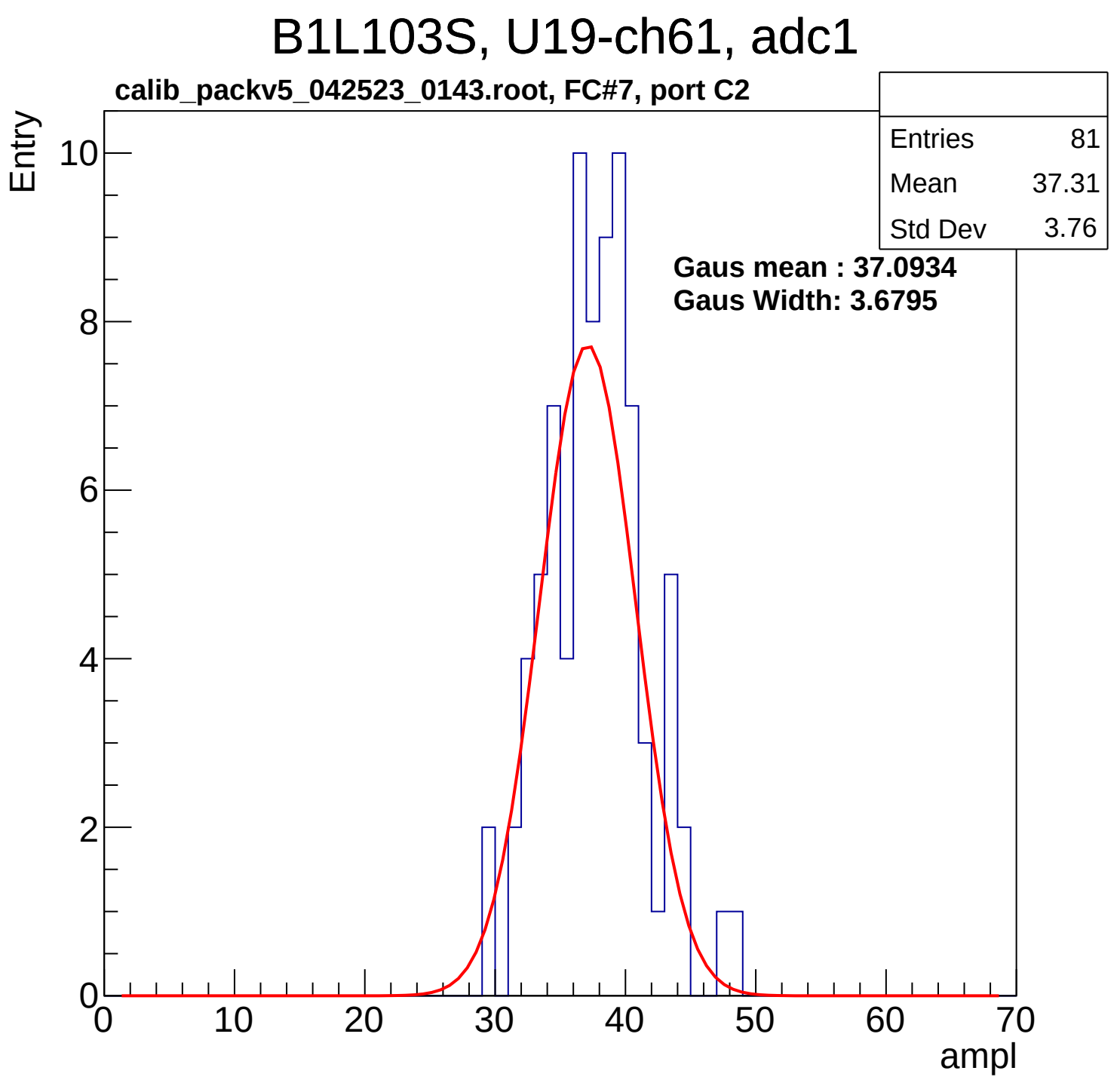
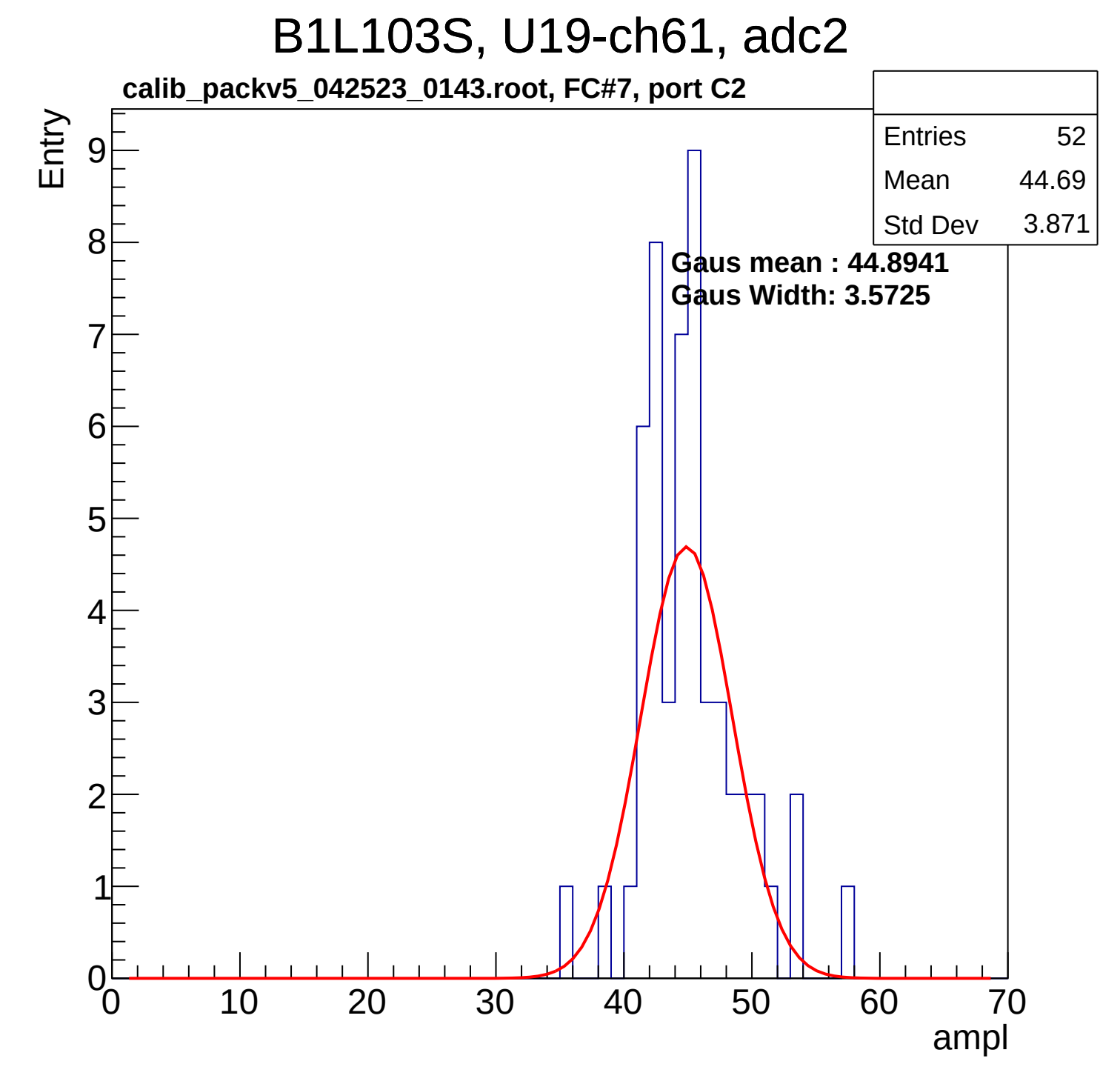

B1L103S, U19-ch61, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 37.31
Std Dev 3.76
Gaus mean : 37.0934
Gaus Width: 3.6795
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch61, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 44.69
Std Dev 3.871
Gaus mean : 44.8941
Gaus Width: 3.5725
Entry
ampl
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2
3
4
5
6
7
8
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0
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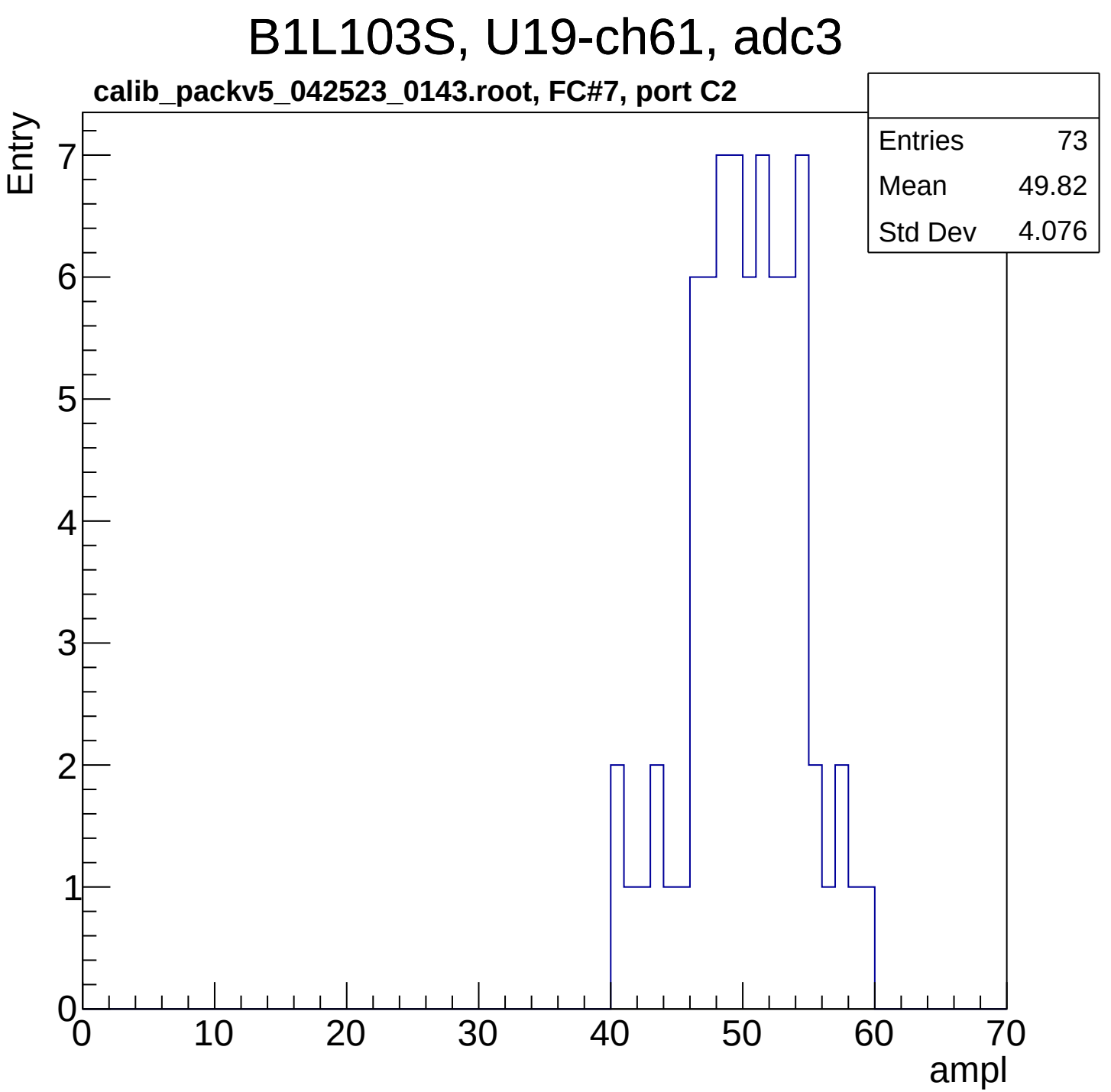
B1L103S, U19-ch61, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.82
Std Dev 4.076
Entry
ampl
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3
4
5
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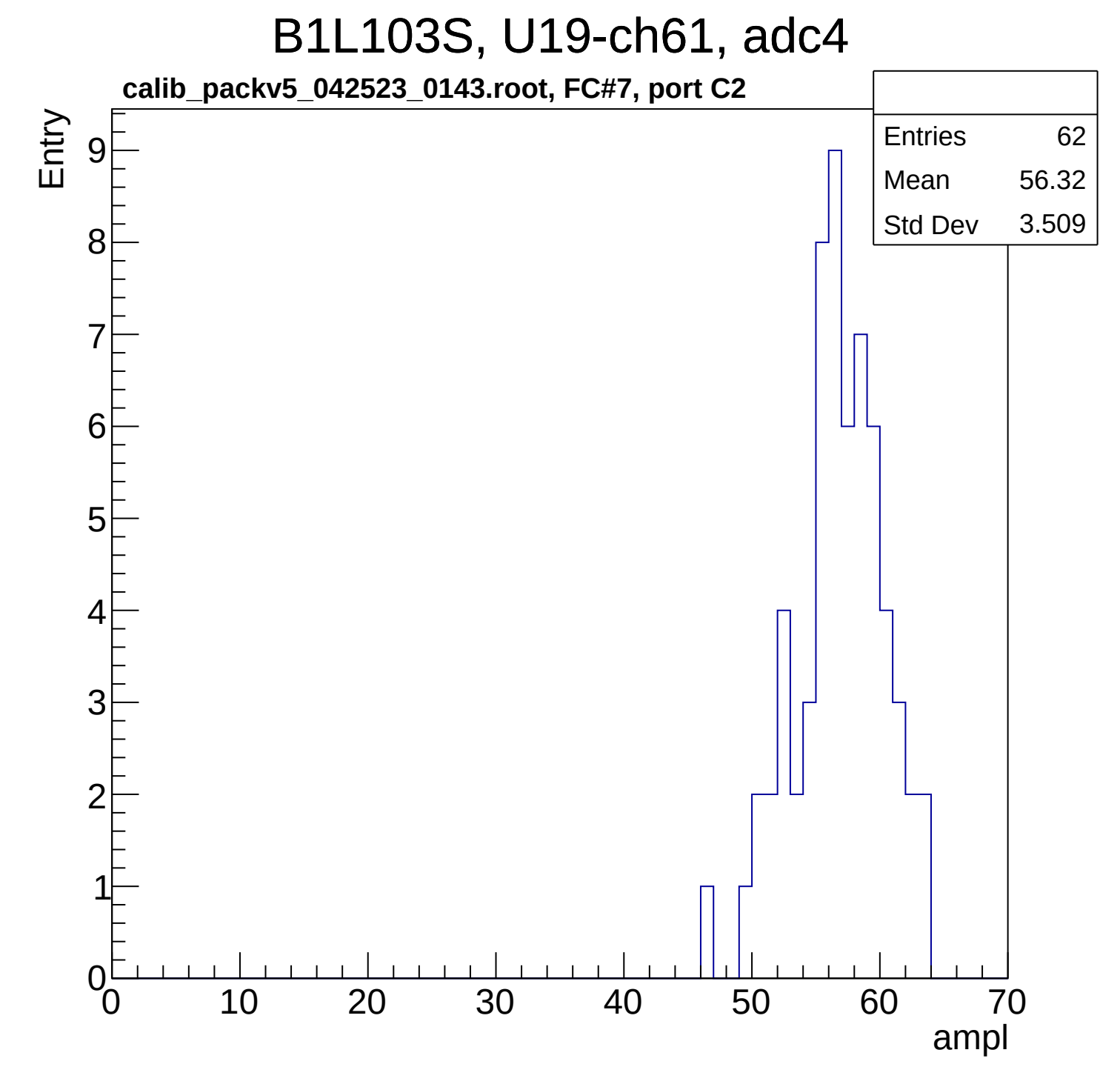
B1L103S, U19-ch61, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 56.32
Std Dev 3.509
Entry
ampl
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3
4
5
6
7
8
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0
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60
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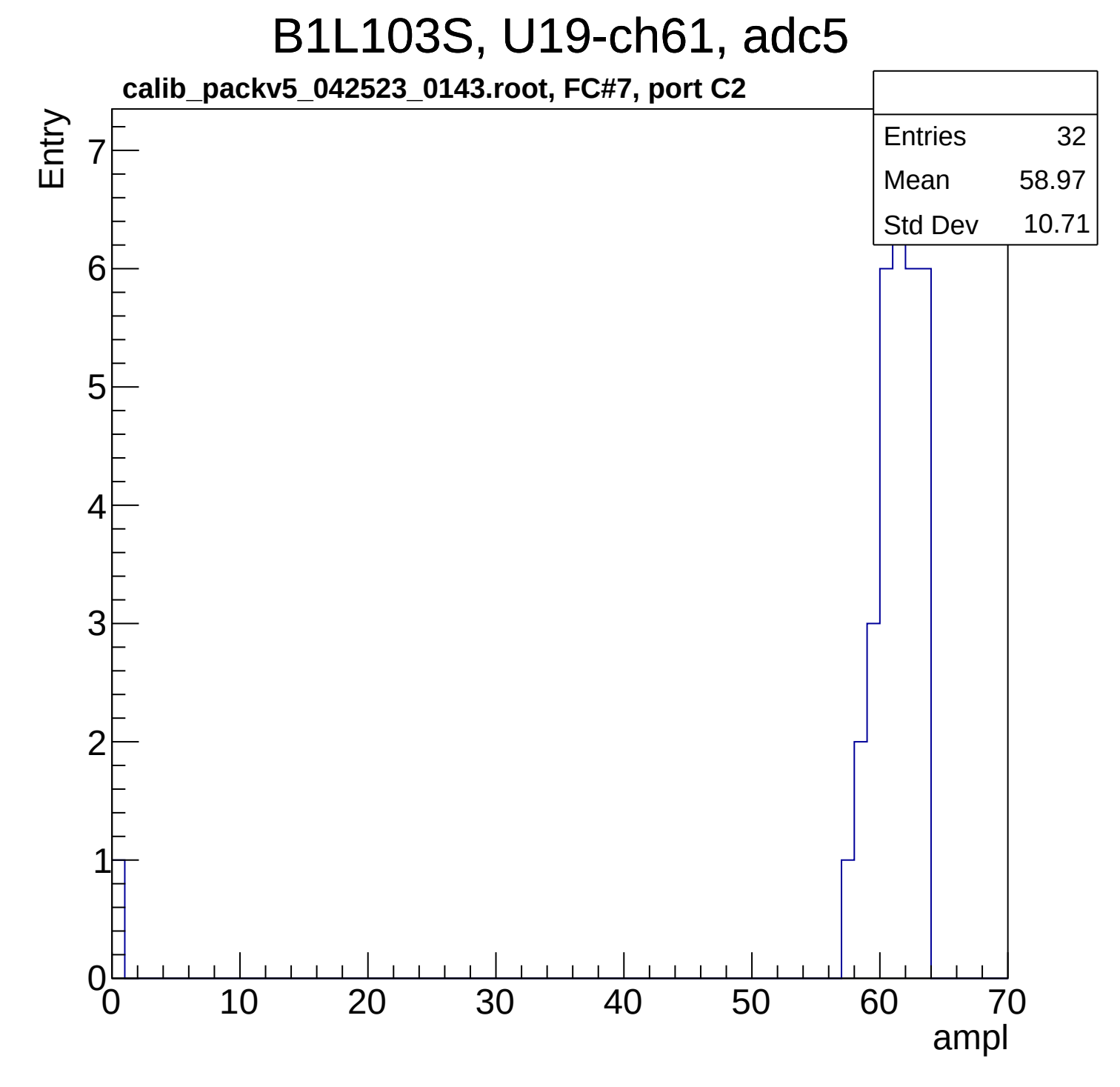
B1L103S, U19-ch61, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.97
Std Dev 10.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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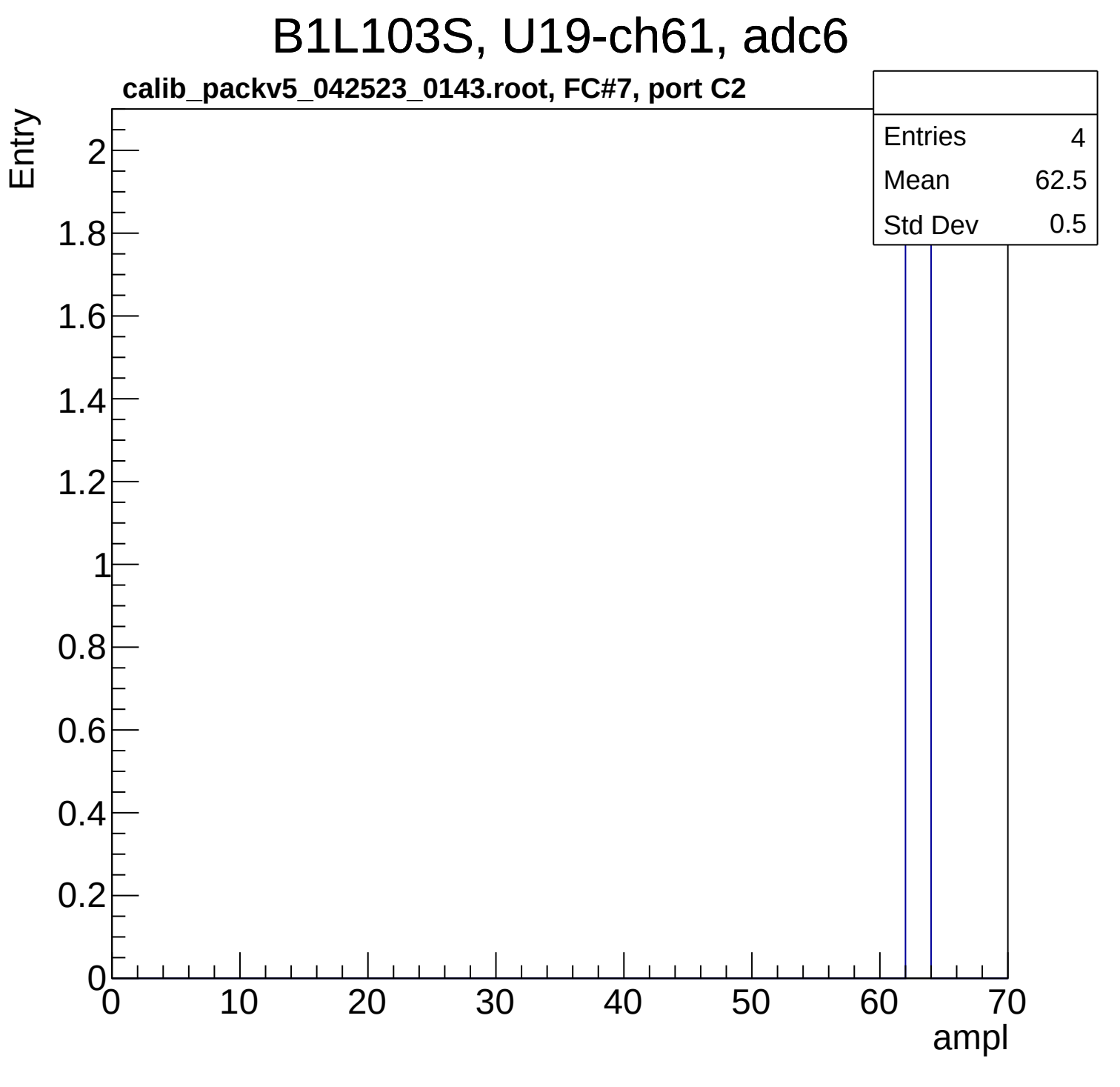
B1L103S, U19-ch61, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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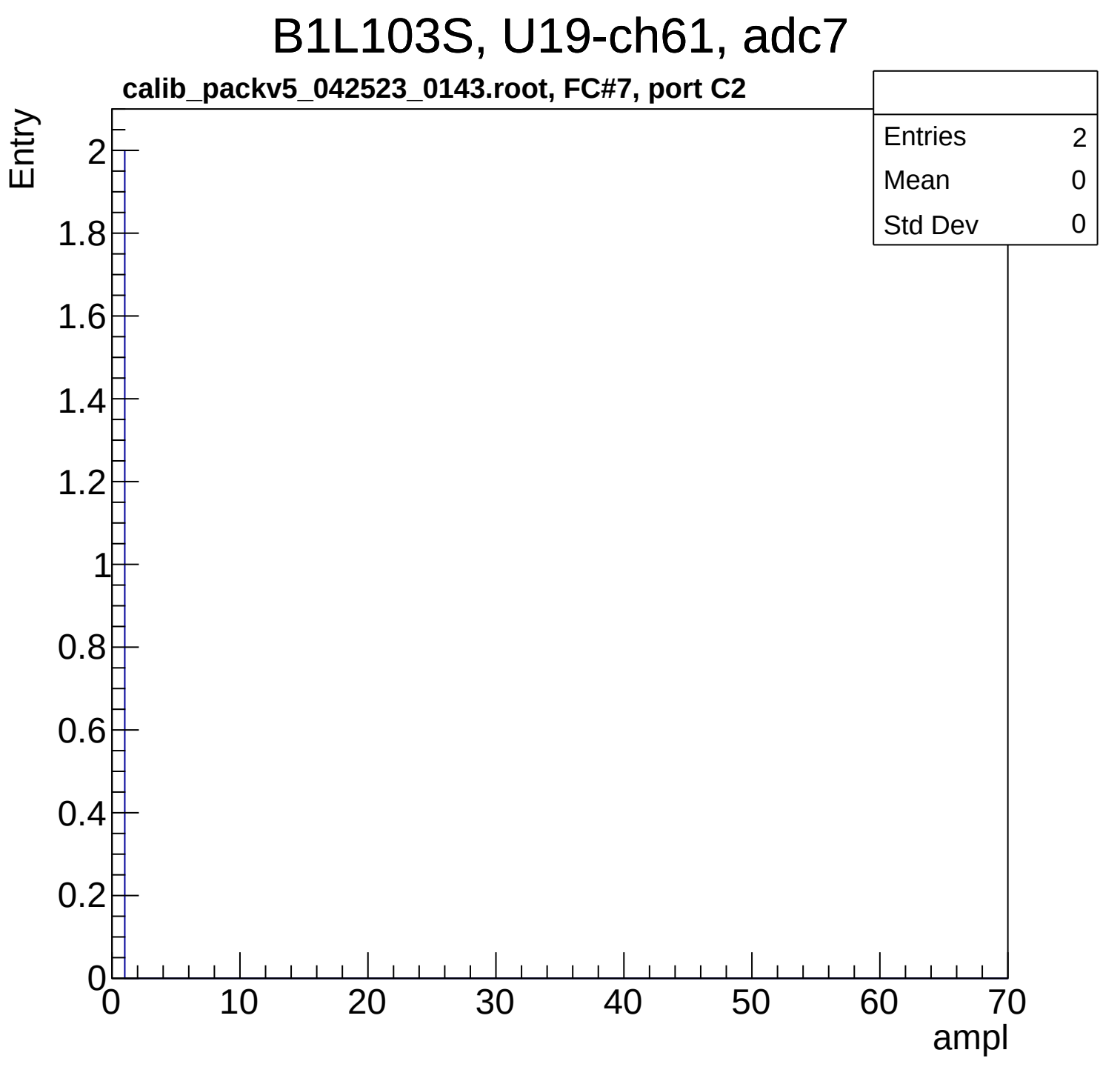
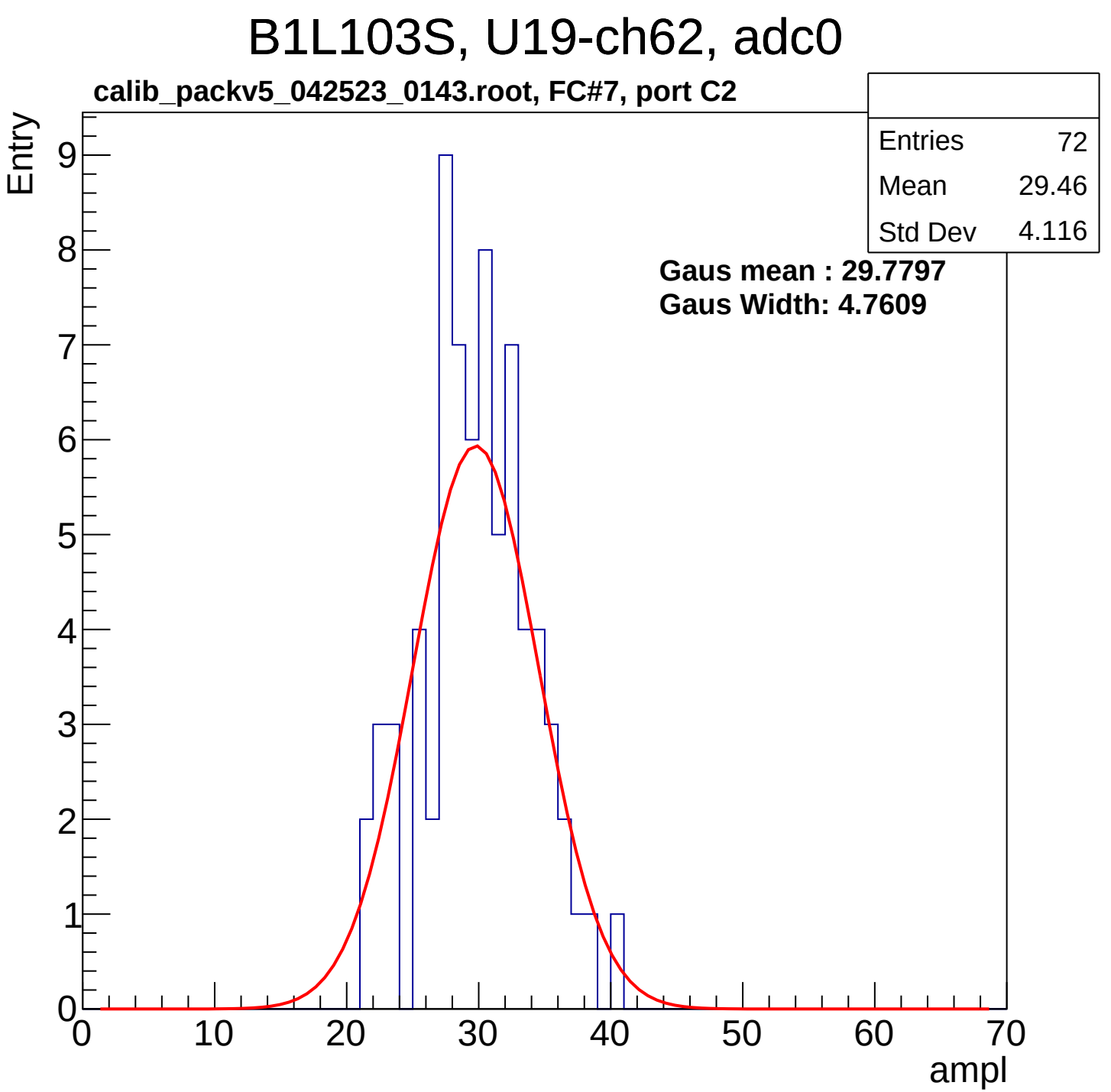

B1L103S, U19-ch61, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch62, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 29.46
Std Dev 4.116
Gaus mean : 29.7797
Gaus Width: 4.7609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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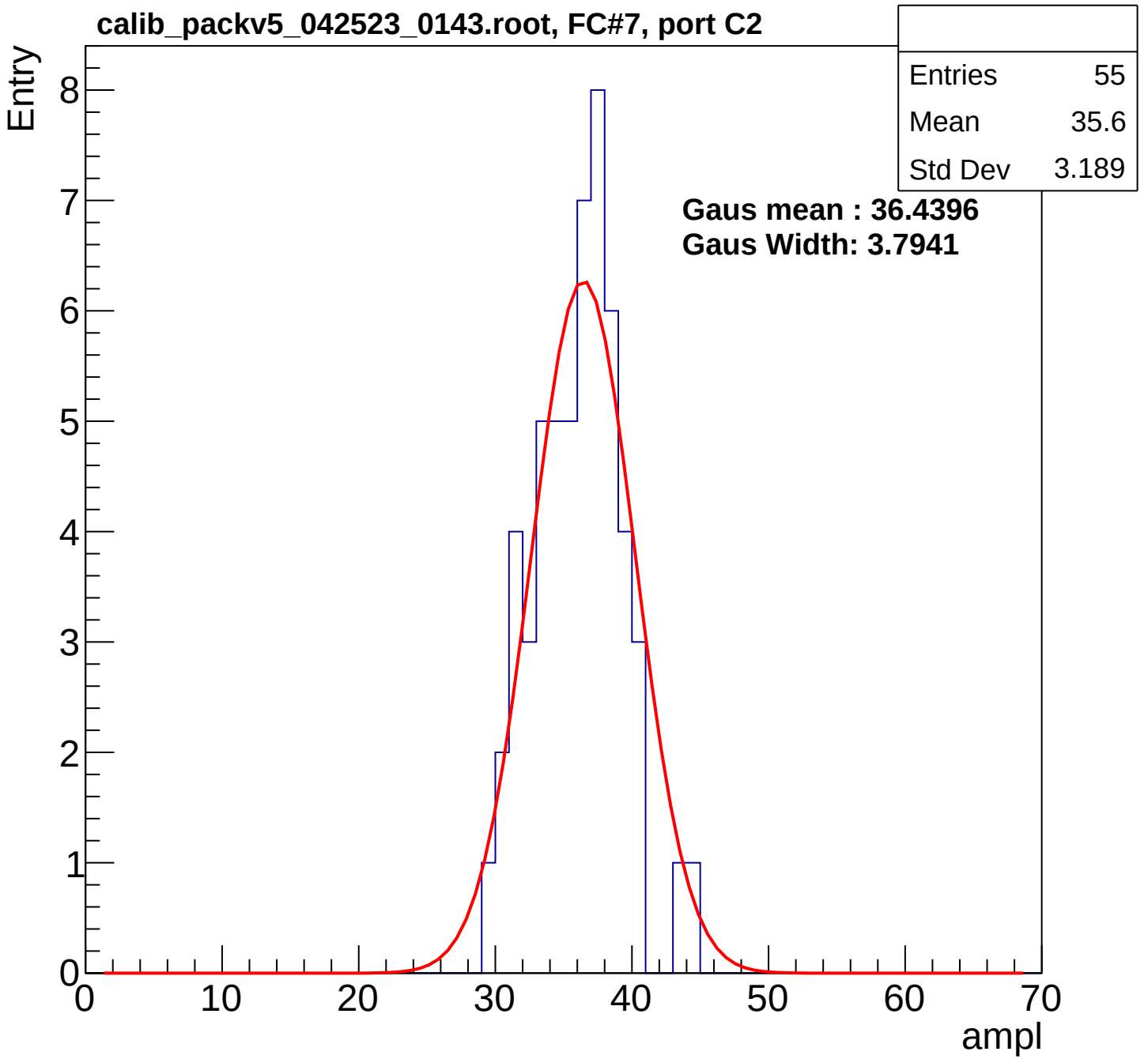
B1L103S, U19-ch62, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 35.6
Std Dev 3.189
Gaus mean : 36.4396
Gaus Width: 3.7941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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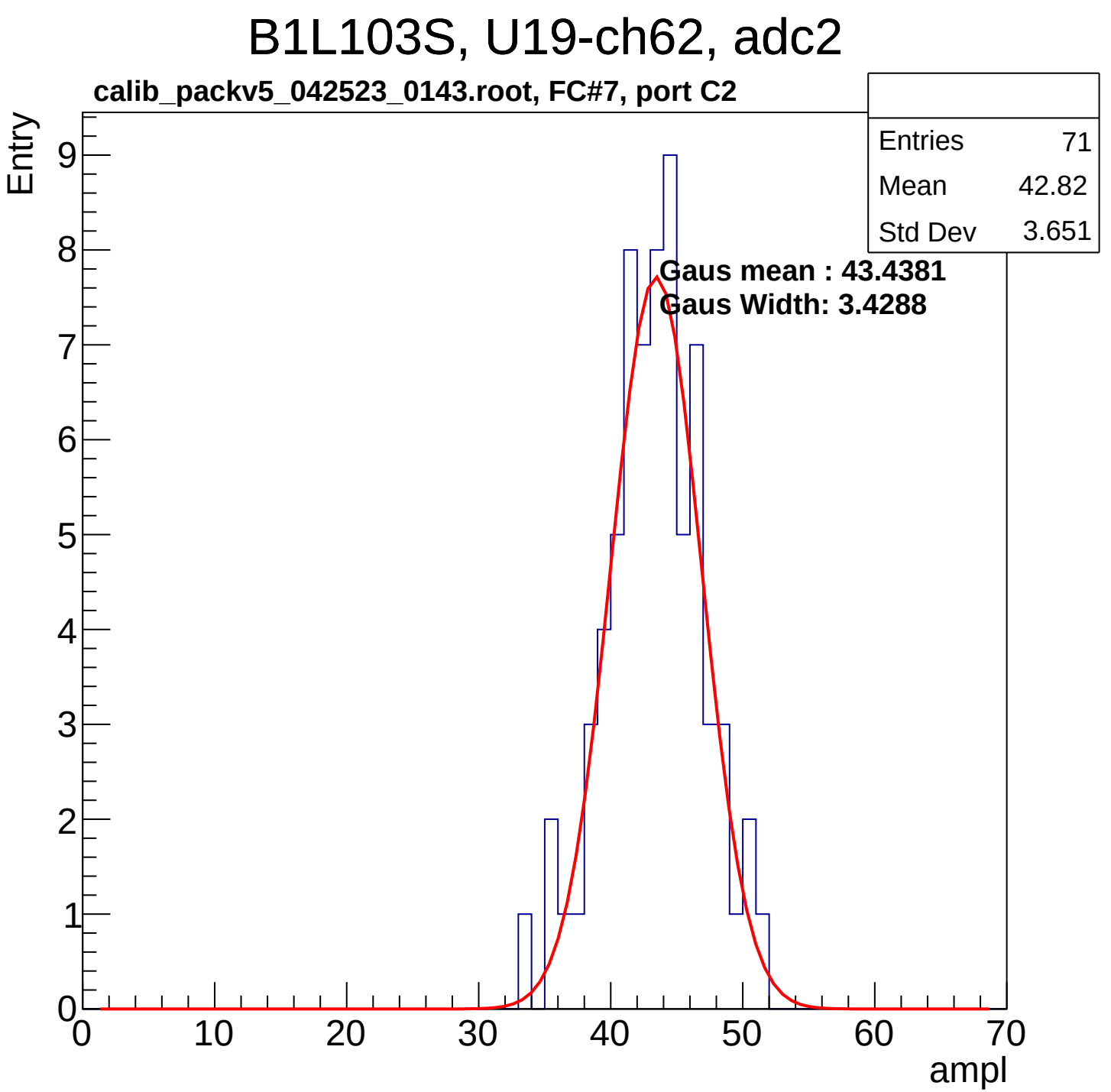
B1L103S, U19-ch62, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.82
Std Dev 3.651
Gaus mean : 43.4381
Gaus Width: 3.4288
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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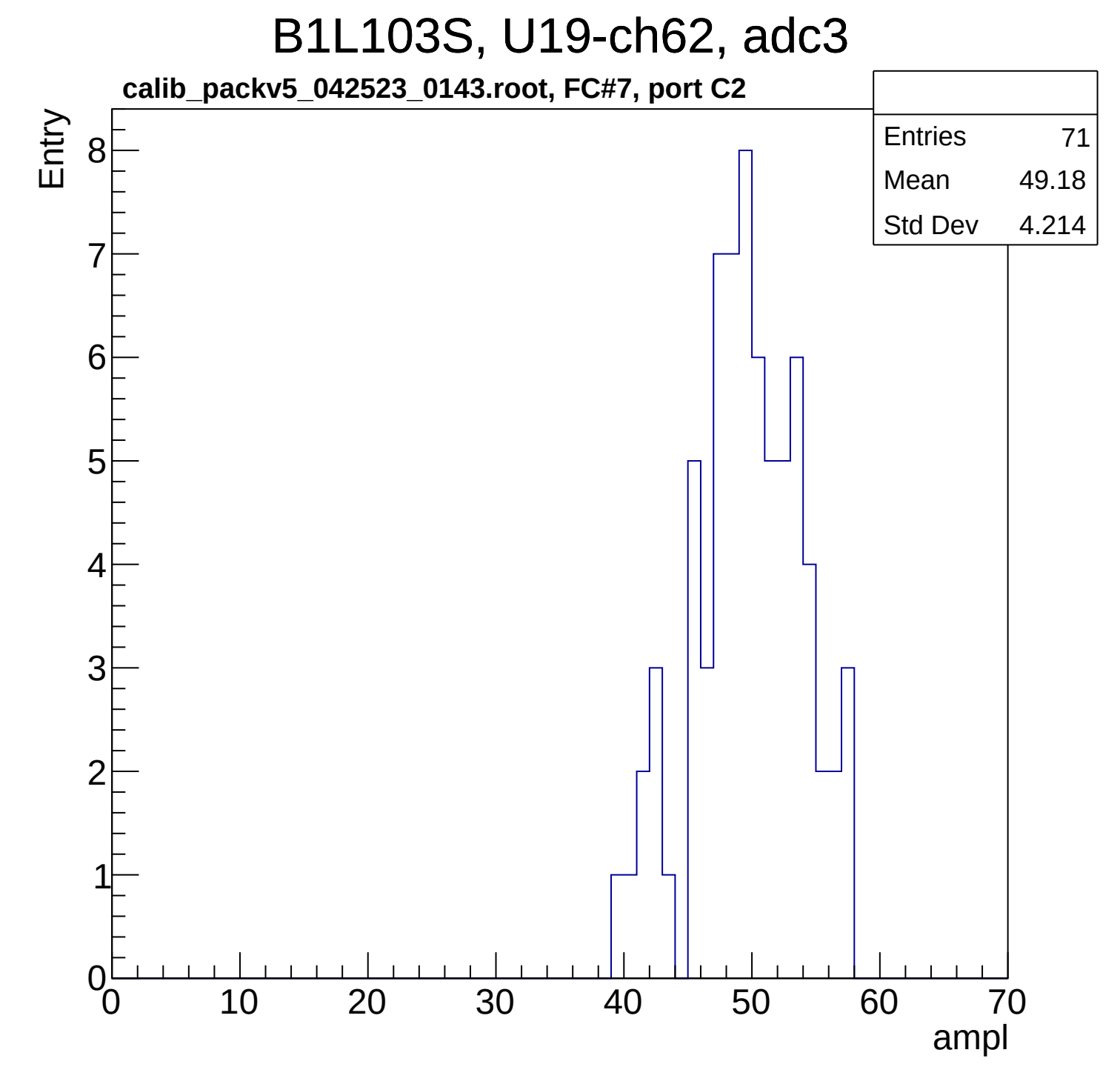
B1L103S, U19-ch62, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.18
Std Dev 4.214
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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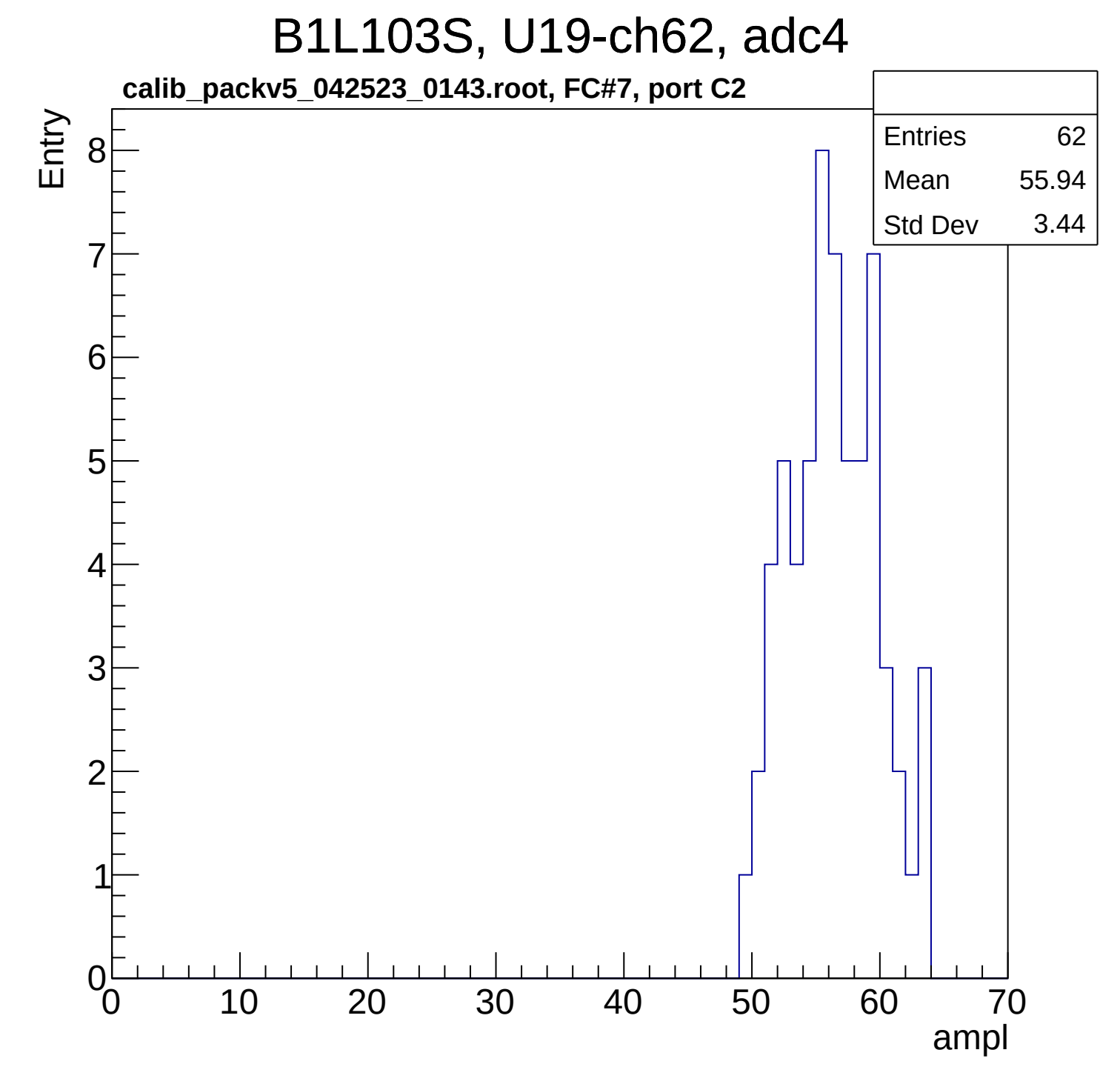
B1L103S, U19-ch62, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.94
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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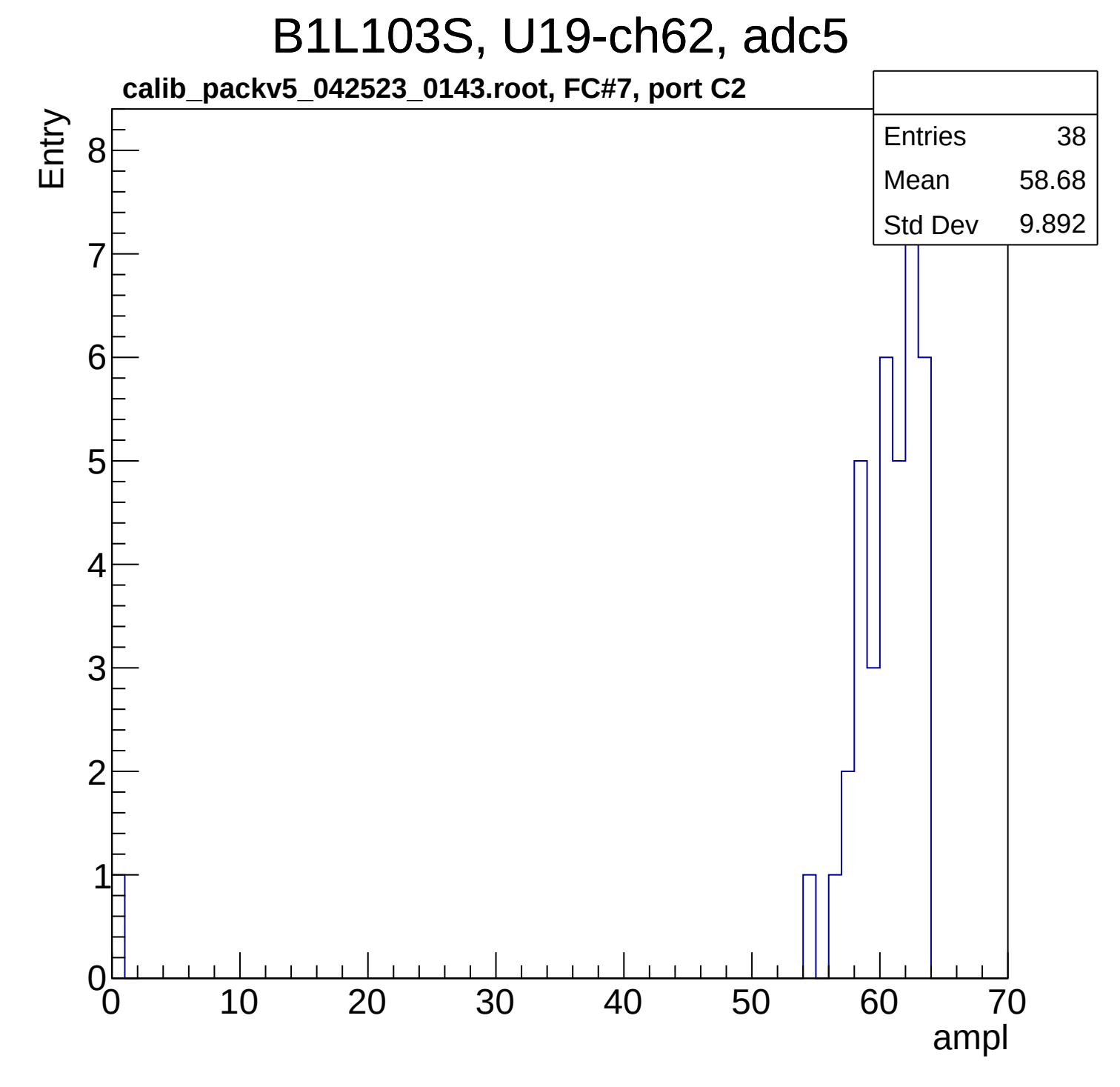
B1L103S, U19-ch62, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.68
Std Dev 9.892
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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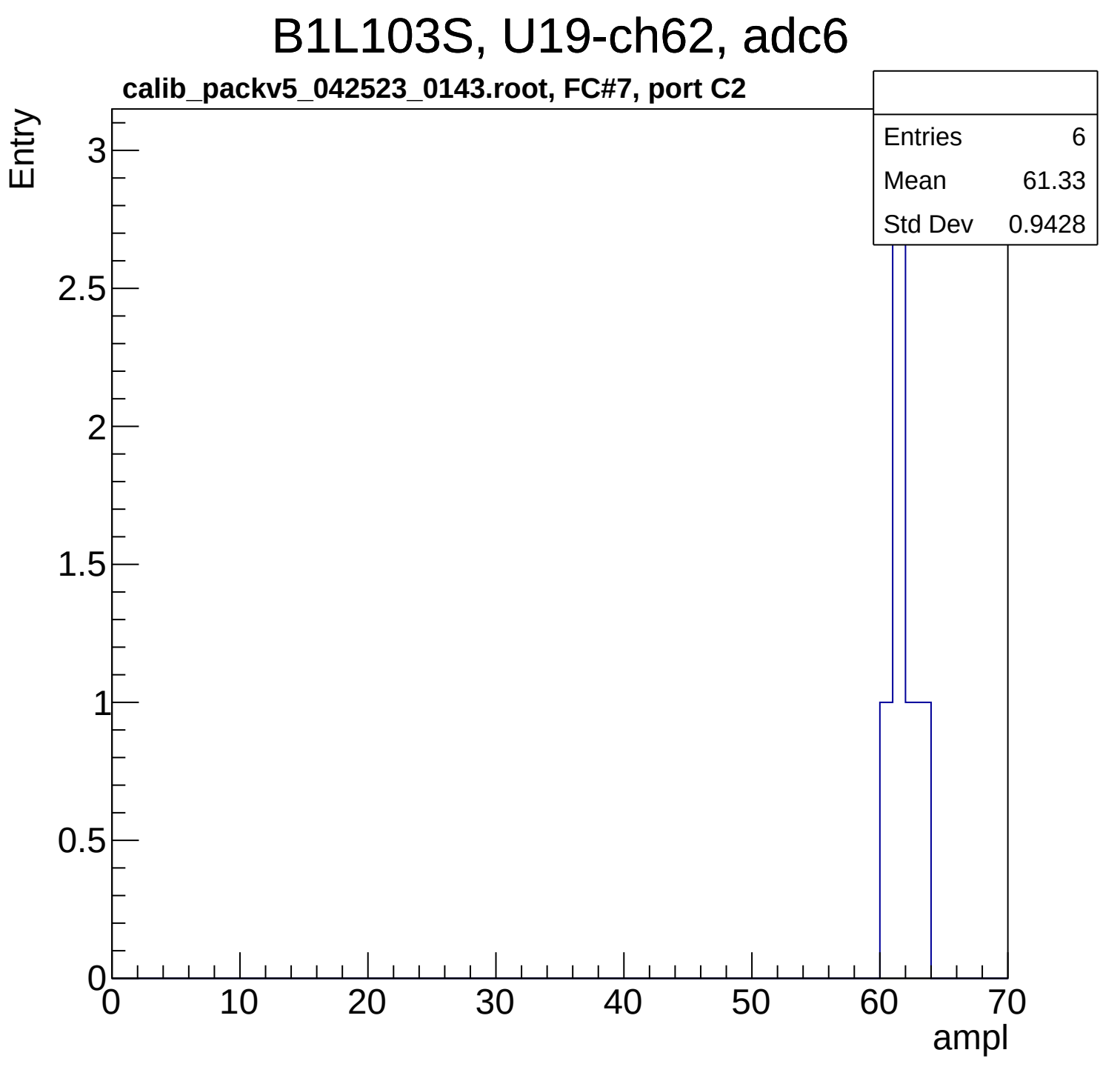
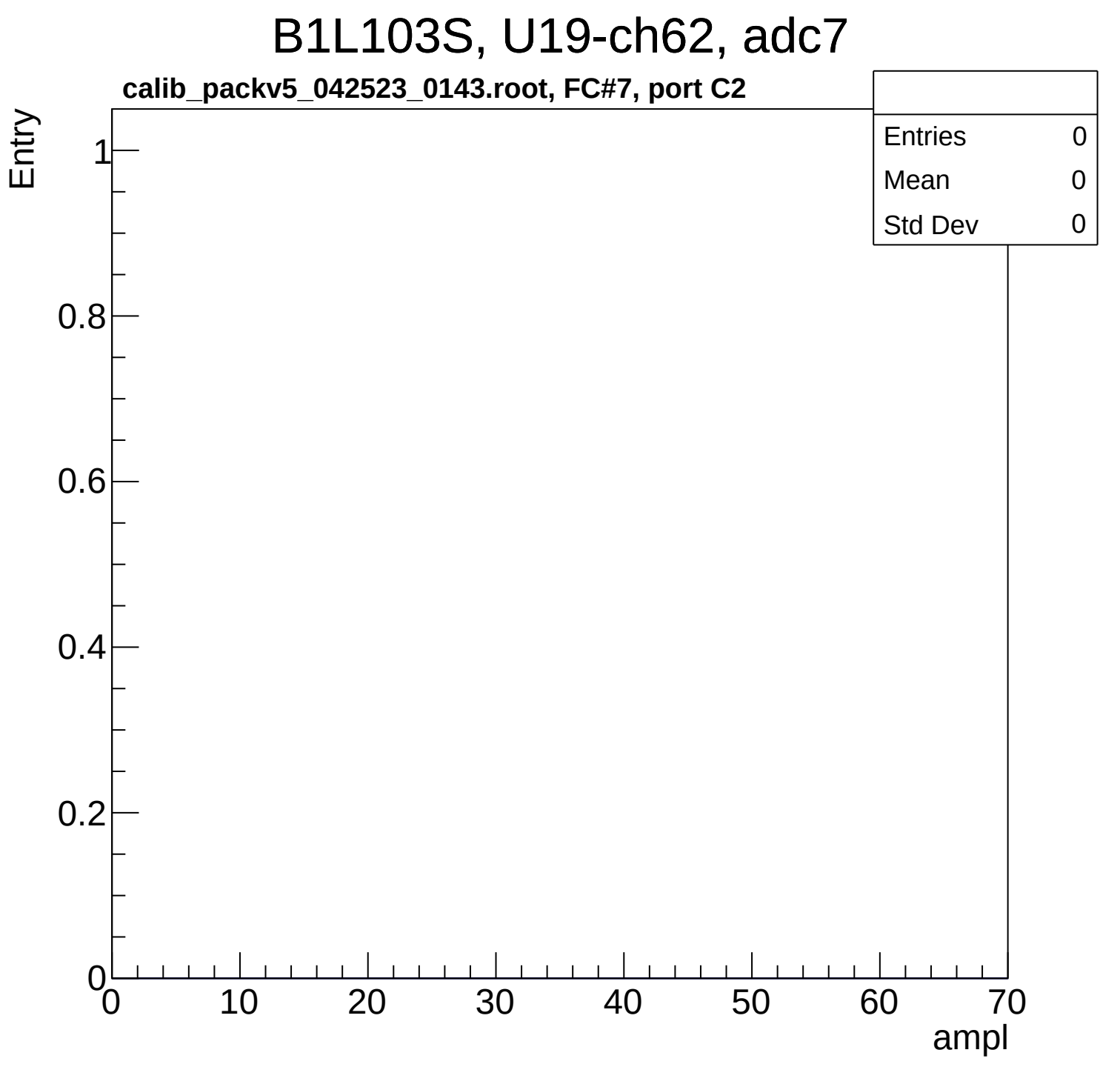
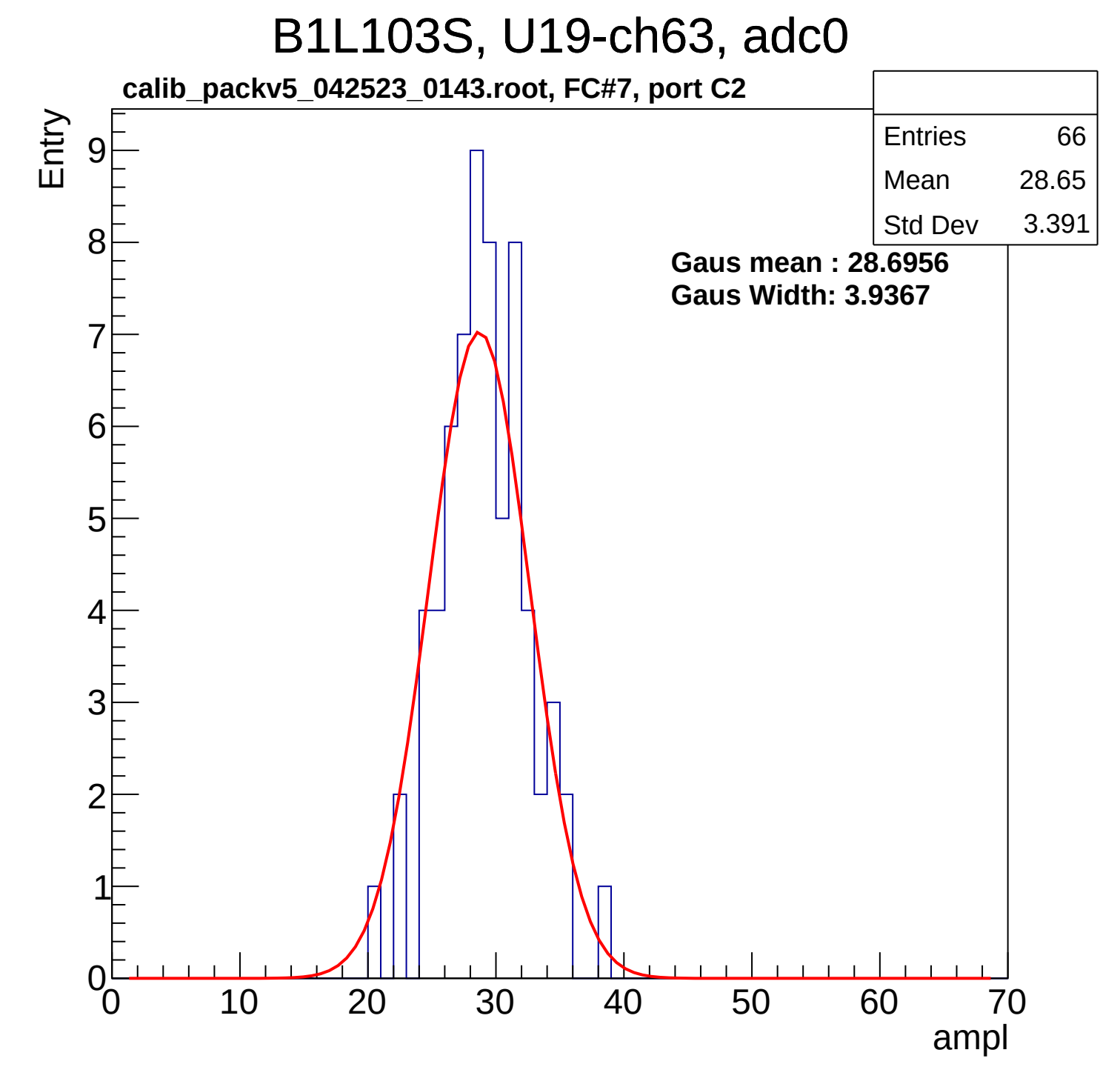

B1L103S, U19-ch62, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch62, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch63, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 28.65
Std Dev 3.391
Gaus mean : 28.6956
Gaus Width: 3.9367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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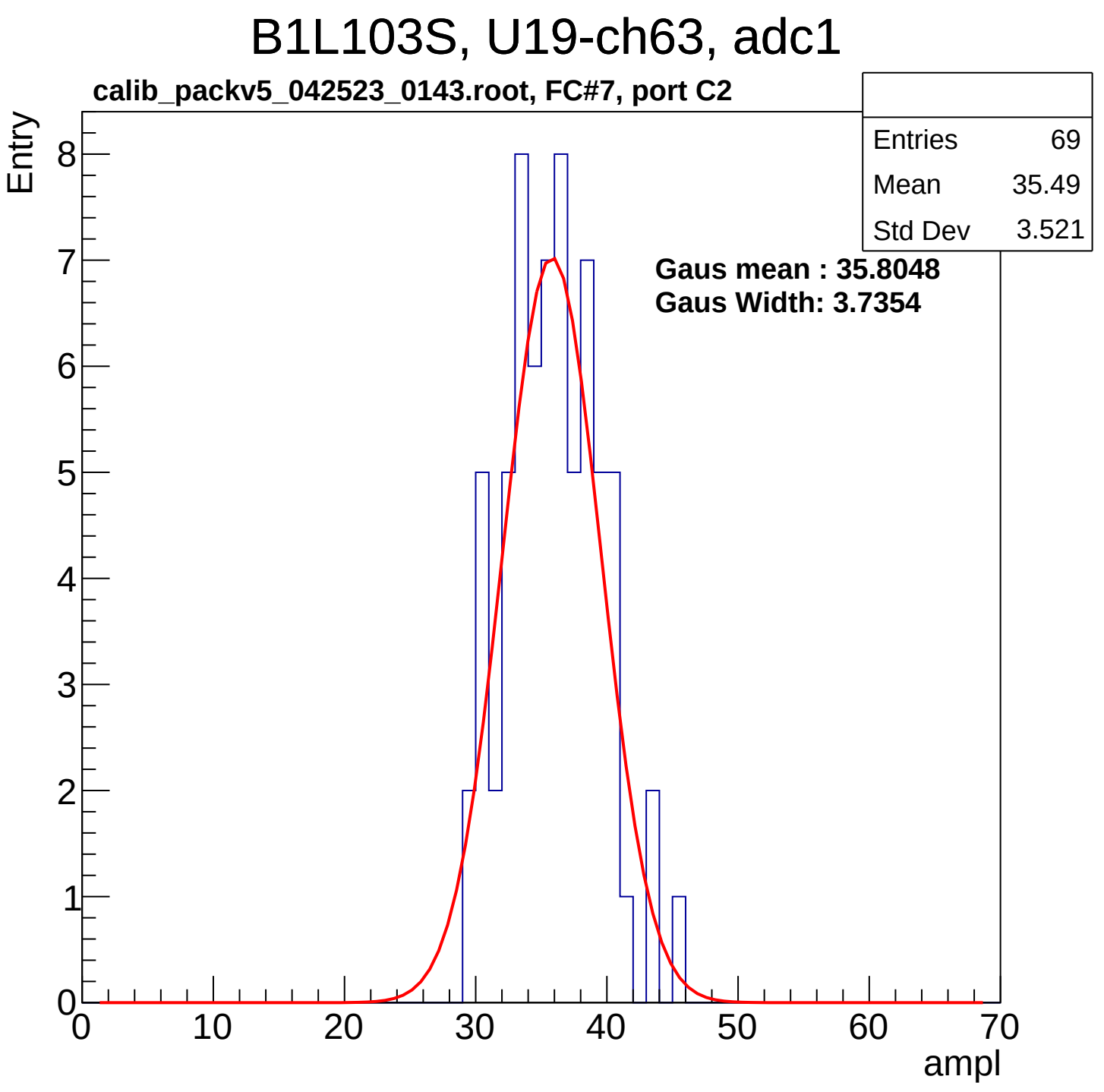
60
70


B1L103S, U19-ch63, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 35.49
Std Dev 3.521
Gaus mean : 35.8048
Gaus Width: 3.7354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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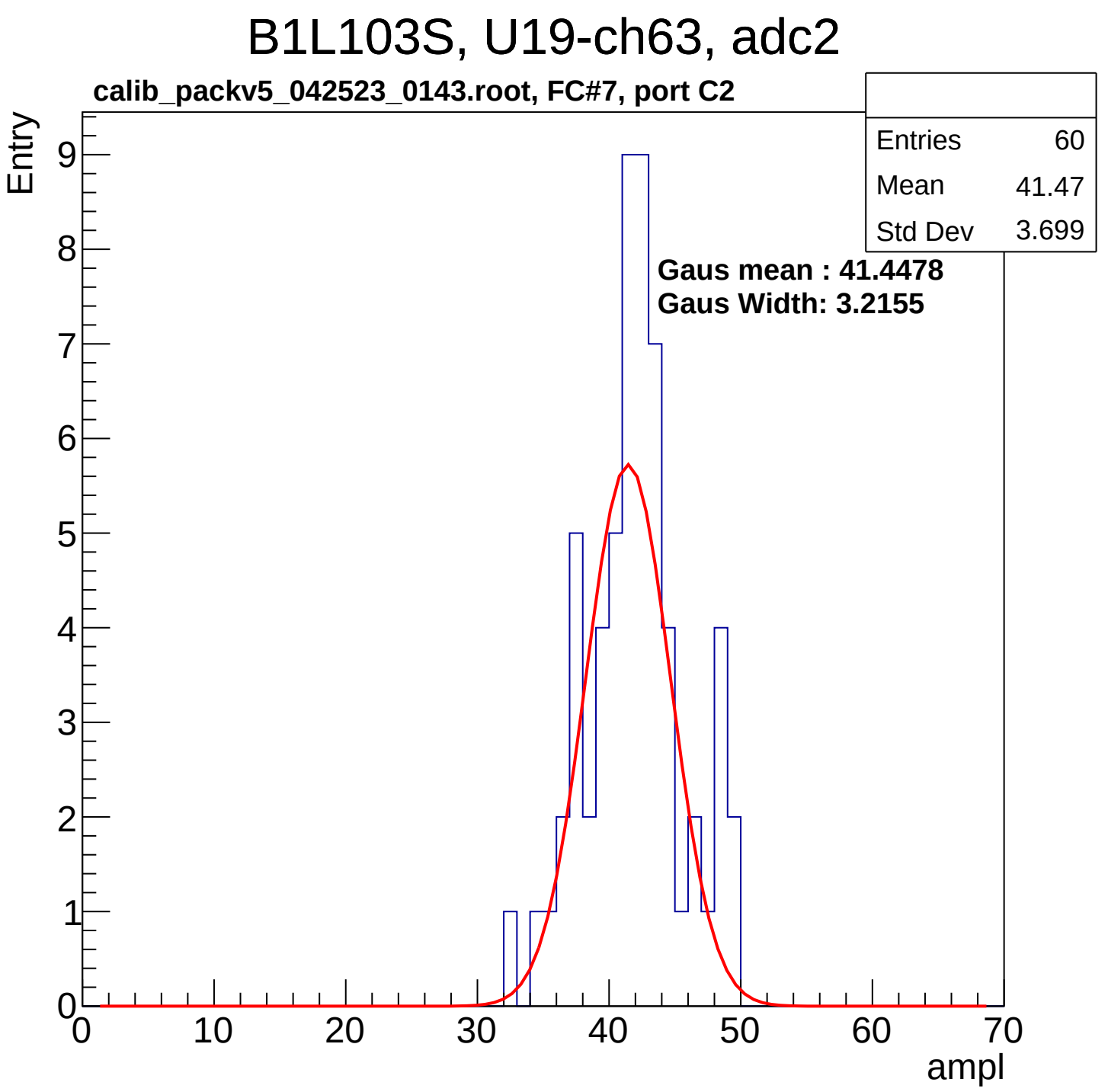
B1L103S, U19-ch63, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 41.47
Std Dev 3.699
Gaus mean : 41.4478
Gaus Width: 3.2155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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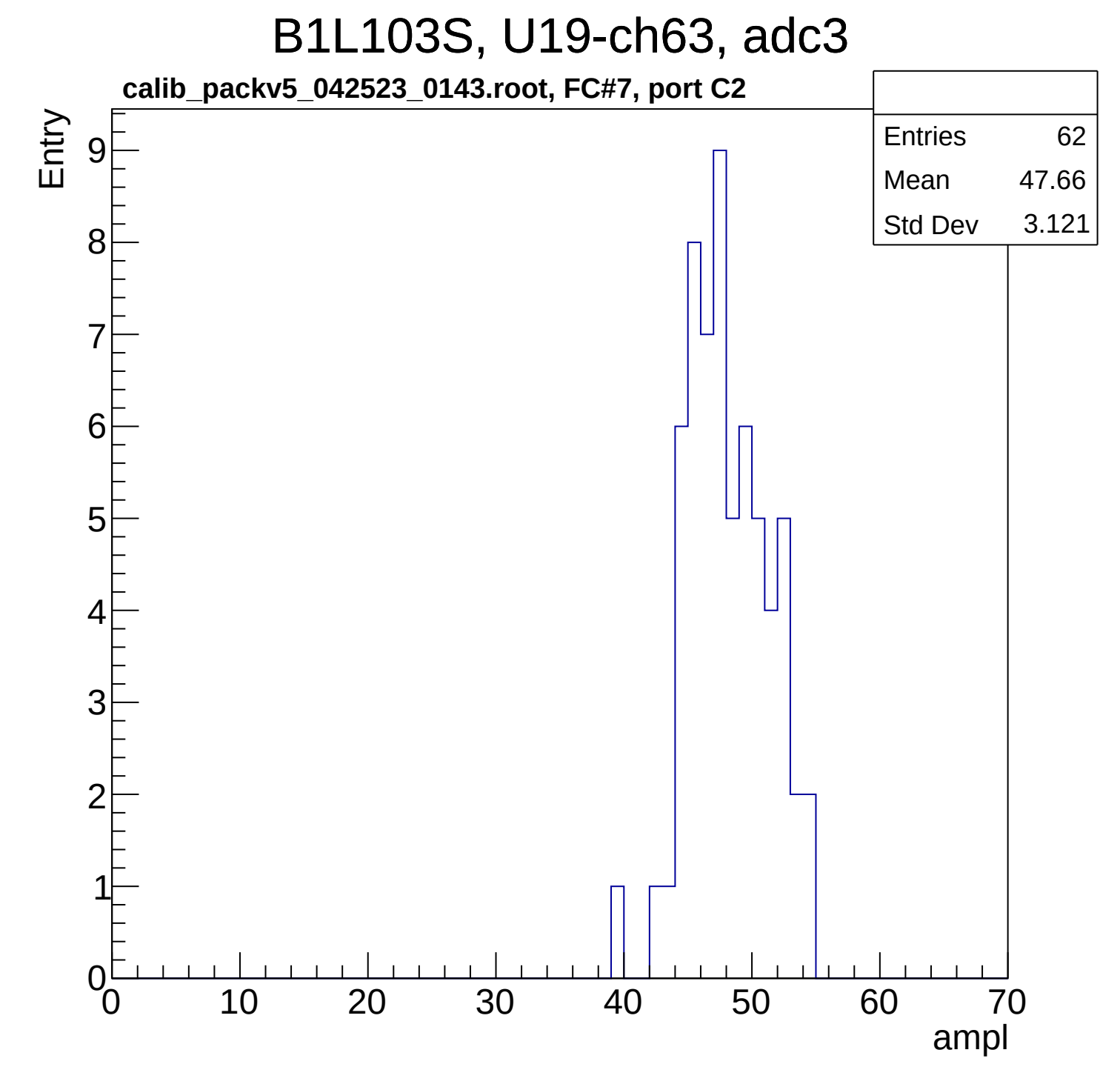
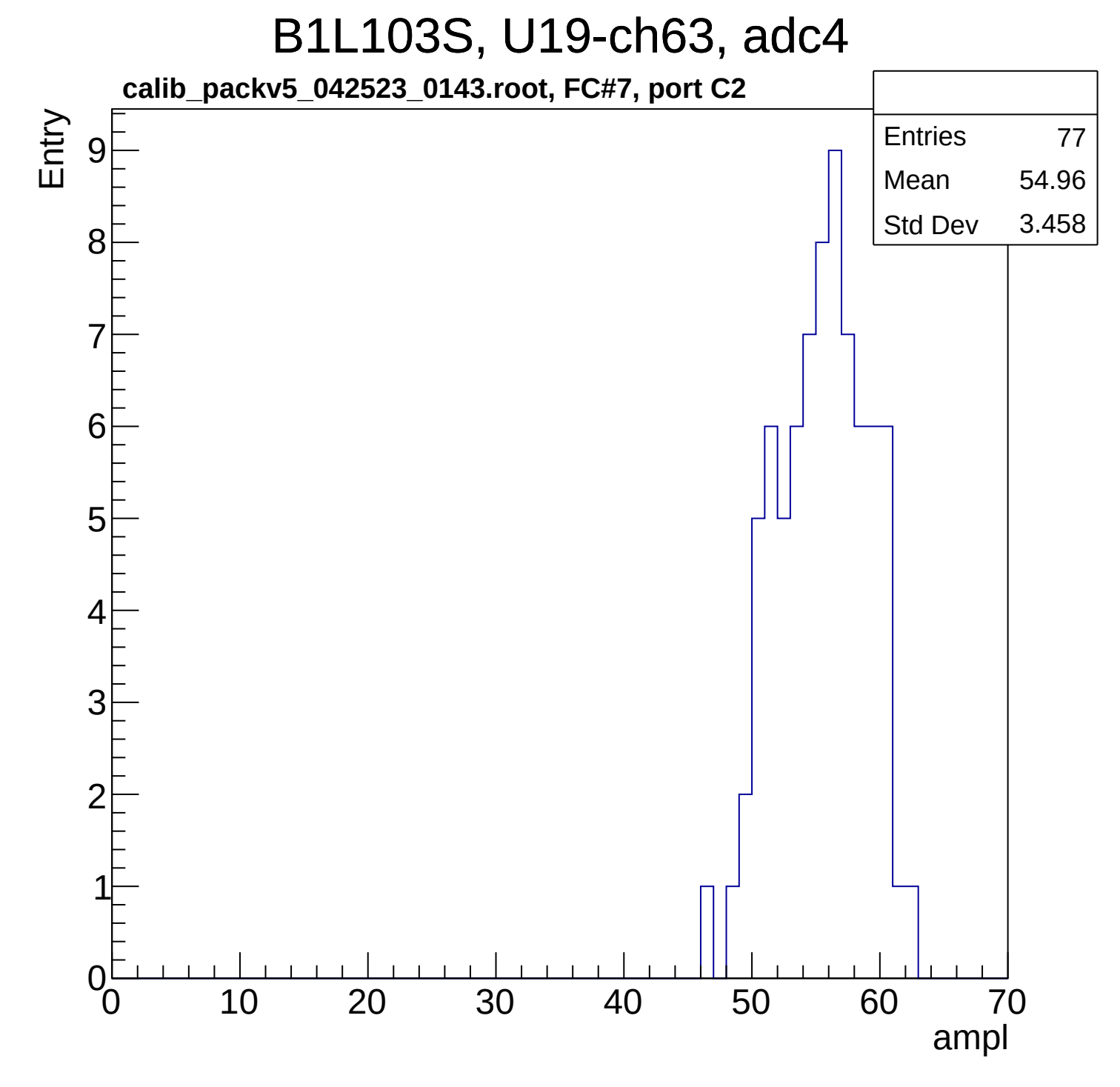

B1L103S, U19-ch63, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 47.66
Std Dev 3.121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch63, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 54.96
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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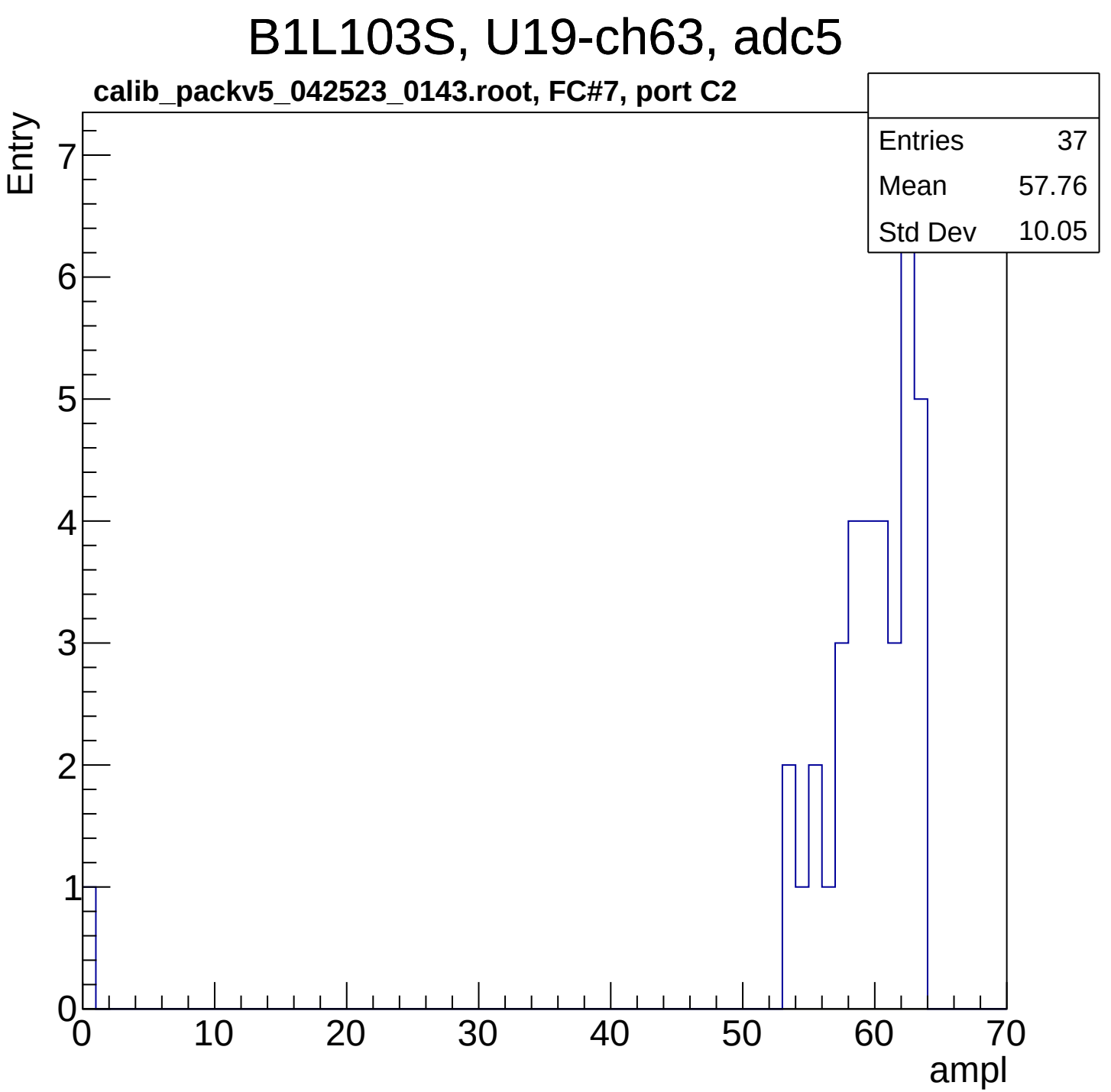
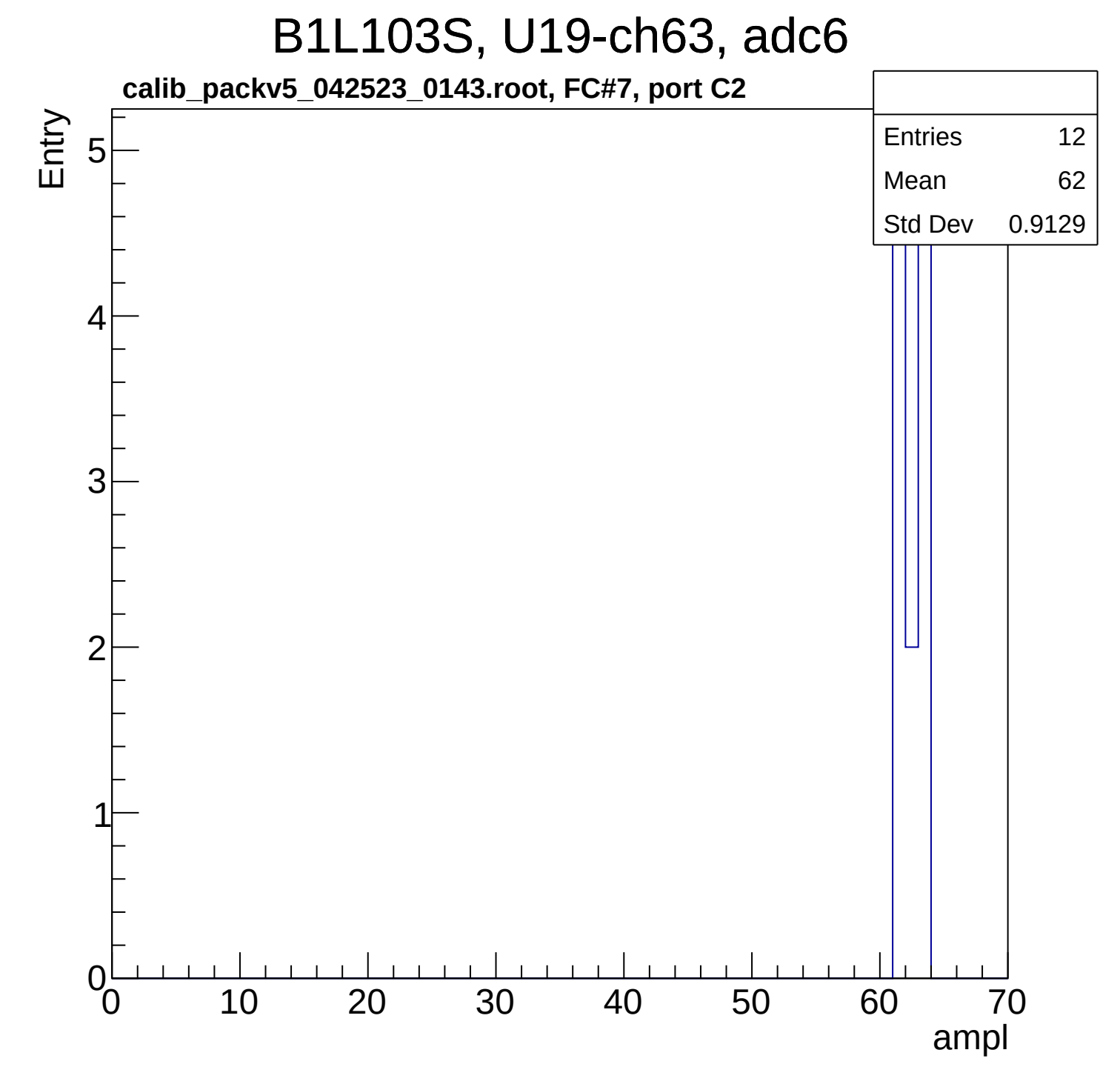
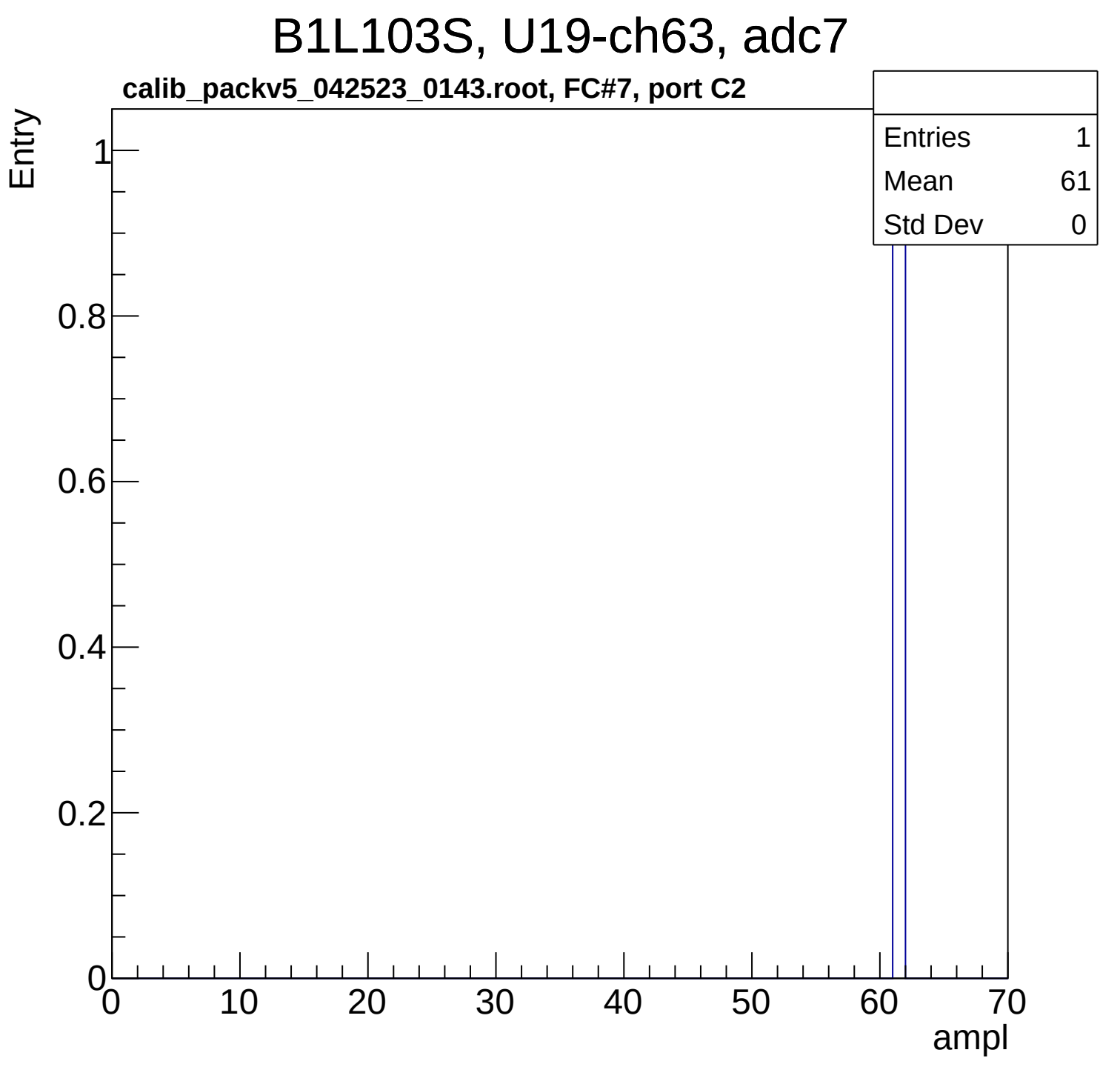
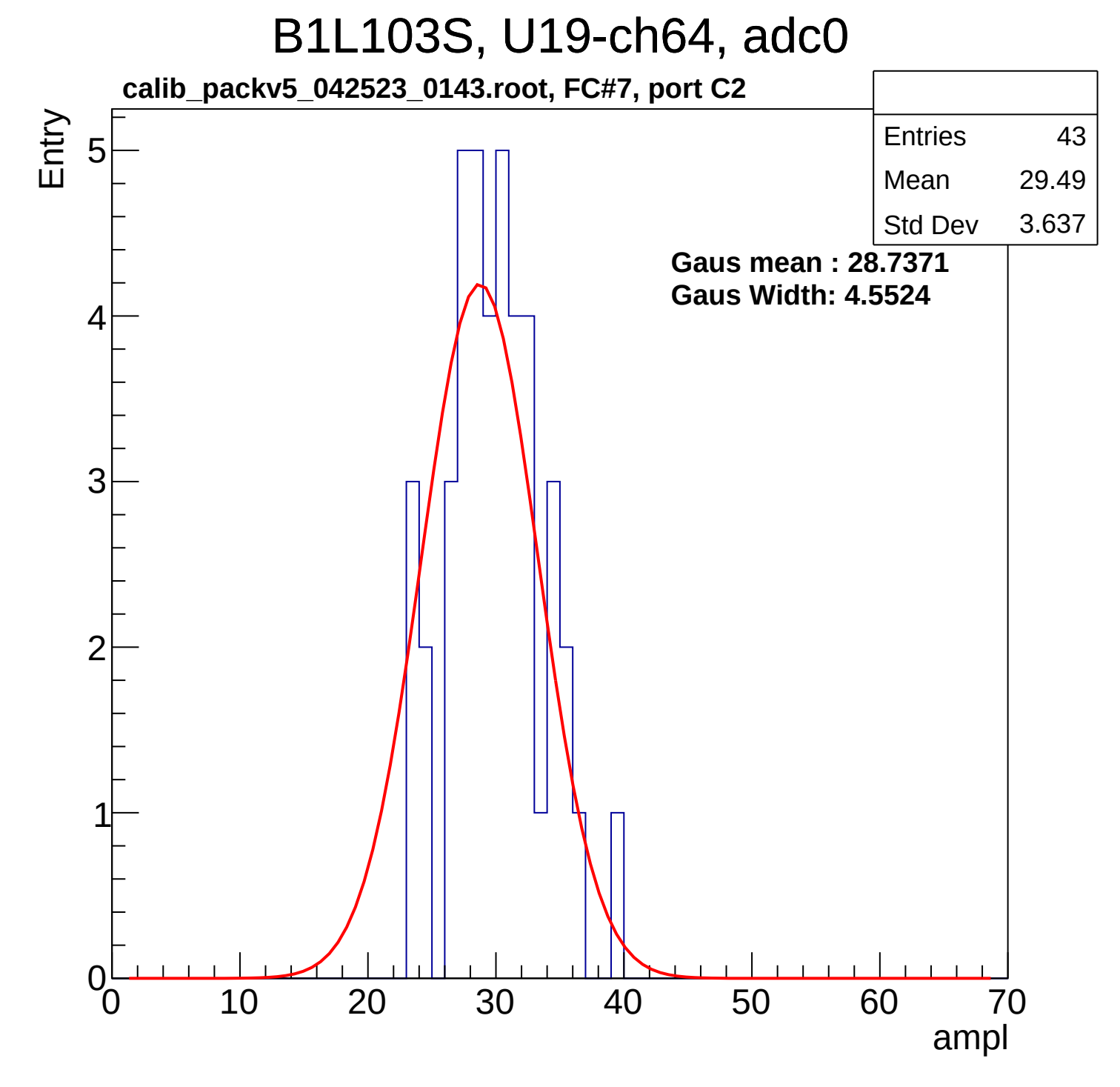
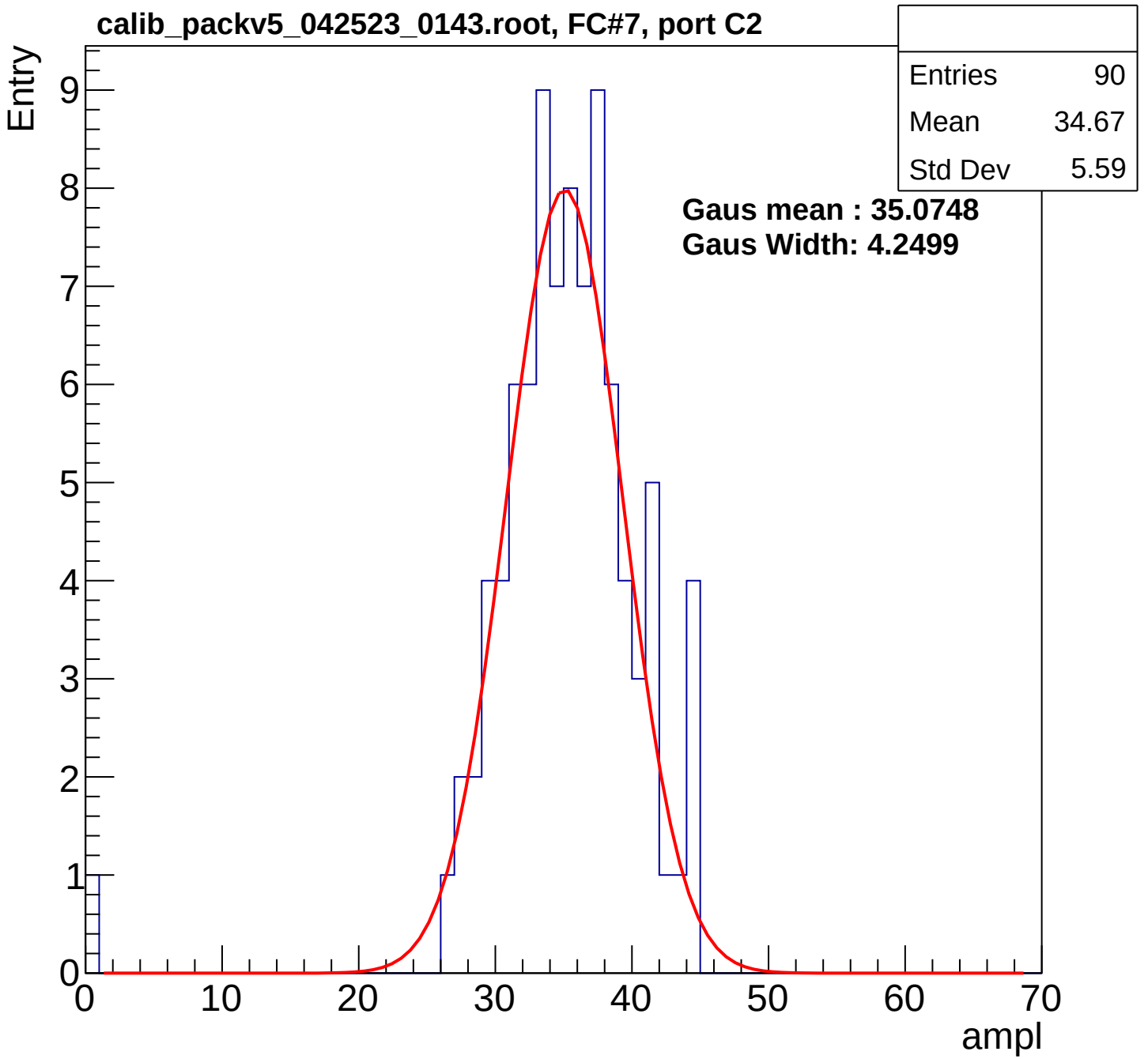
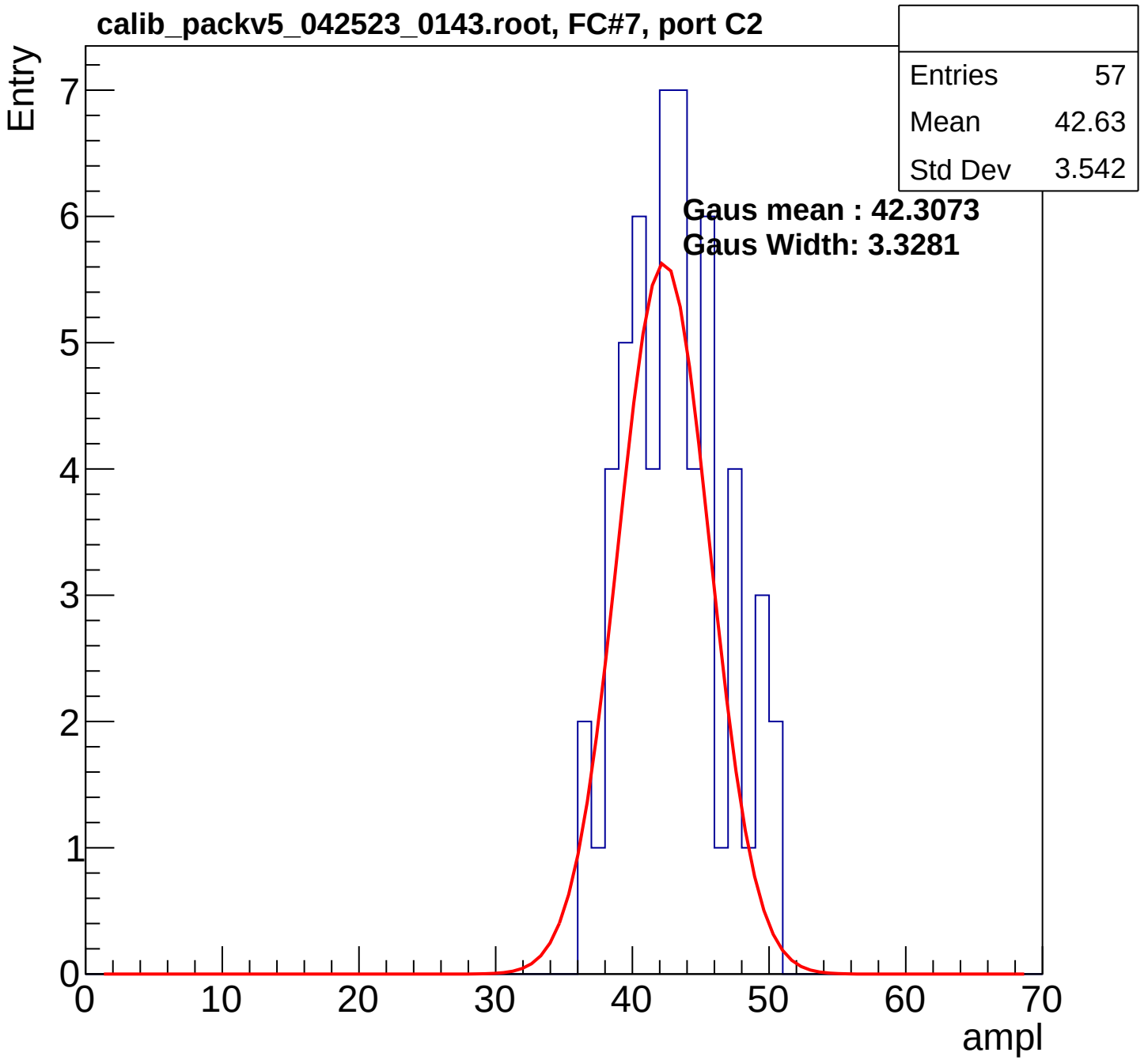

B1L103S, U19-ch63, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 57.76
Std Dev 10.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch63, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 62
Std Dev 0.9129
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch63, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch64, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 29.49
Std Dev 3.637
Gaus mean : 28.7371
Gaus Width: 4.5524
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch64, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 34.67
Std Dev 5.59
Gaus mean : 35.0748
Gaus Width: 4.2499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch64, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.63
Std Dev 3.542
Gaus mean : 42.3073
Gaus Width: 3.3281
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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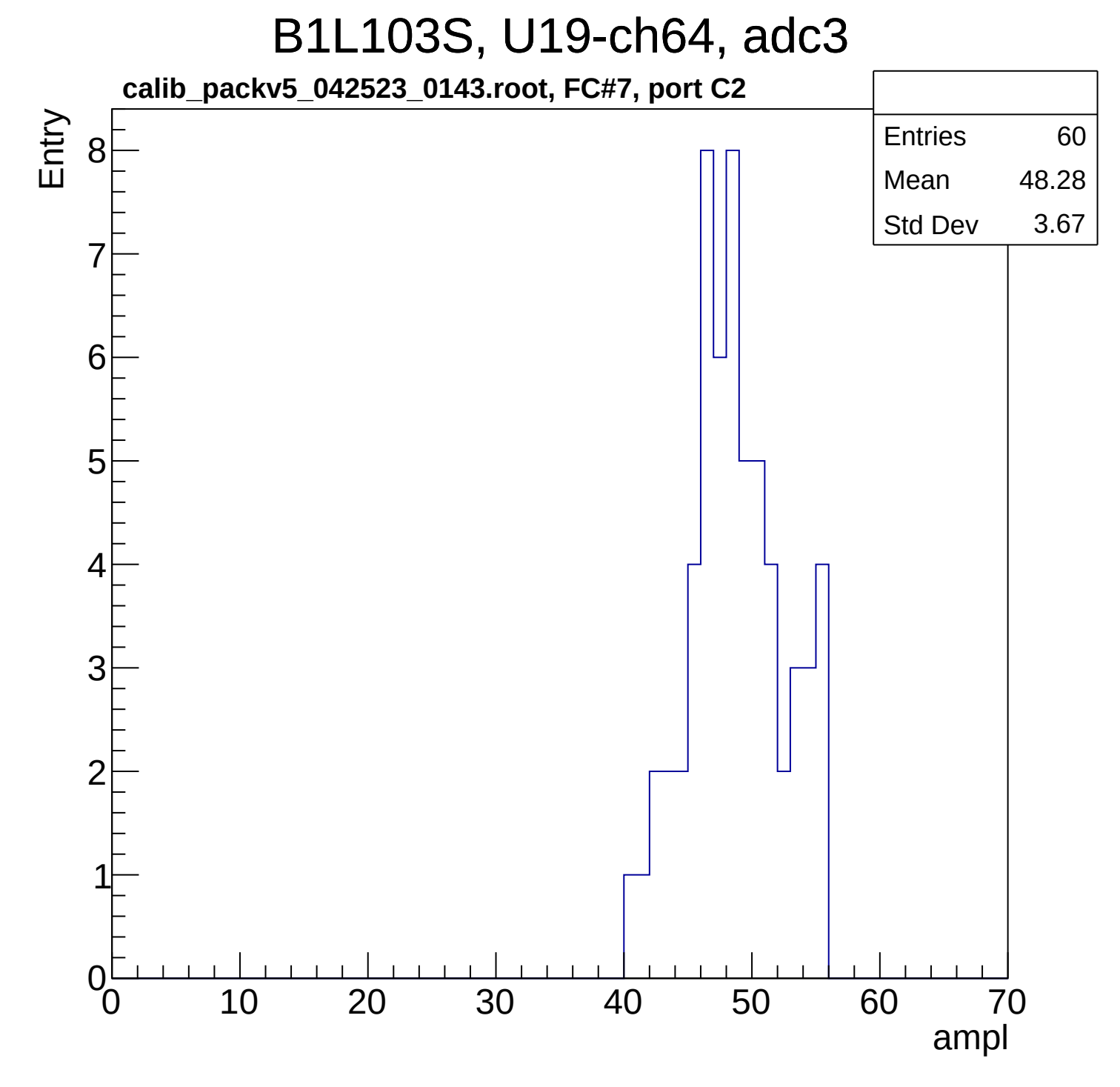
B1L103S, U19-ch64, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.28
Std Dev 3.67
Entry
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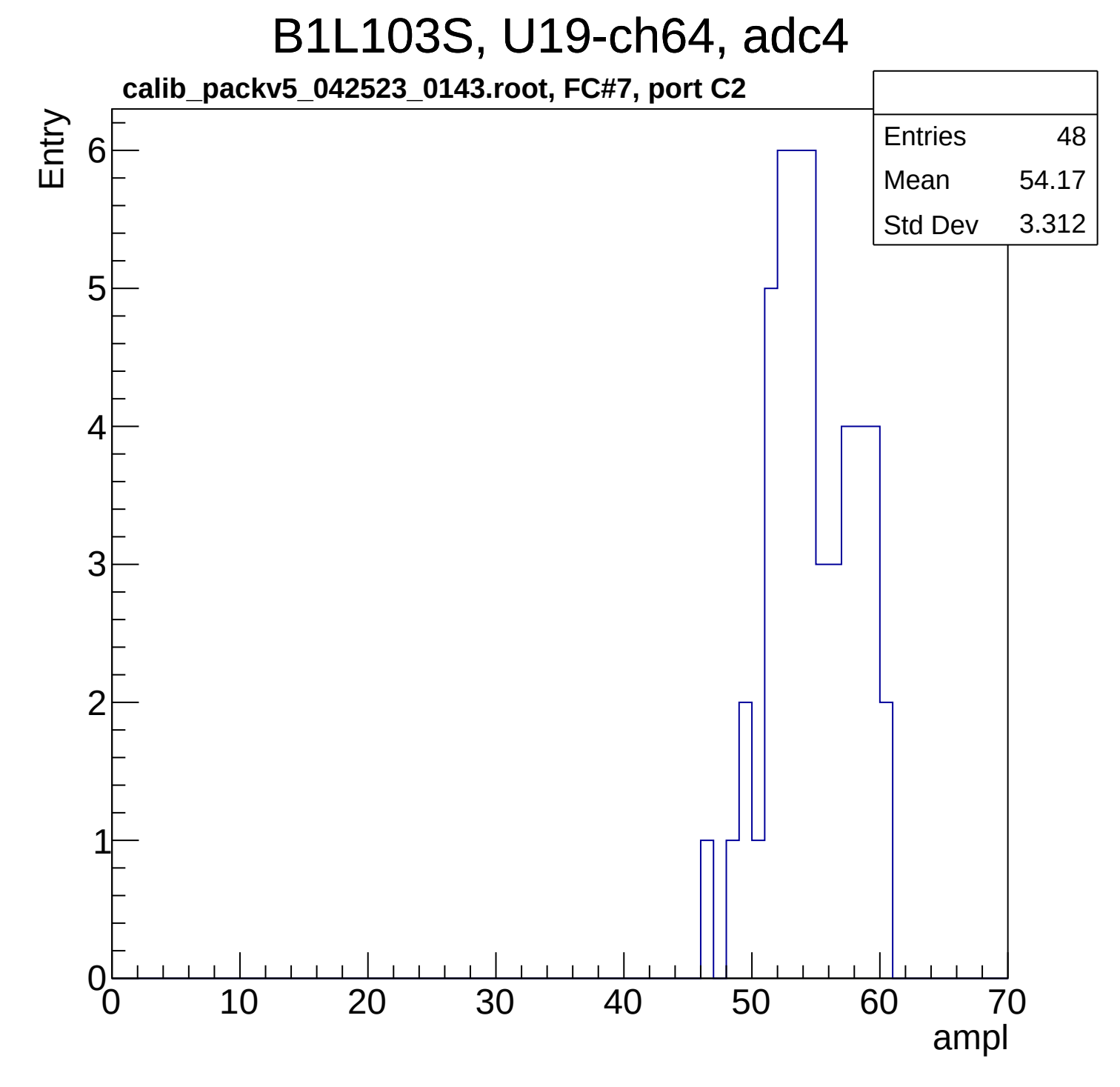
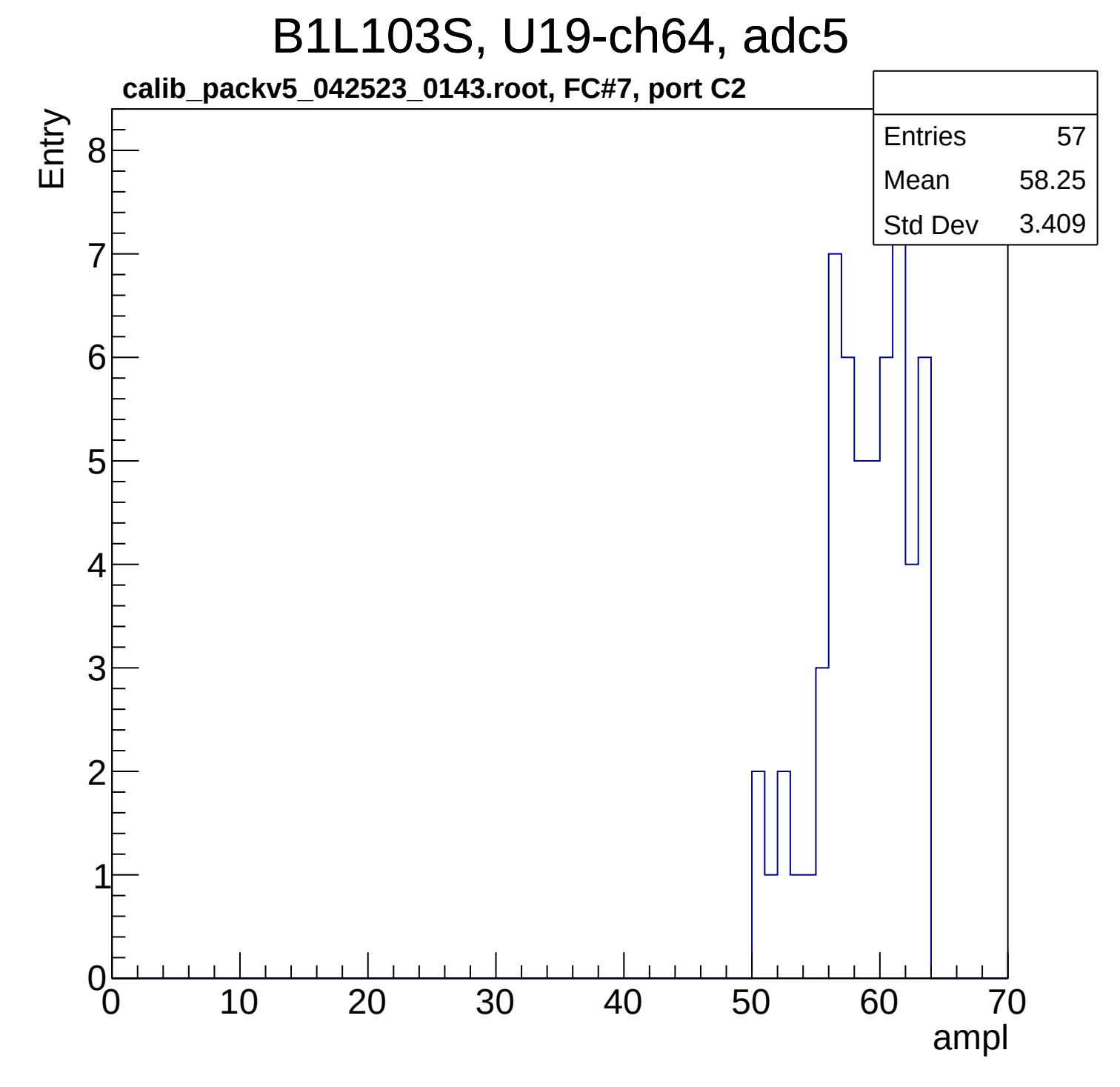
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch64, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.17
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch64, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 58.25
Std Dev 3.409
Entry
ampl
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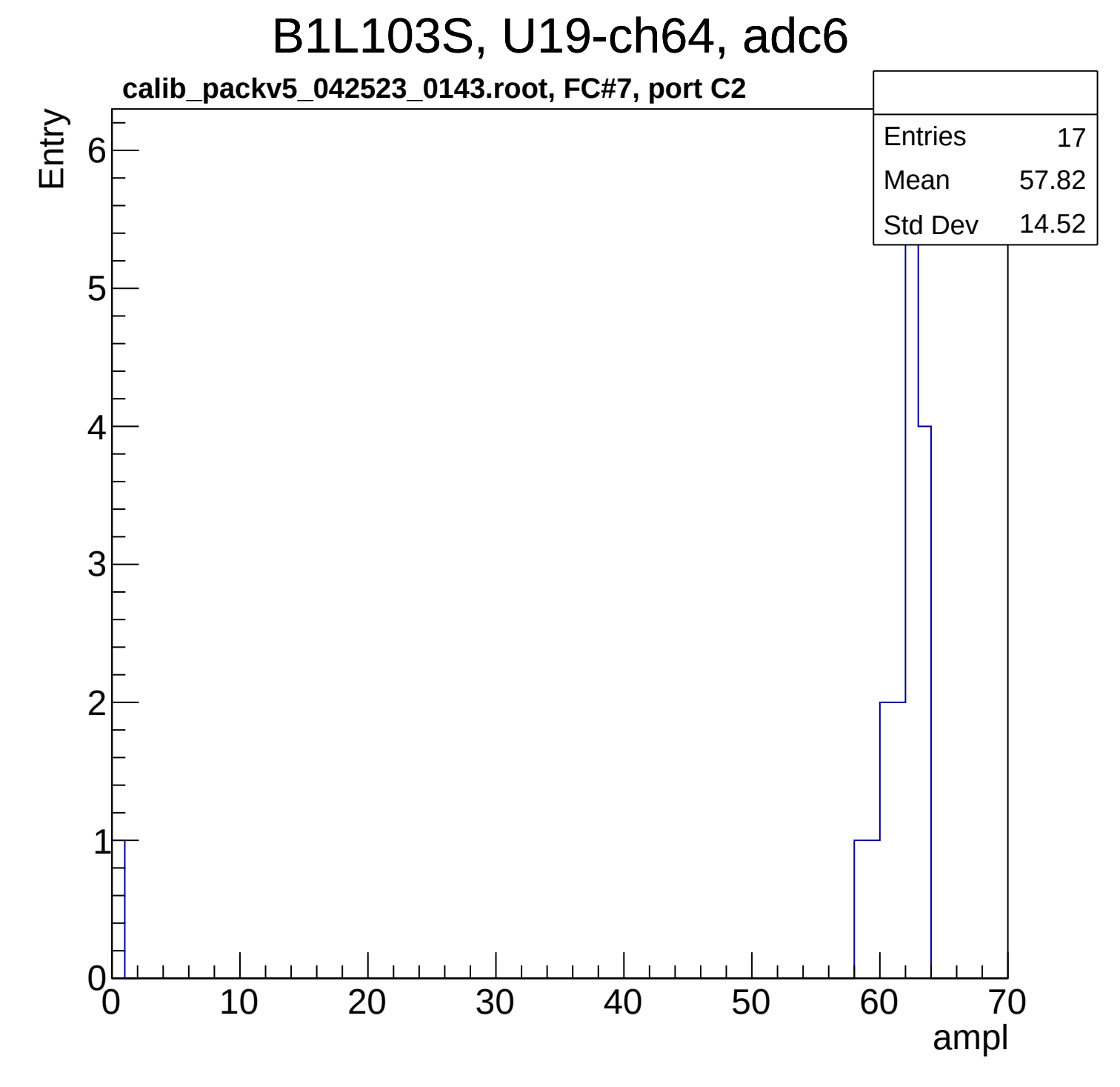
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch64, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 57.82
Std Dev 14.52
Entry
0
1
2
3
4
5
6
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10
20
30
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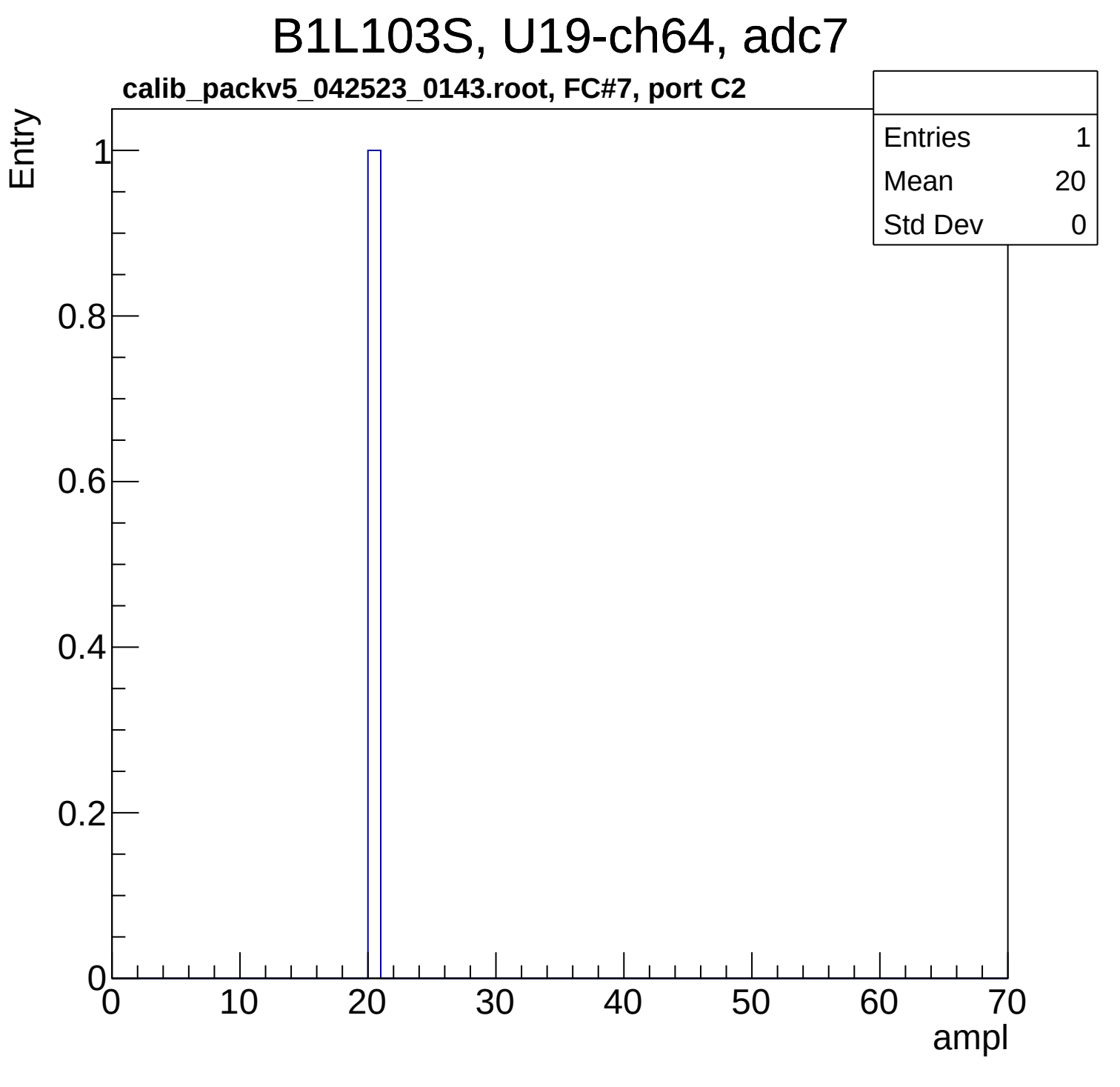
50
60
70
ampl


B1L103S, U19-ch64, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
0.2
0
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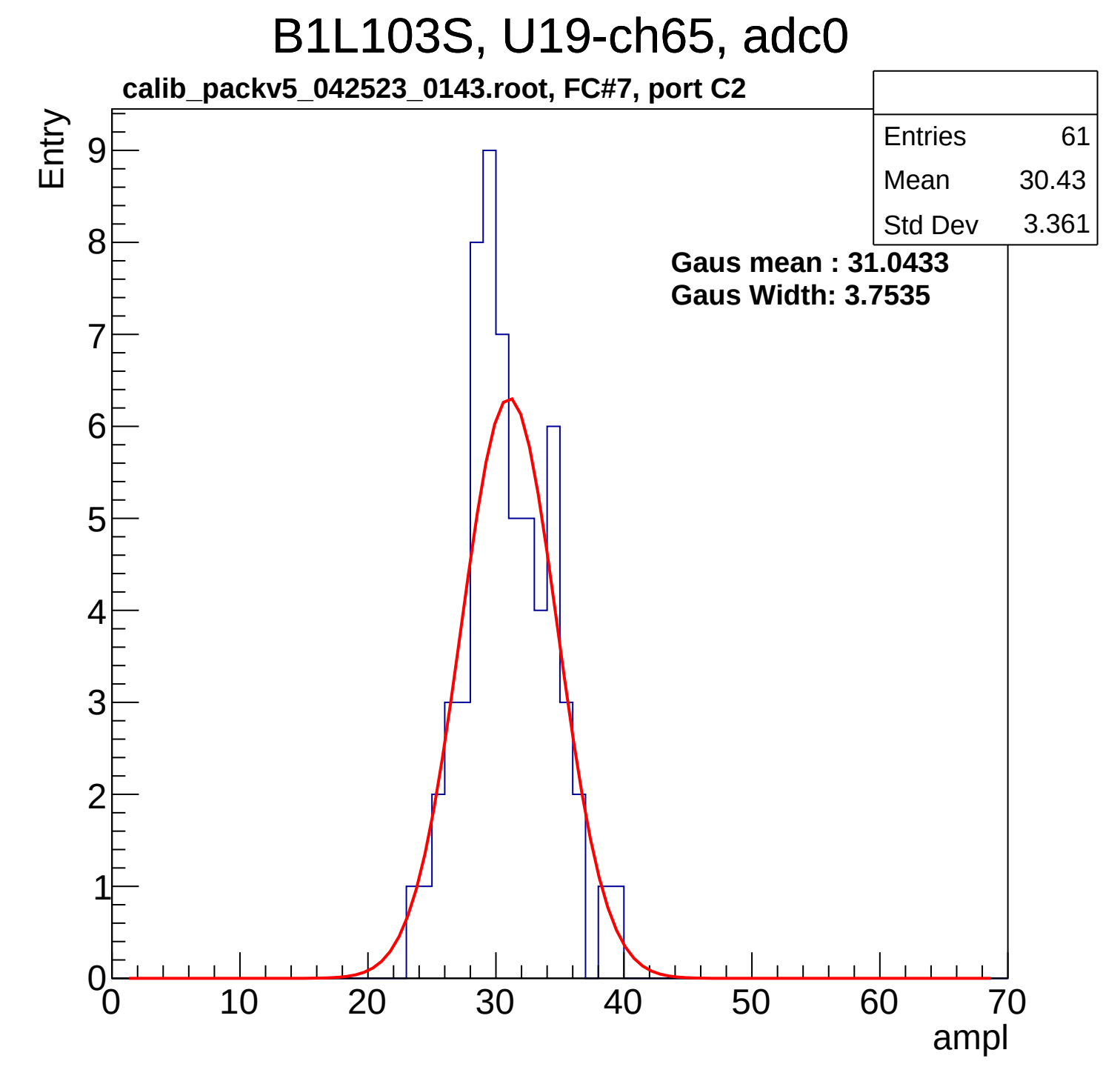
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch65, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 30.43
Std Dev 3.361
Gaus mean : 31.0433
Gaus Width: 3.7535
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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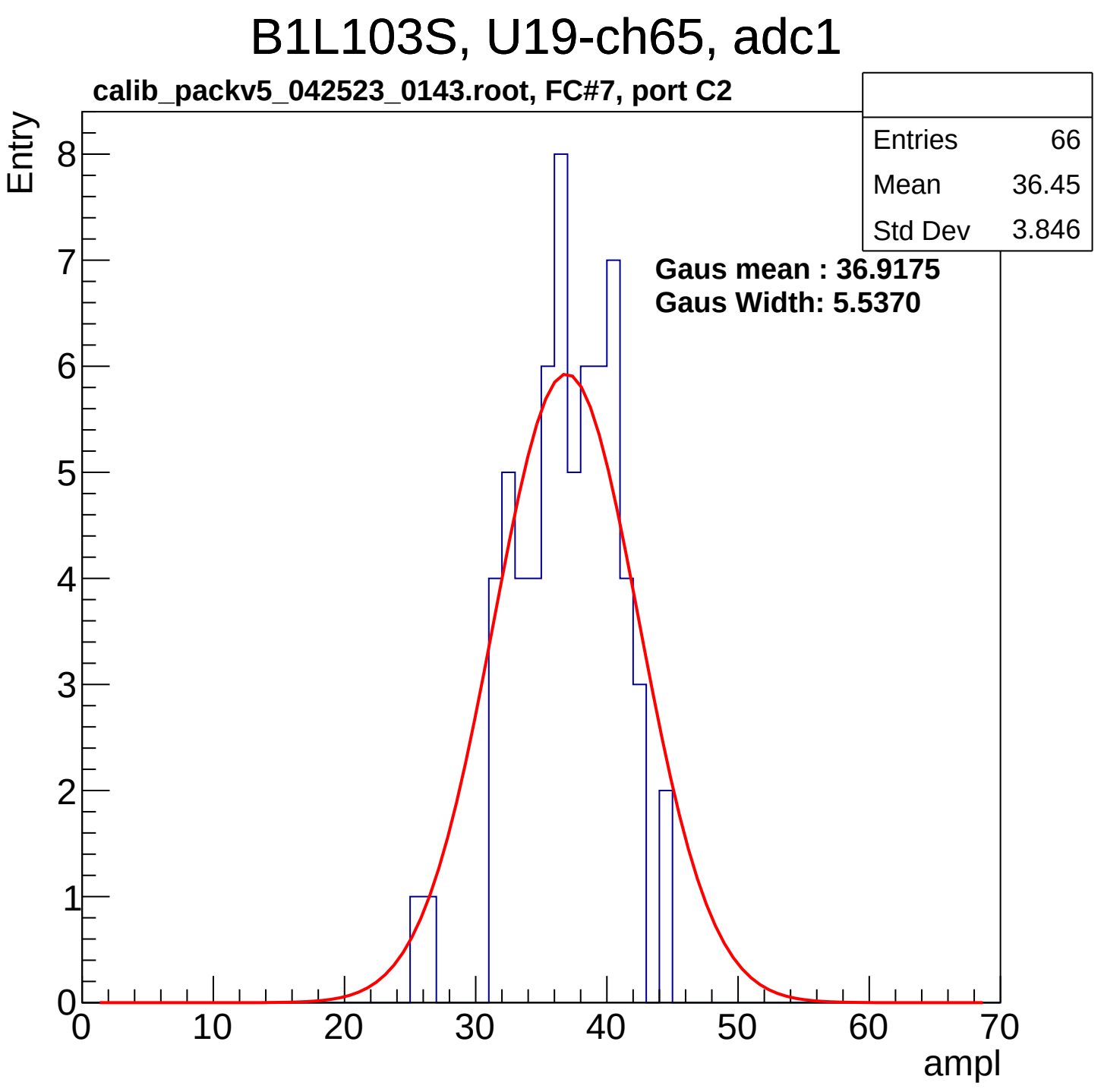
B1L103S, U19-ch65, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 36.45
Std Dev 3.846
Gaus mean : 36.9175
Gaus Width: 5.5370
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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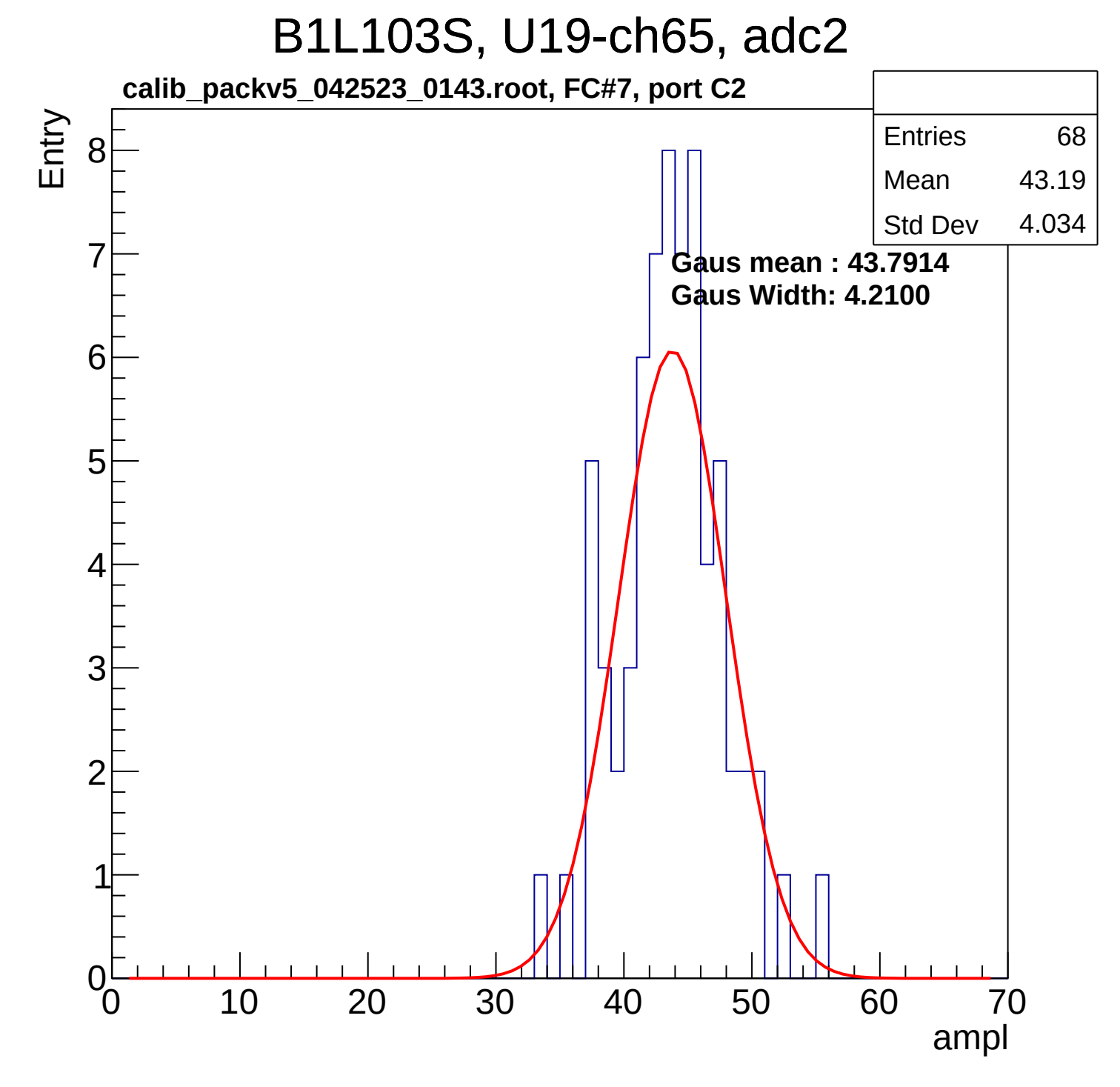
B1L103S, U19-ch65, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 43.19
Std Dev 4.034
Gaus mean : 43.7914
Gaus Width: 4.2100
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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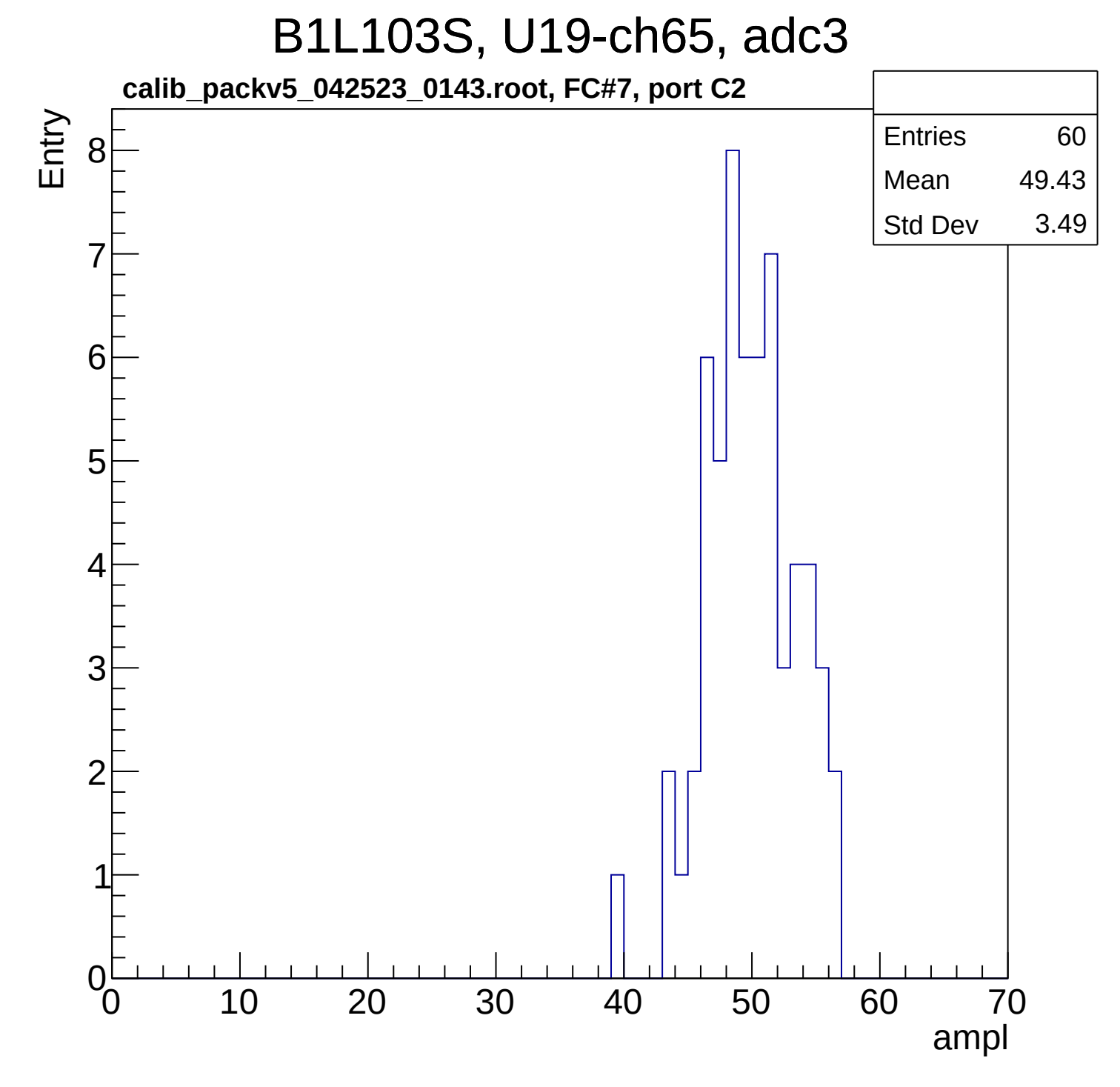
B1L103S, U19-ch65, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.43
Std Dev 3.49
Entry
ampl
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2
3
4
5
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0
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40
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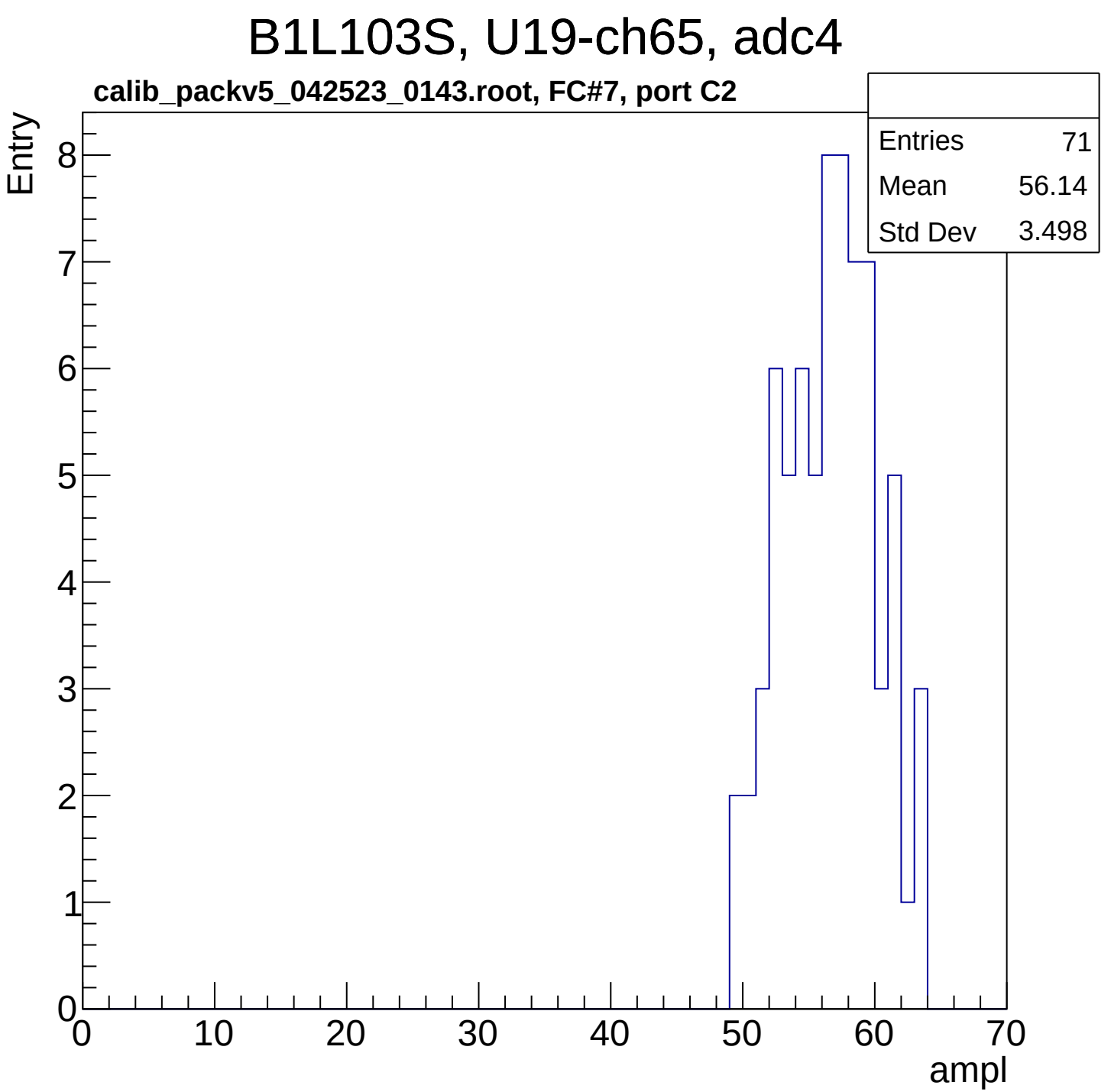
60
70


B1L103S, U19-ch65, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 56.14
Std Dev 3.498
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
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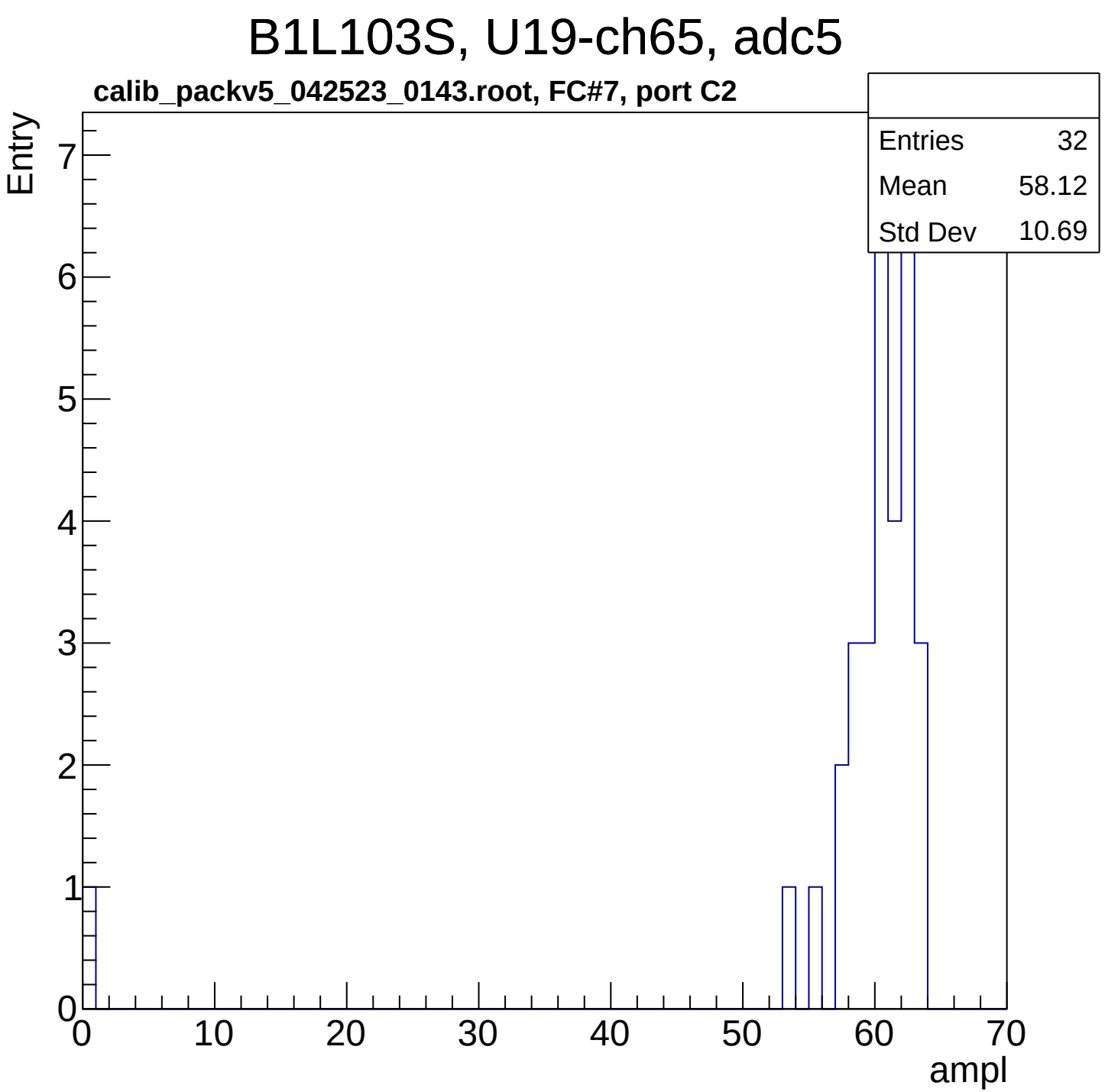
50
60
70
ampl


B1L103S, U19-ch65, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.12
Std Dev 10.69
Entry
ampl
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1
2
3
4
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60
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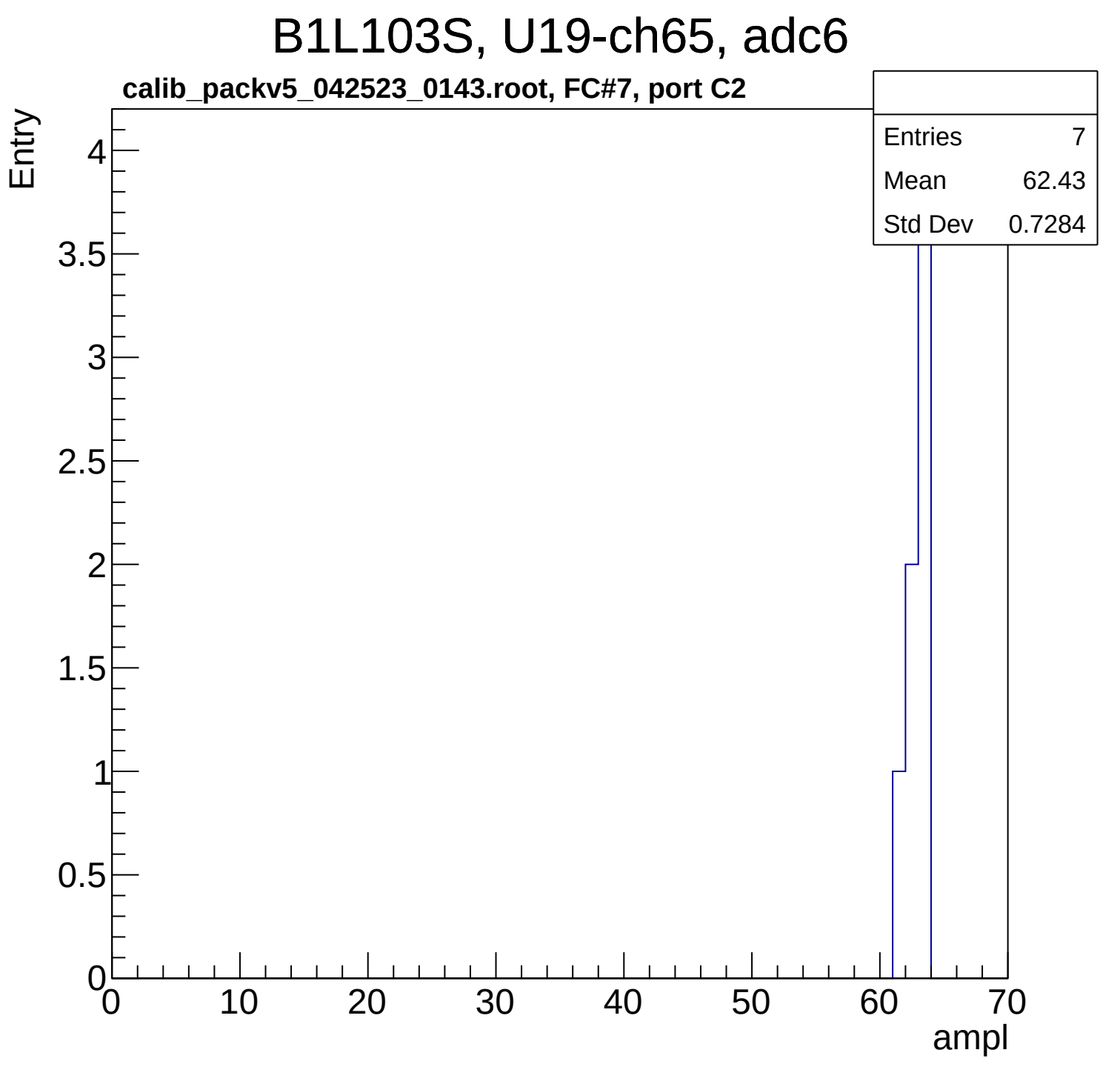
B1L103S, U19-ch65, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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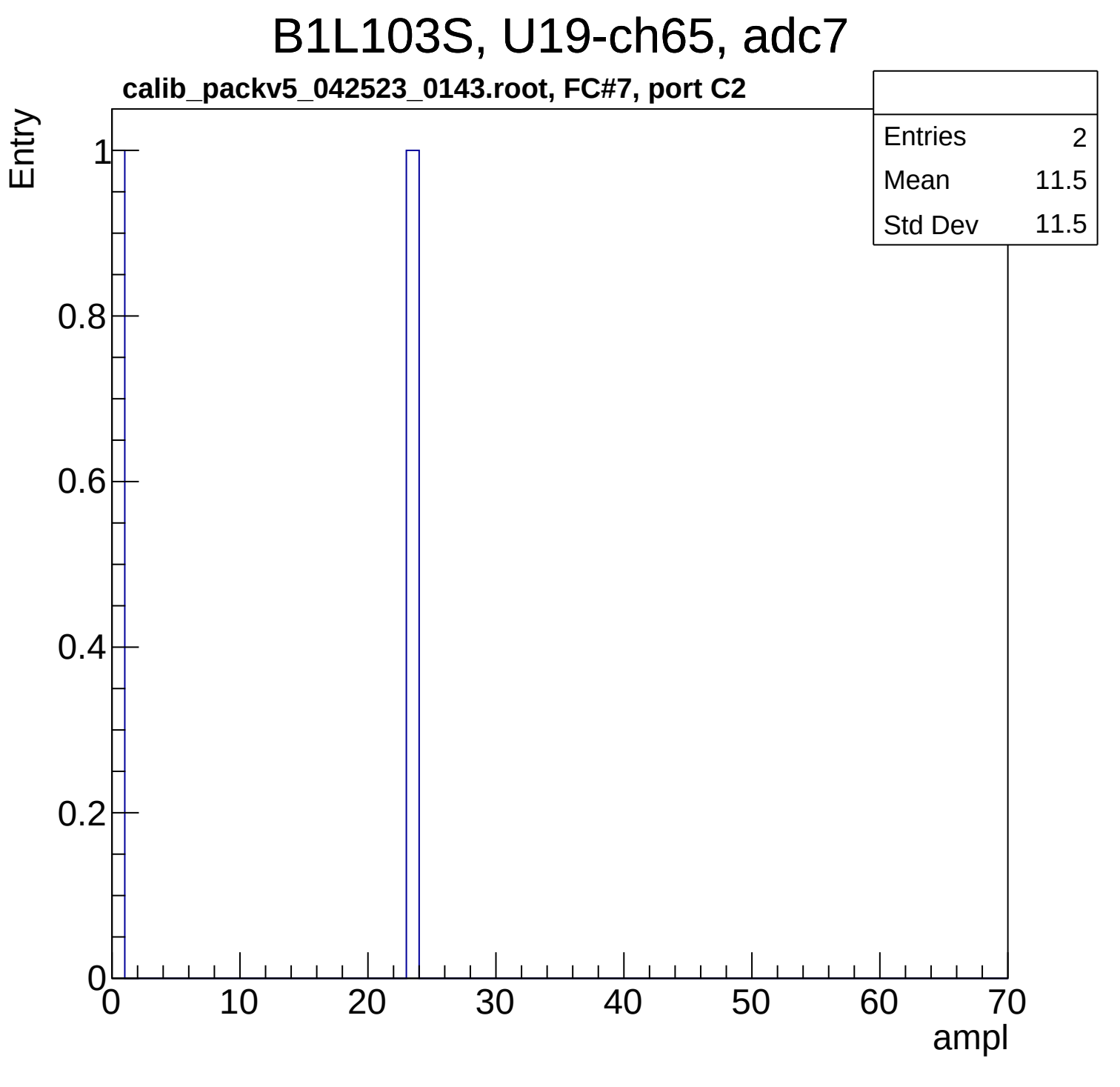
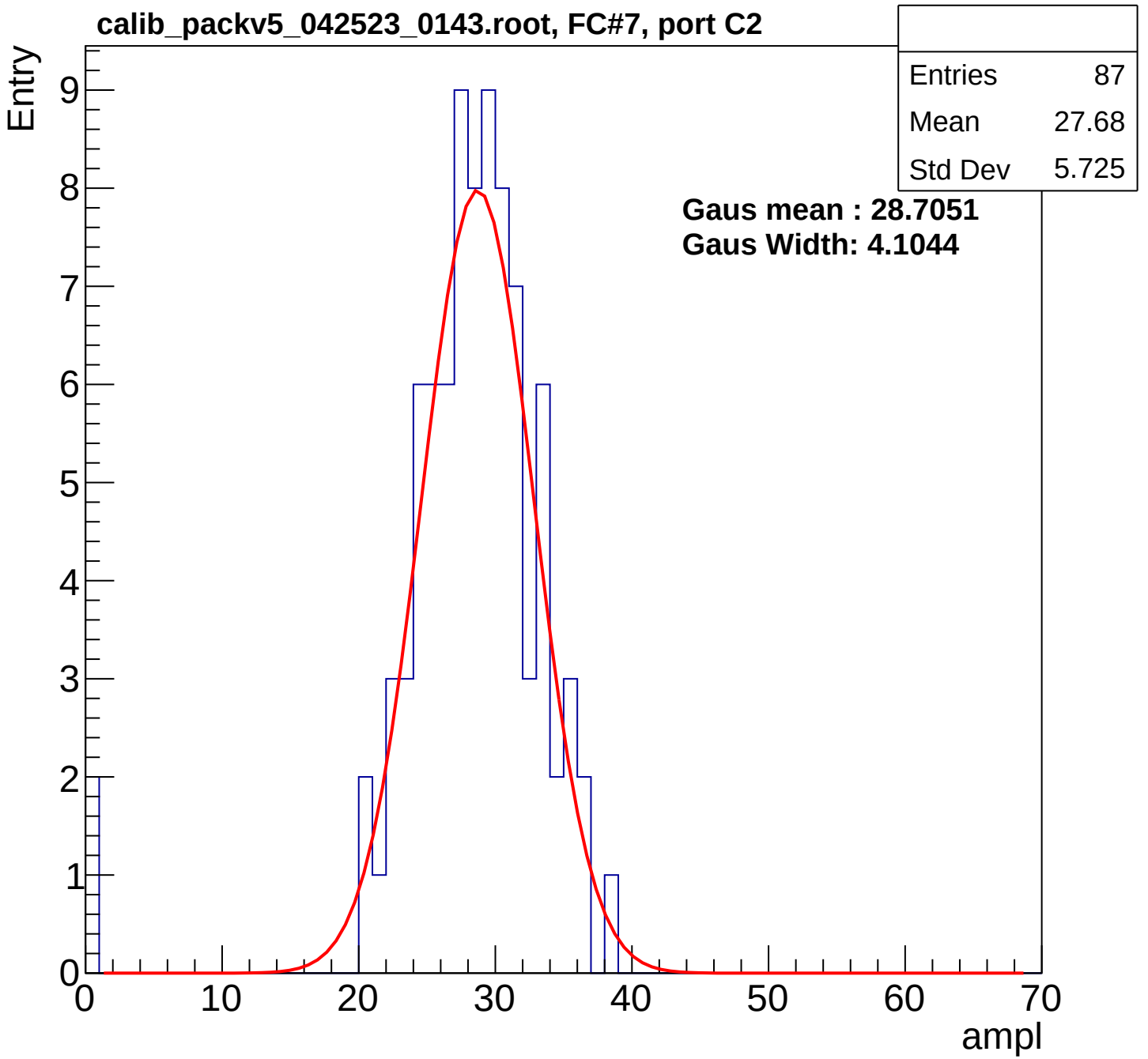
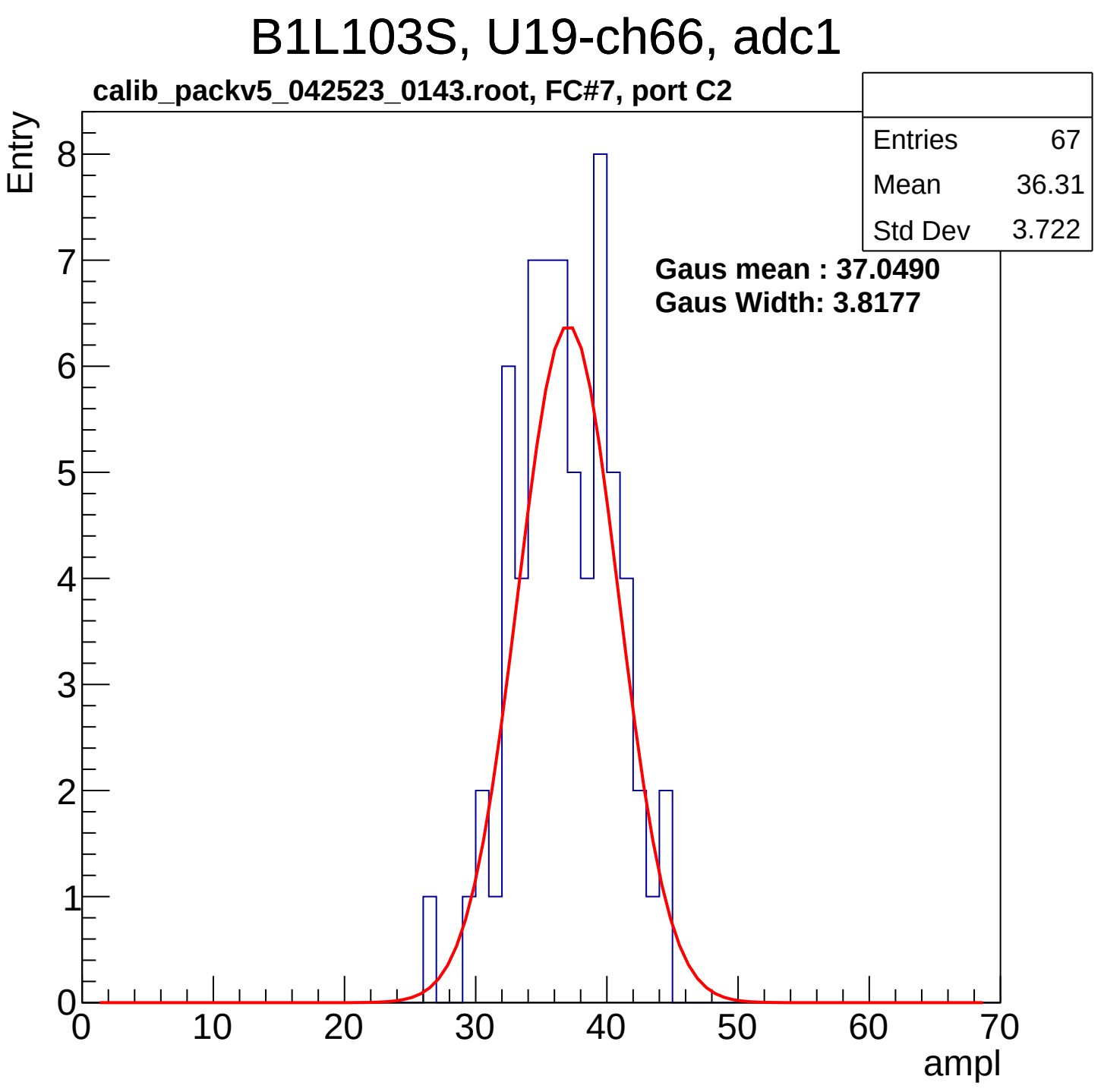

B1L103S, U19-ch65, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch66, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 27.68
Std Dev 5.725
Gaus mean : 28.7051
Gaus Width: 4.1044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch66, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.31
Std Dev 3.722
Gaus mean : 37.0490
Gaus Width: 3.8177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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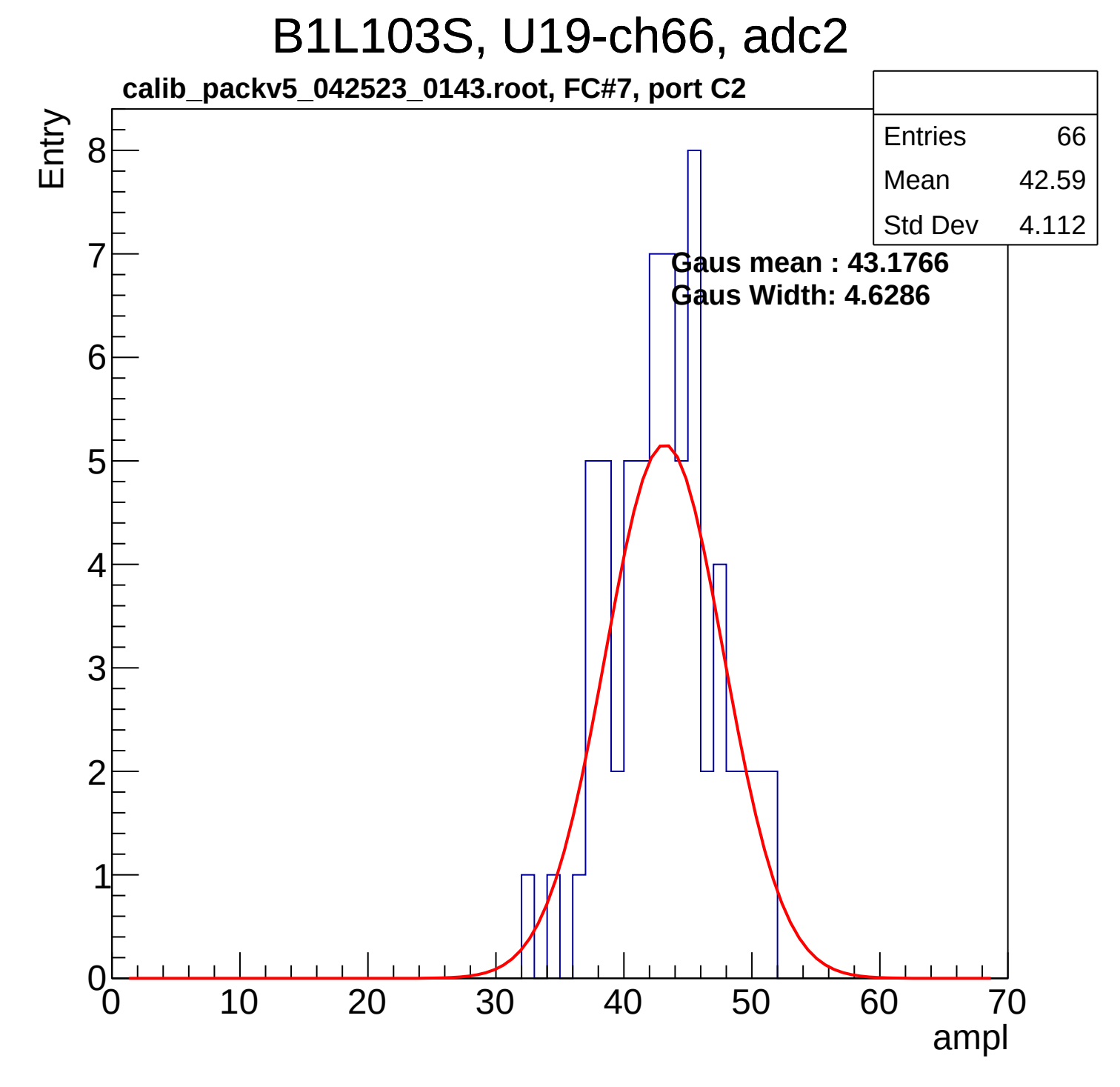
B1L103S, U19-ch66, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.59
Std Dev 4.112
Gaus mean : 43.1766
Gaus Width: 4.6286
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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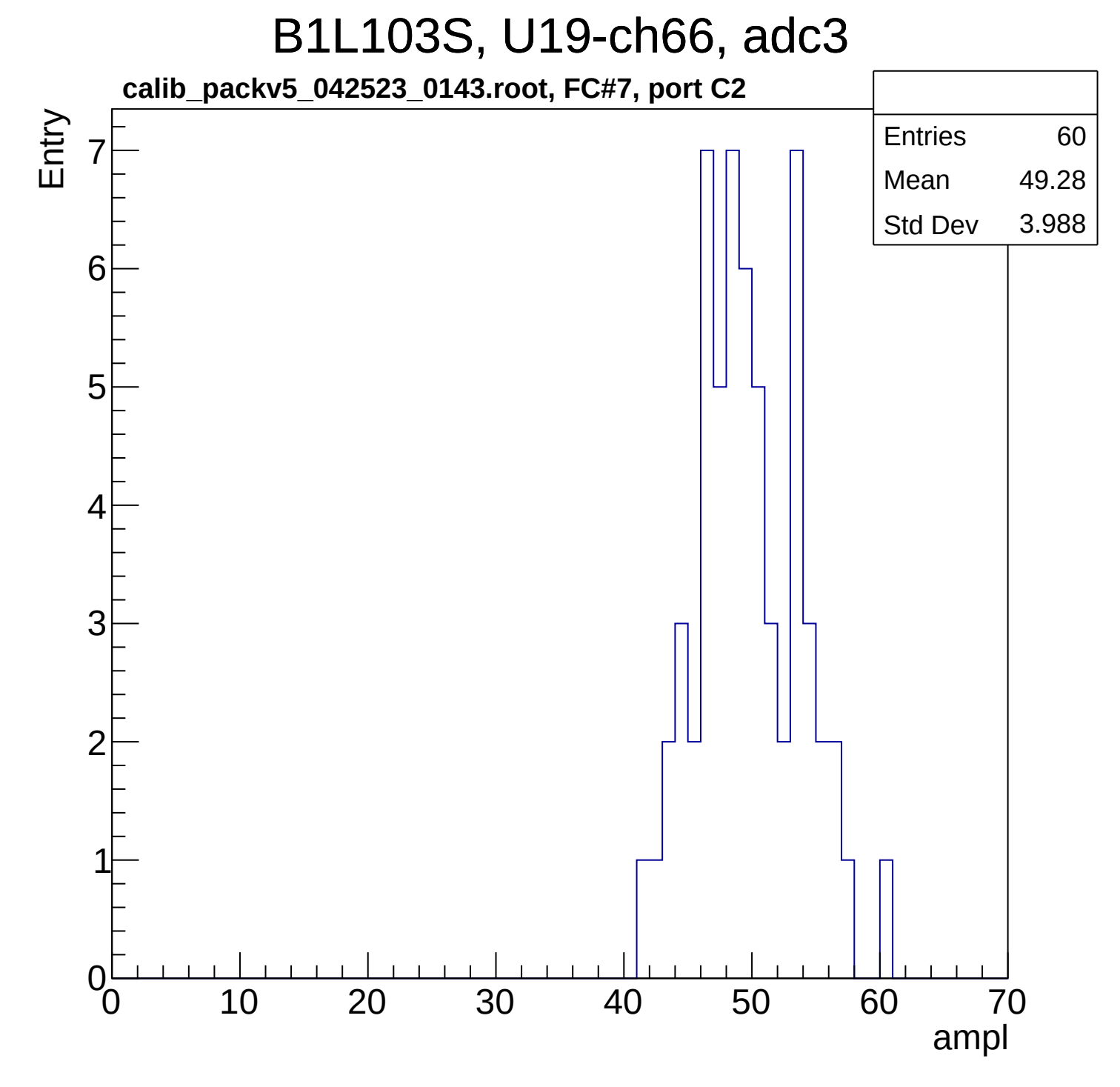
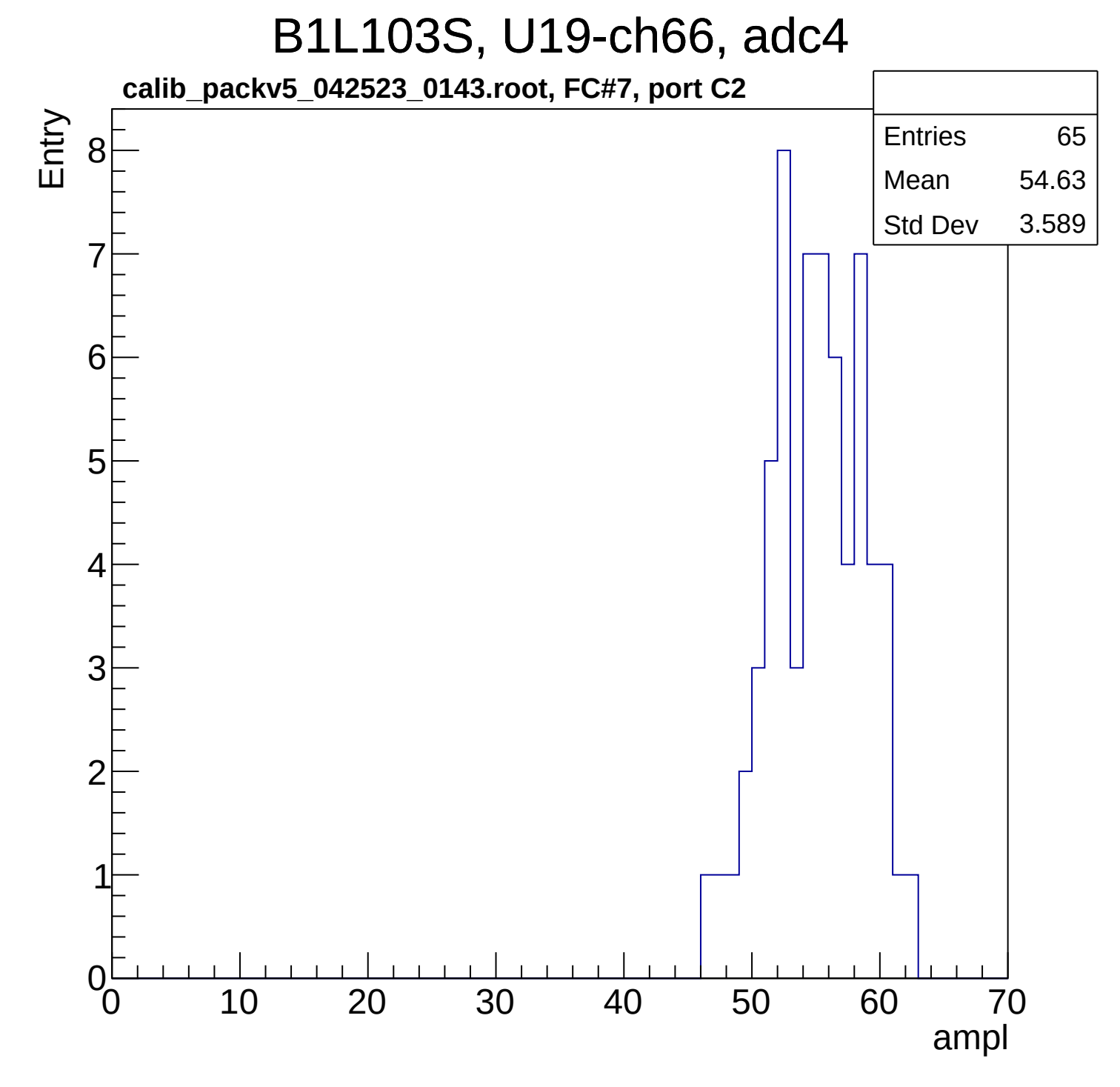
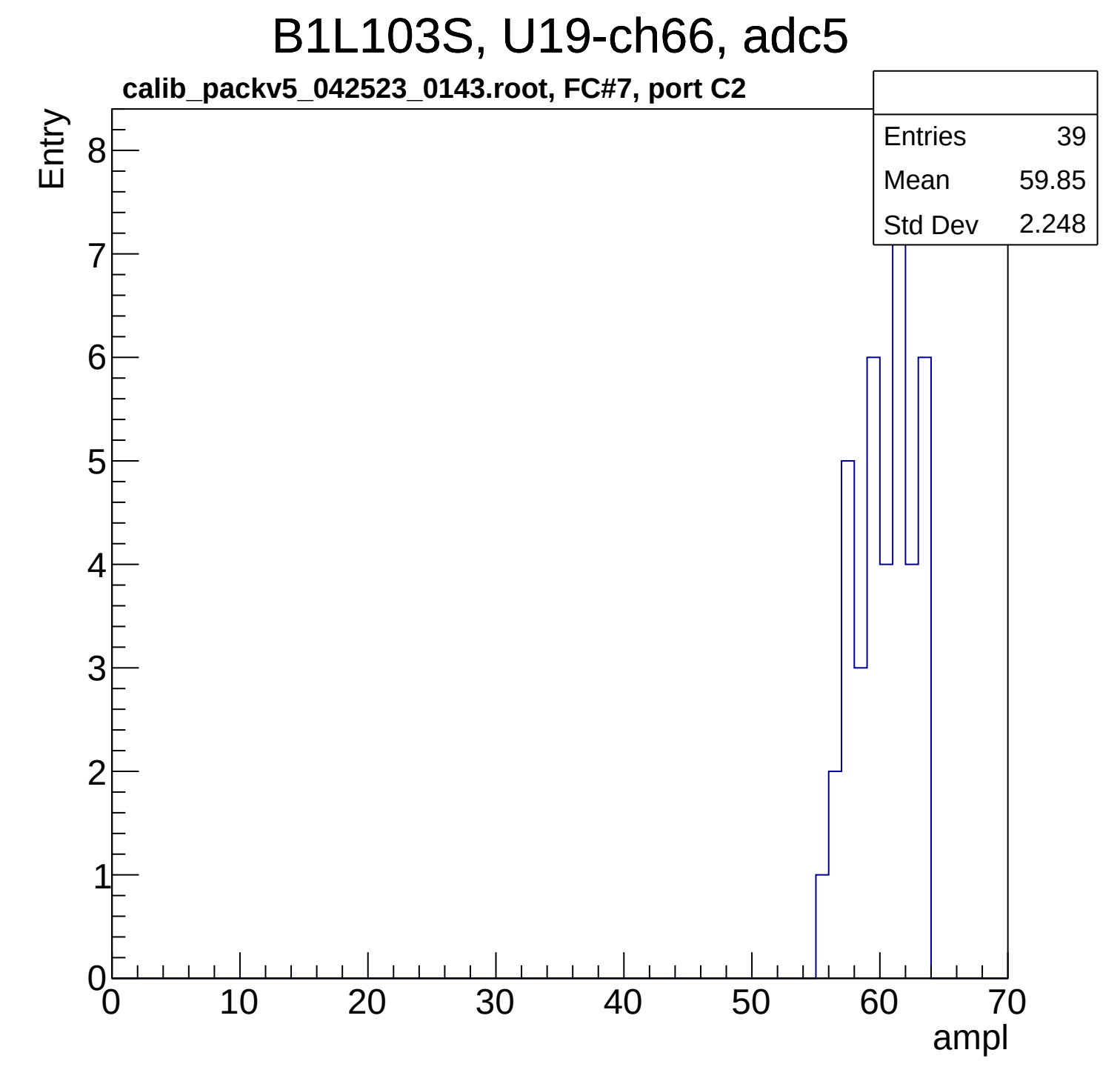

B1L103S, U19-ch66, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.28
Std Dev 3.988
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch66, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 54.63
Std Dev 3.589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch66, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 59.85
Std Dev 2.248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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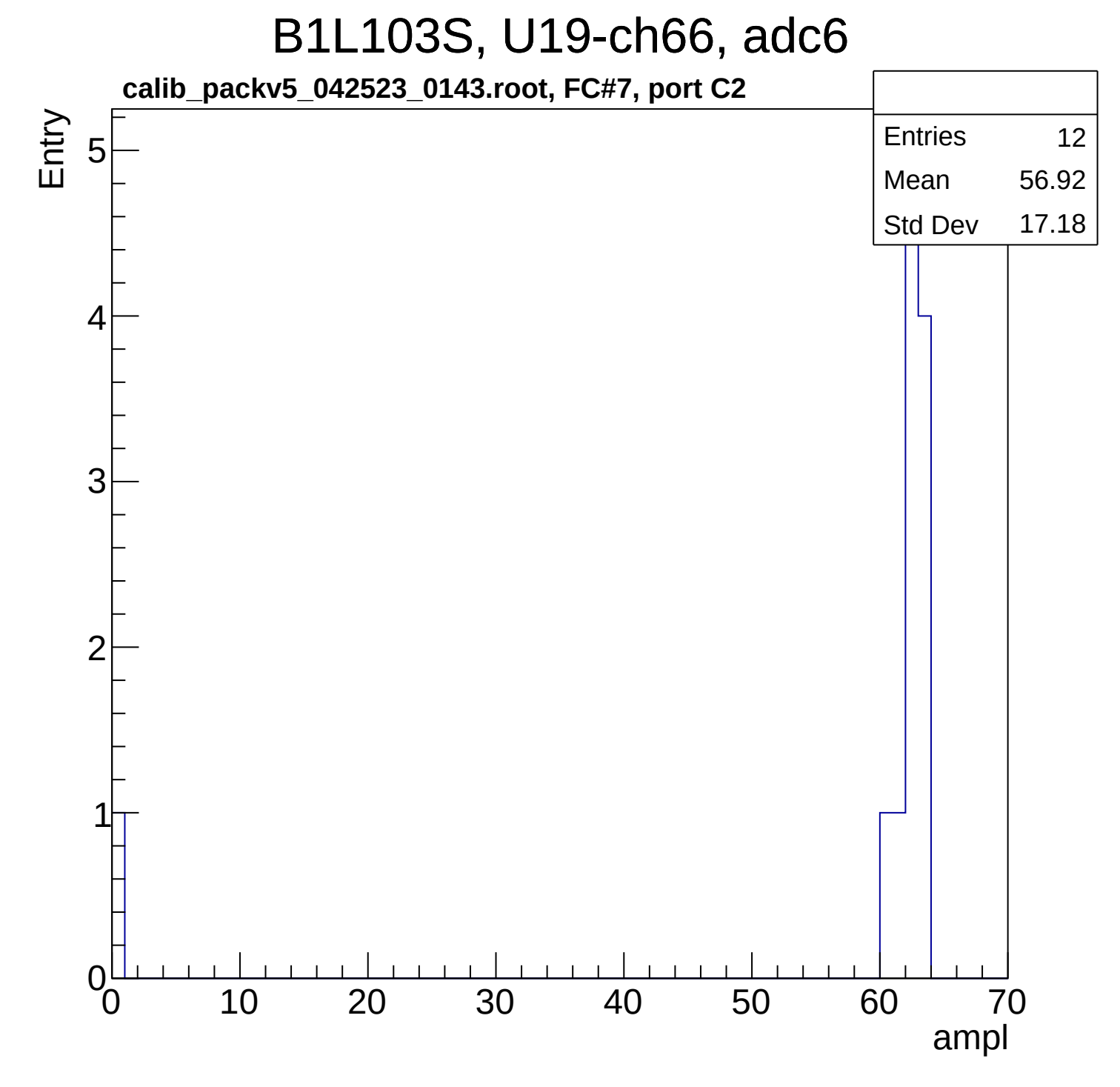
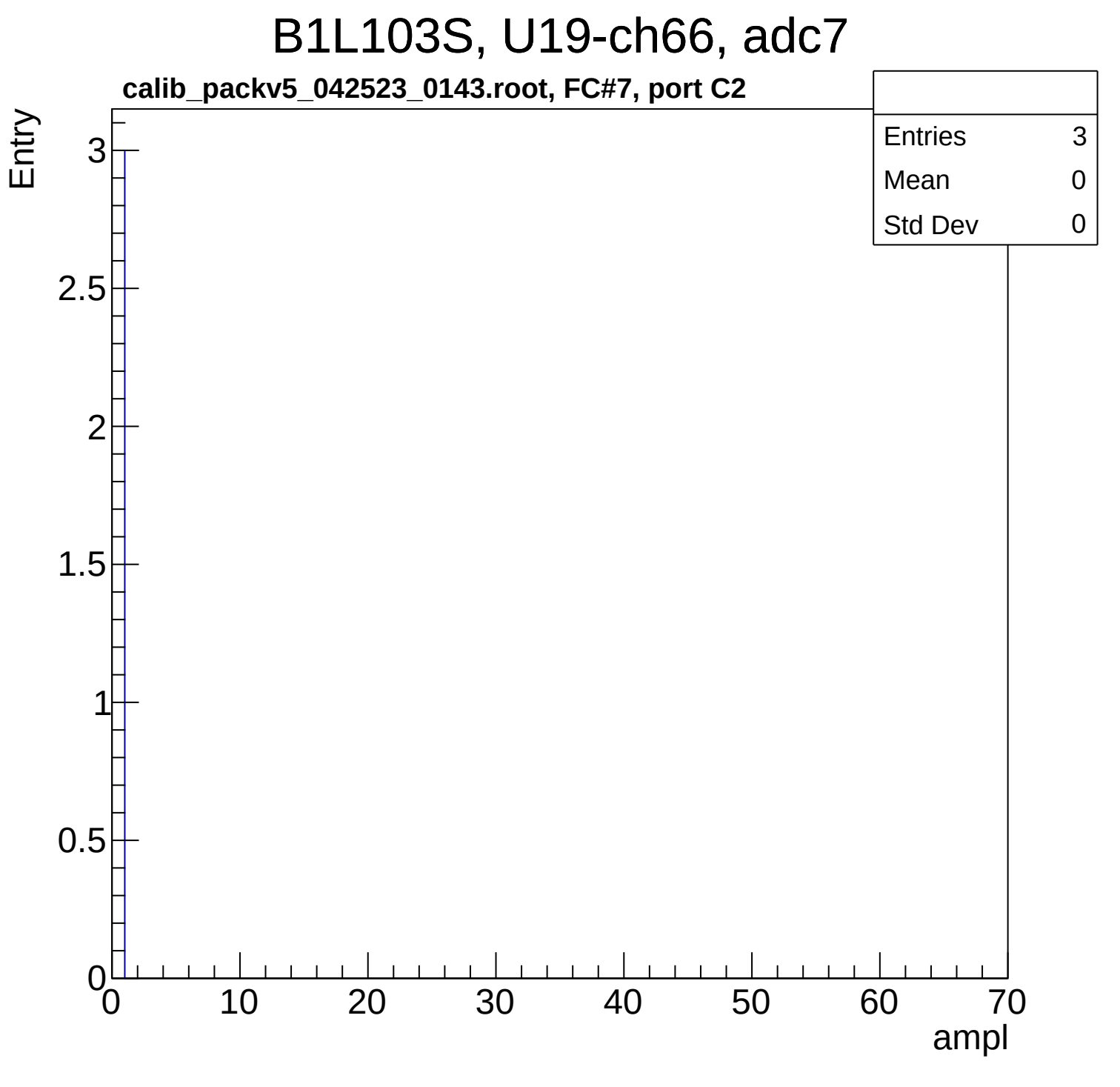
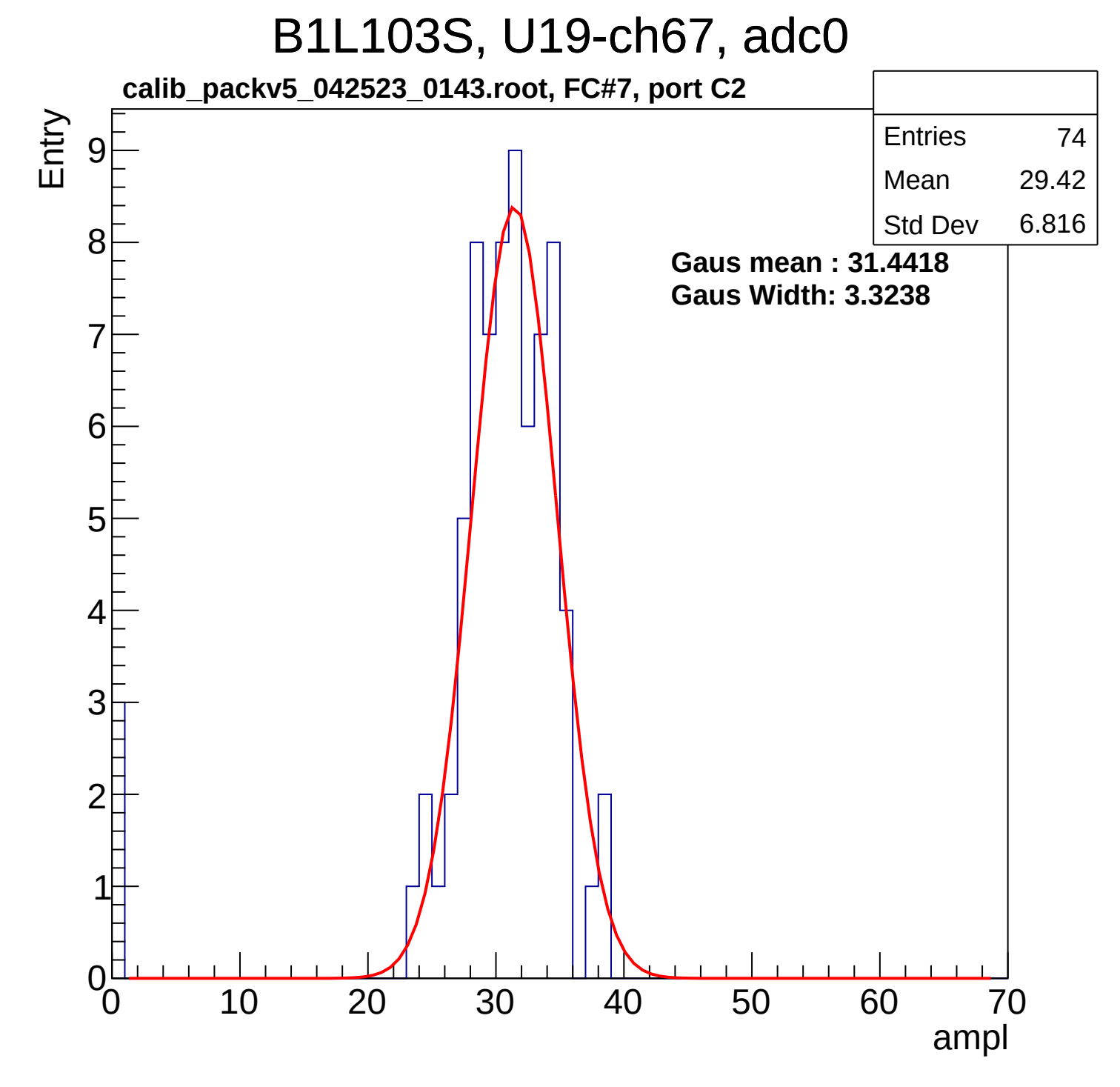

B1L103S, U19-ch66, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.92
Std Dev 17.18
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch66, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch67, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.42
Std Dev 6.816
Gaus mean : 31.4418
Gaus Width: 3.3238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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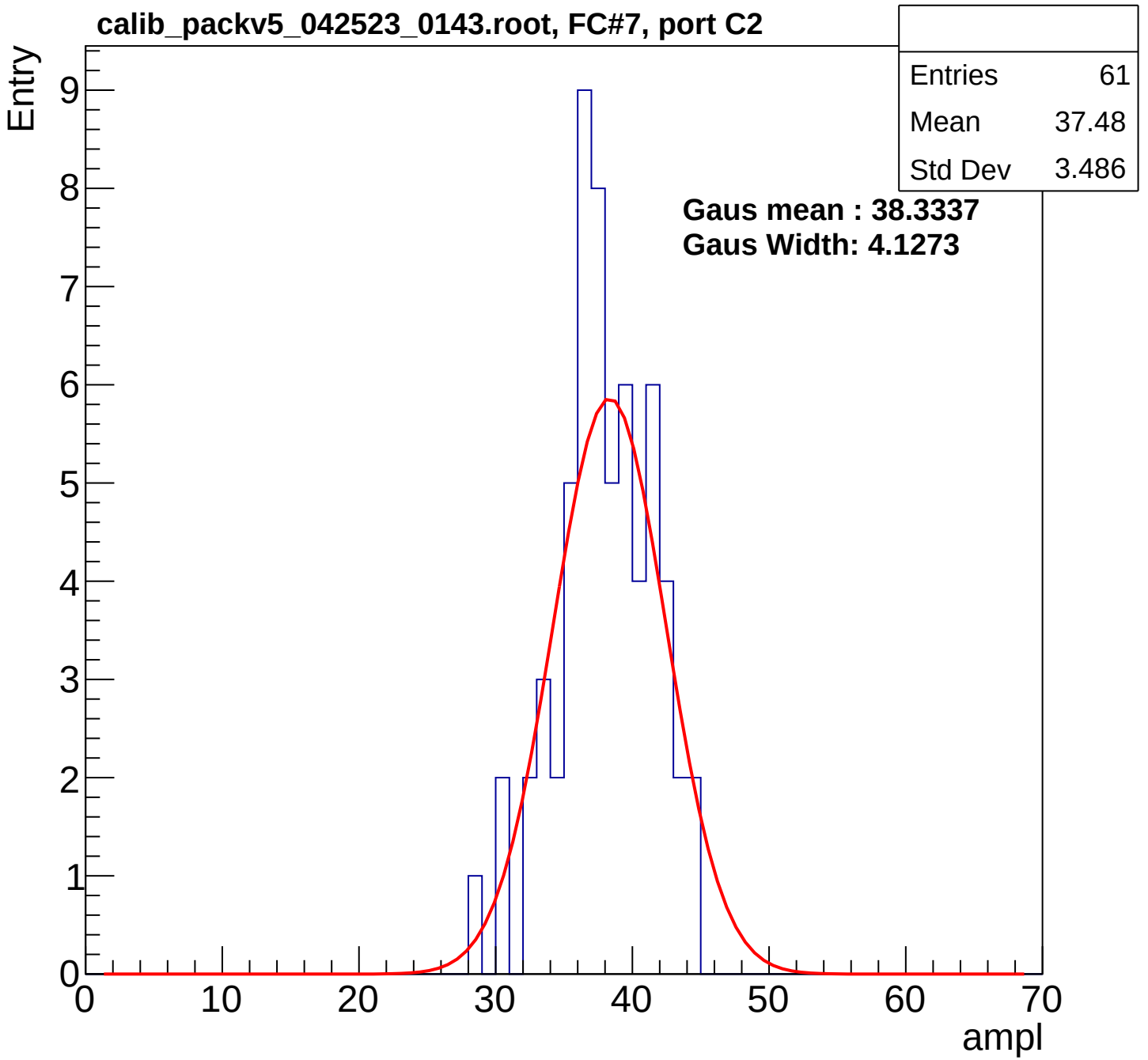
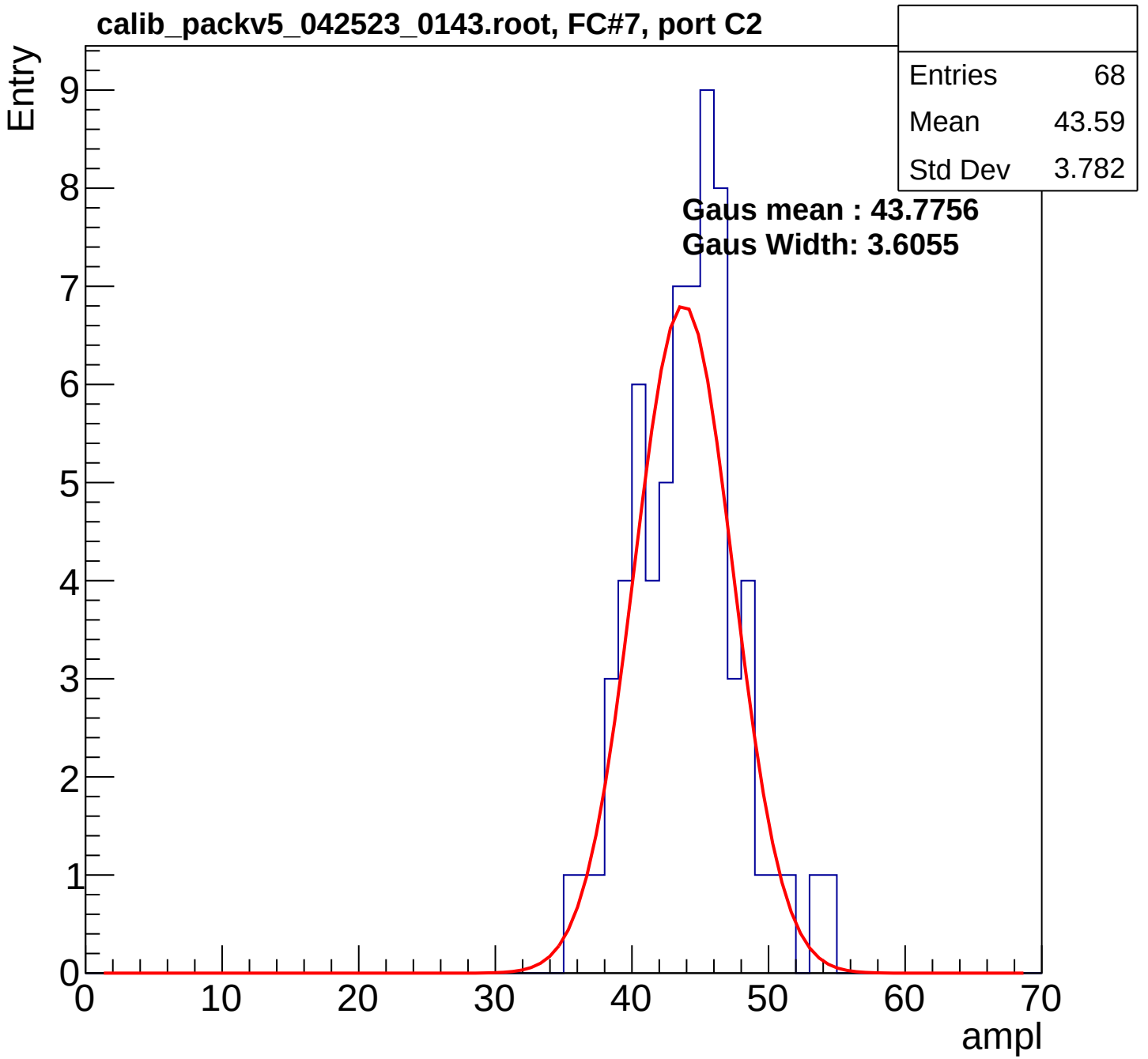

B1L103S, U19-ch67, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37.48
Std Dev 3.486
Gaus mean : 38.3337
Gaus Width: 4.1273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch67, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 43.59
Std Dev 3.782
Gaus mean : 43.7756
Gaus Width: 3.6055
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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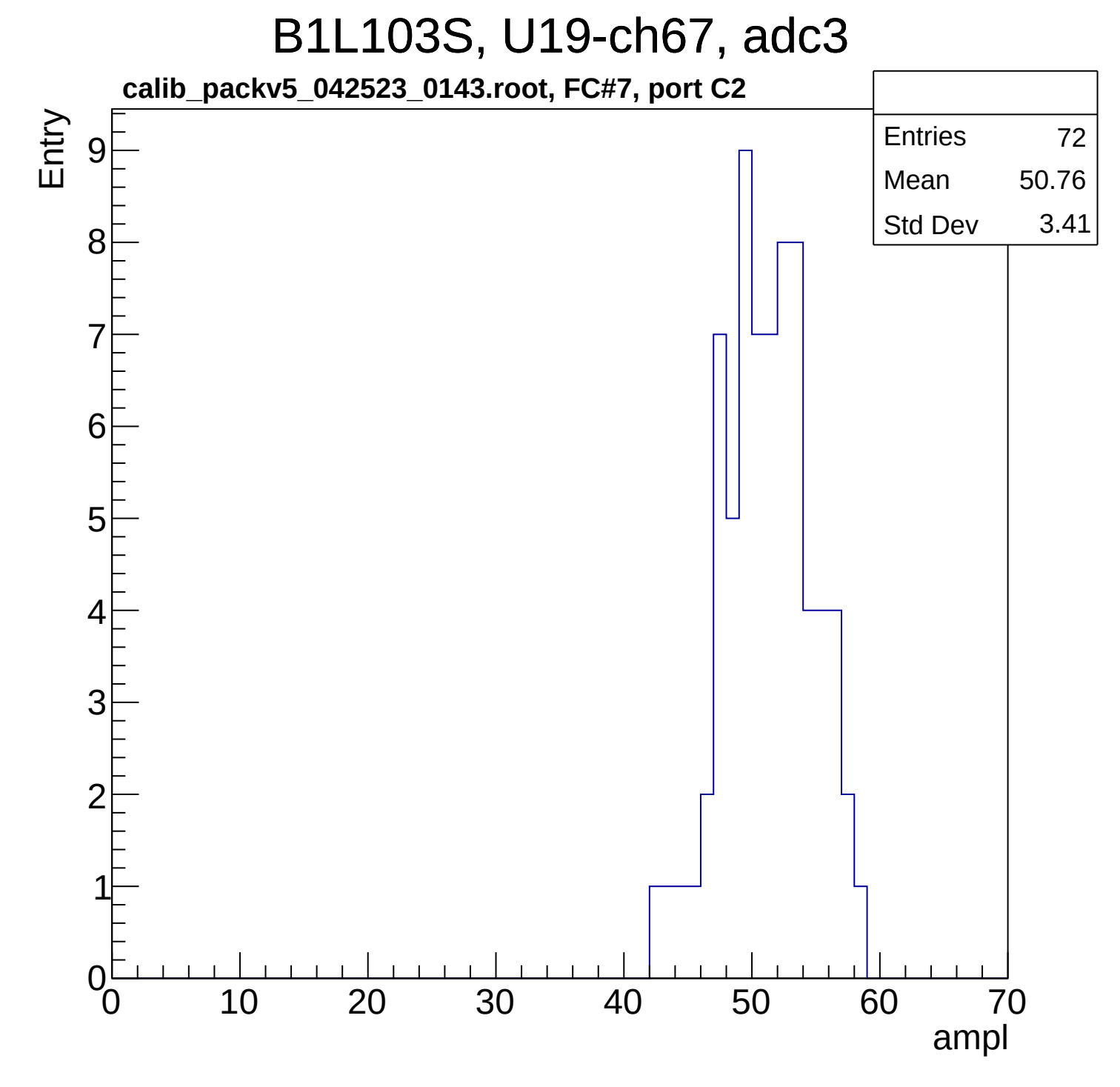
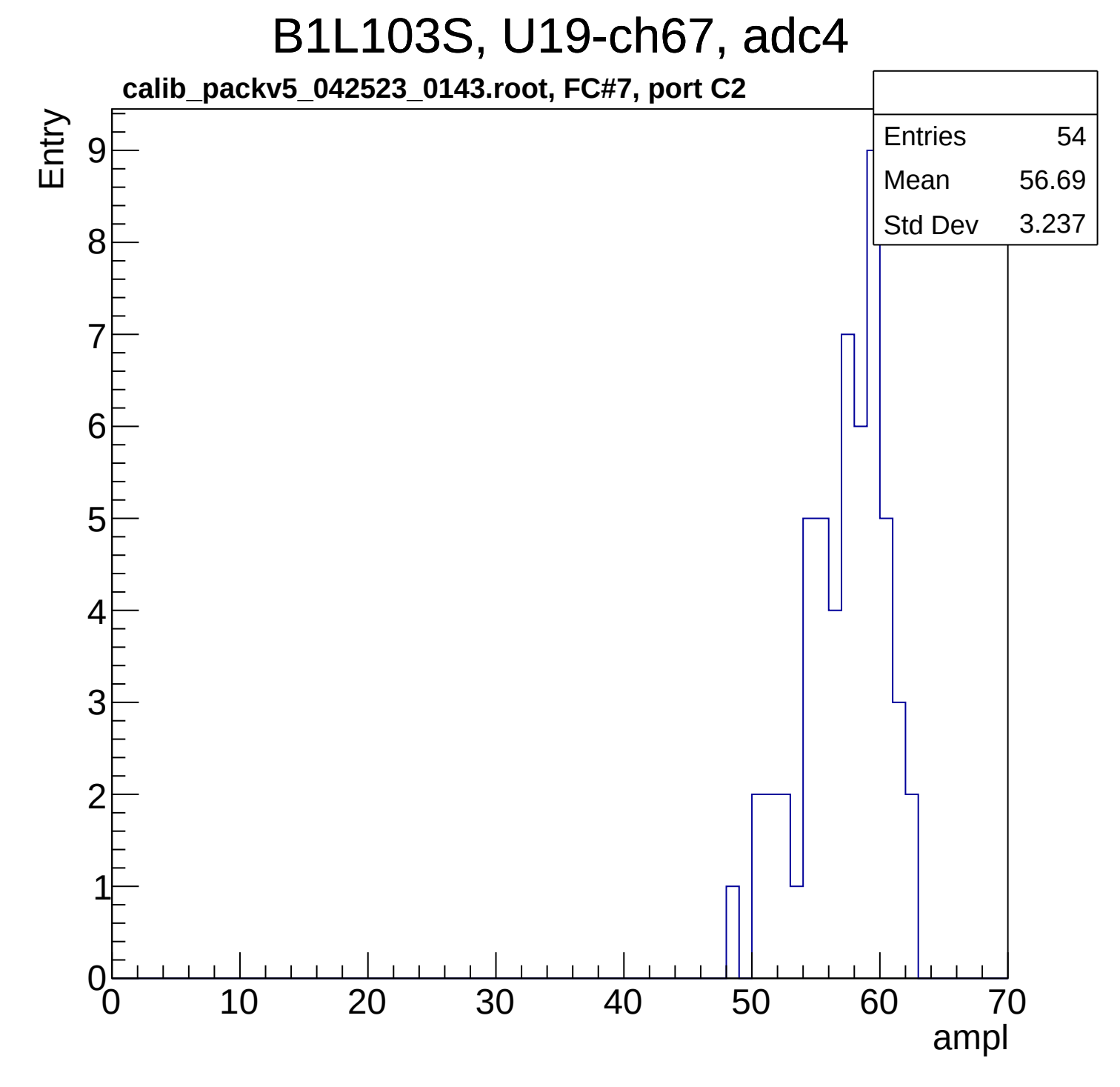
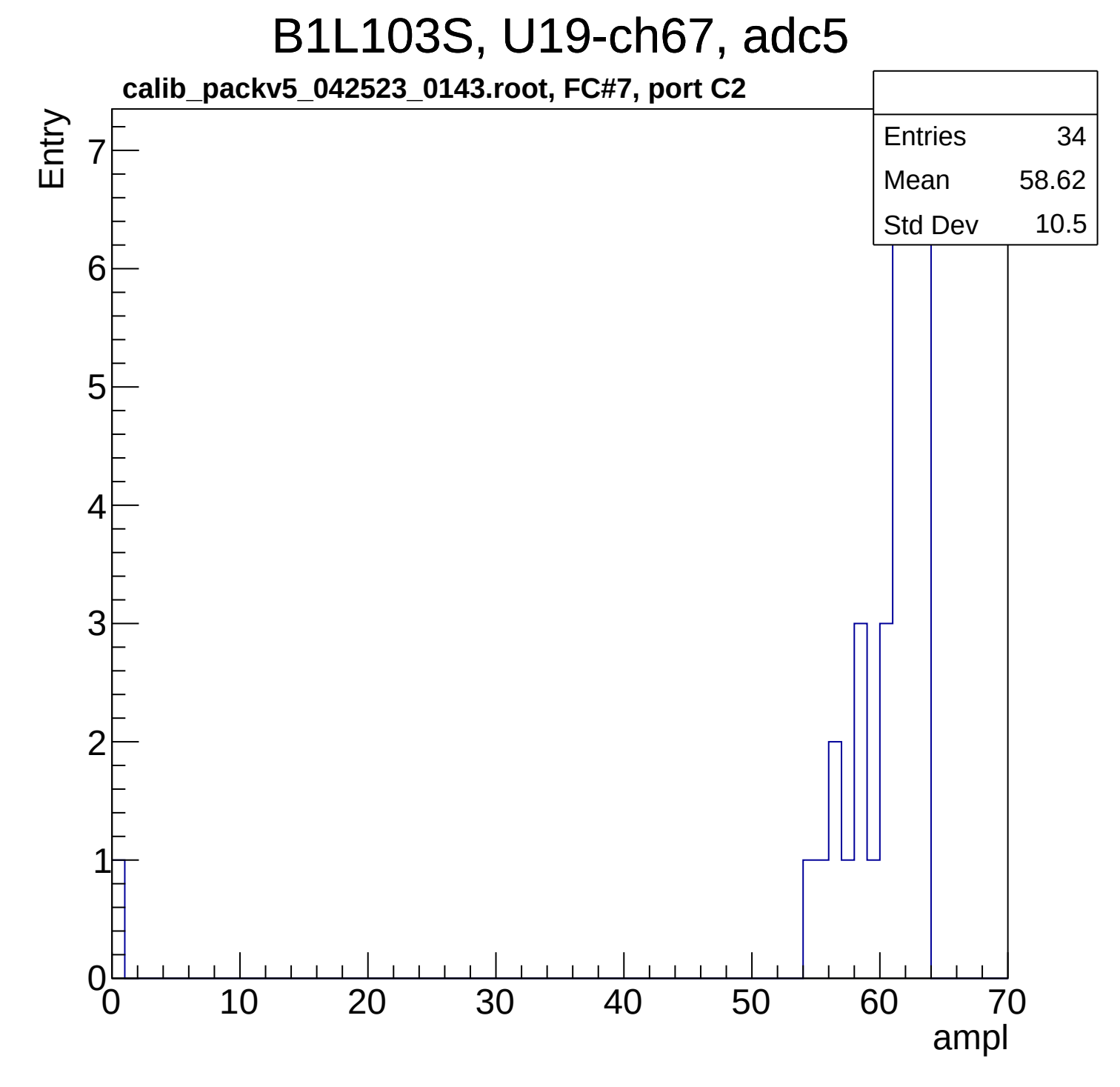
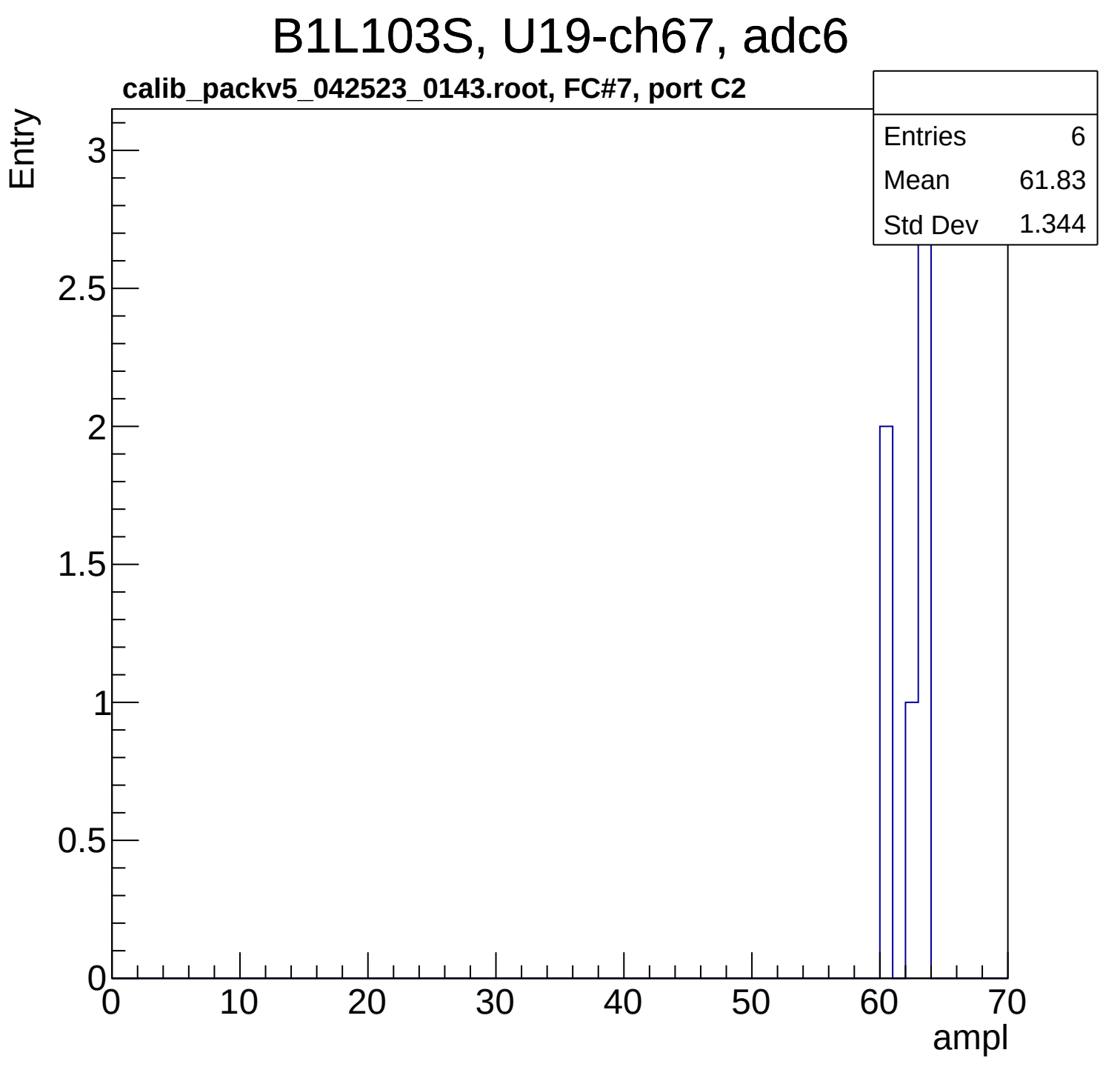

B1L103S, U19-ch67, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.76
Std Dev 3.41
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch67, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.69
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch67, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.62
Std Dev 10.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch67, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.83
Std Dev 1.344
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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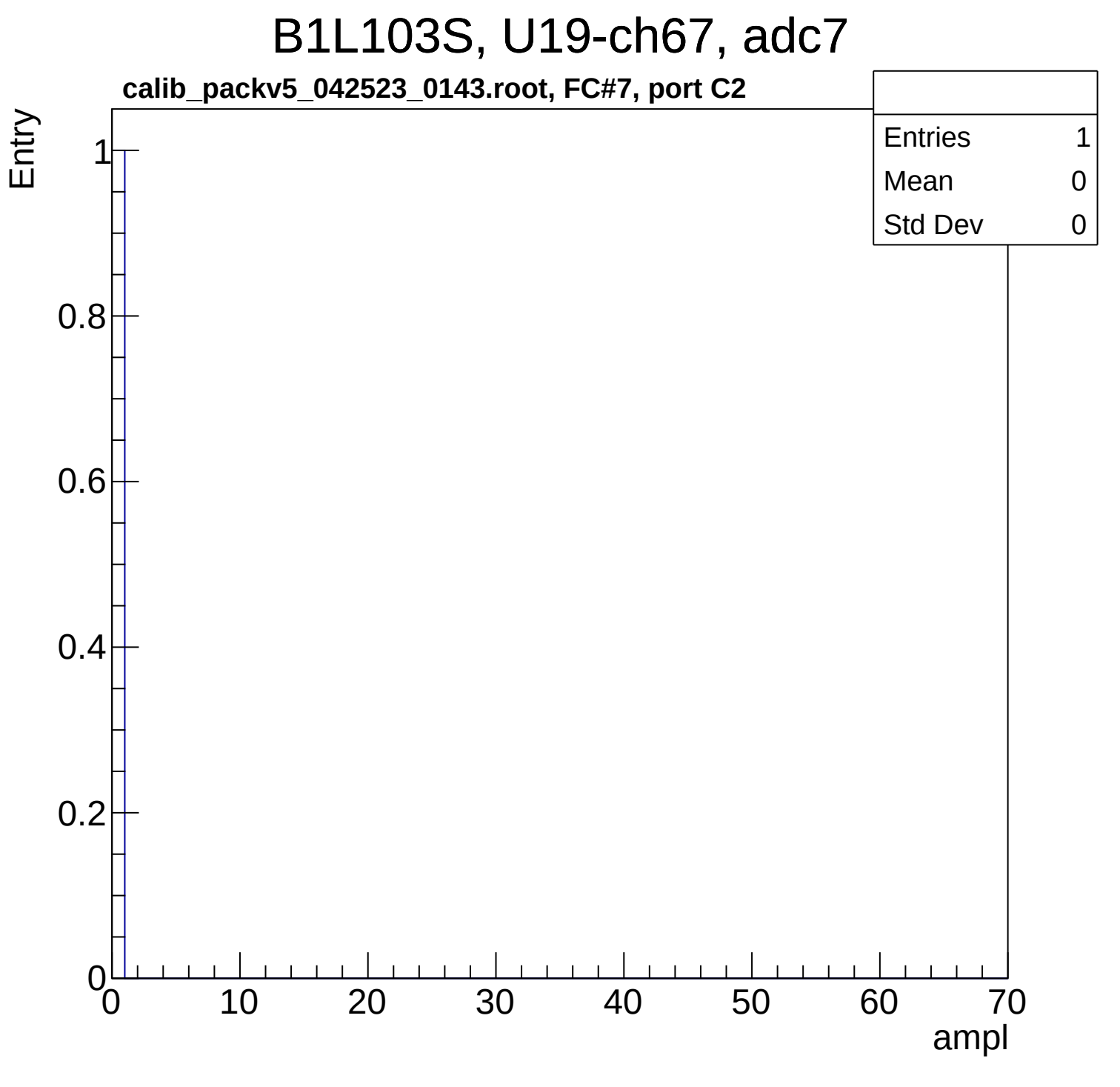
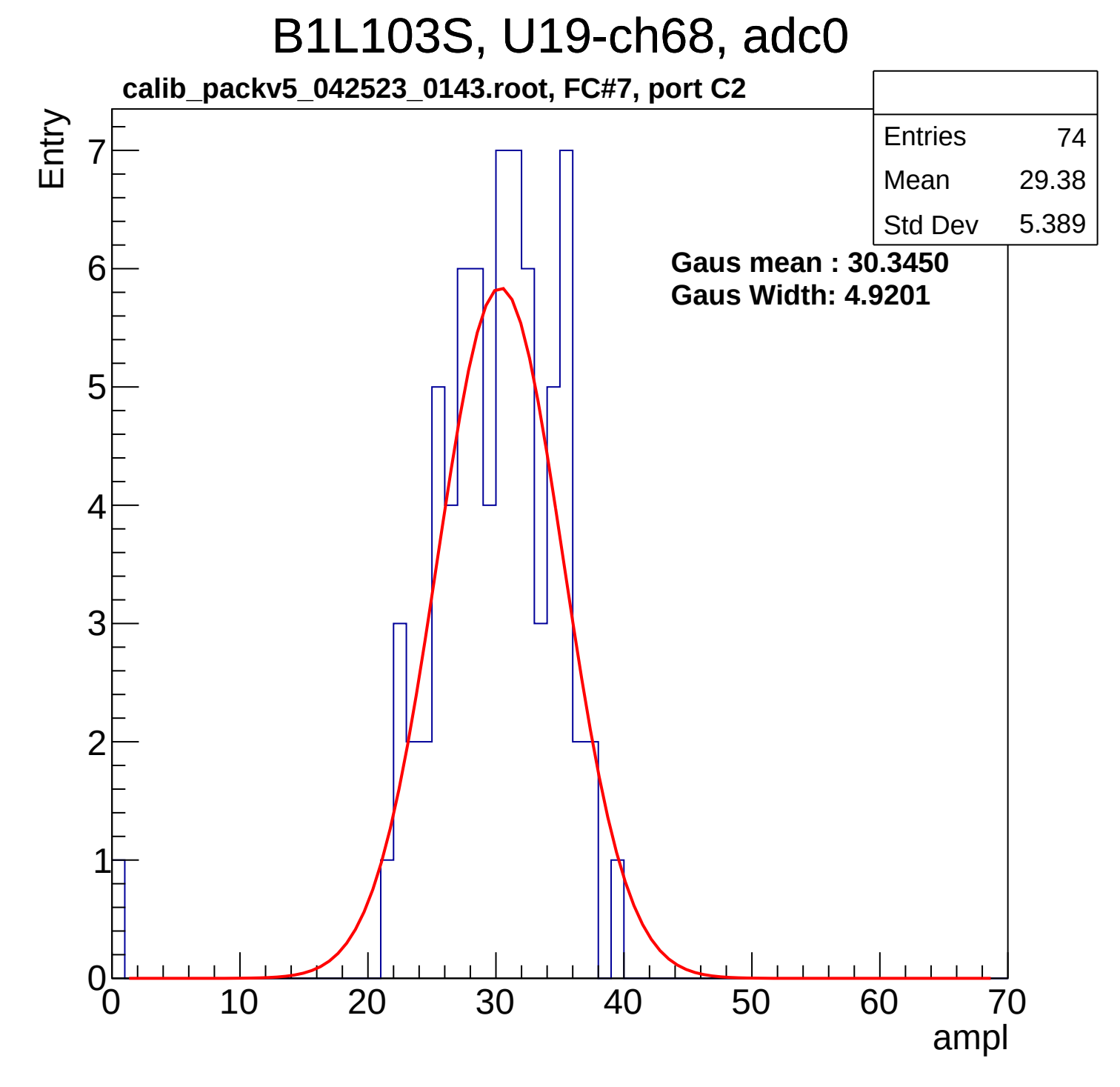
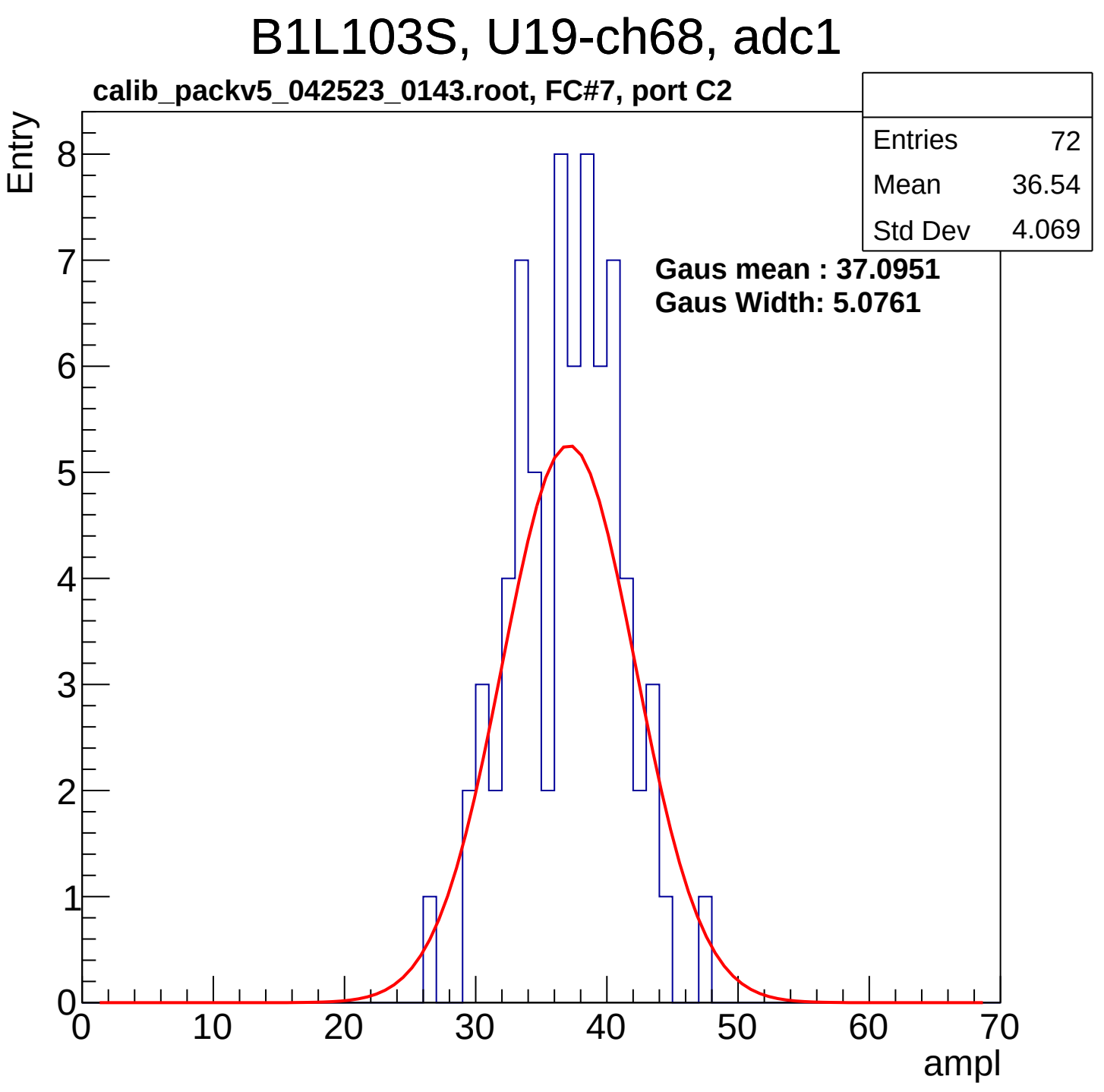

B1L103S, U19-ch67, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch68, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 29.38
Std Dev 5.389
Gaus mean : 30.3450
Gaus Width: 4.9201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch68, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.54
Std Dev 4.069
Gaus mean : 37.0951
Gaus Width: 5.0761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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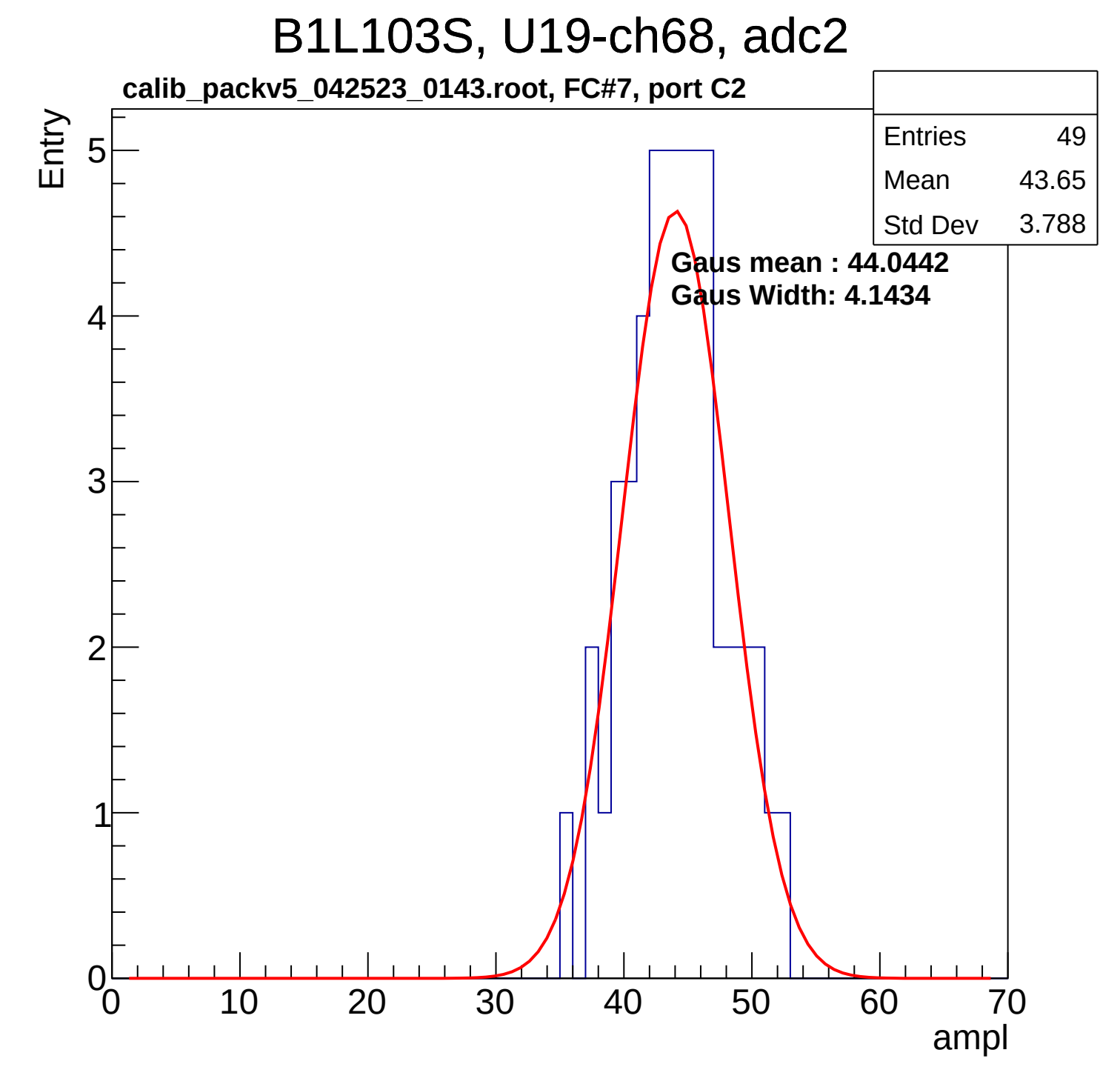
B1L103S, U19-ch68, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 43.65
Std Dev 3.788
Gaus mean : 44.0442
Gaus Width: 4.1434
Entry
ampl
0
1
2
3
4
5
0
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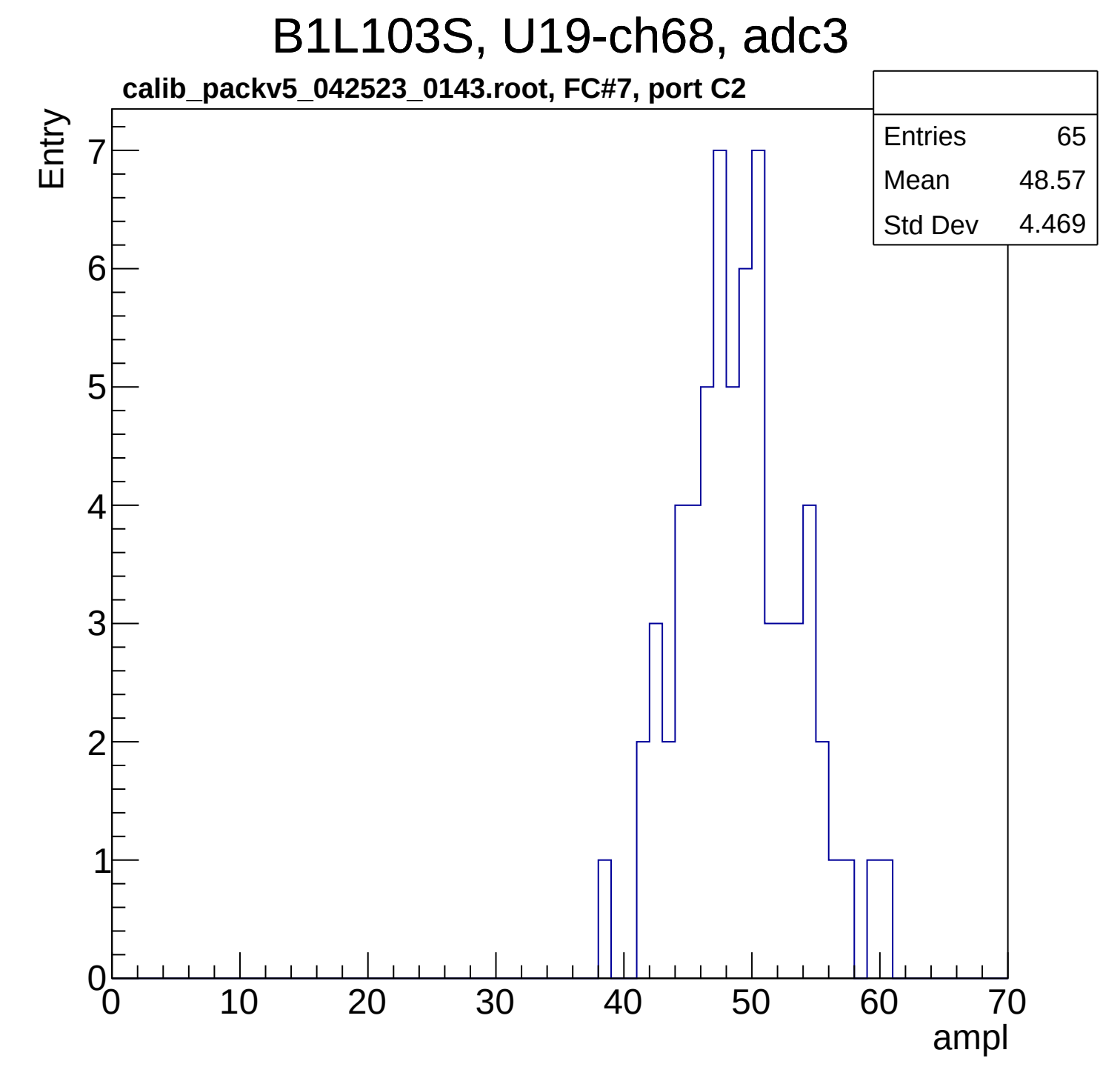
20
30
40
50
60
70


B1L103S, U19-ch68, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.57
Std Dev 4.469
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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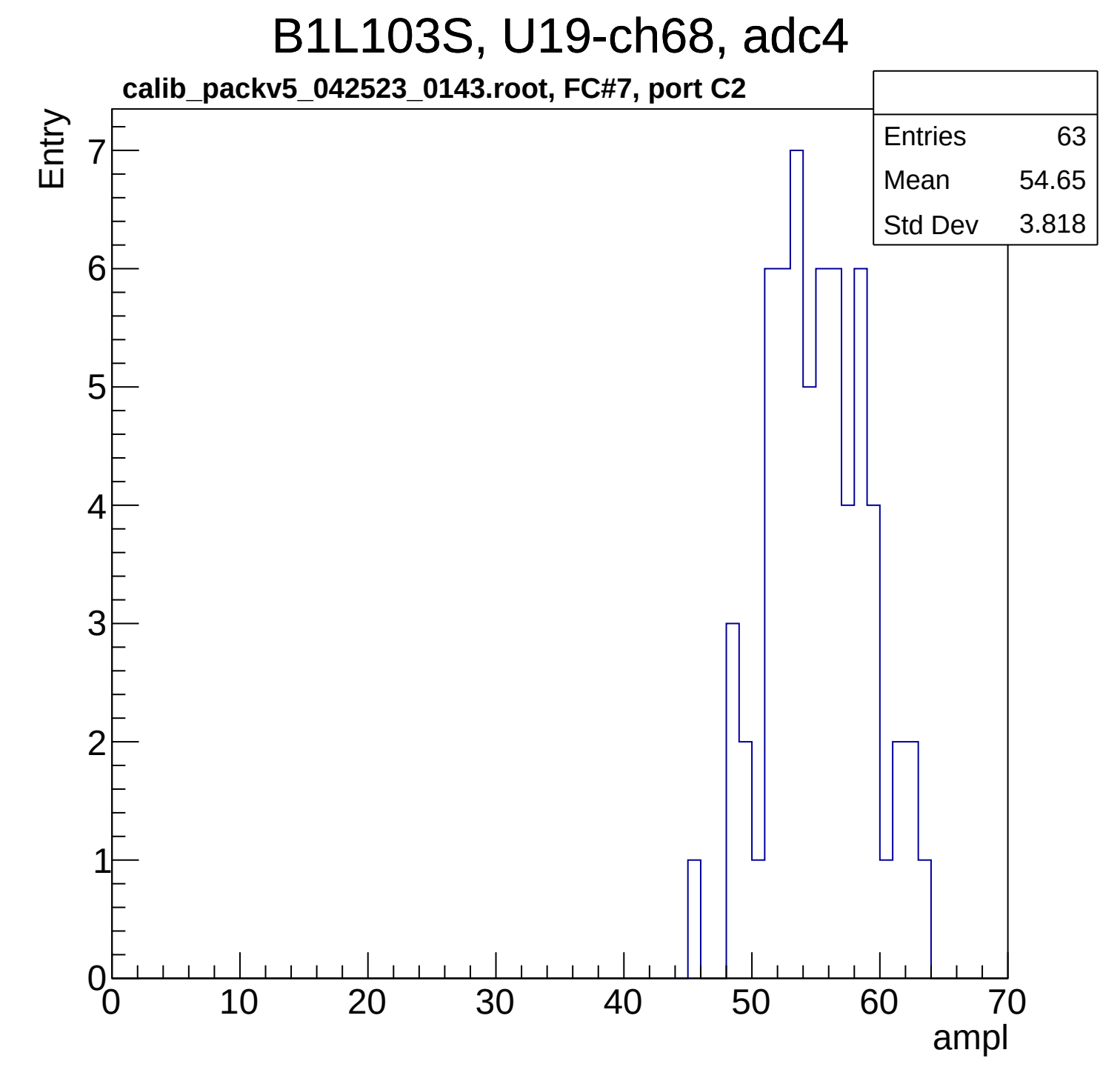
B1L103S, U19-ch68, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.65
Std Dev 3.818
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
50
60
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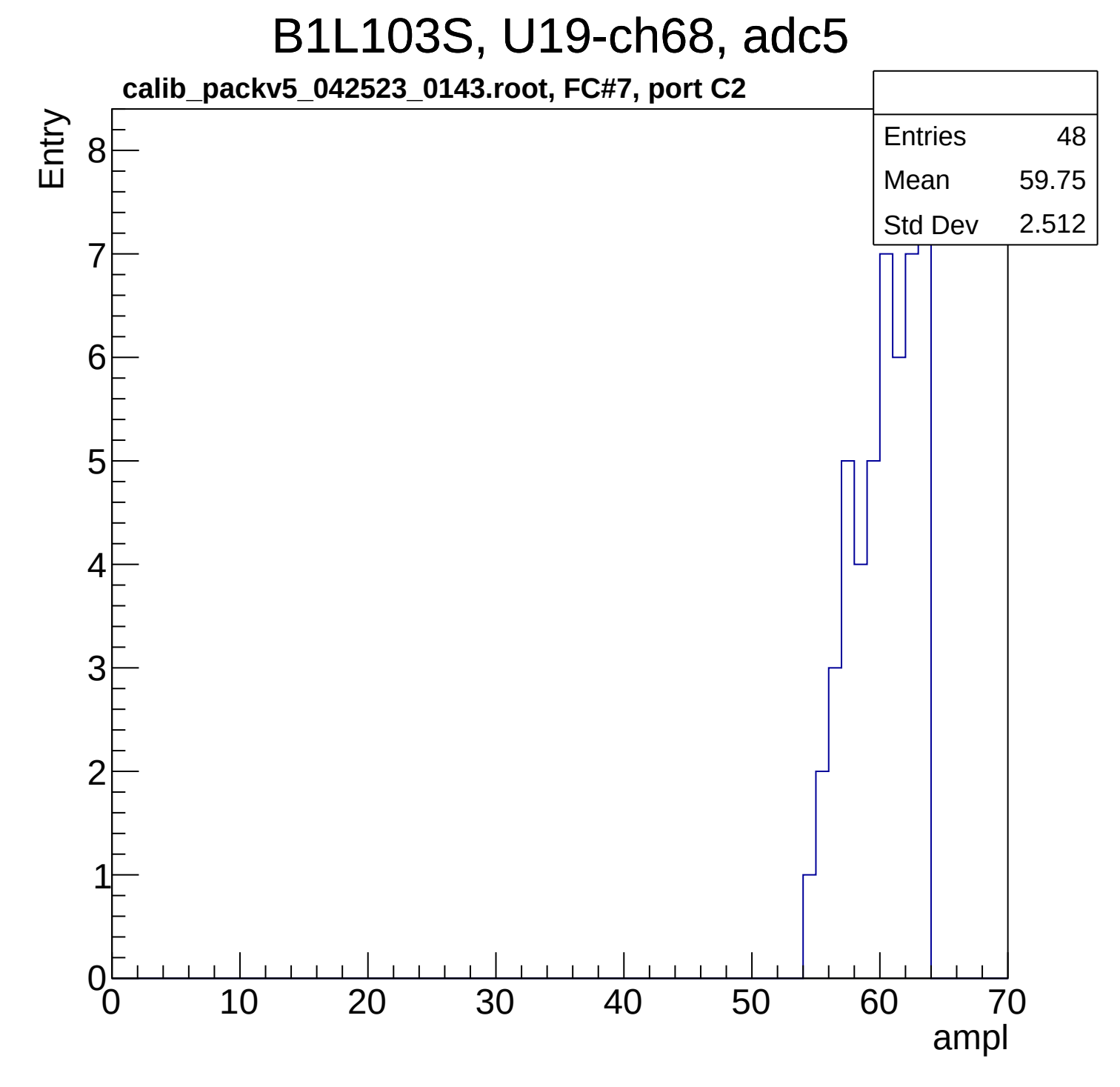
B1L103S, U19-ch68, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.75
Std Dev 2.512
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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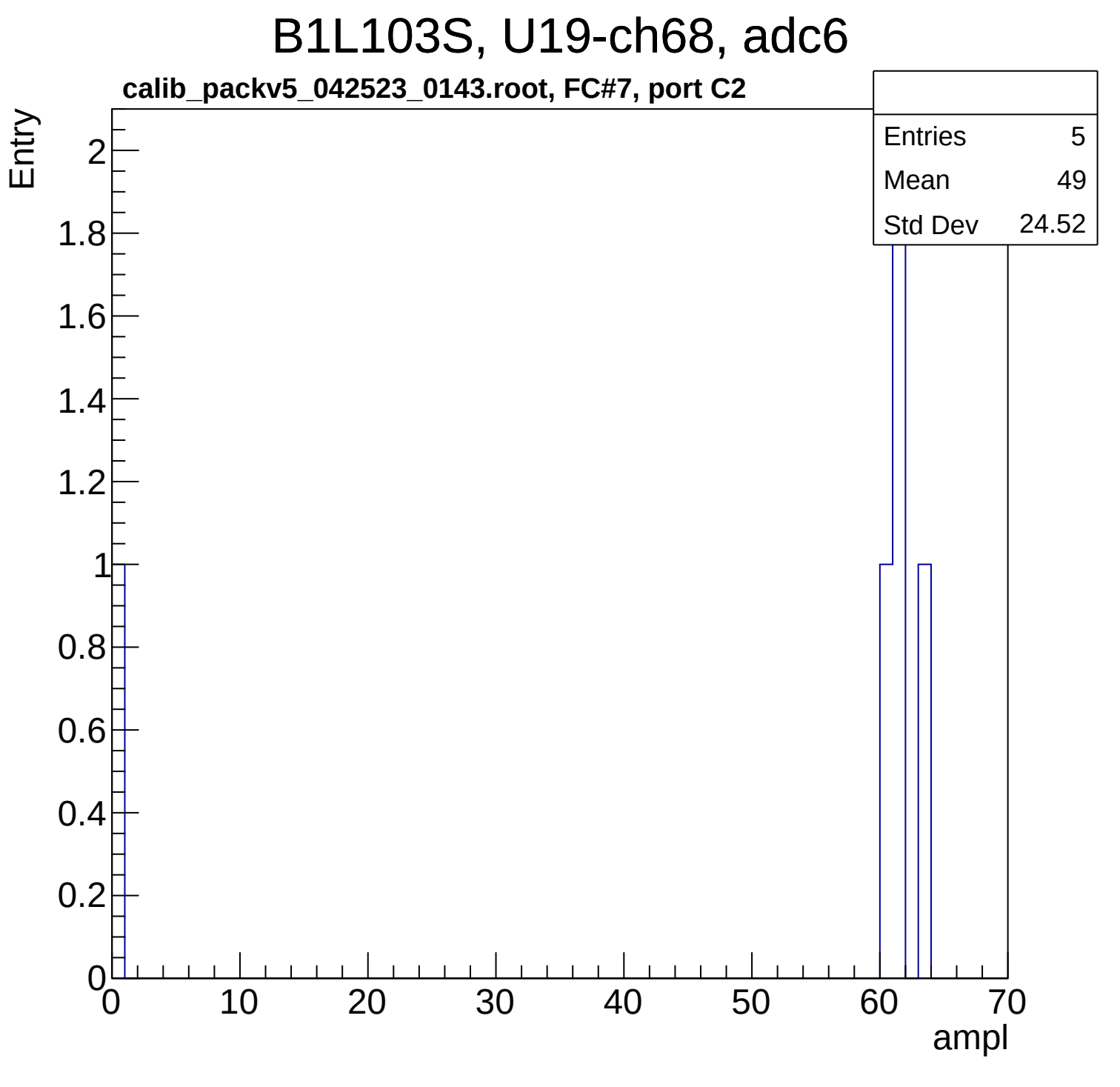
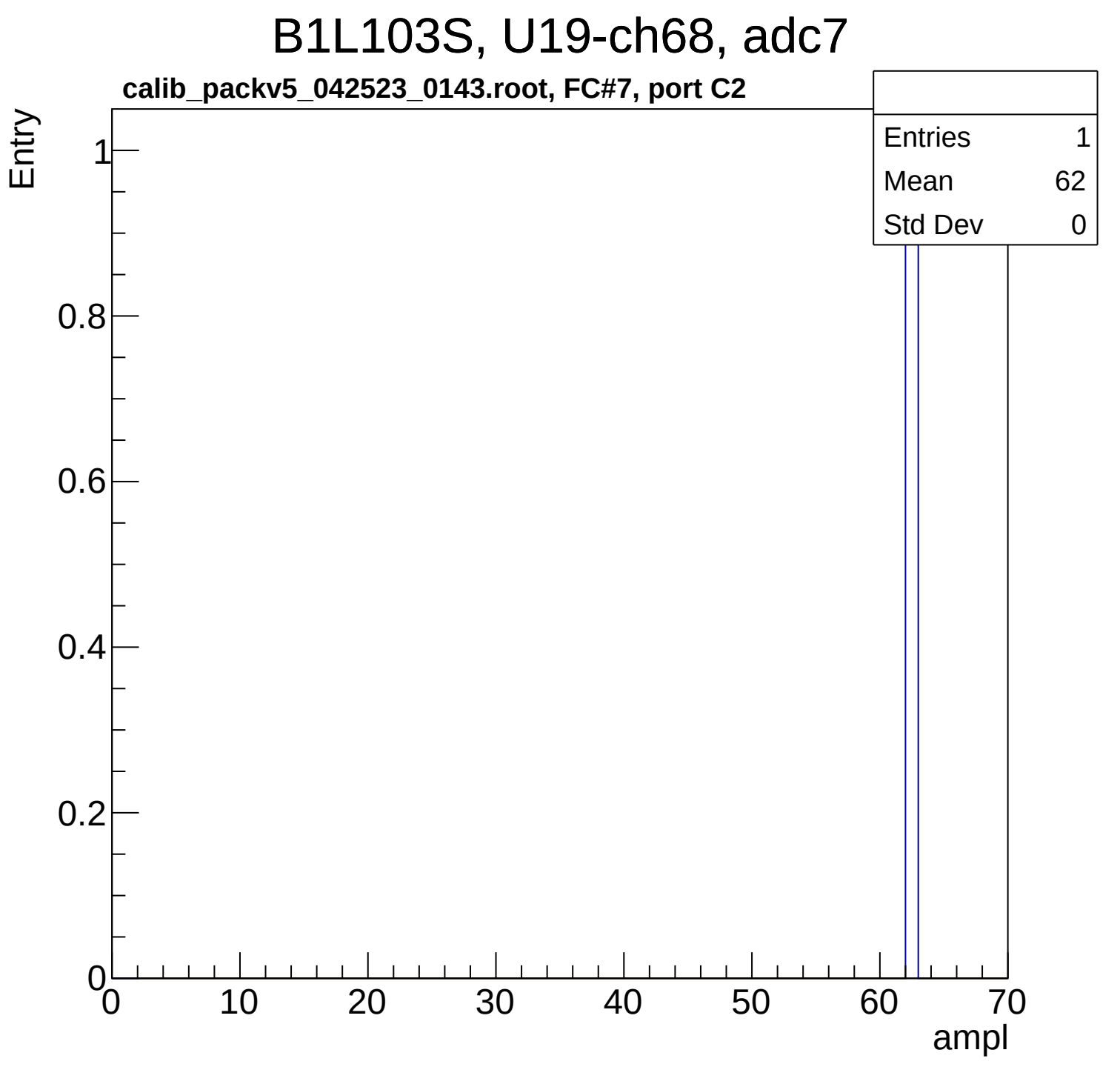

B1L103S, U19-ch68, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49
Std Dev 24.52
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch68, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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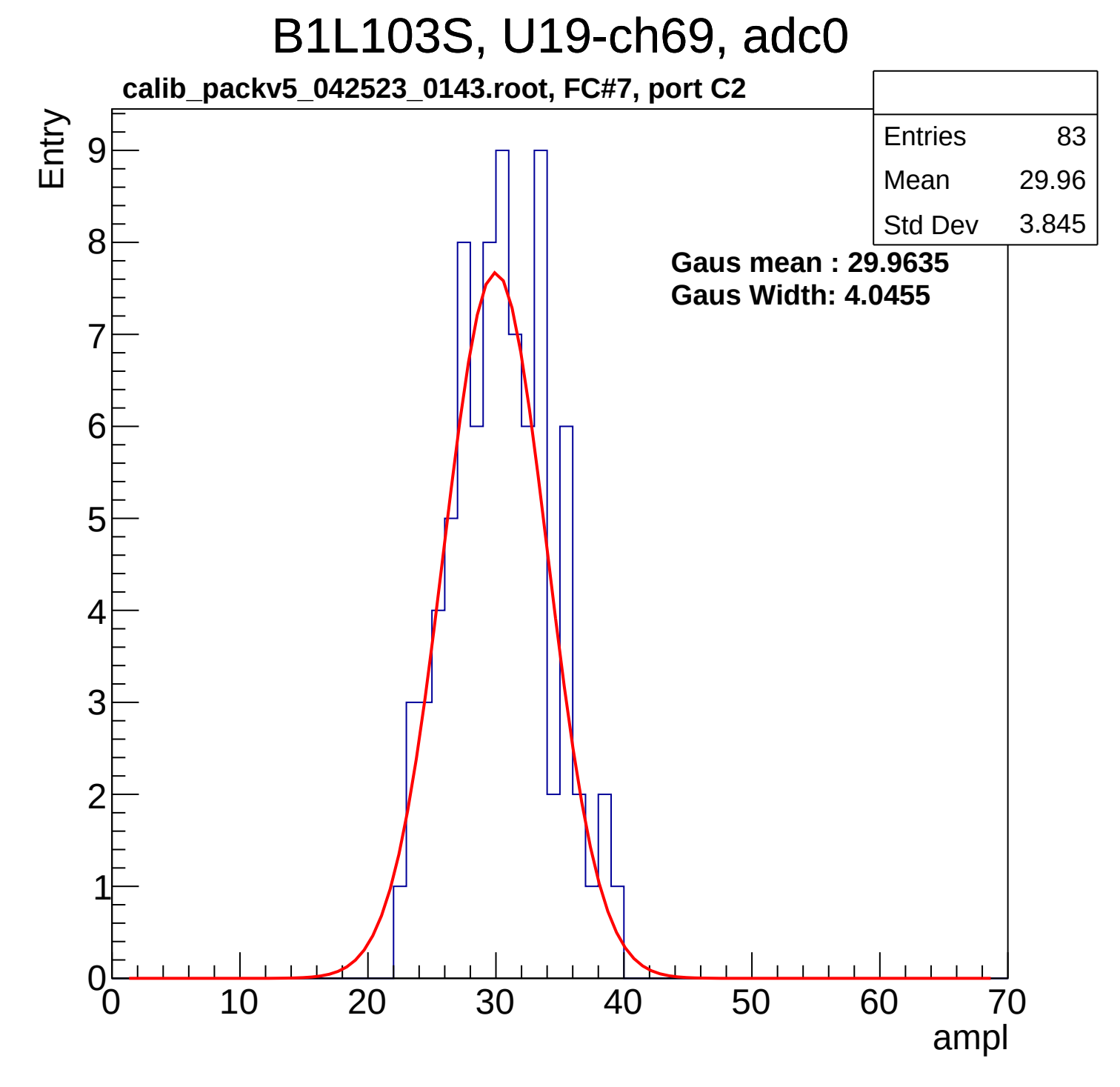
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch69, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 29.96
Std Dev 3.845
Gaus mean : 29.9635
Gaus Width: 4.0455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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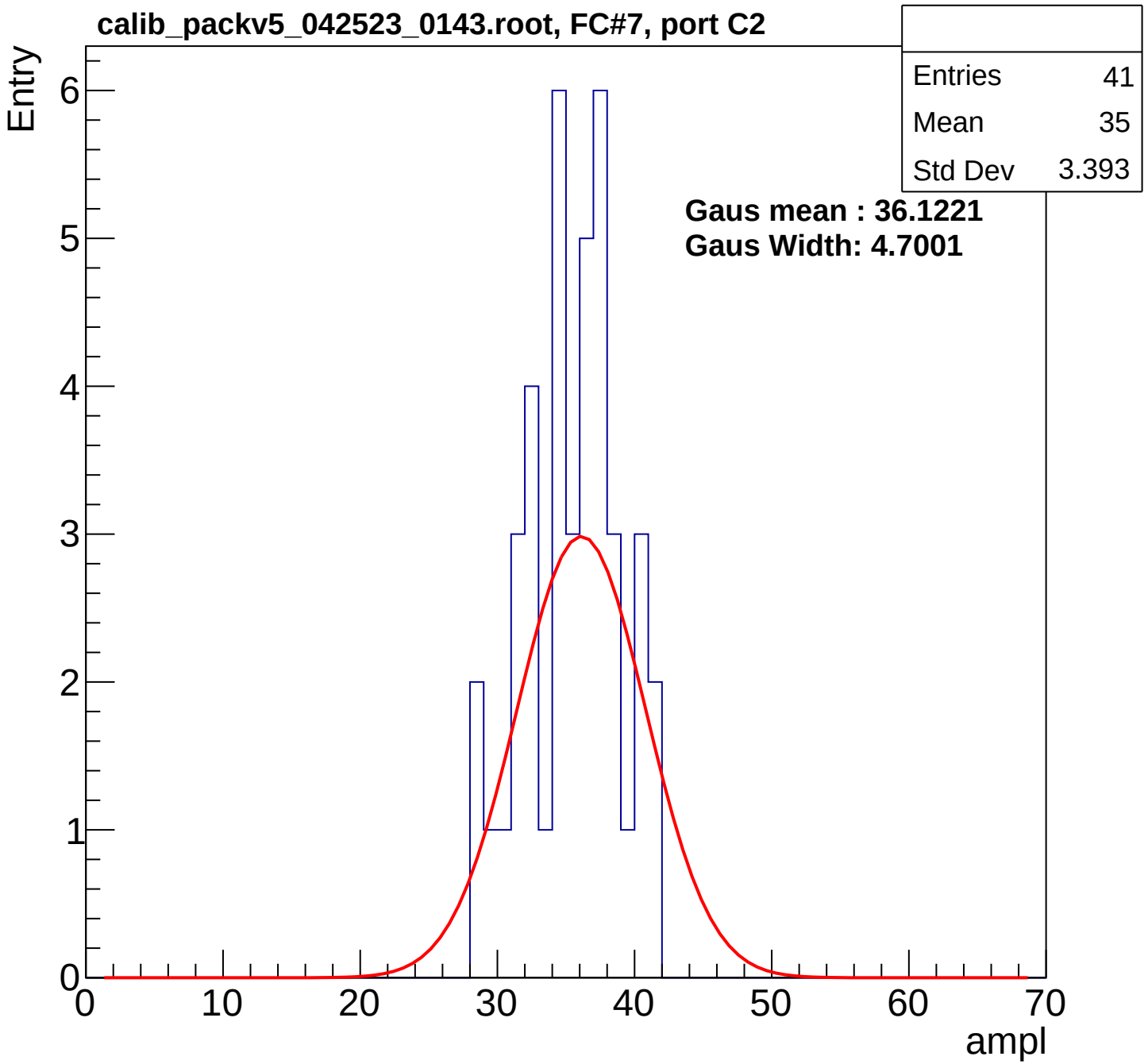
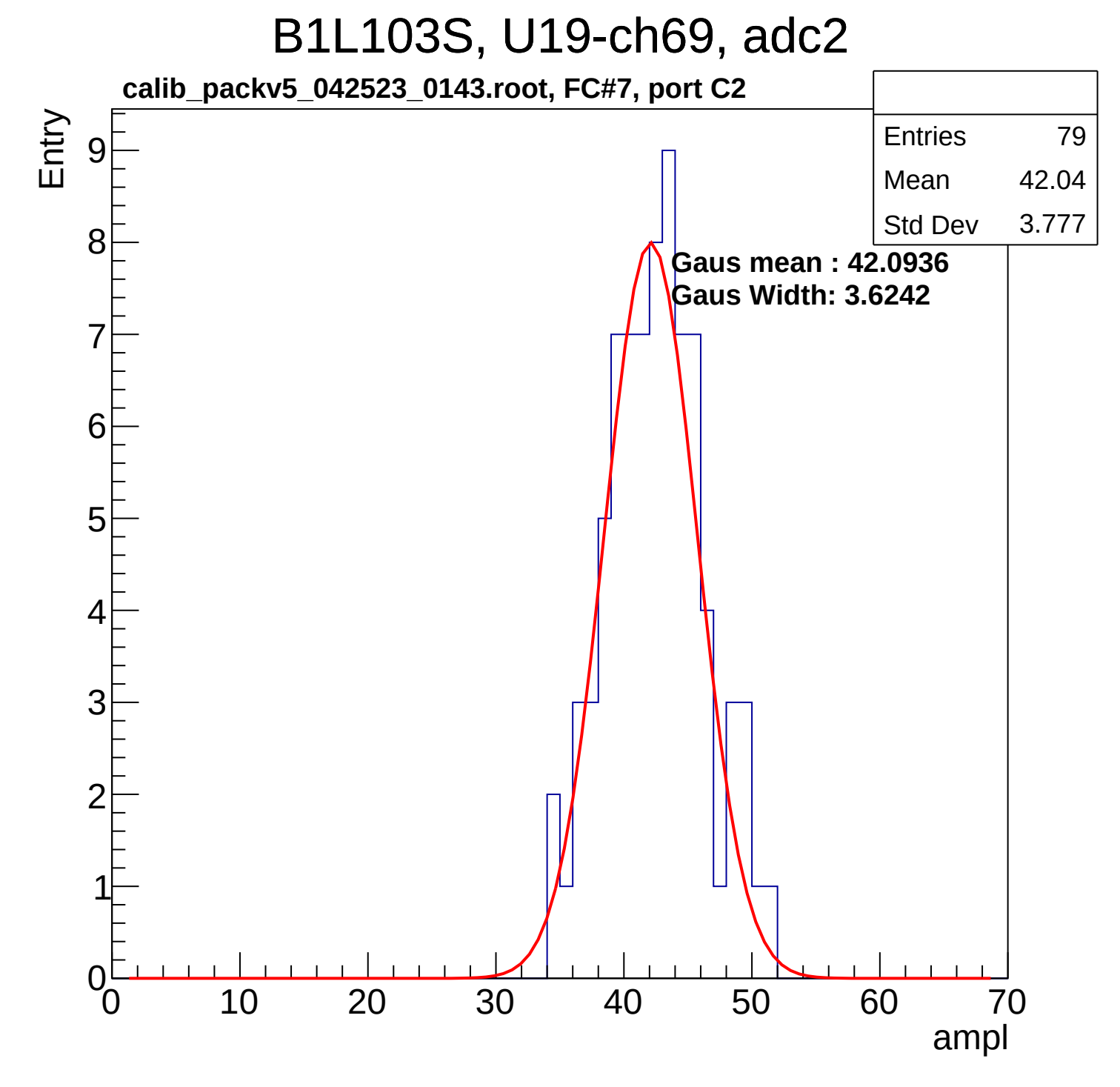

B1L103S, U19-ch69, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 35
Std Dev 3.393
Gaus mean : 36.1221
Gaus Width: 4.7001
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch69, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 42.04
Std Dev 3.777
Gaus mean : 42.0936
Gaus Width: 3.6242
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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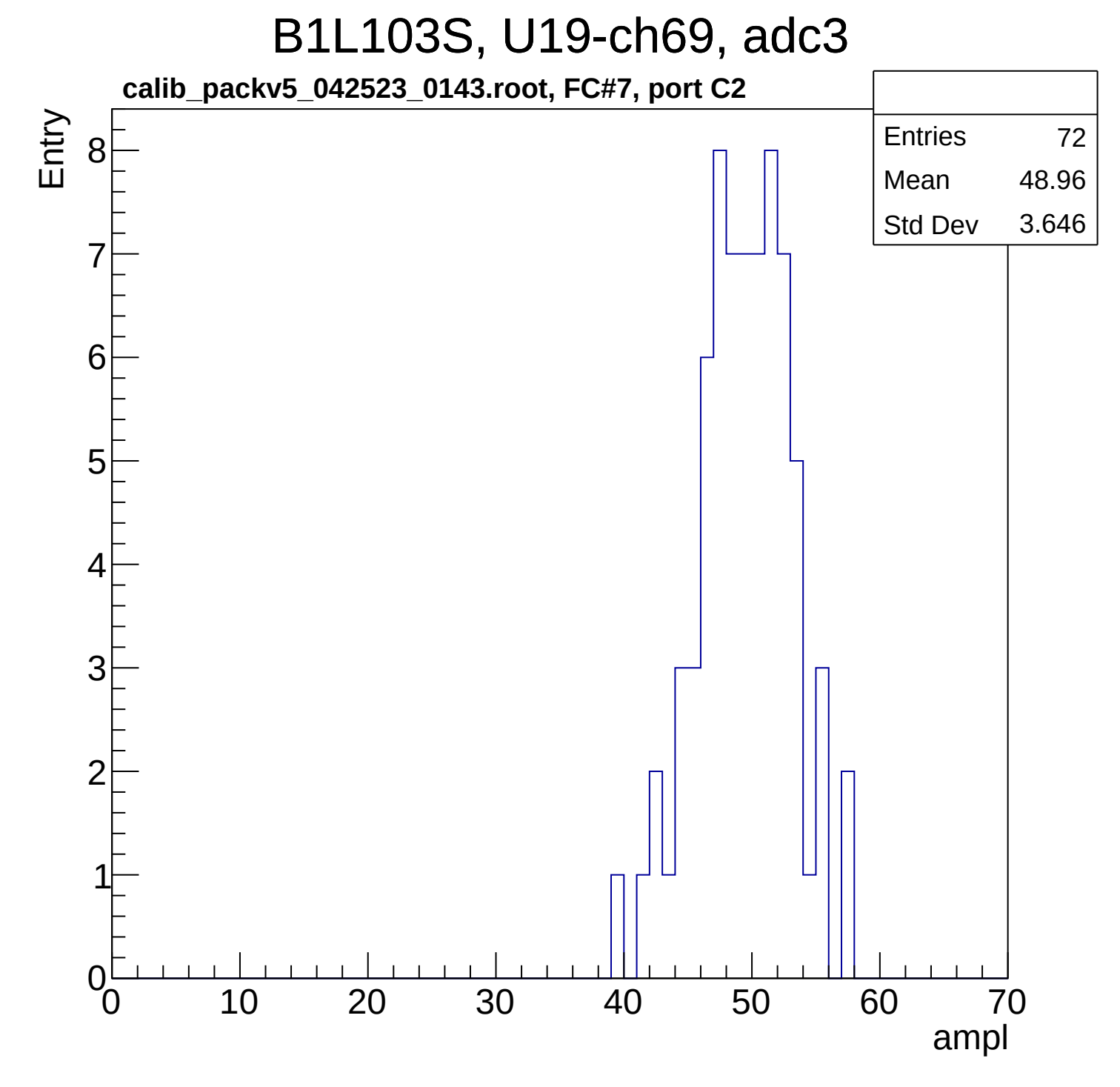
B1L103S, U19-ch69, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 48.96
Std Dev 3.646
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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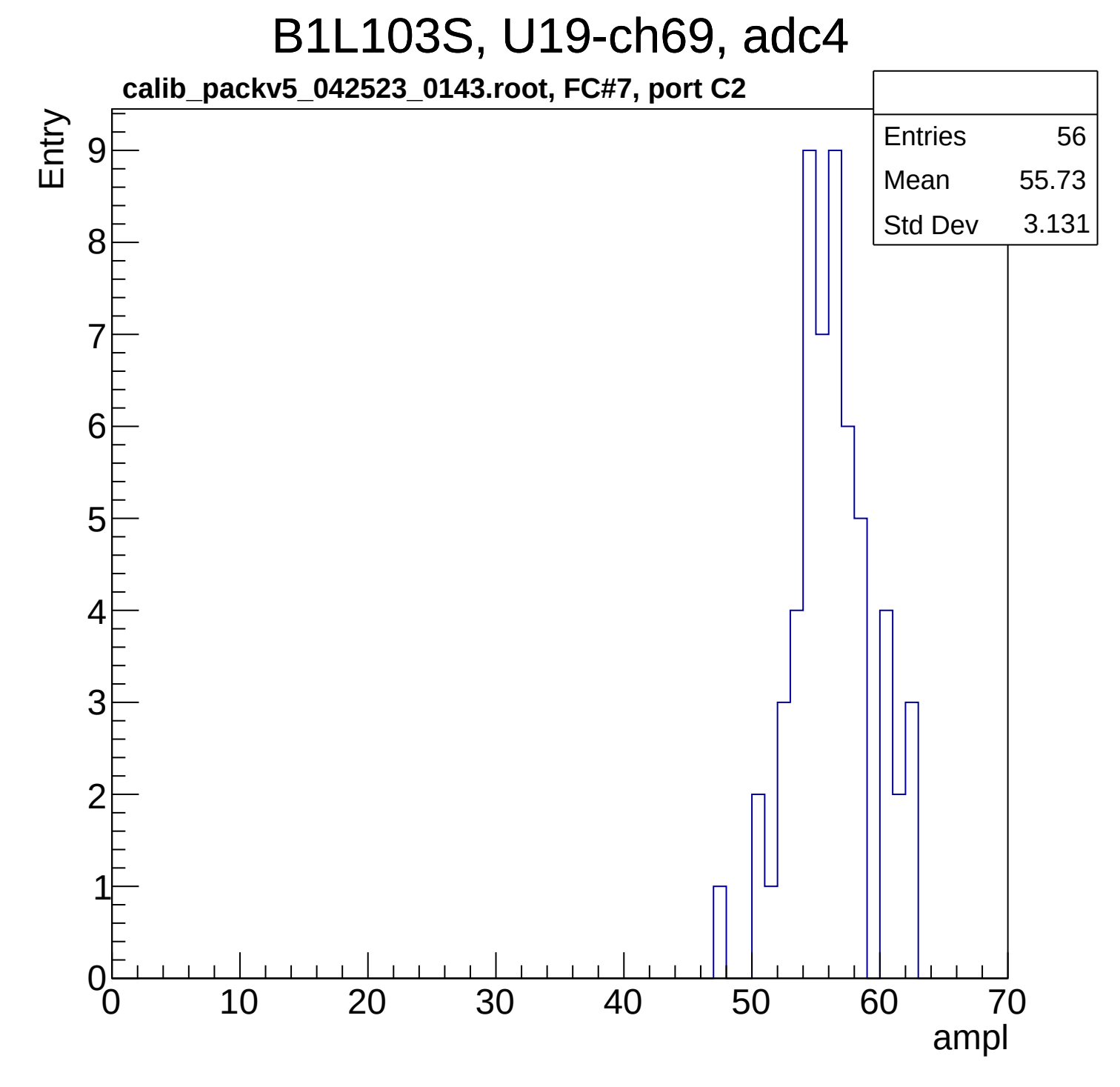
B1L103S, U19-ch69, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55.73
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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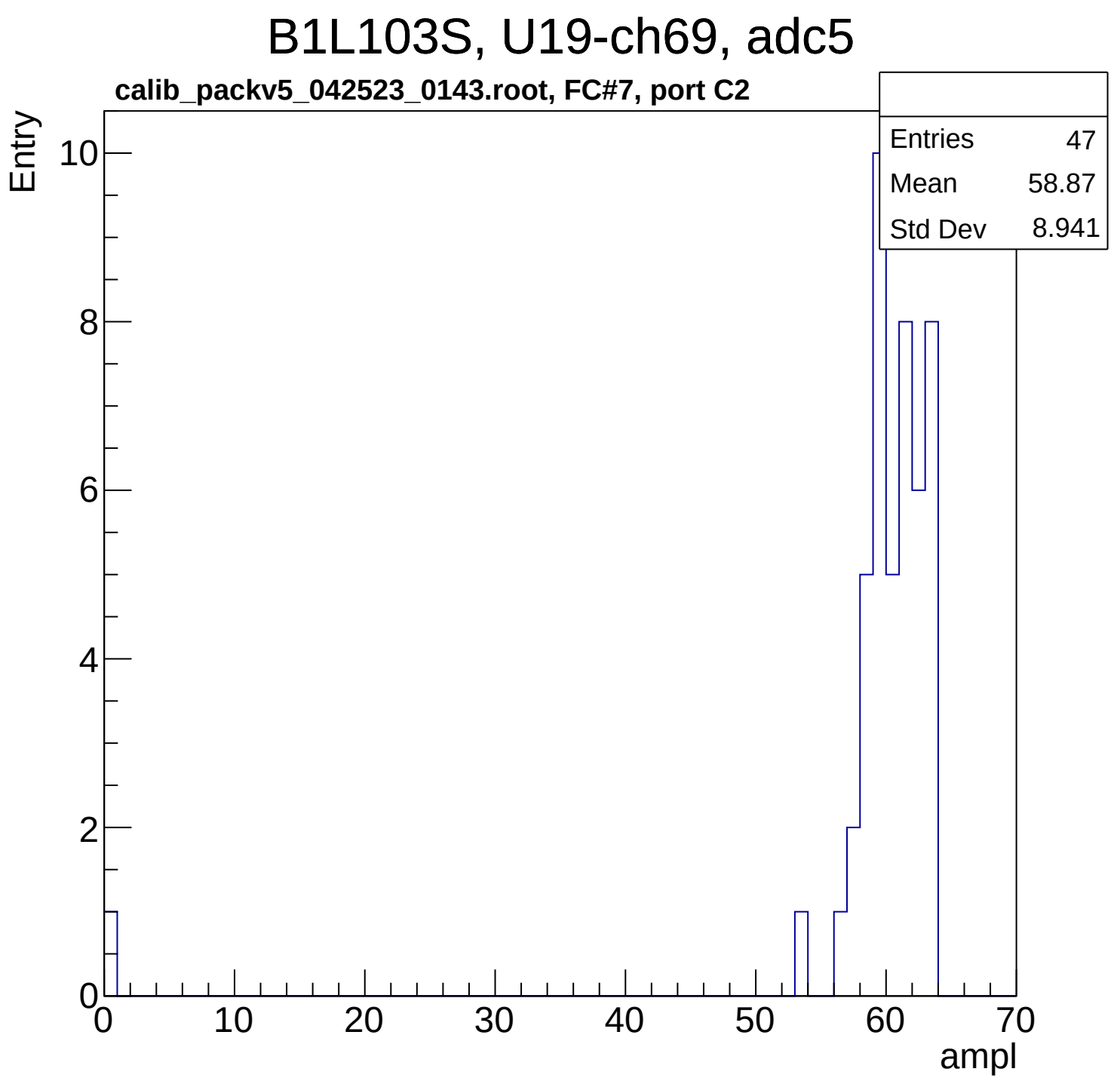
B1L103S, U19-ch69, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.87
Std Dev 8.941
Entry
ampl
0
2
4
6
8
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0
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40
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60
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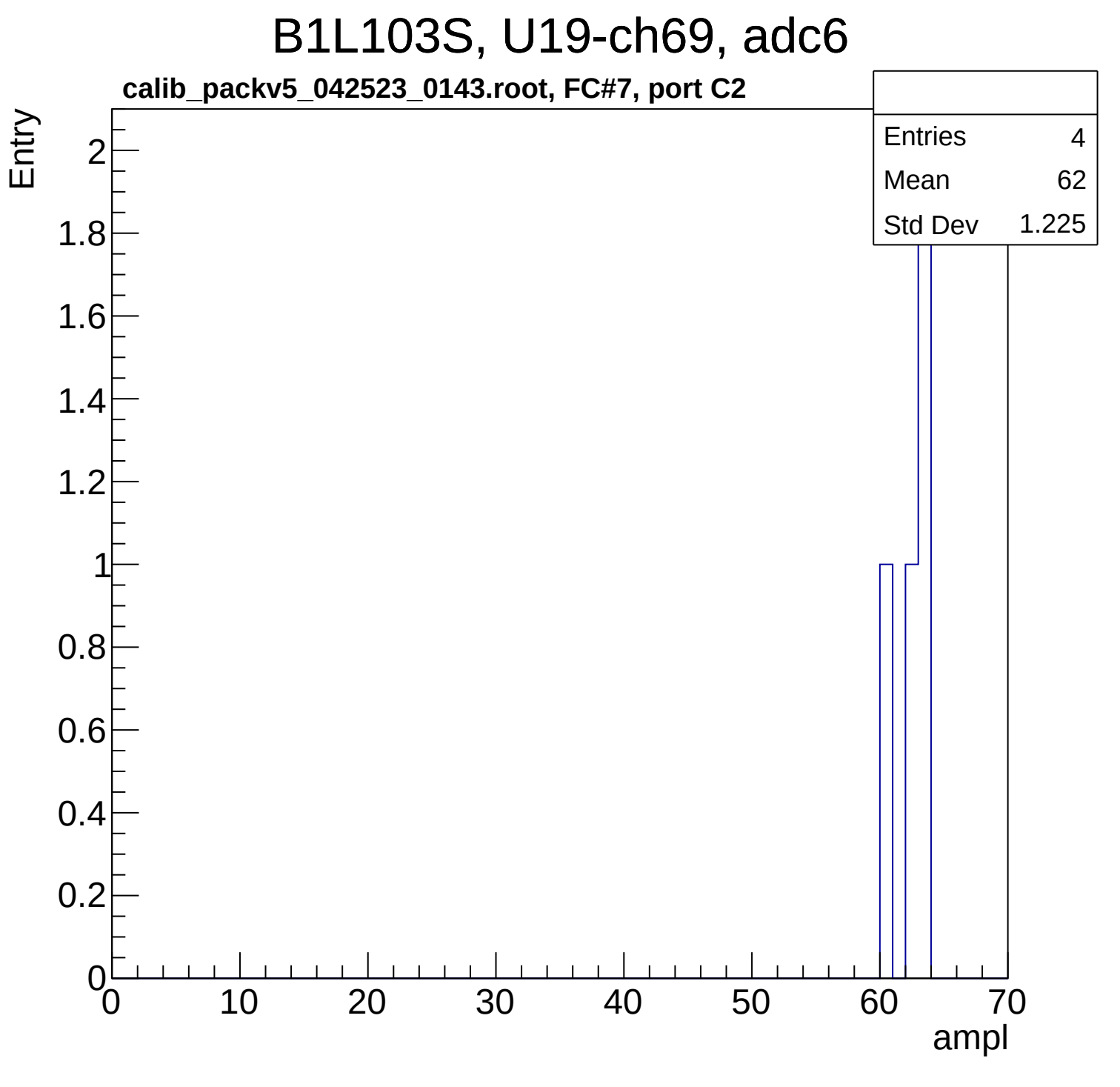
B1L103S, U19-ch69, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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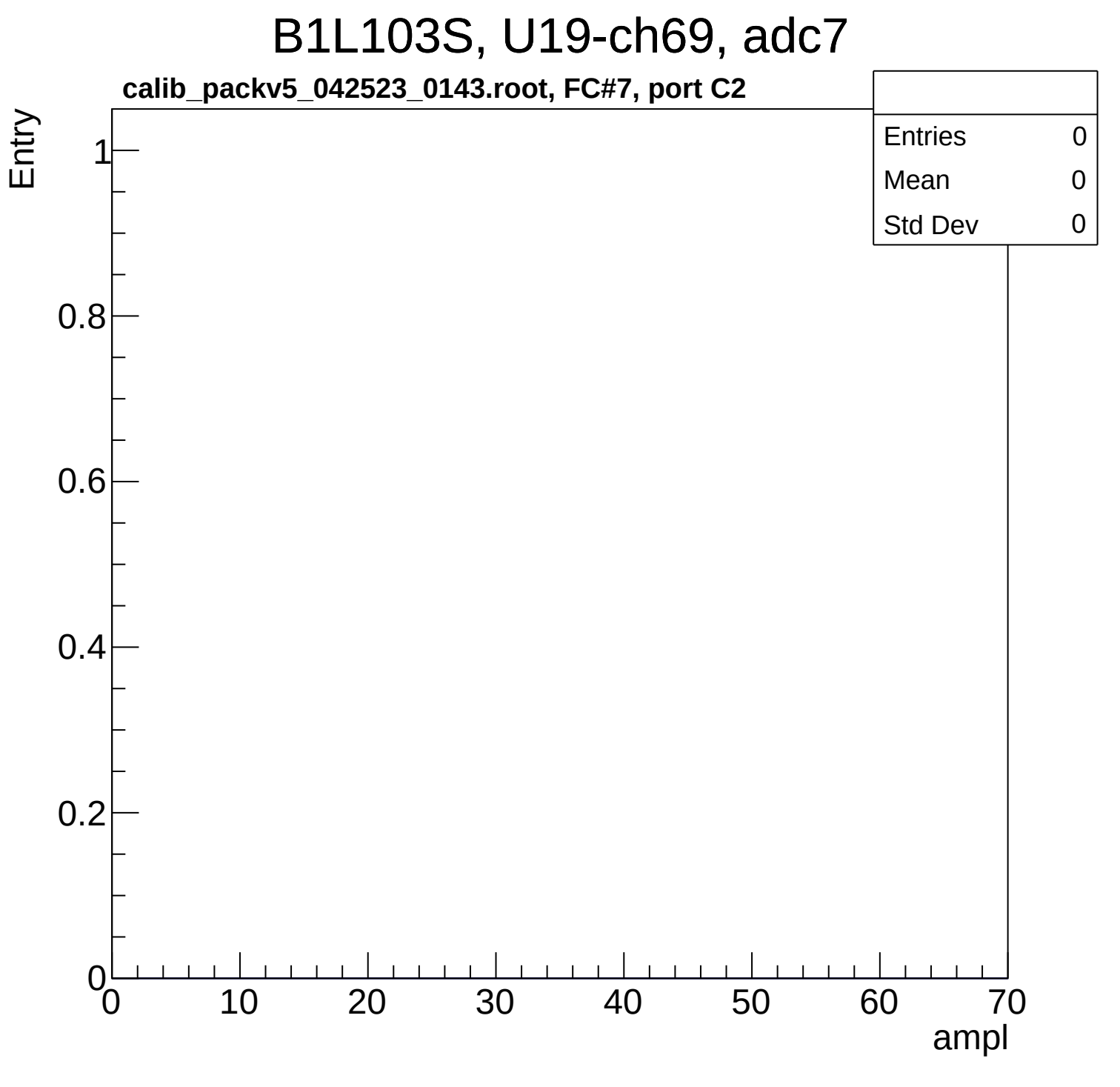
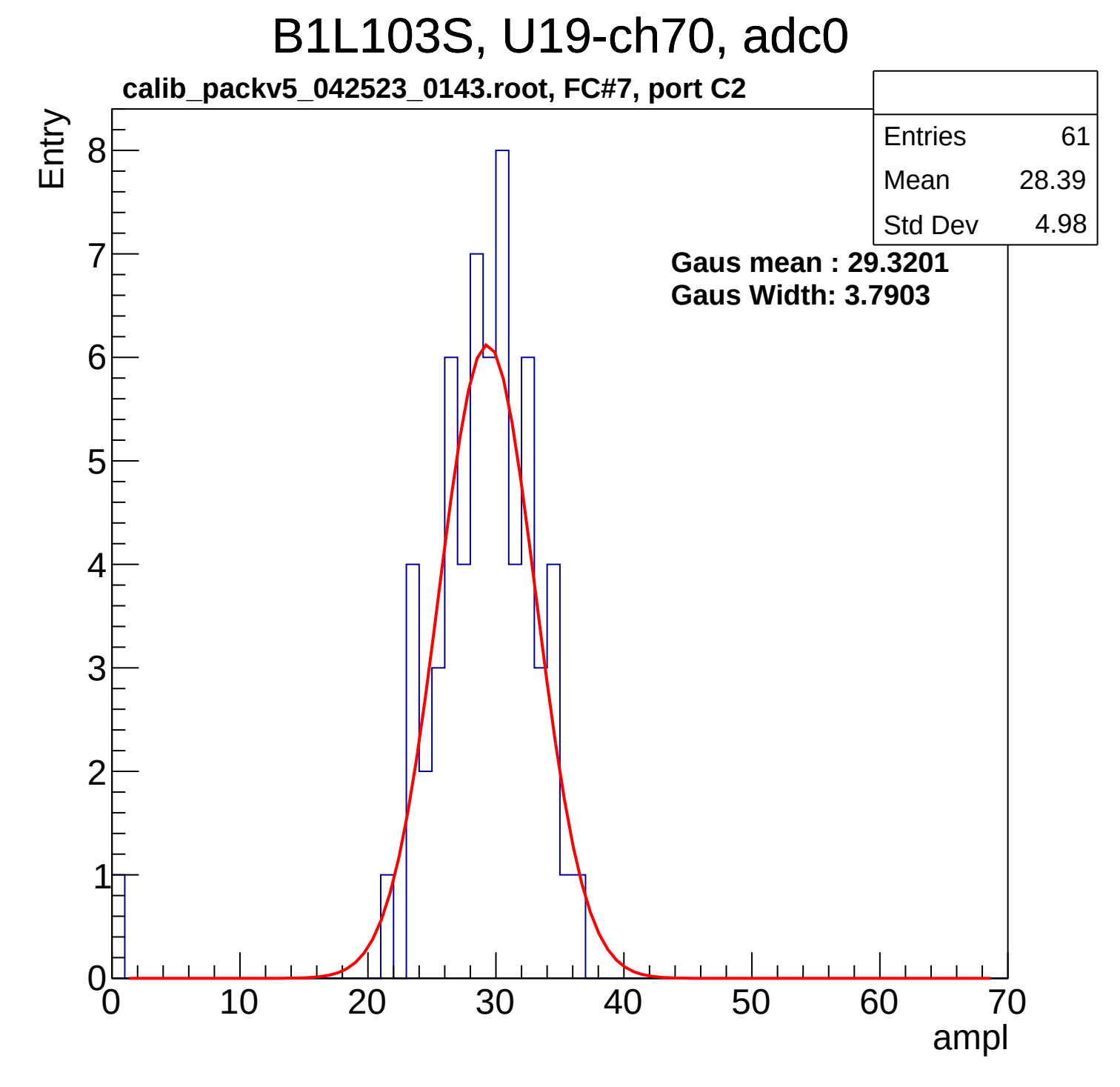

B1L103S, U19-ch69, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch70, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 28.39
Std Dev 4.98
Gaus mean : 29.3201
Gaus Width: 3.7903
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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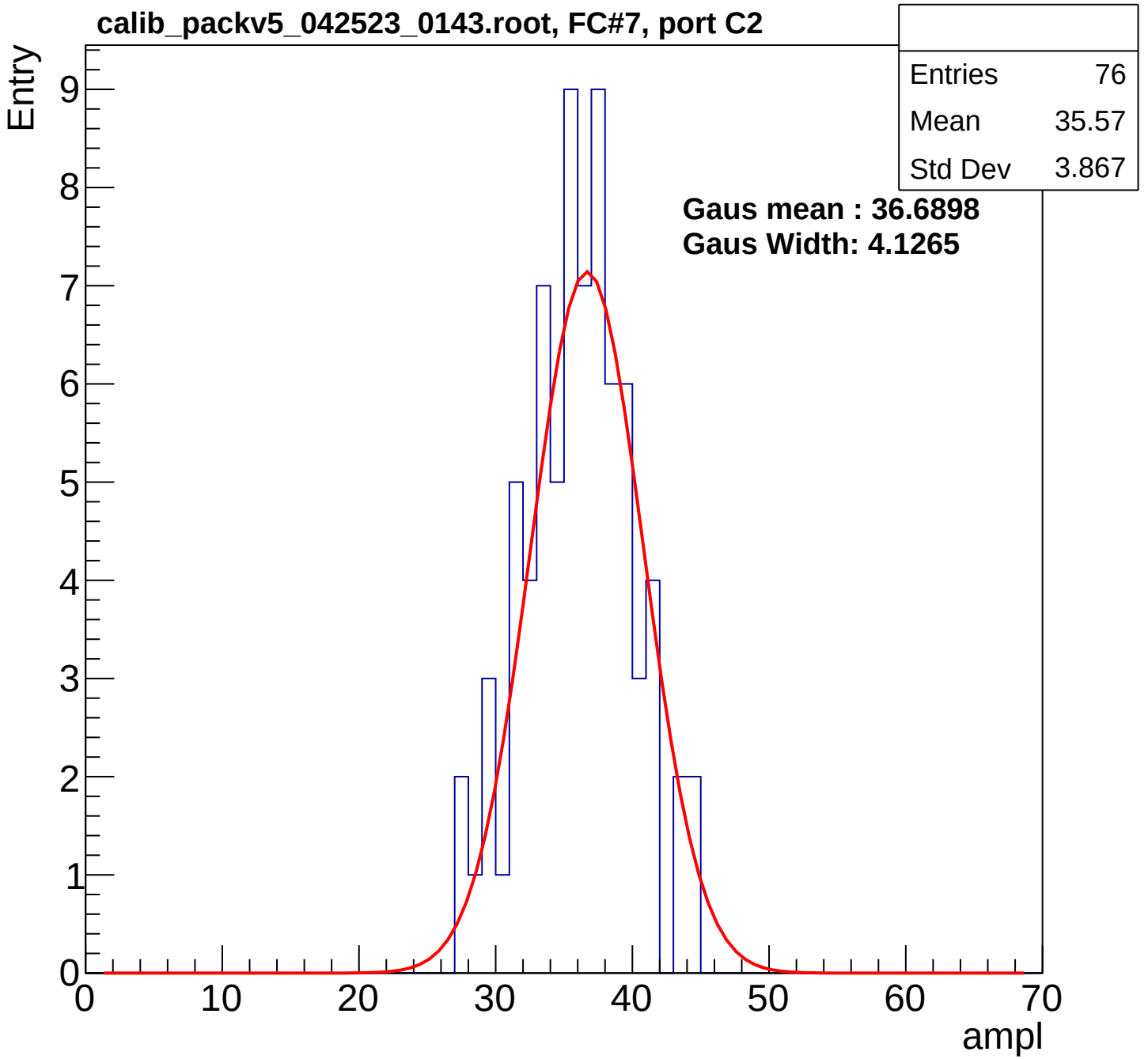
B1L103S, U19-ch70, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 35.57
Std Dev 3.867
Gaus mean : 36.6898
Gaus Width: 4.1265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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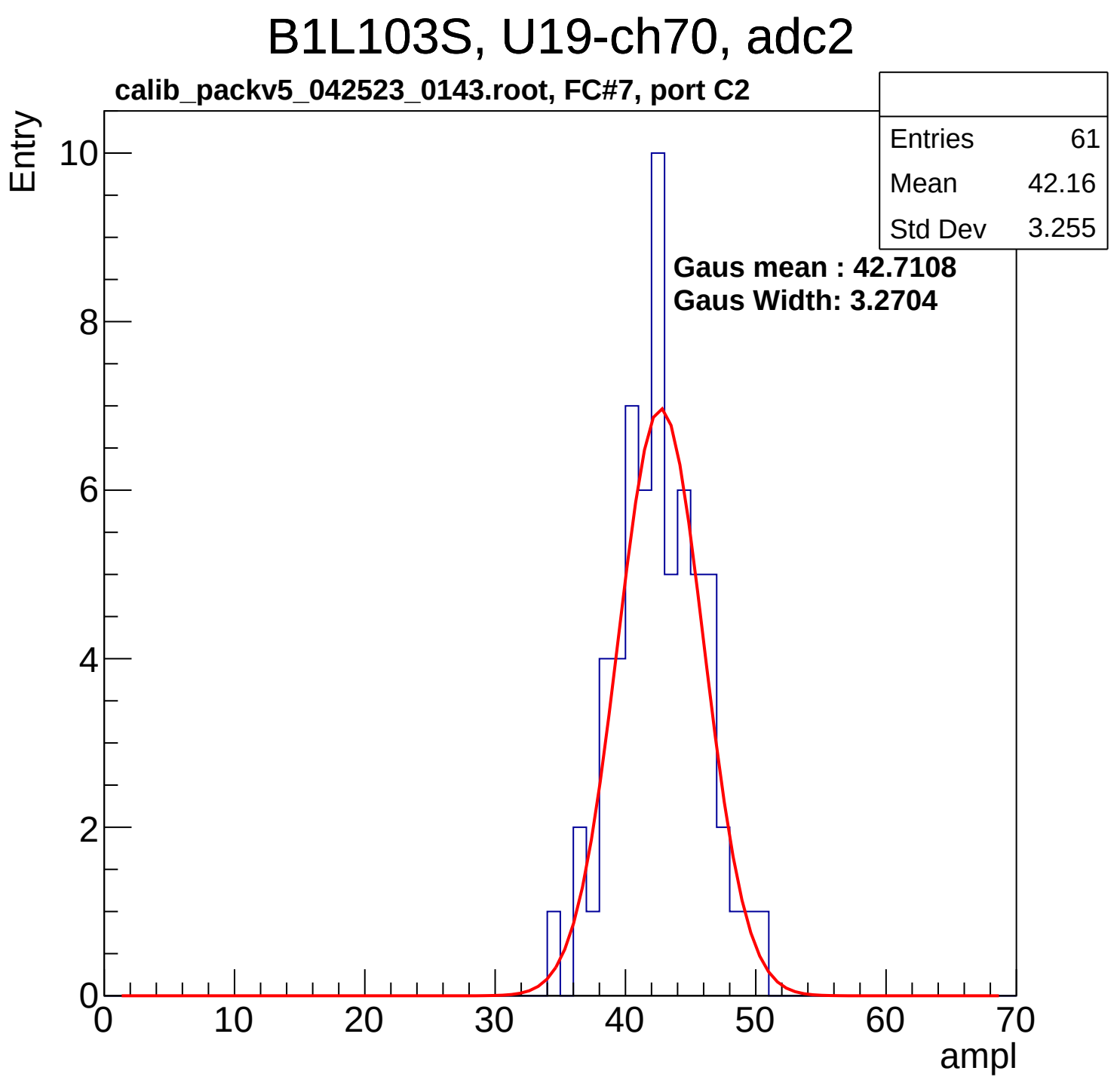
B1L103S, U19-ch70, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.16
Std Dev 3.255
Gaus mean : 42.7108
Gaus Width: 3.2704
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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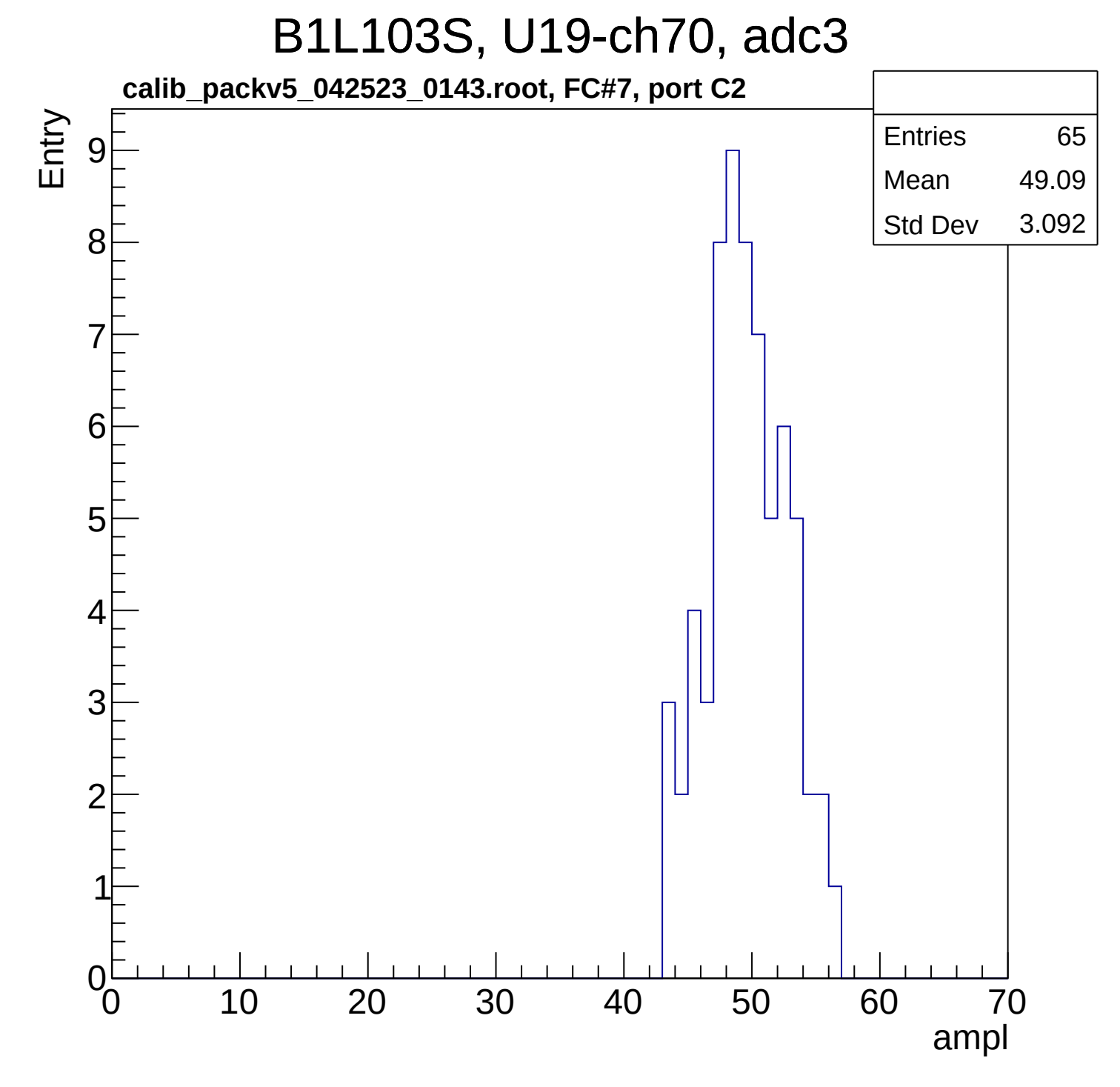
B1L103S, U19-ch70, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 49.09
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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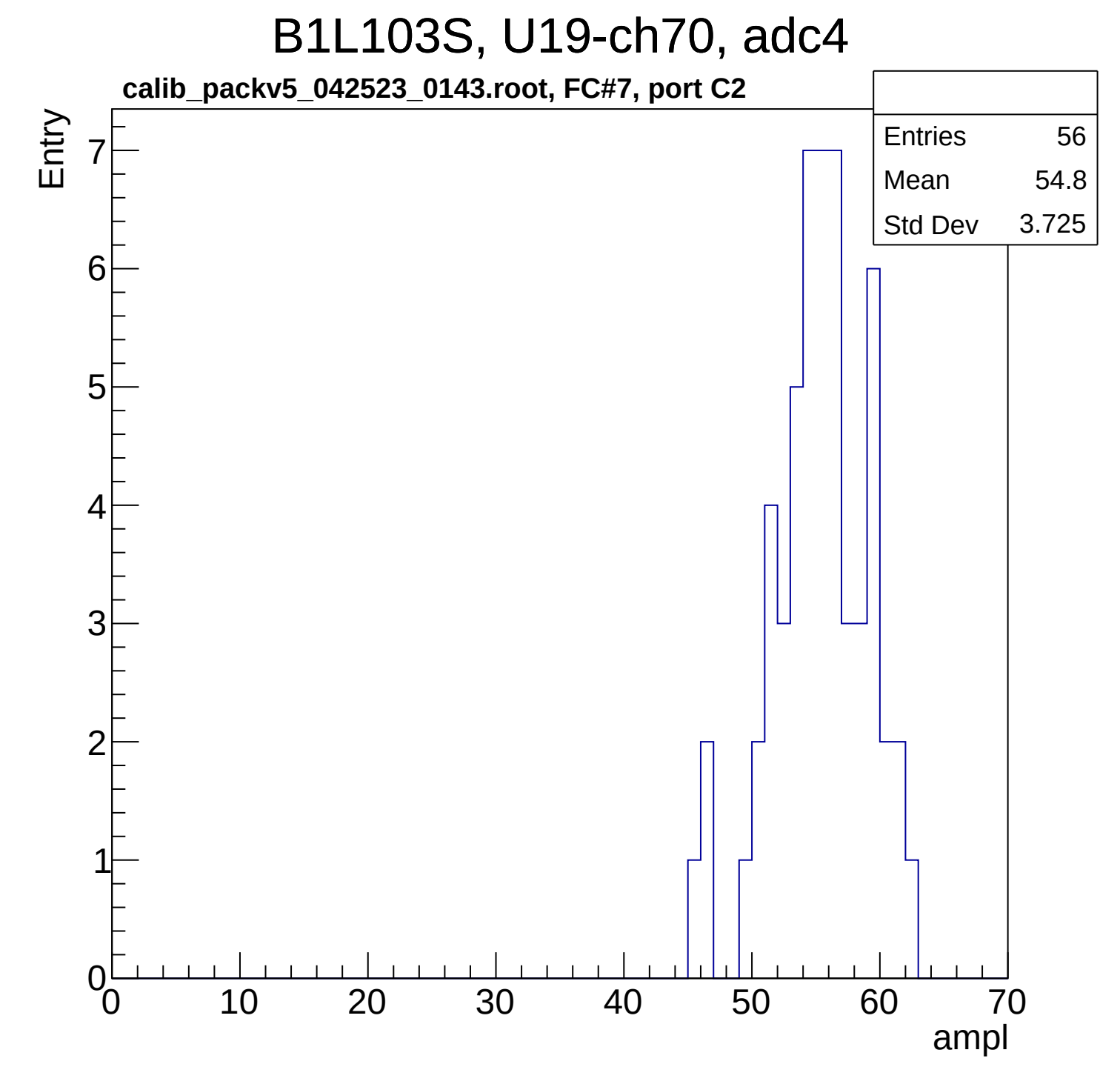
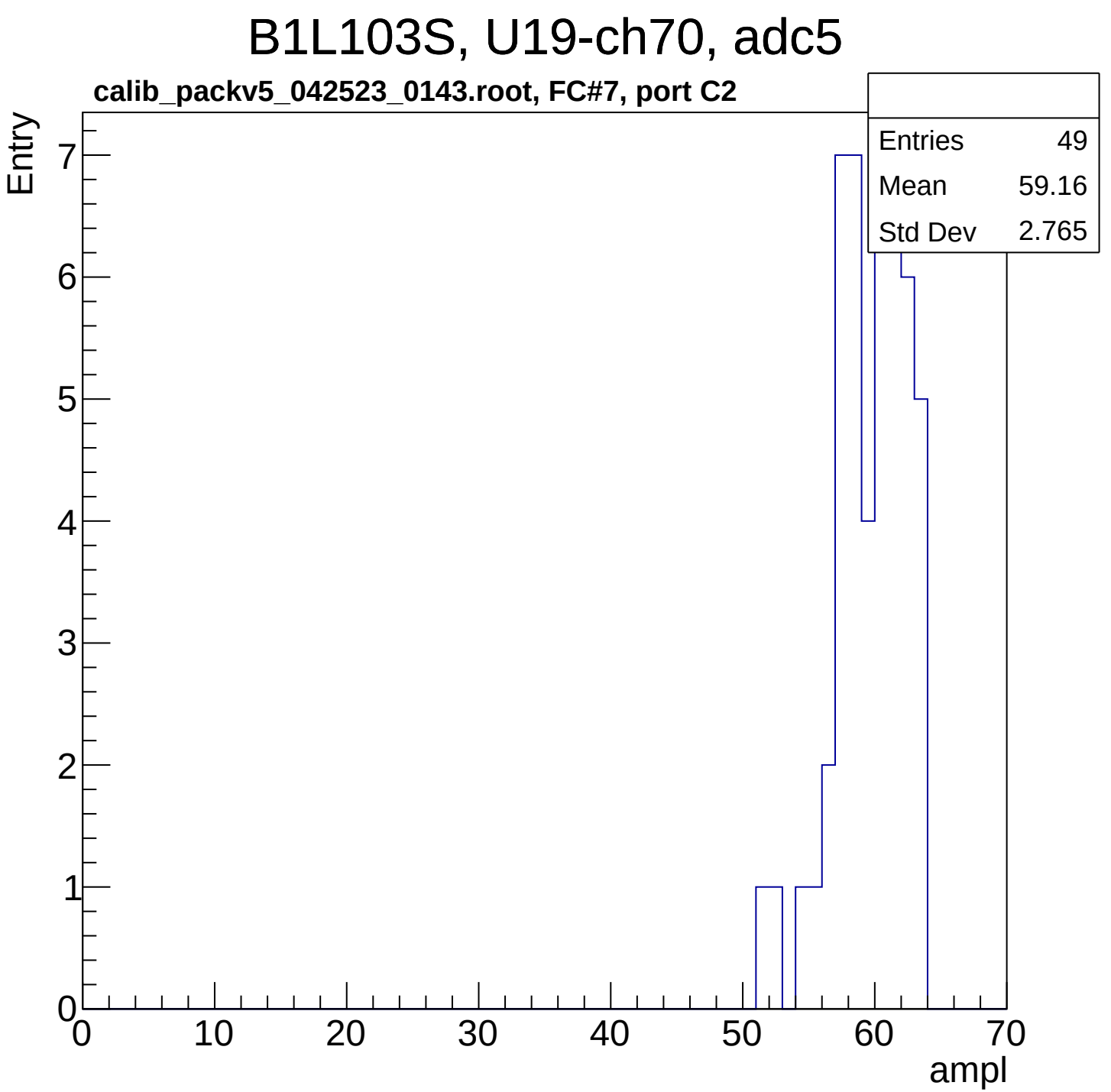

B1L103S, U19-ch70, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 54.8
Std Dev 3.725
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch70, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.16
Std Dev 2.765
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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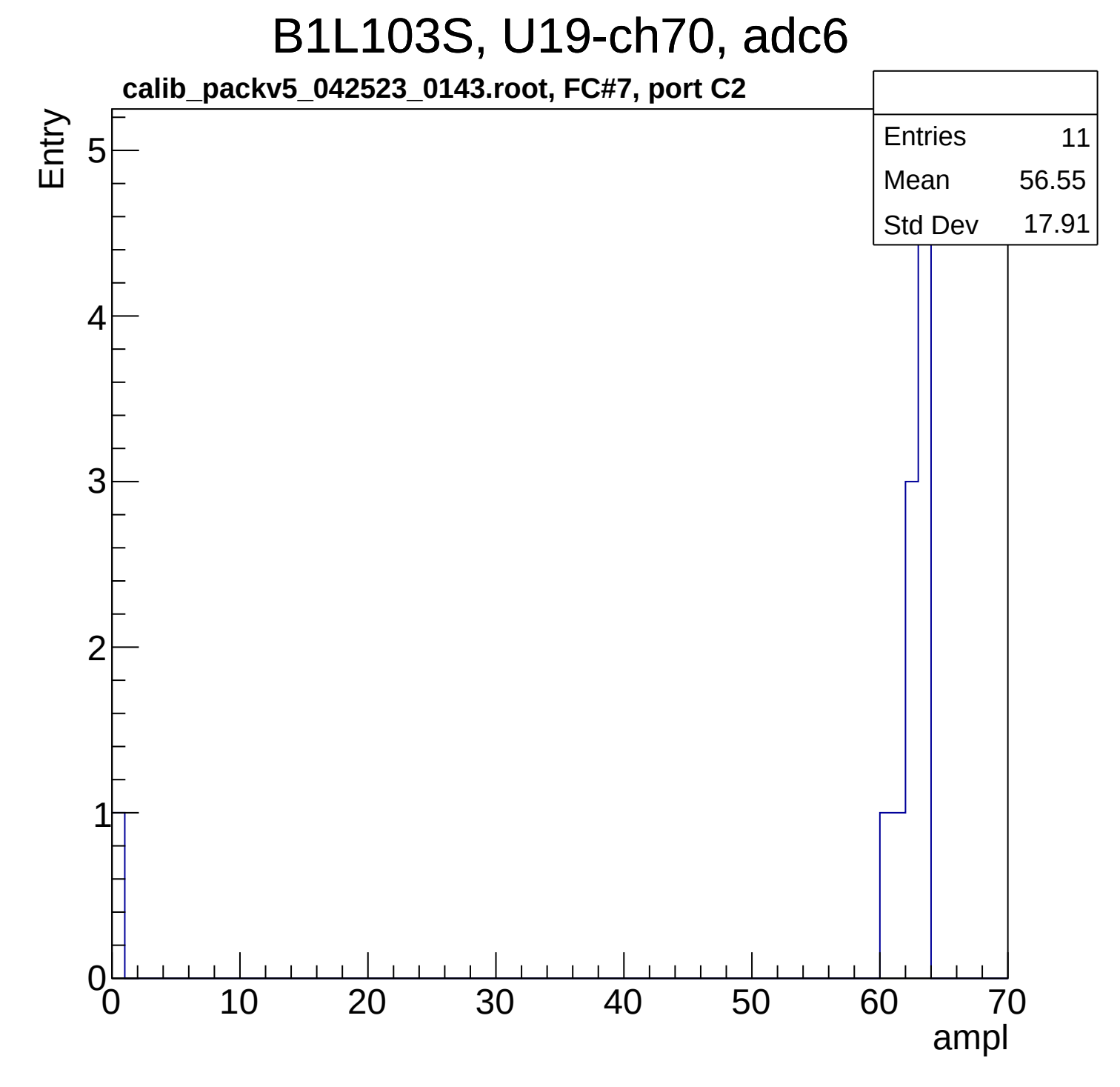
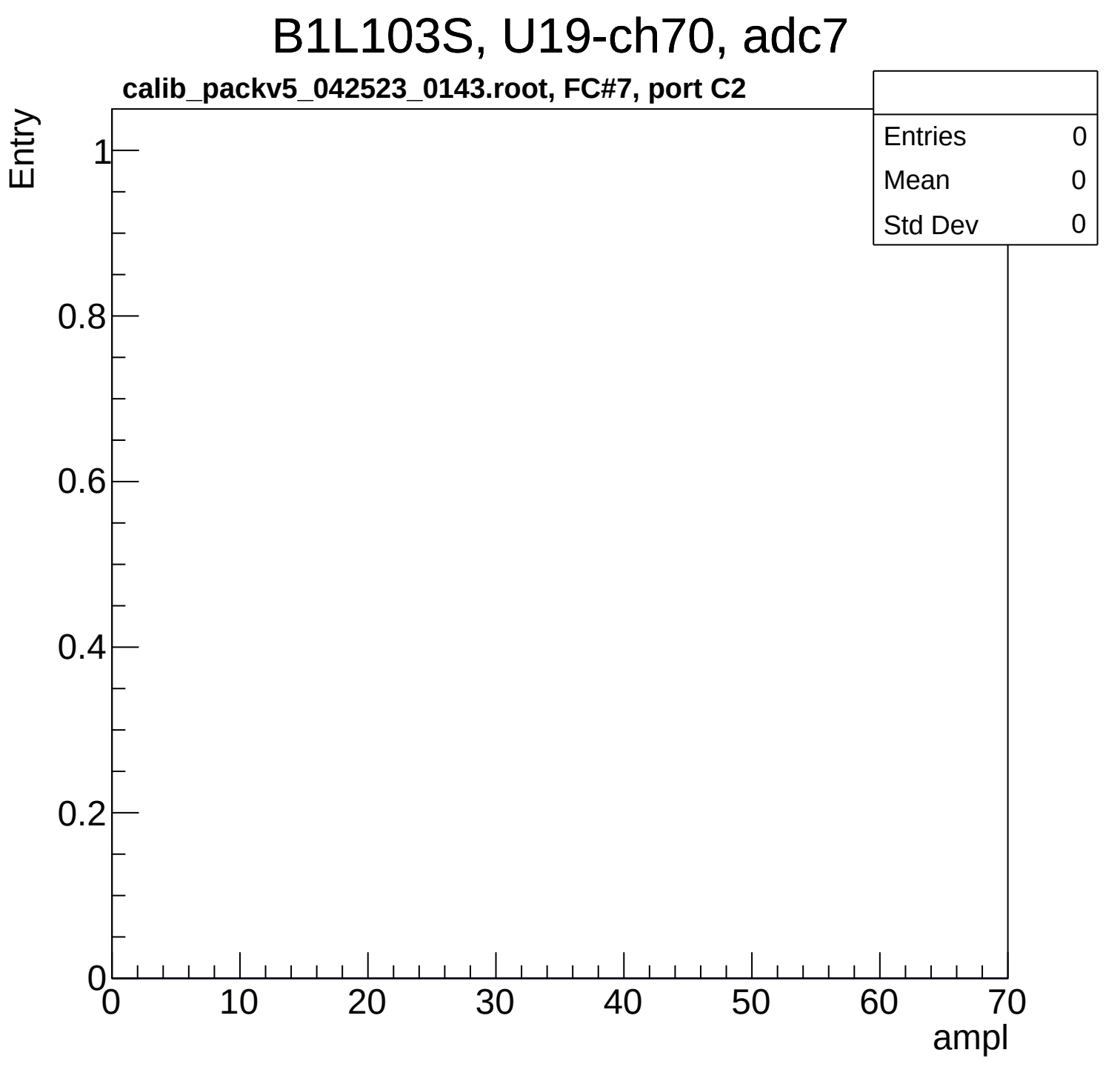
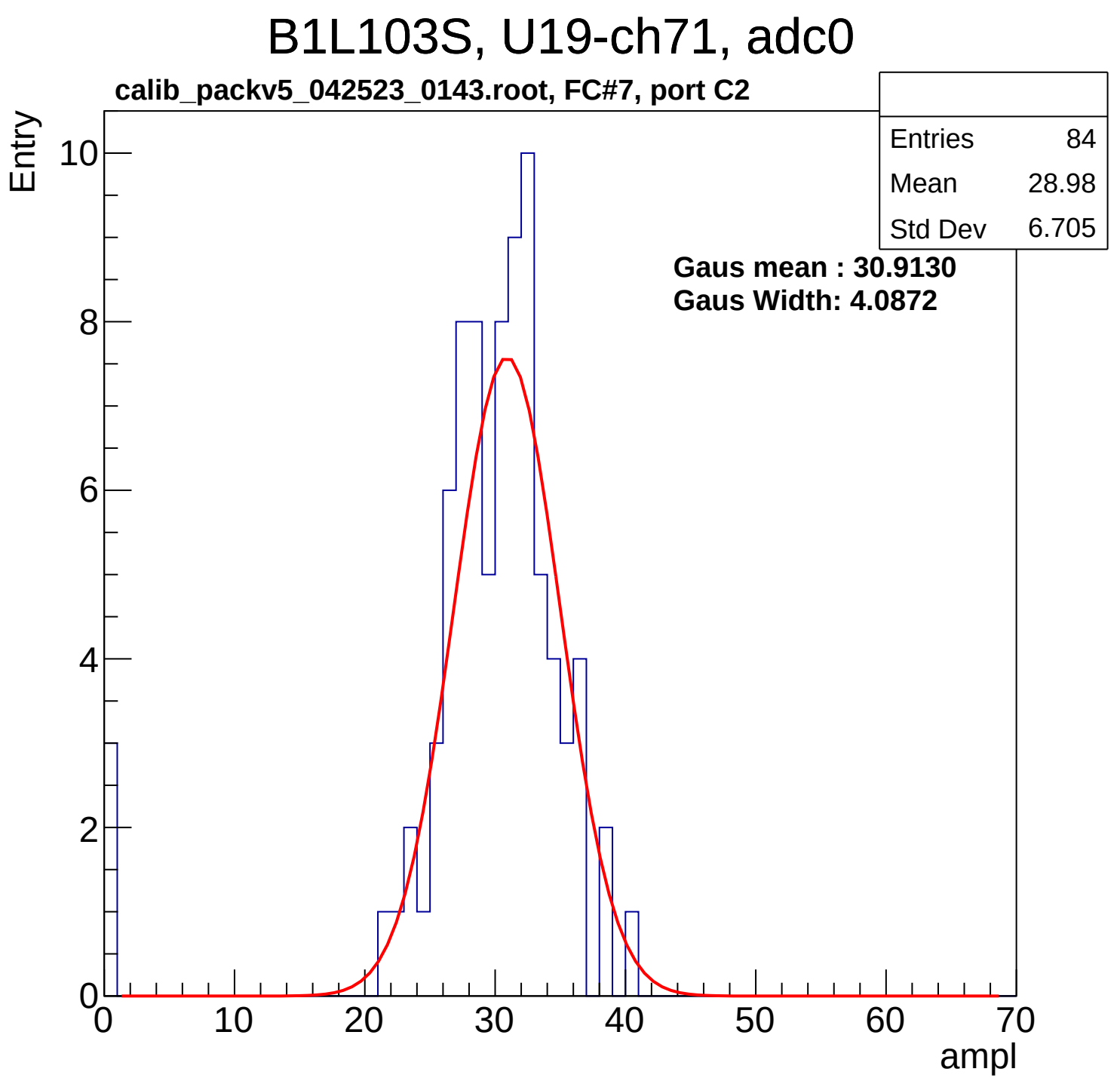
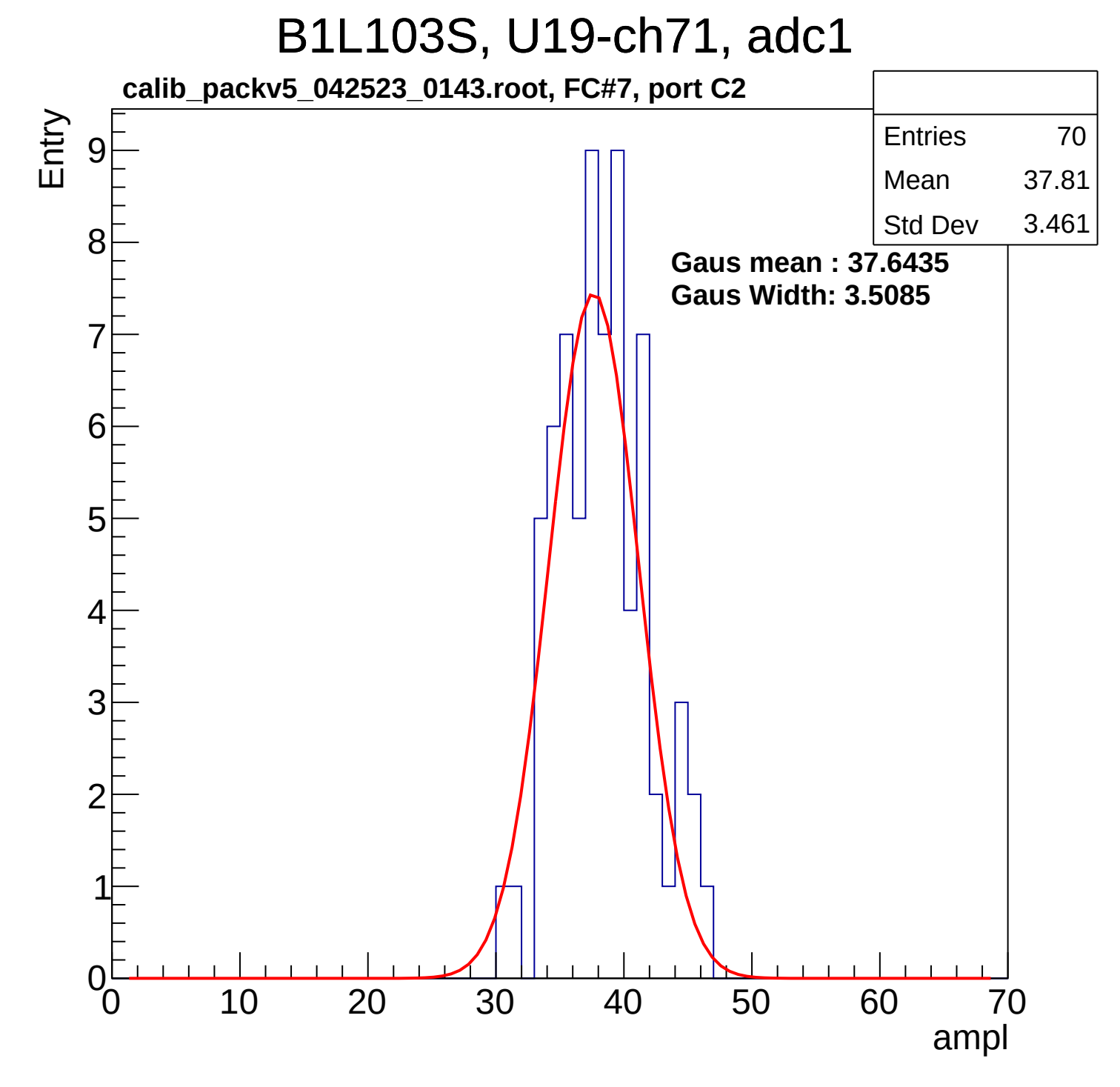
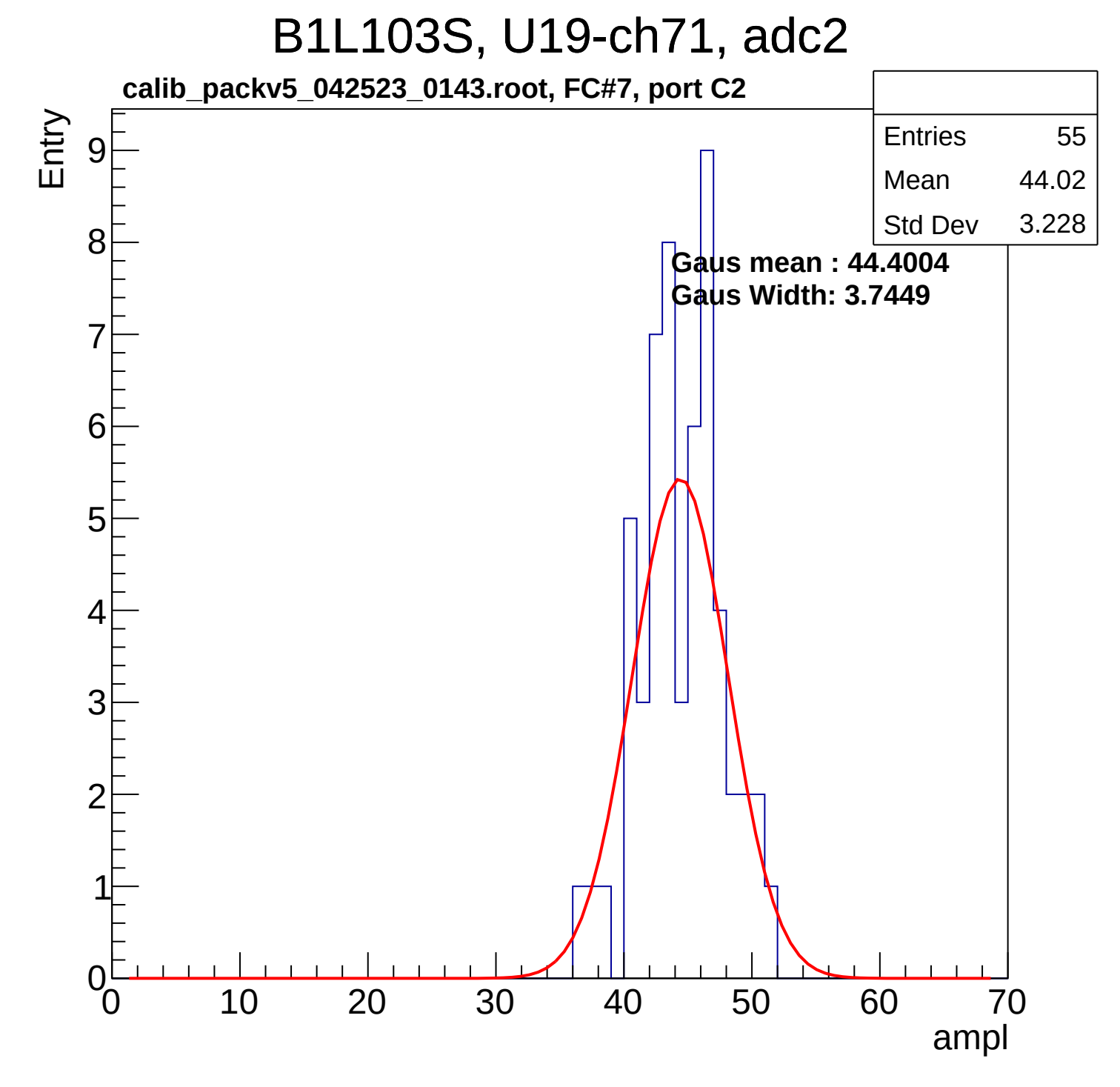

B1L103S, U19-ch70, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 56.55
Std Dev 17.91
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch70, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch71, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 28.98
Std Dev 6.705
Gaus mean : 30.9130
Gaus Width: 4.0872
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch71, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 37.81
Std Dev 3.461
Gaus mean : 37.6435
Gaus Width: 3.5085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch71, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 44.02
Std Dev 3.228
Gaus mean : 44.4004
Gaus Width: 3.7449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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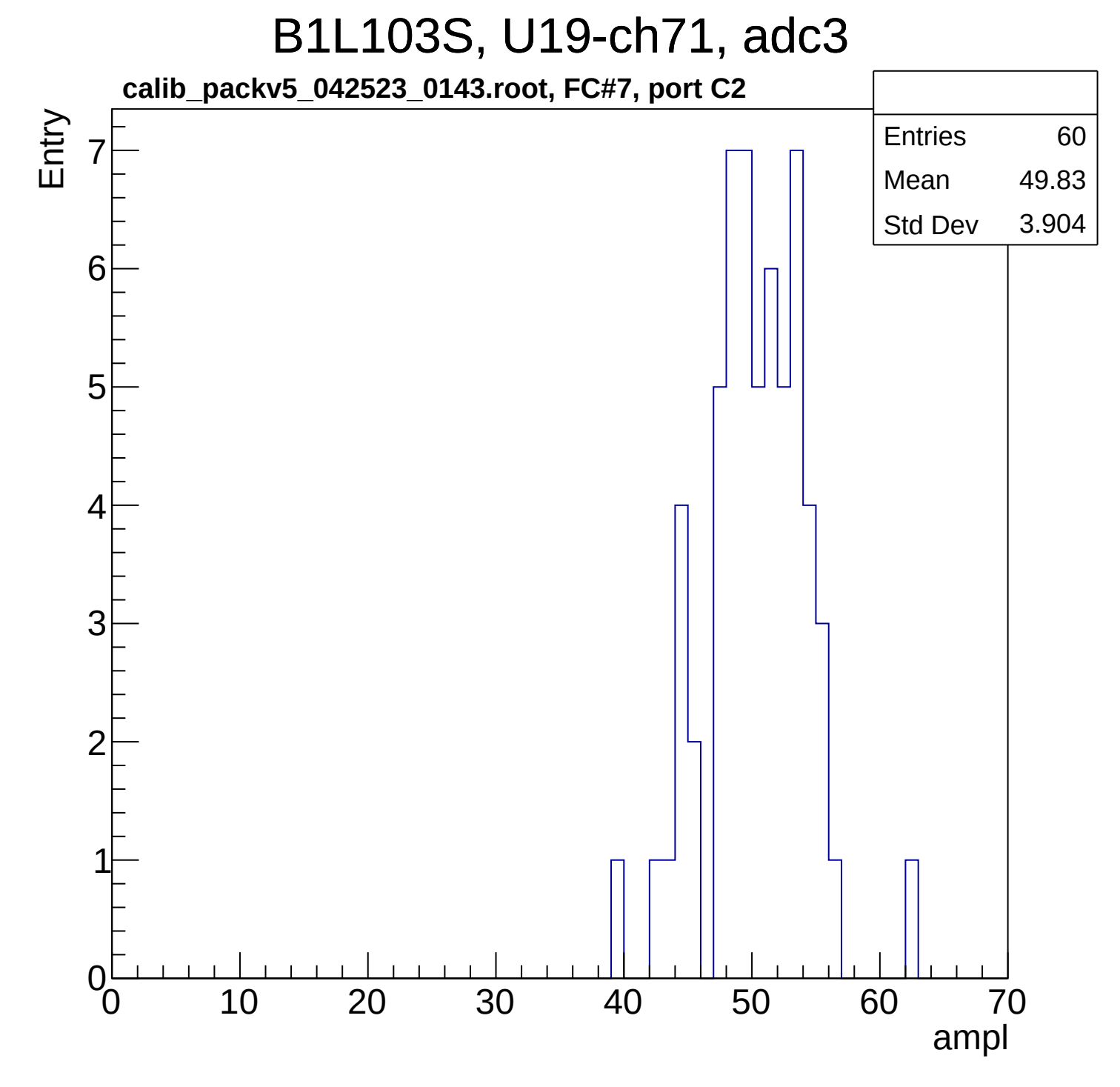
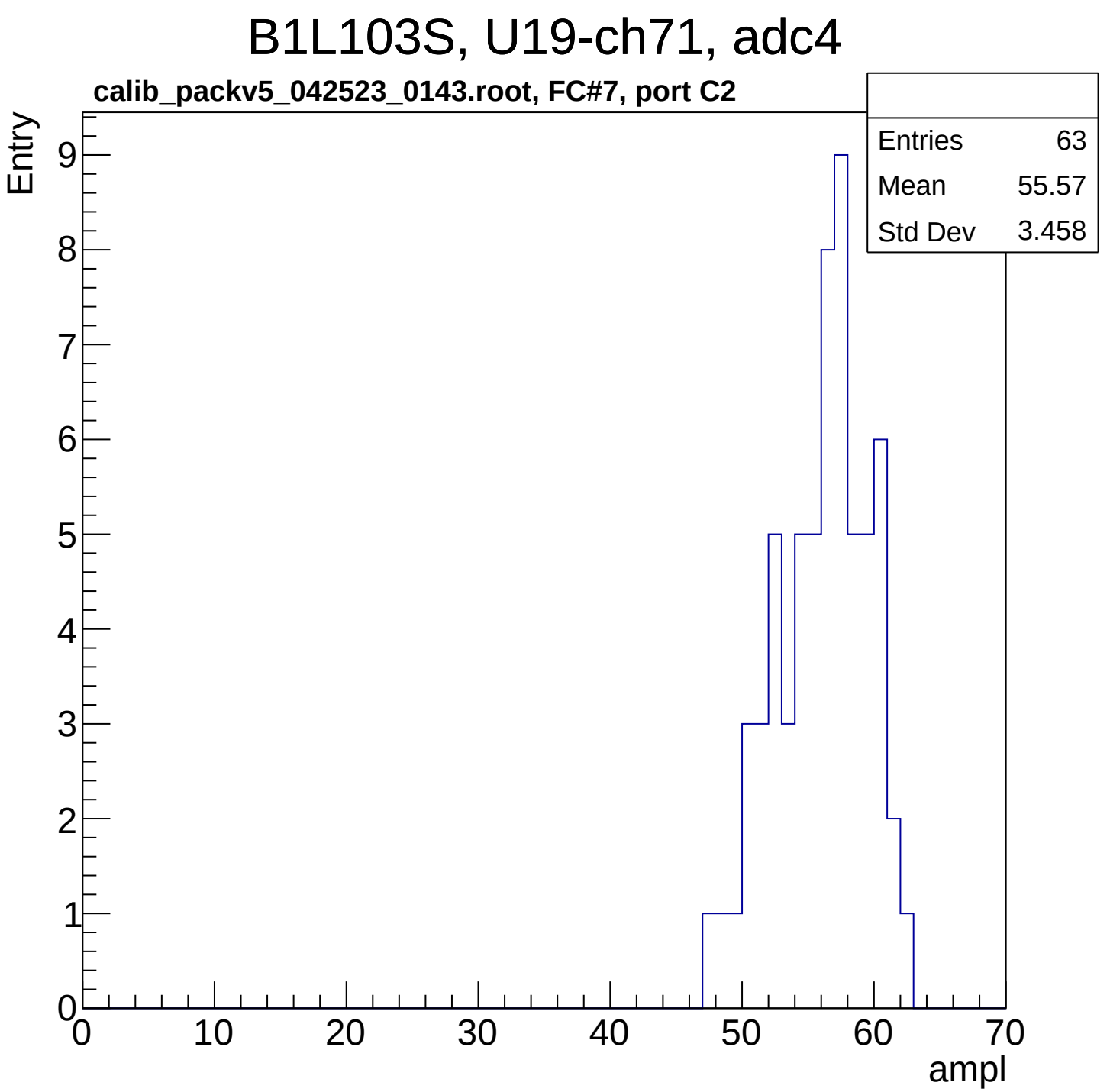

B1L103S, U19-ch71, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.83
Std Dev 3.904
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch71, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 55.57
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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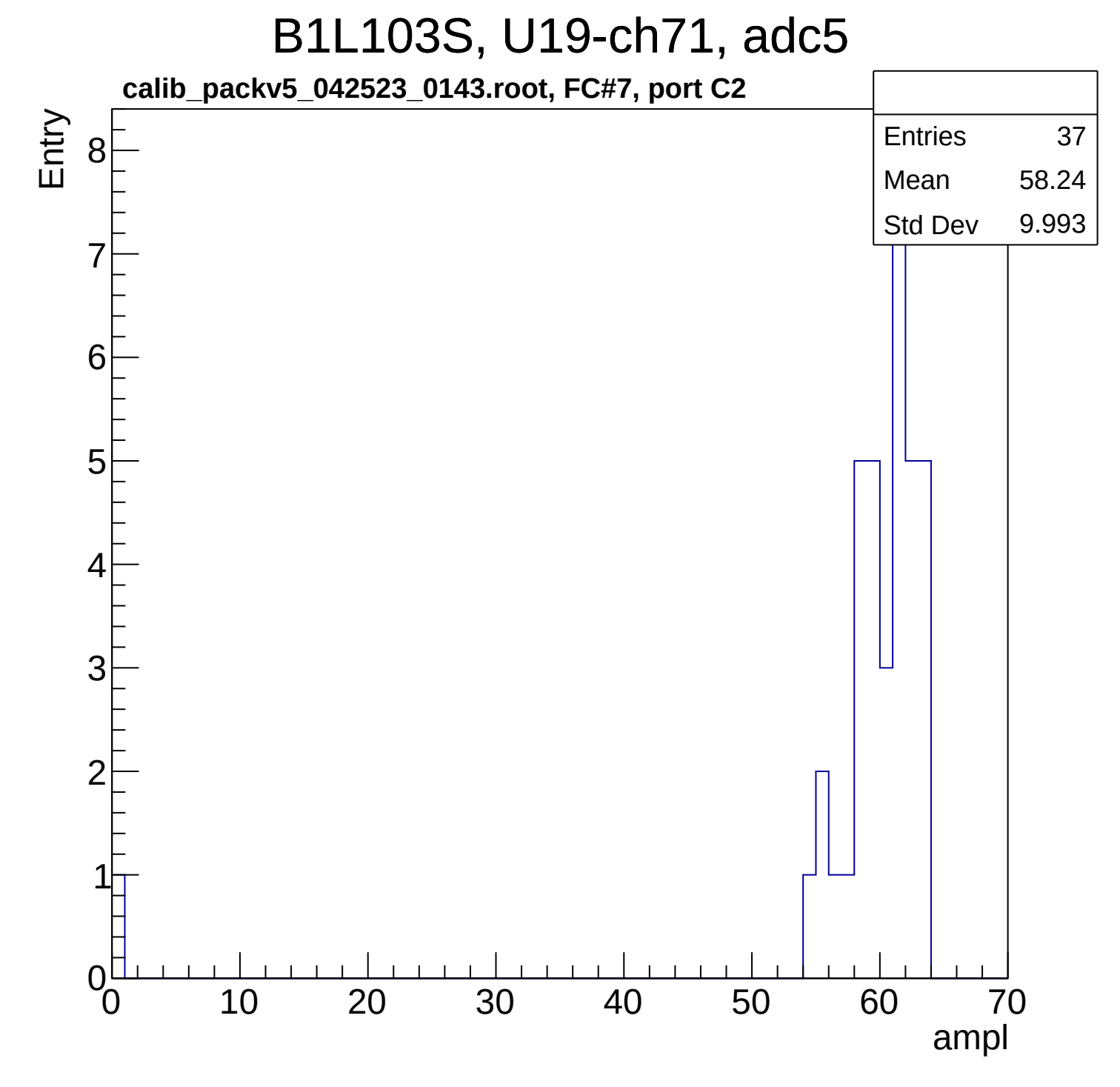
B1L103S, U19-ch71, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 58.24
Std Dev 9.993
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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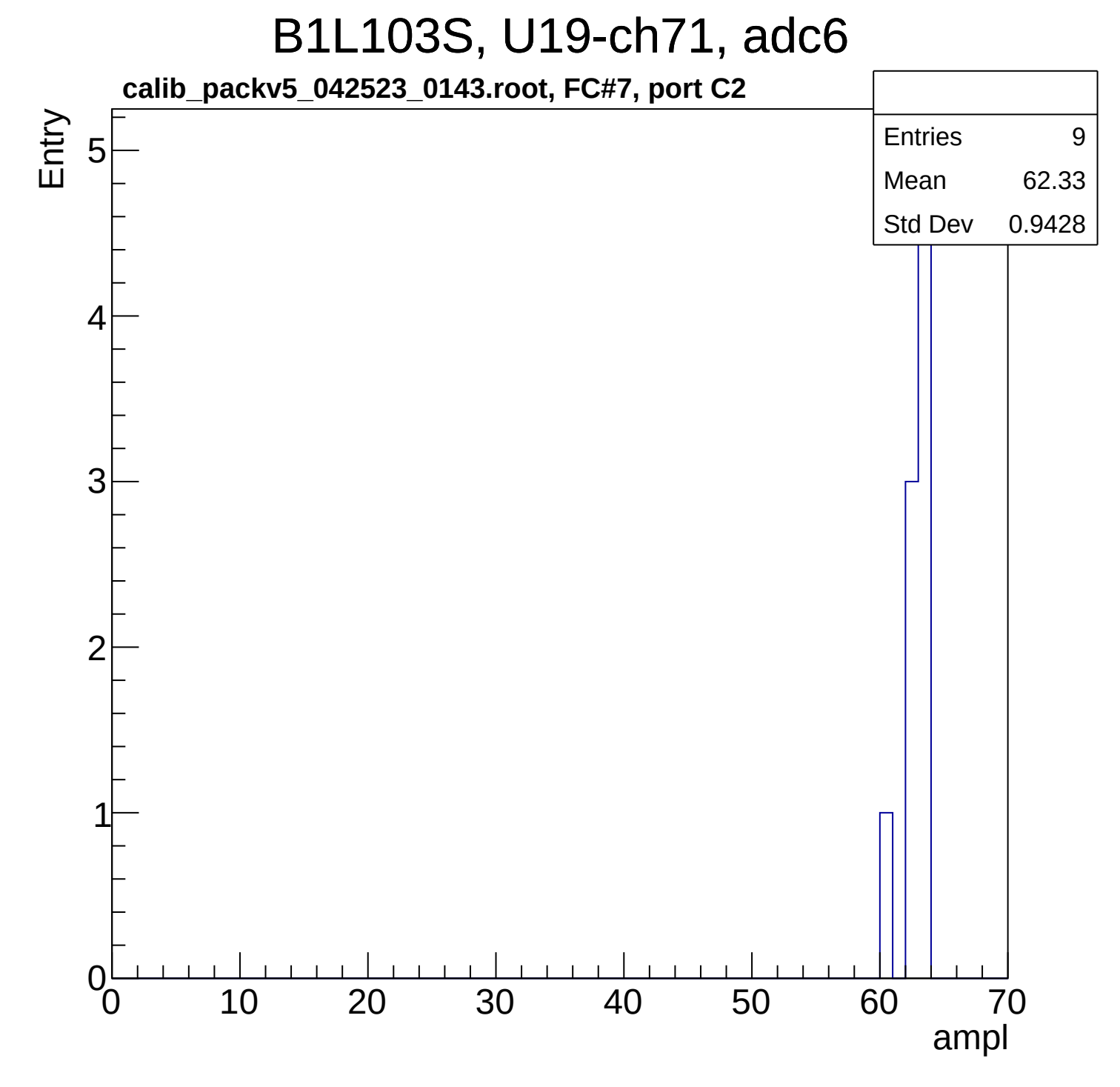
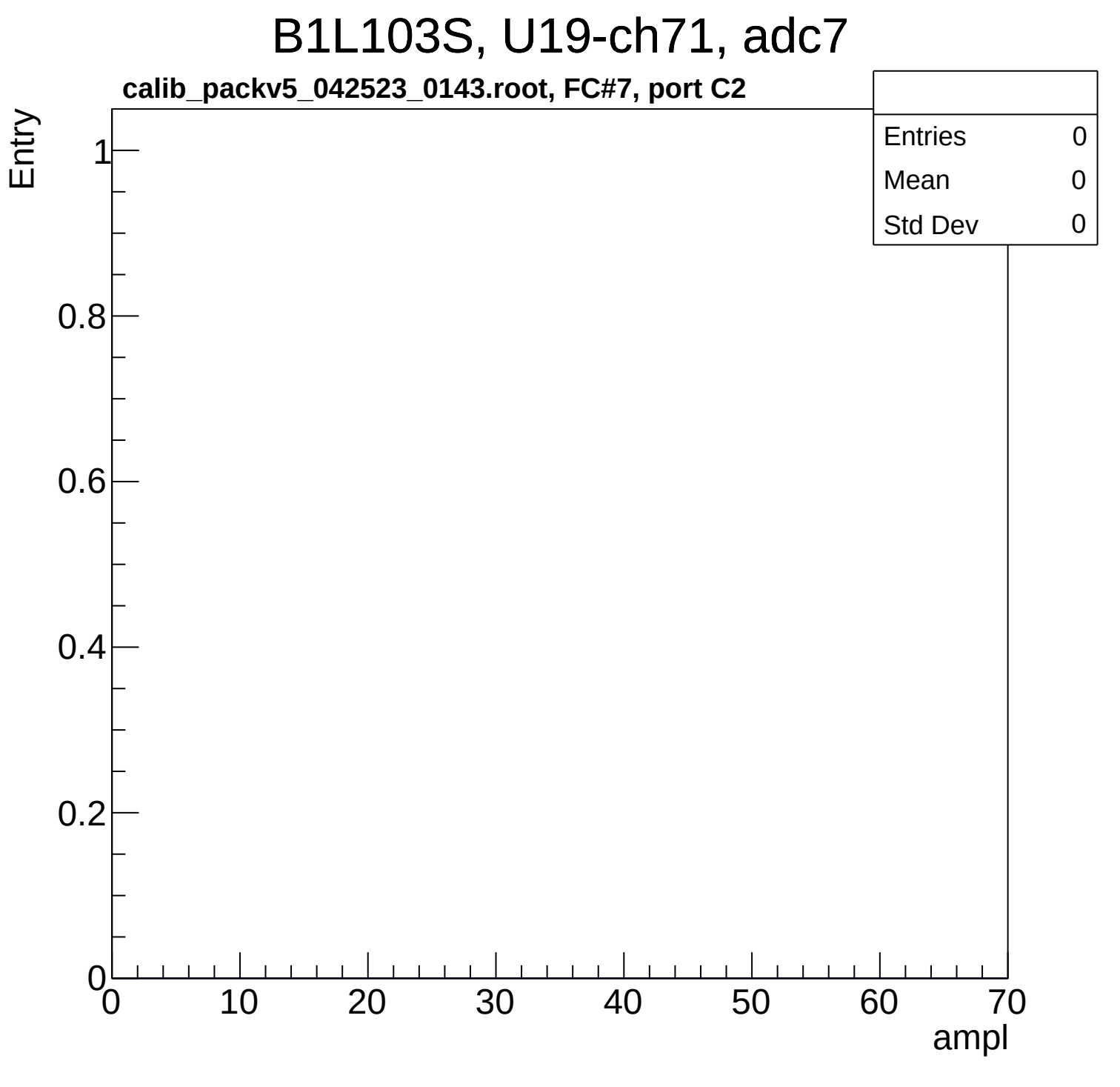

B1L103S, U19-ch71, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 62.33
Std Dev 0.9428
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch71, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
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10
20
30
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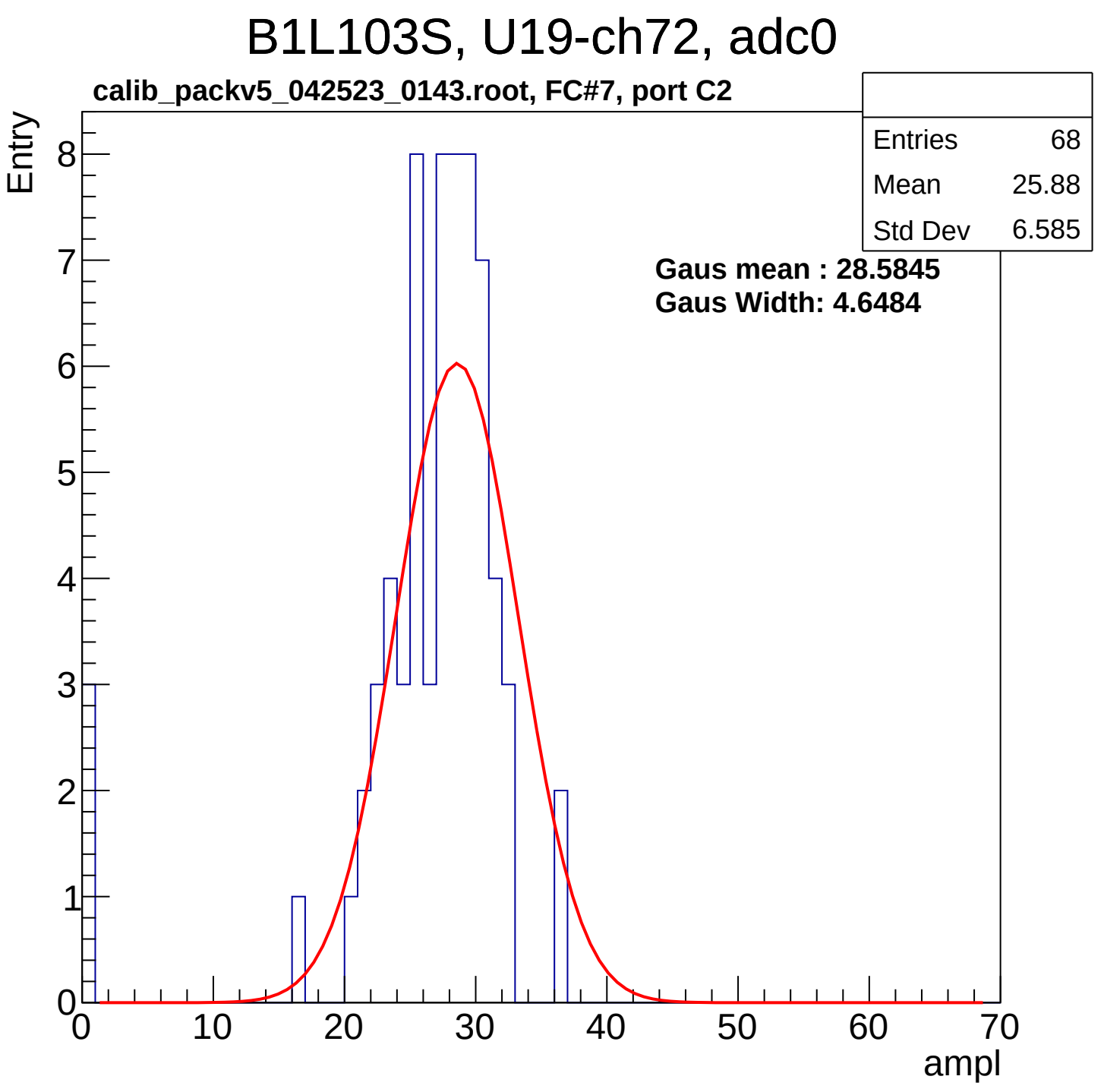
50
60
70
ampl


B1L103S, U19-ch72, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 25.88
Std Dev 6.585
Gaus mean : 28.5845
Gaus Width: 4.6484
Entry
ampl
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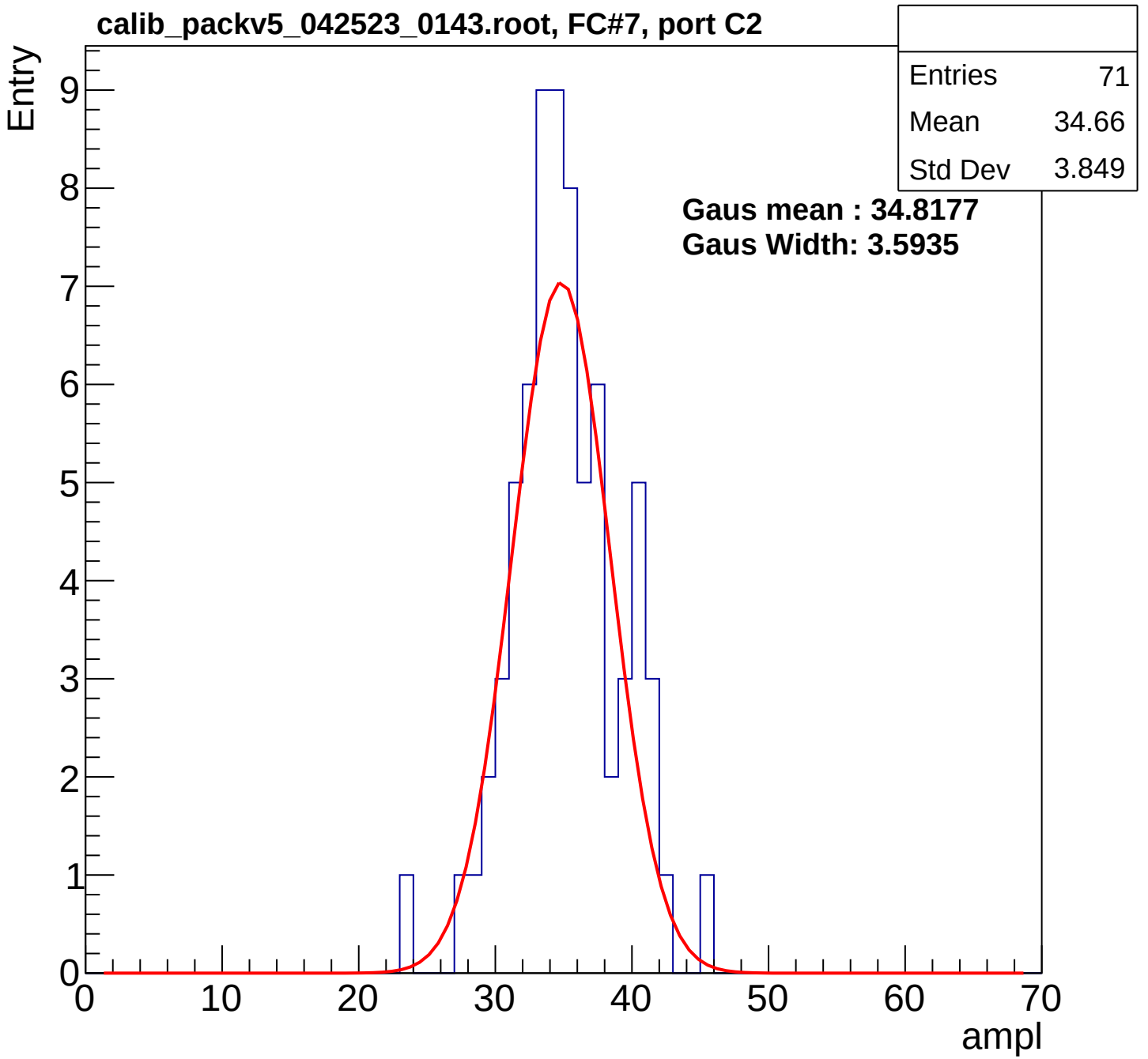
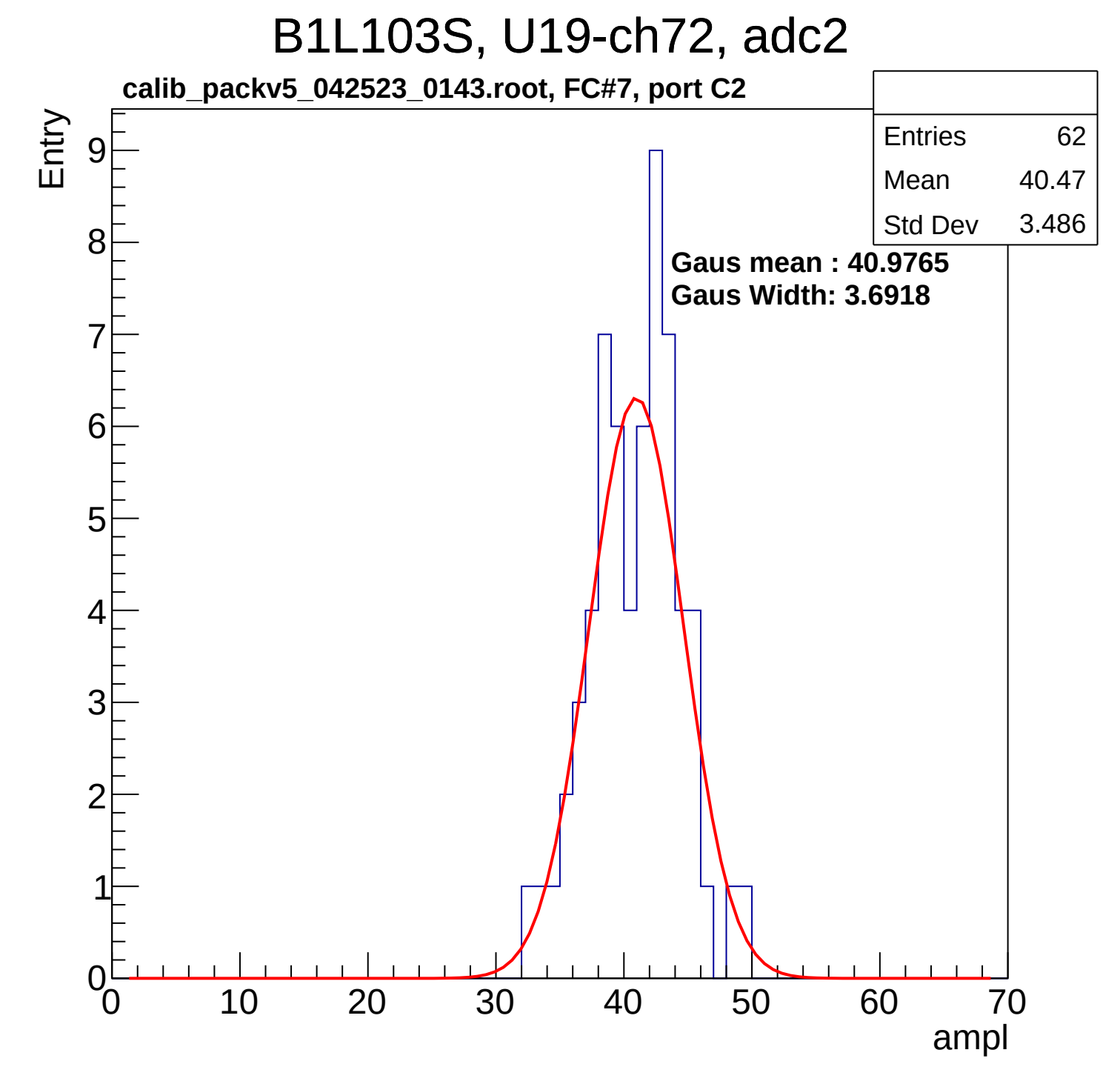
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch72, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 34.66
Std Dev 3.849
Gaus mean : 34.8177
Gaus Width: 3.5935
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch72, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 40.47
Std Dev 3.486
Gaus mean : 40.9765
Gaus Width: 3.6918
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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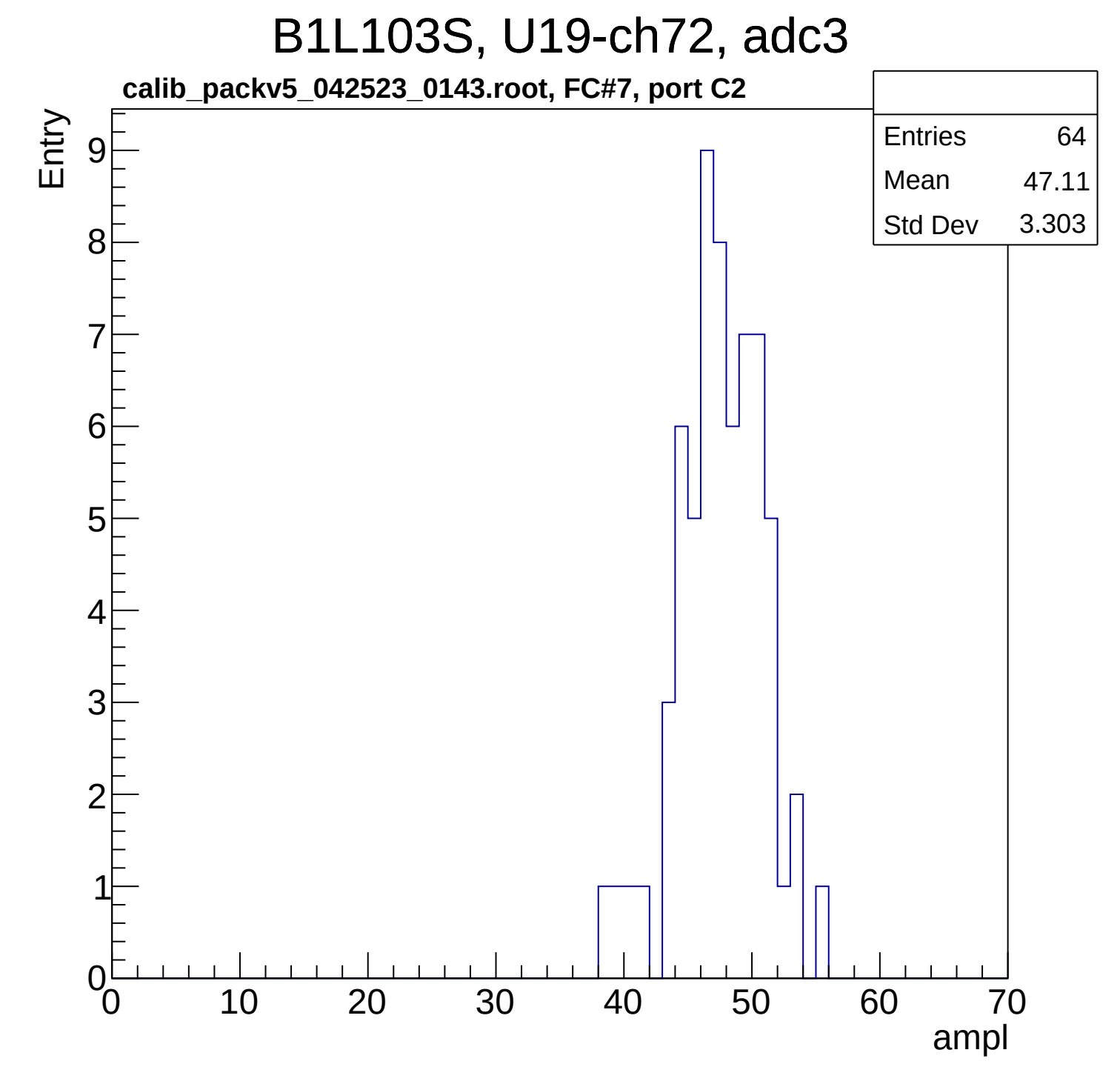
B1L103S, U19-ch72, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.11
Std Dev 3.303
Entry
ampl
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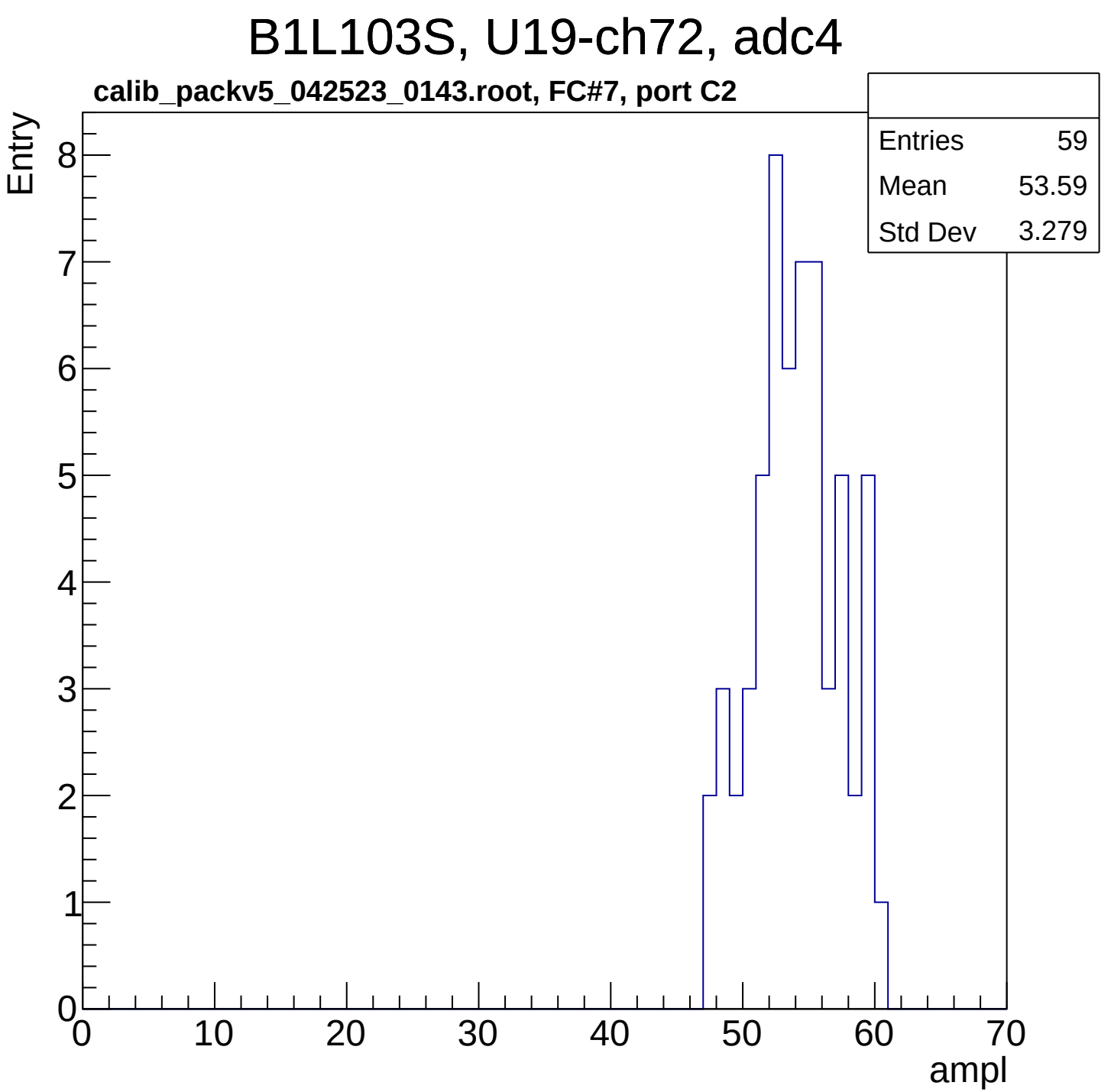
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch72, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 53.59
Std Dev 3.279
Entry
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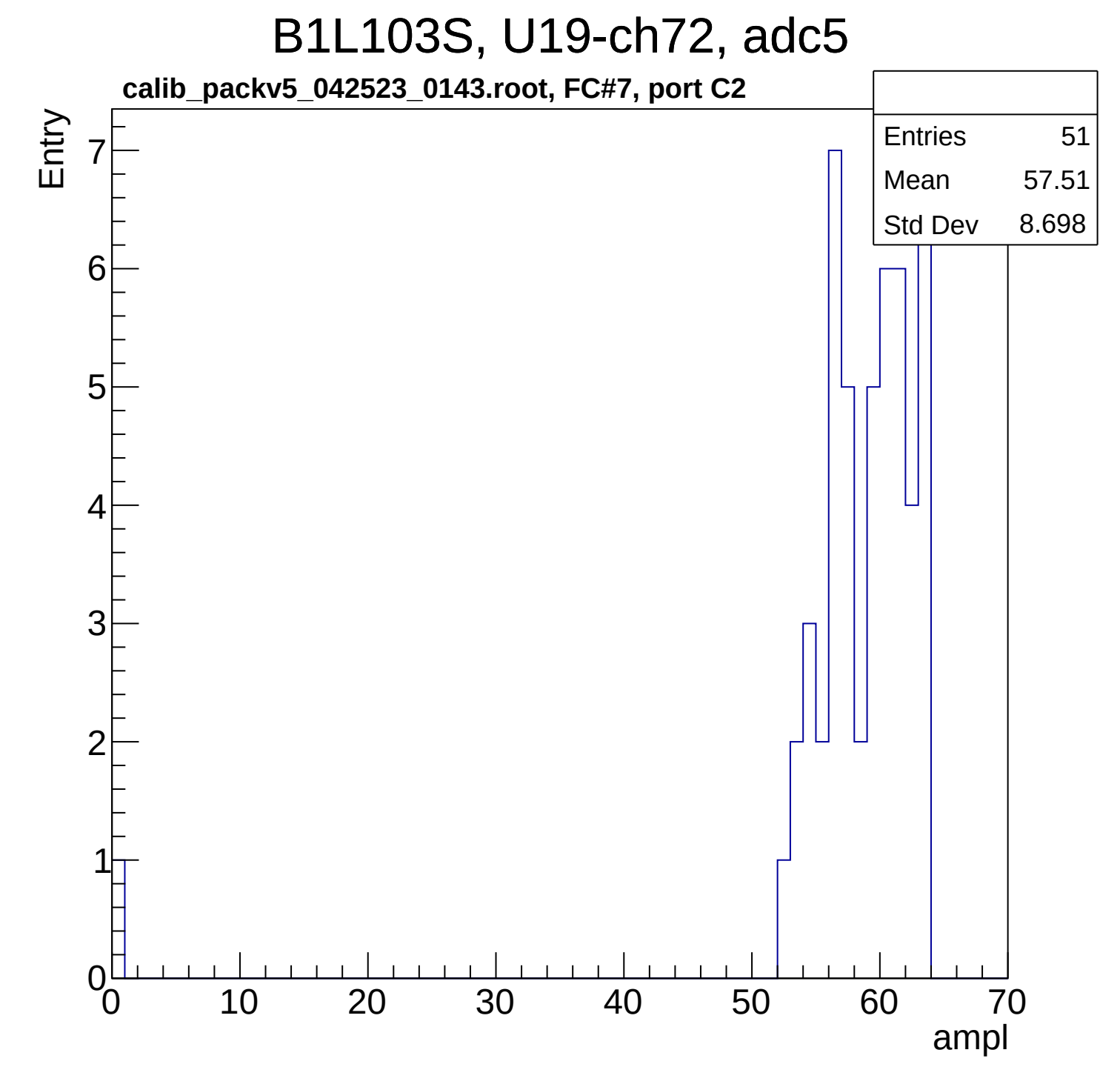
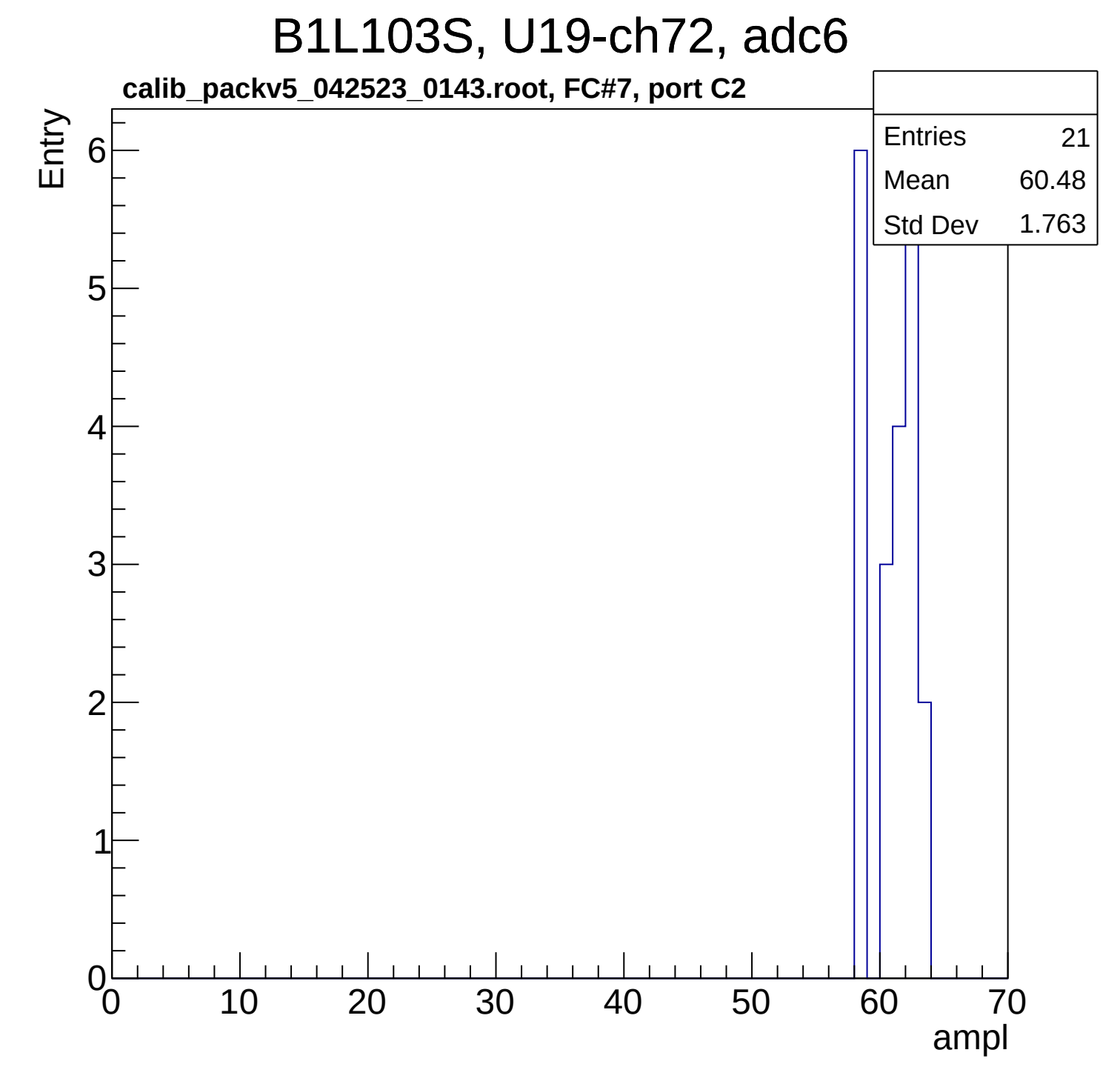
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch72, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.51
Std Dev 8.698
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch72, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 21
Mean 60.48
Std Dev 1.763
Entry
0
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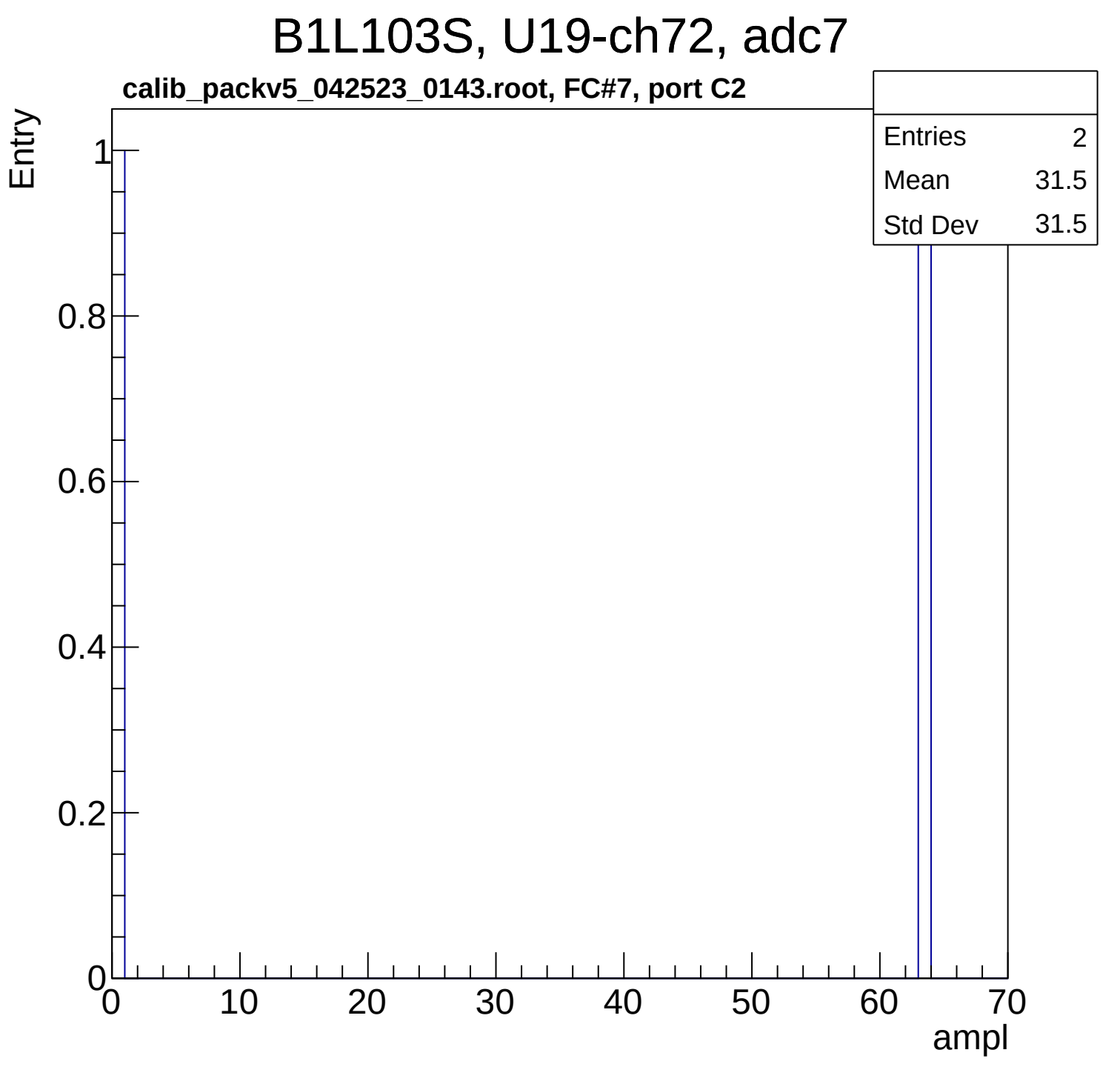
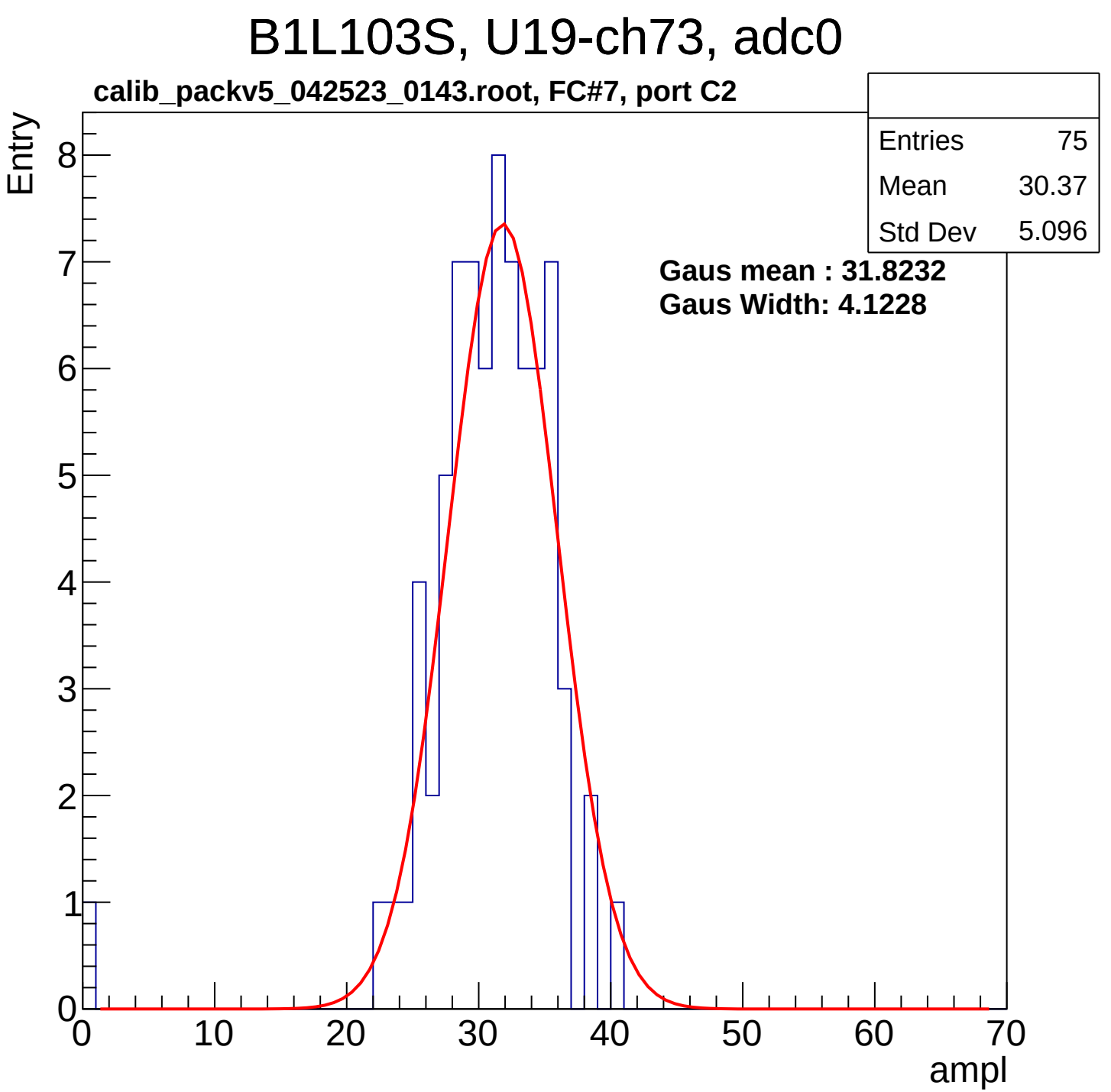
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch72, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch73, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.37
Std Dev 5.096
Gaus mean : 31.8232
Gaus Width: 4.1228
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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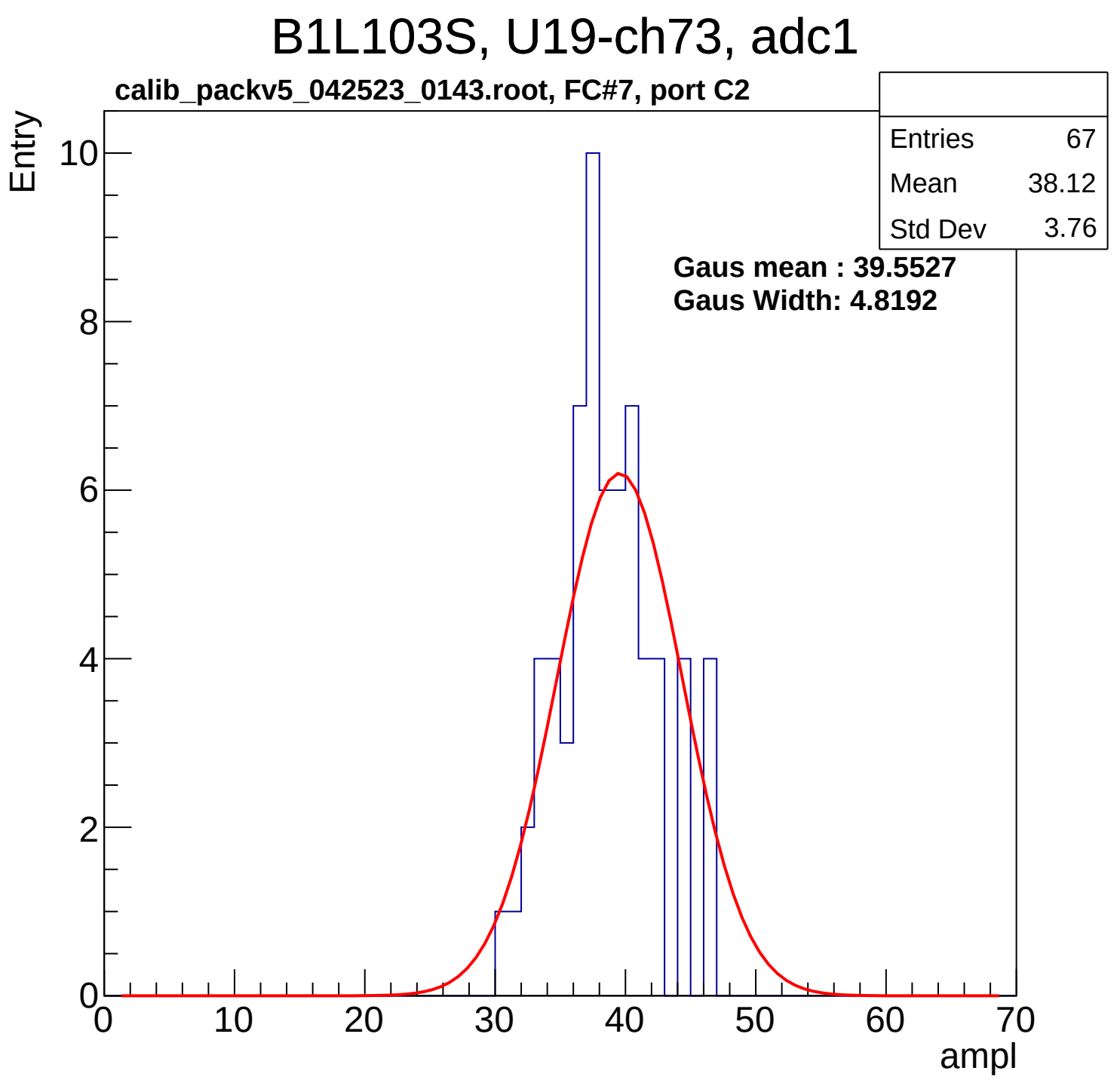
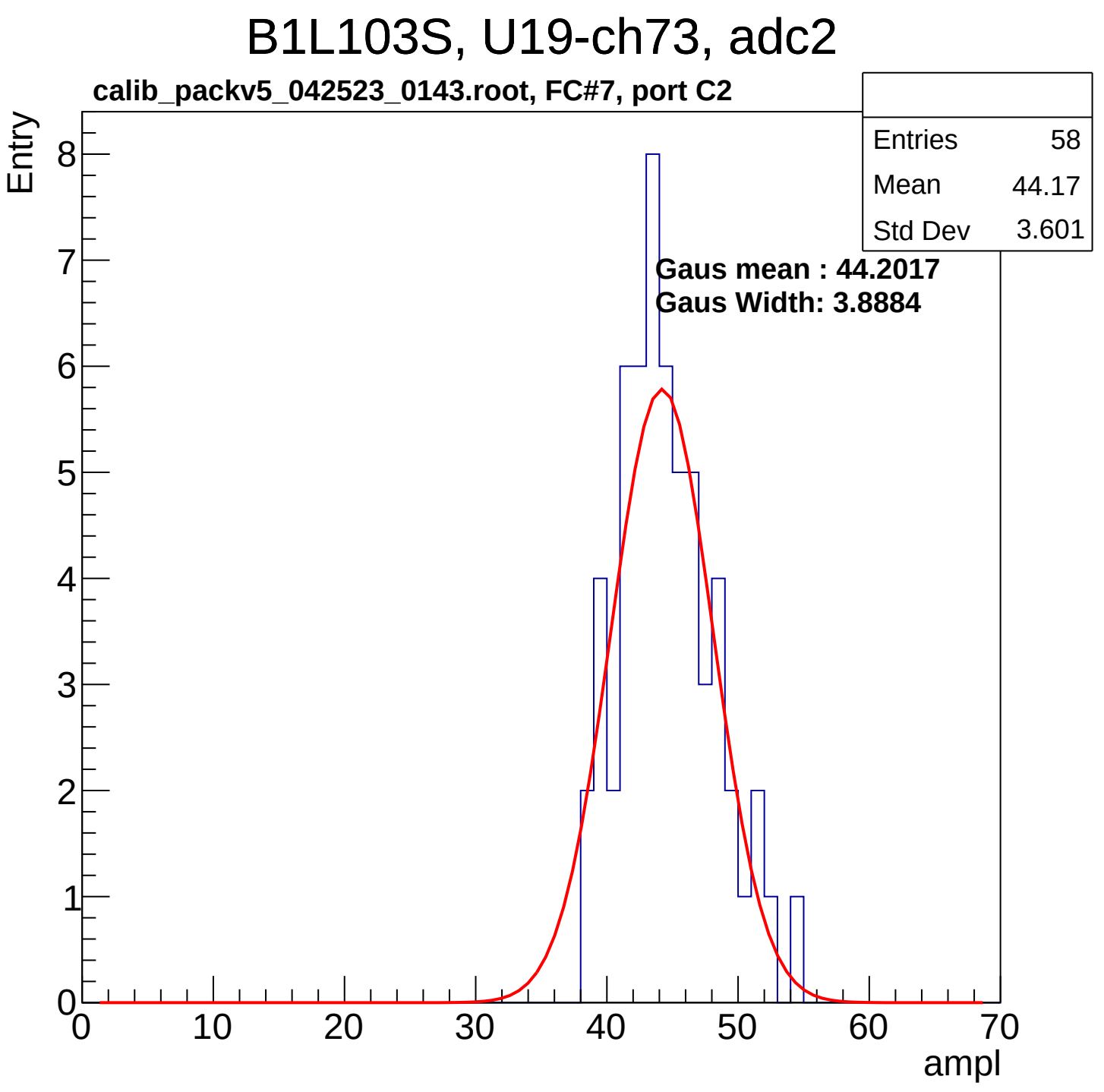

B1L103S, U19-ch73, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 38.12
Std Dev 3.76
Gaus mean : 39.5527
Gaus Width: 4.8192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch73, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 44.17
Std Dev 3.601
Gaus mean : 44.2017
Gaus Width: 3.8884
Entry
ampl
0
1
2
3
4
5
6
7
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0
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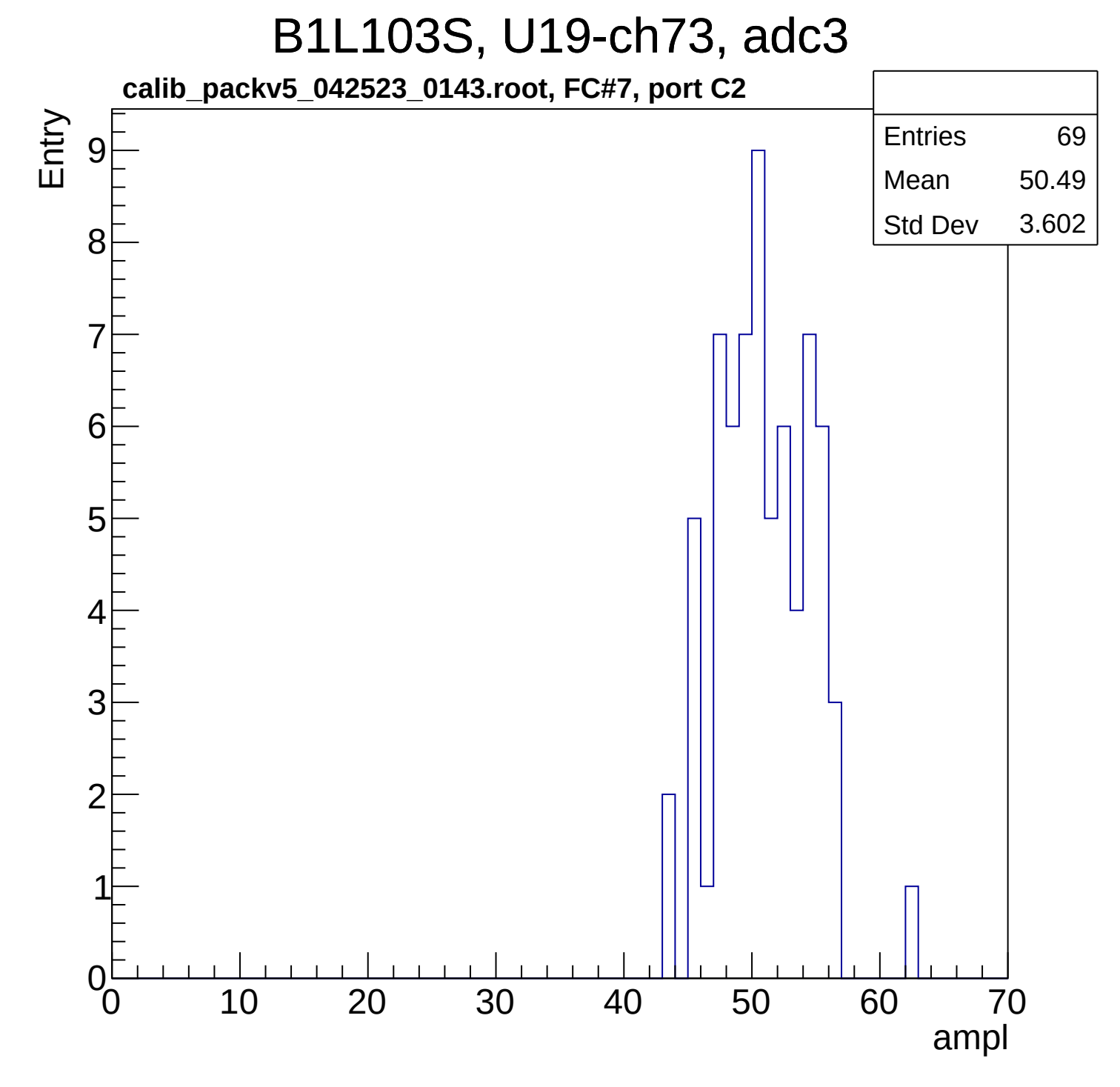
B1L103S, U19-ch73, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.49
Std Dev 3.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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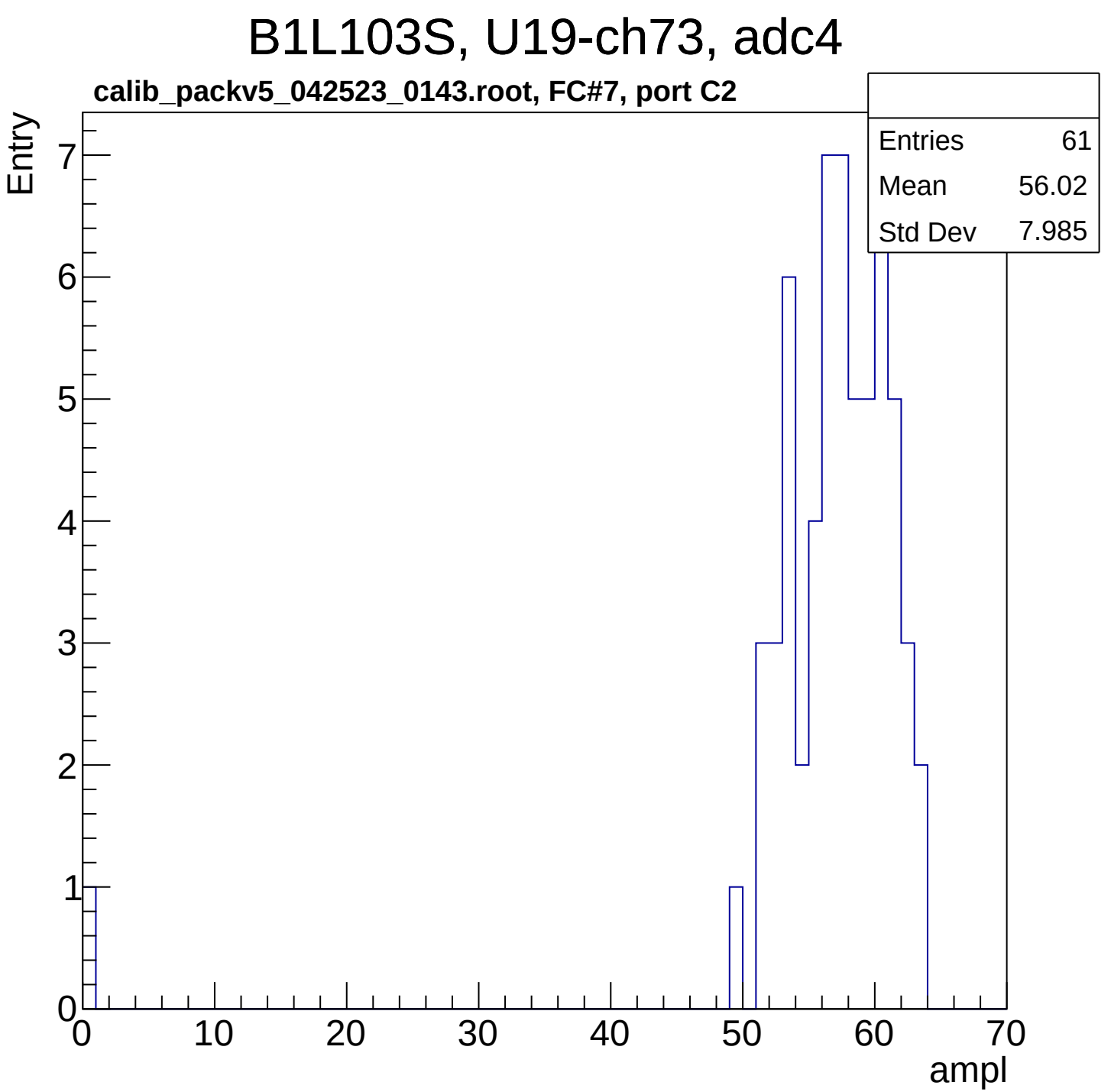
B1L103S, U19-ch73, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.02
Std Dev 7.985
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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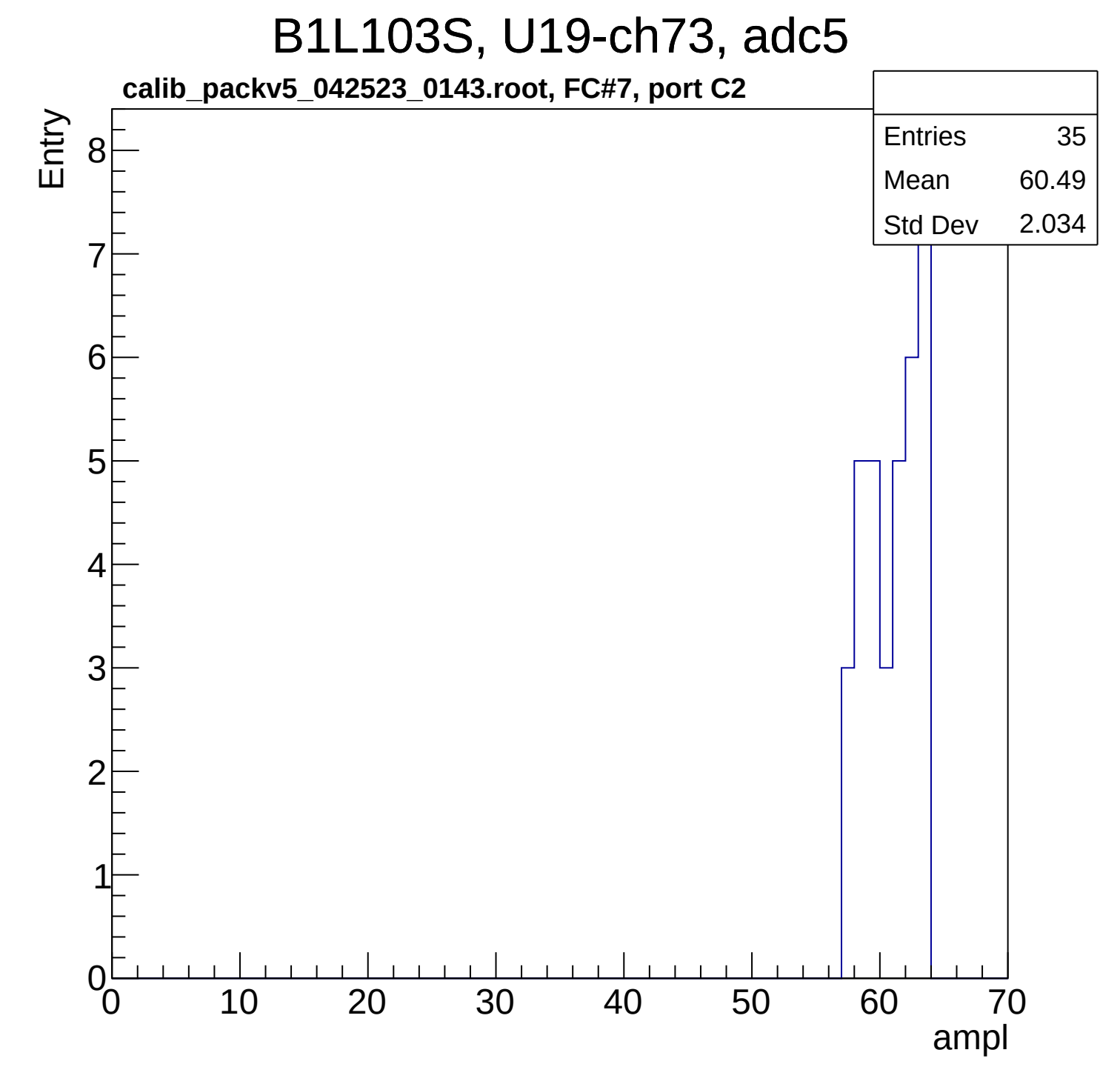
B1L103S, U19-ch73, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 60.49
Std Dev 2.034
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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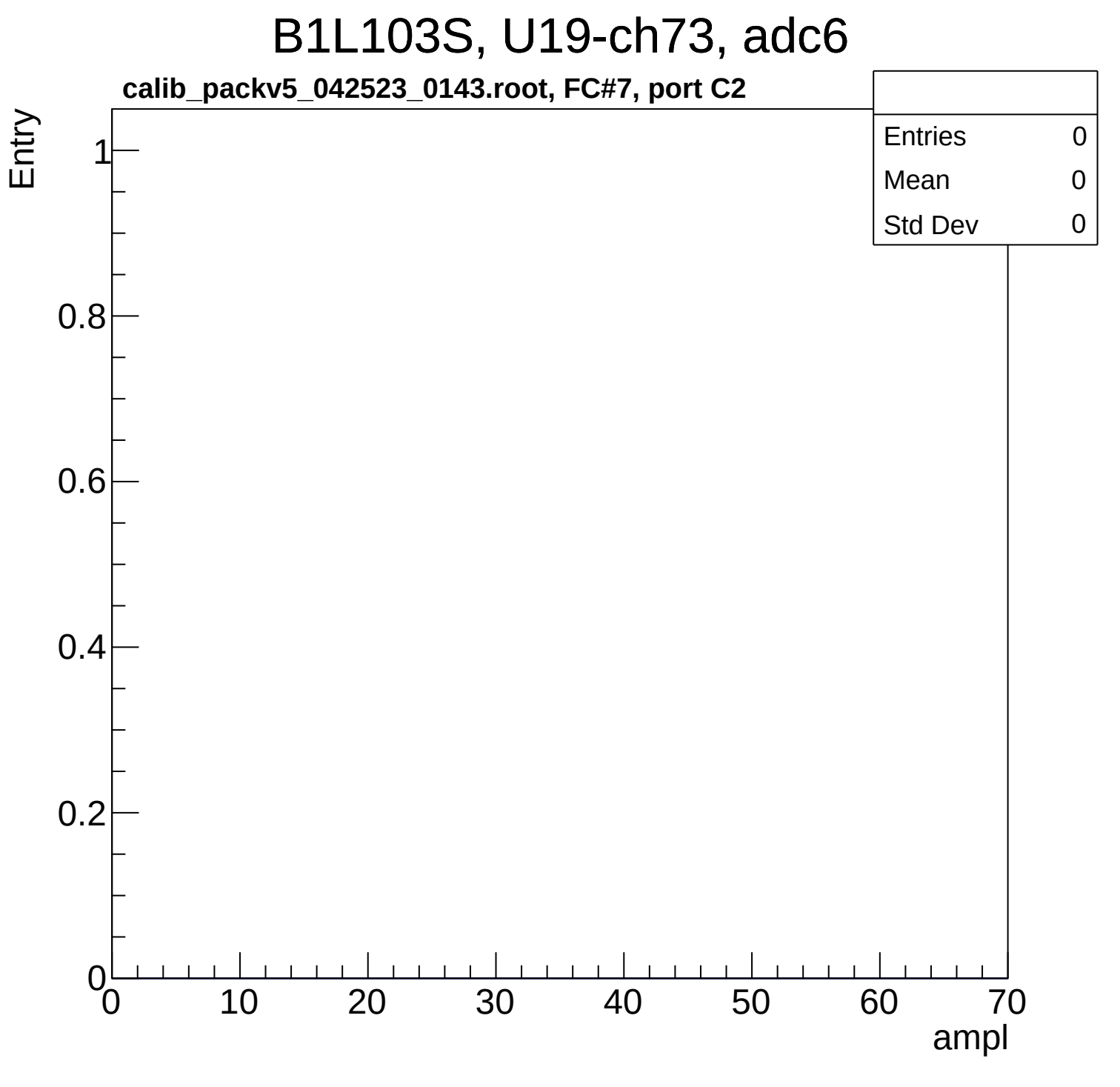
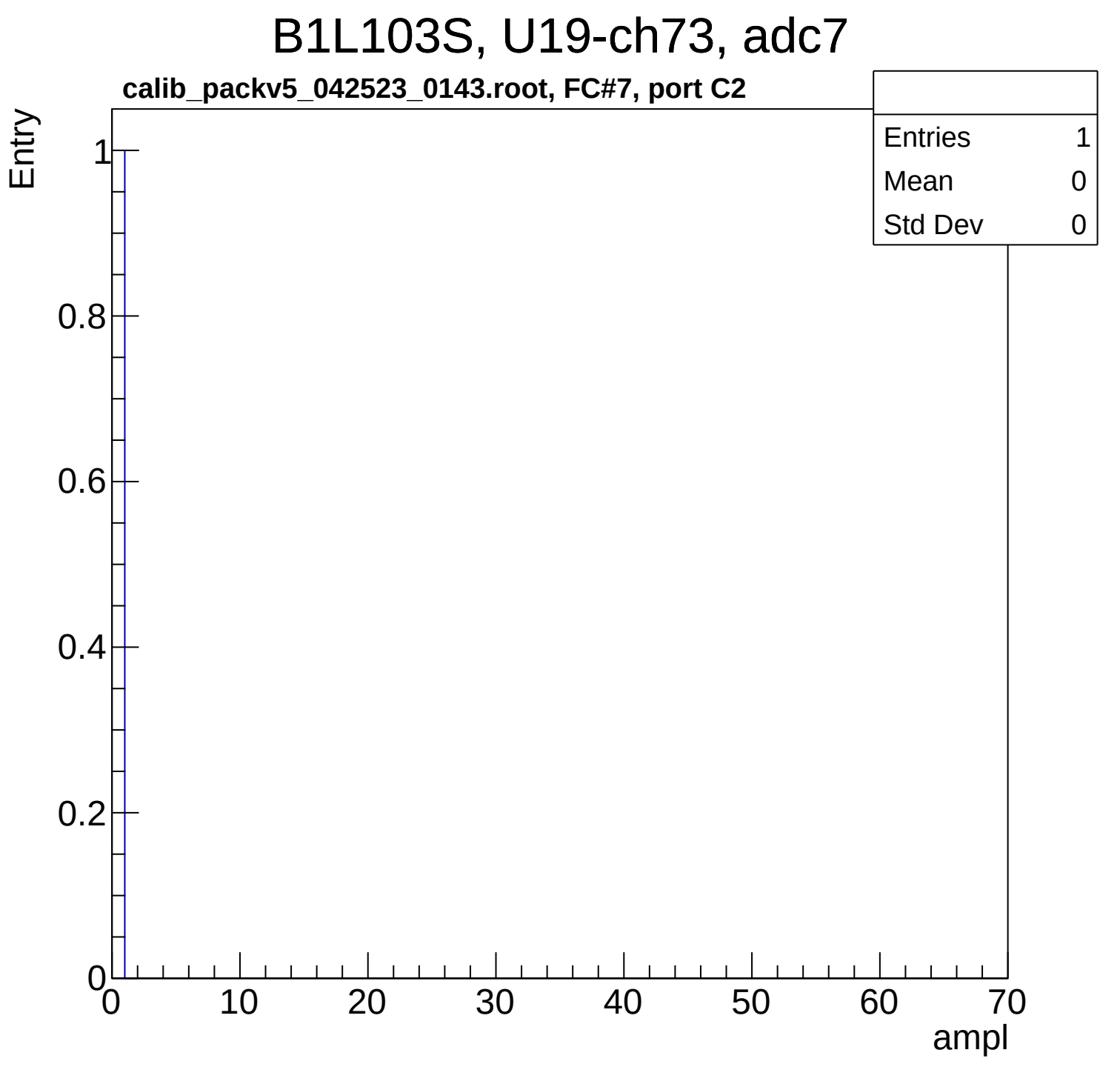
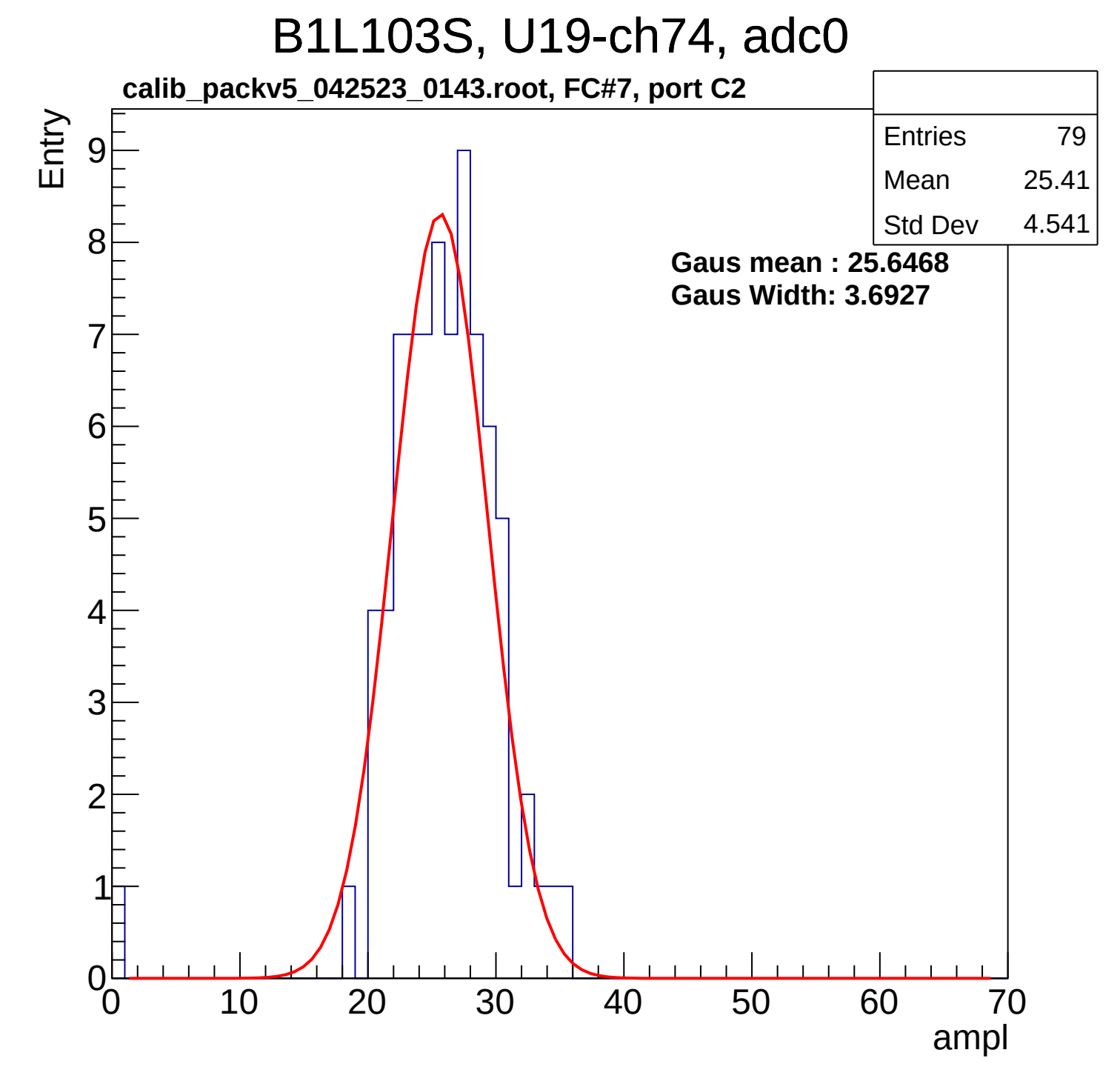

B1L103S, U19-ch73, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch73, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch74, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 25.41
Std Dev 4.541
Gaus mean : 25.6468
Gaus Width: 3.6927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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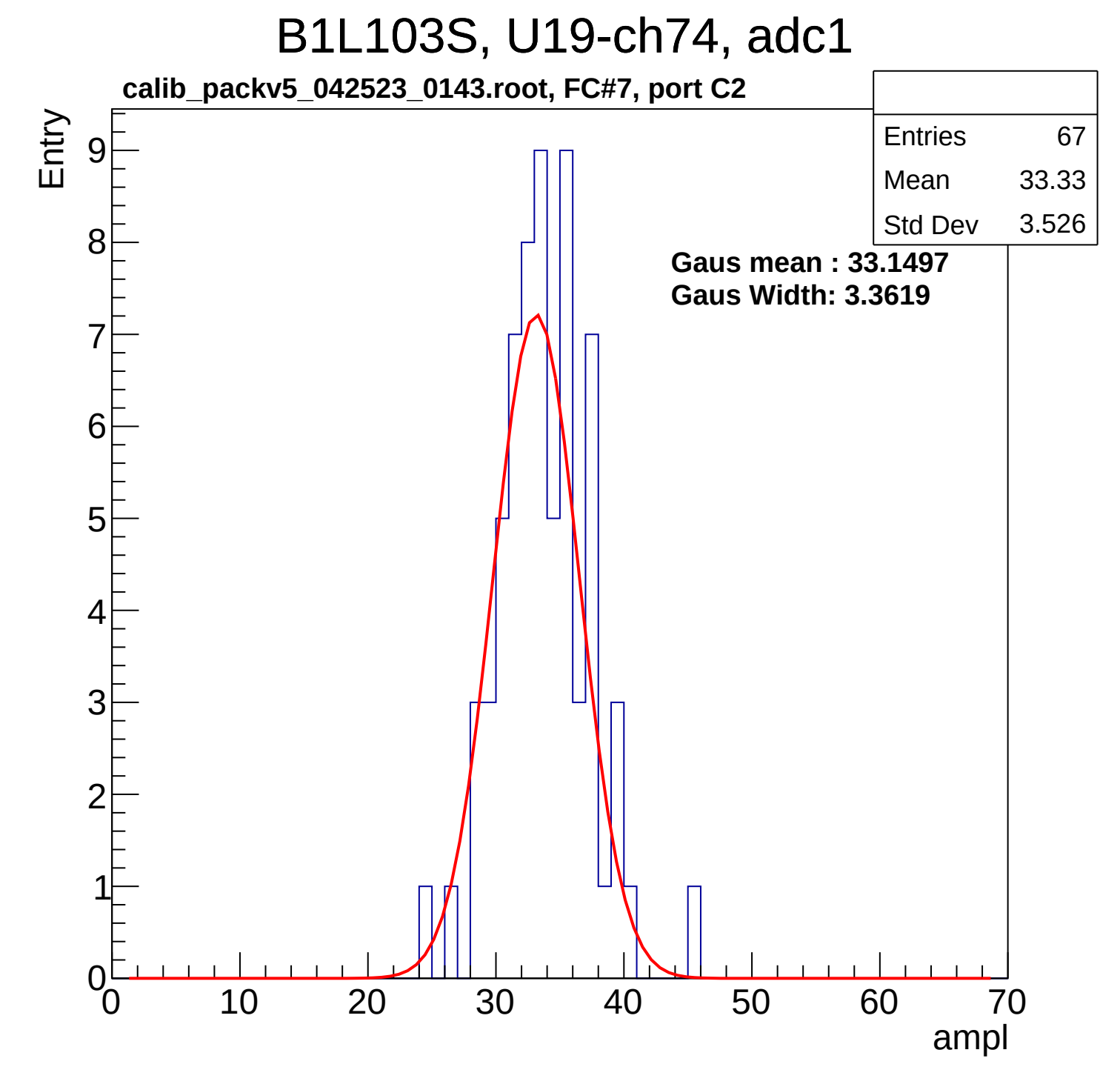
B1L103S, U19-ch74, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 33.33
Std Dev 3.526
Gaus mean : 33.1497
Gaus Width: 3.3619
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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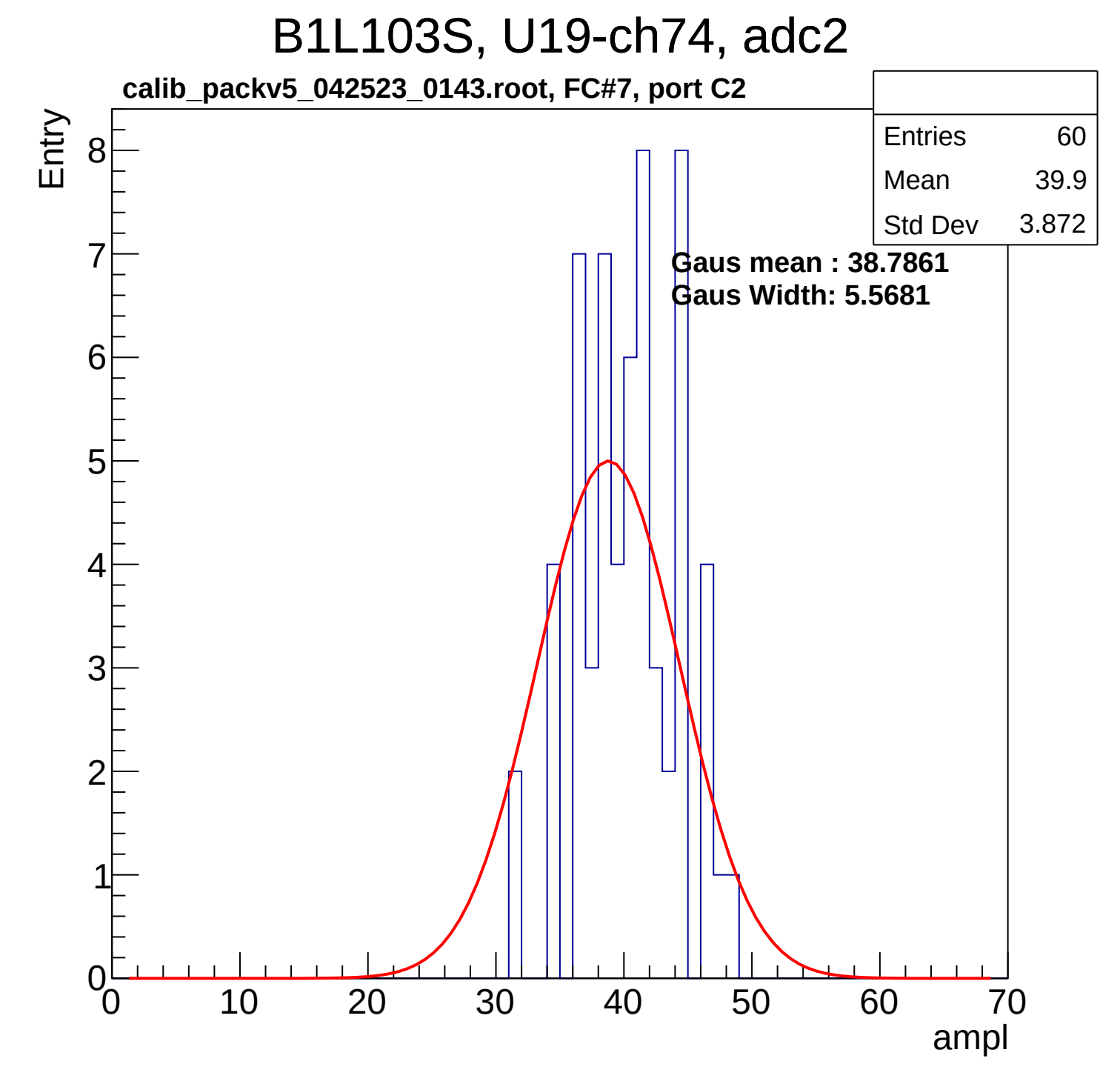
B1L103S, U19-ch74, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 39.9
Std Dev 3.872
Gaus mean : 38.7861
Gaus Width: 5.5681
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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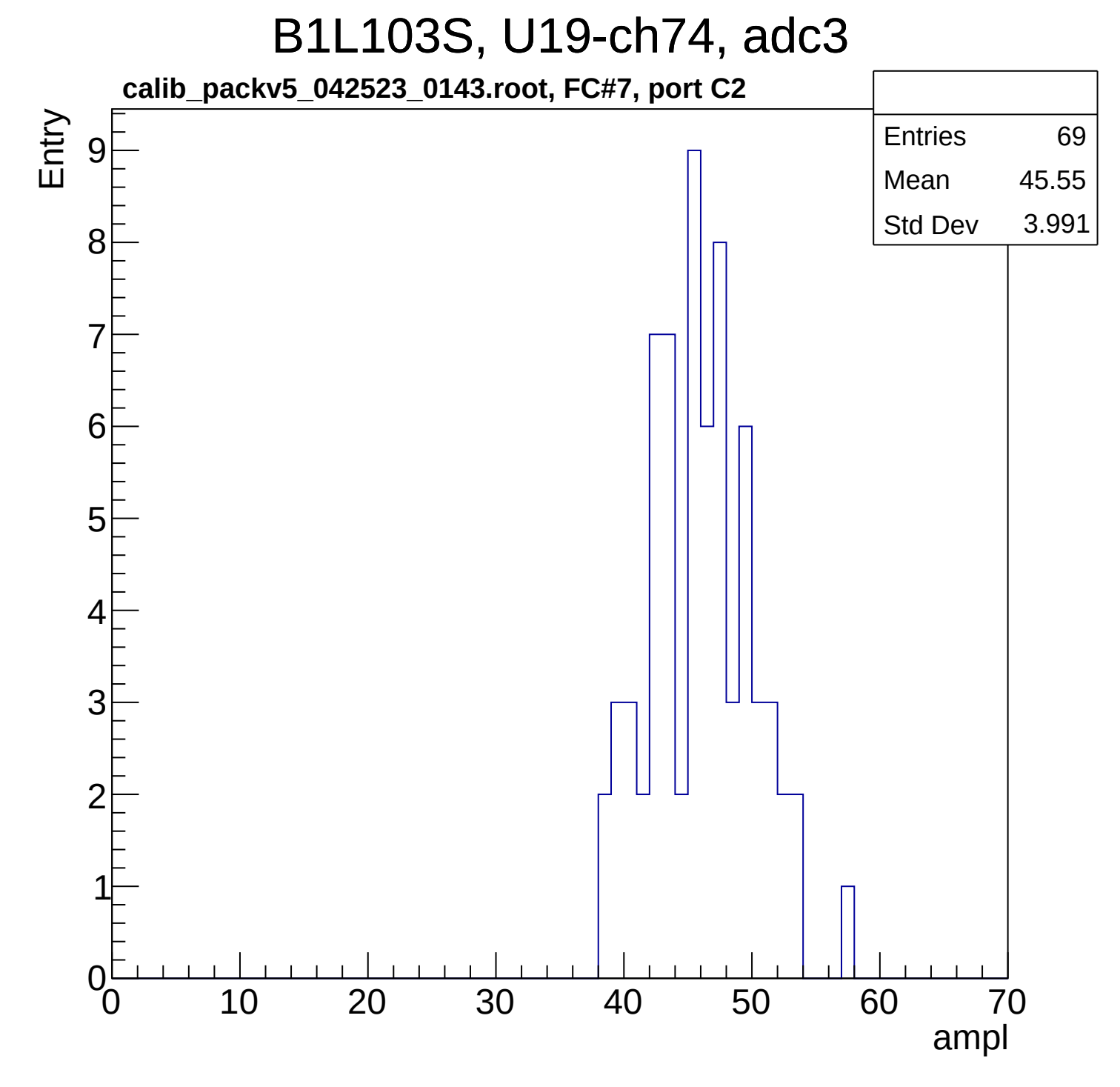
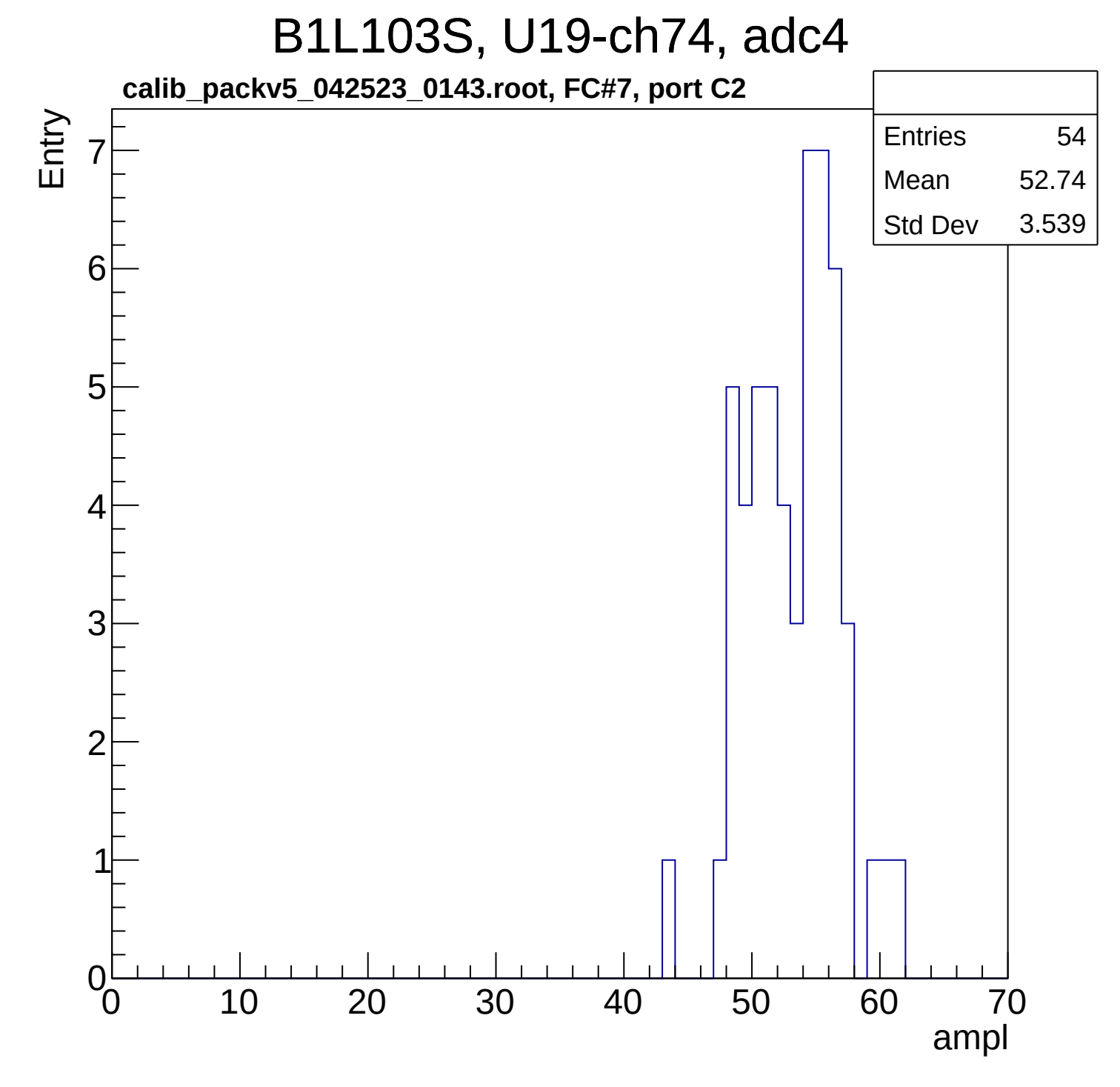
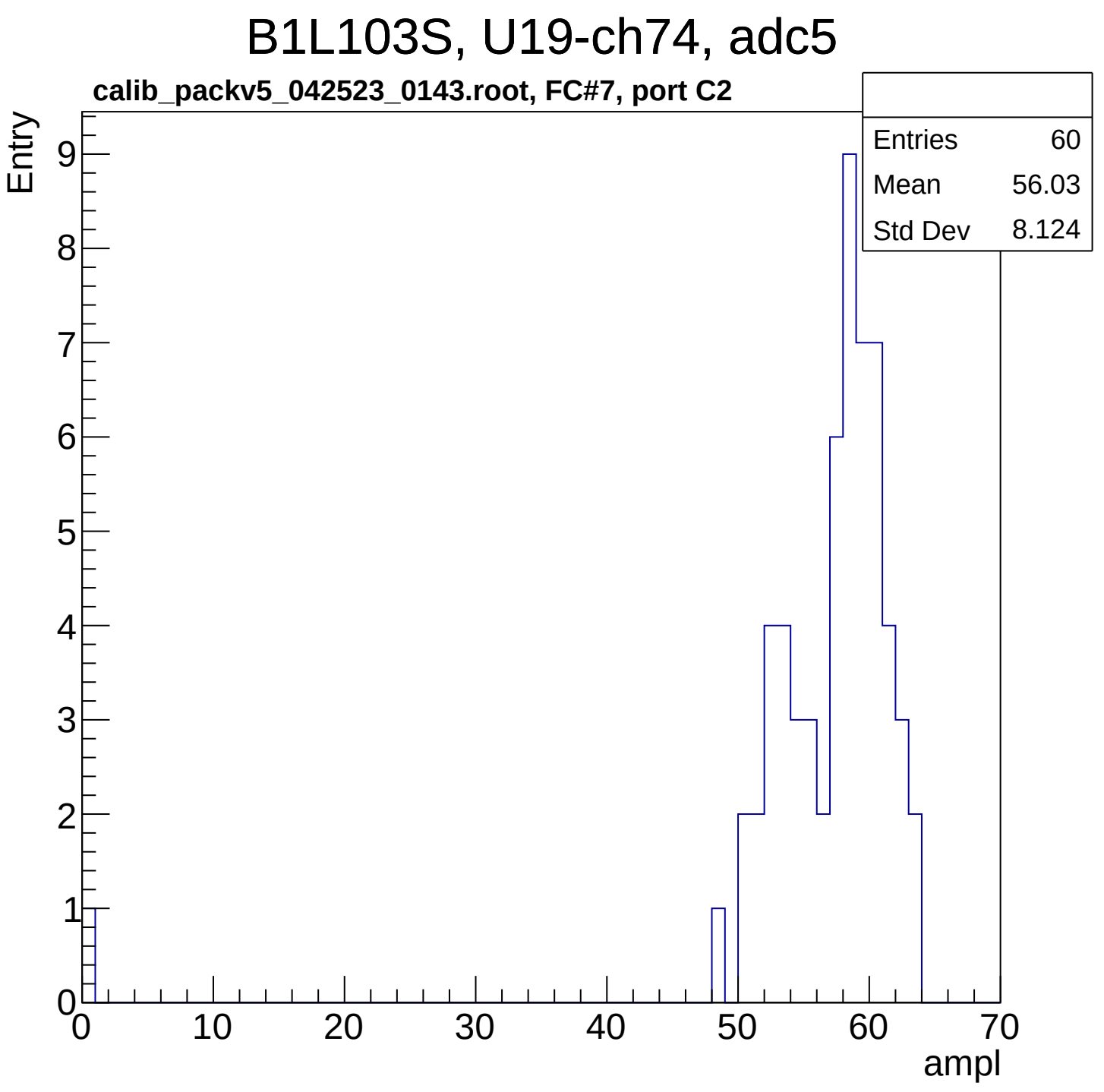

B1L103S, U19-ch74, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 45.55
Std Dev 3.991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch74, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 52.74
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch74, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.03
Std Dev 8.124
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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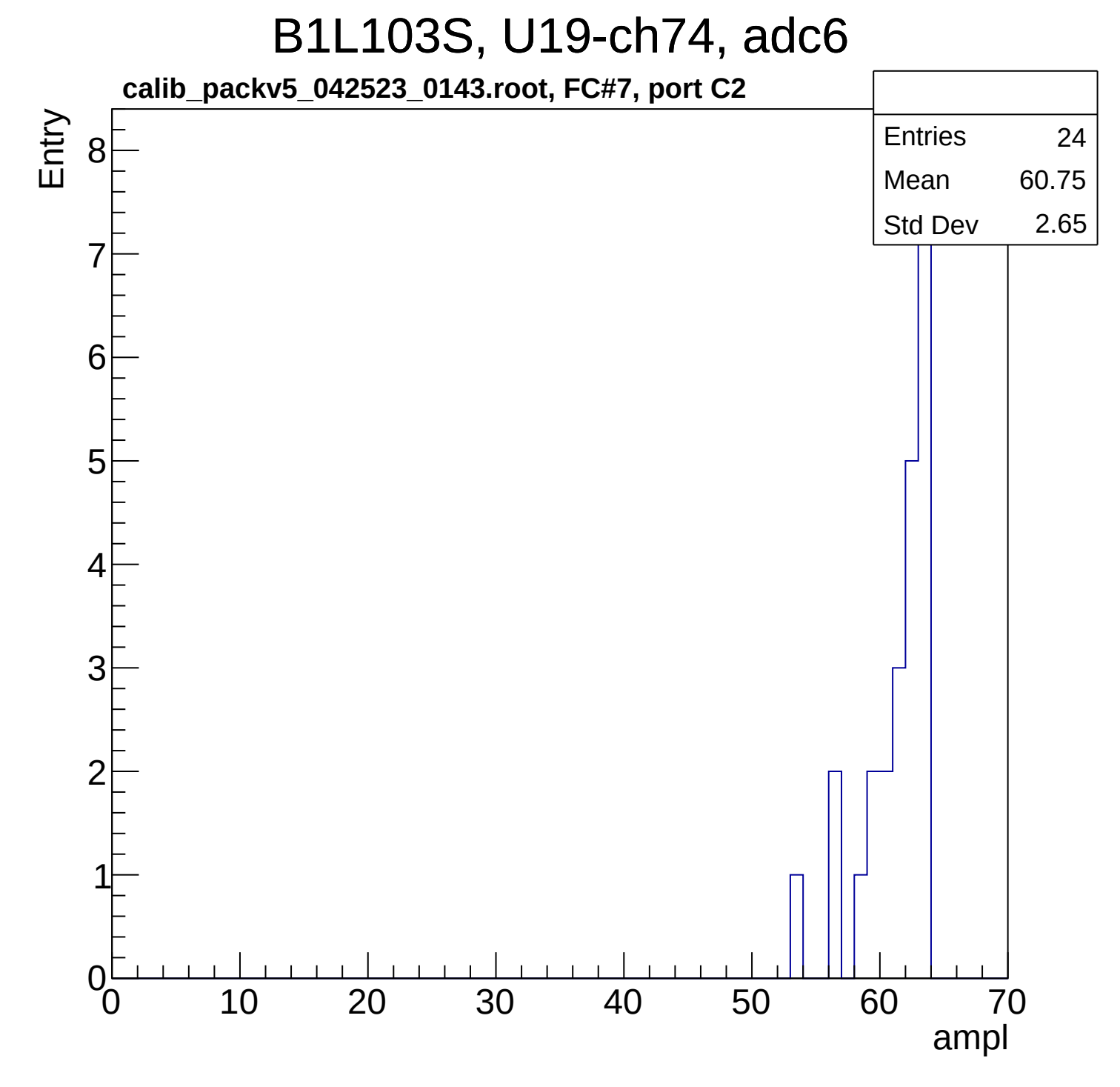
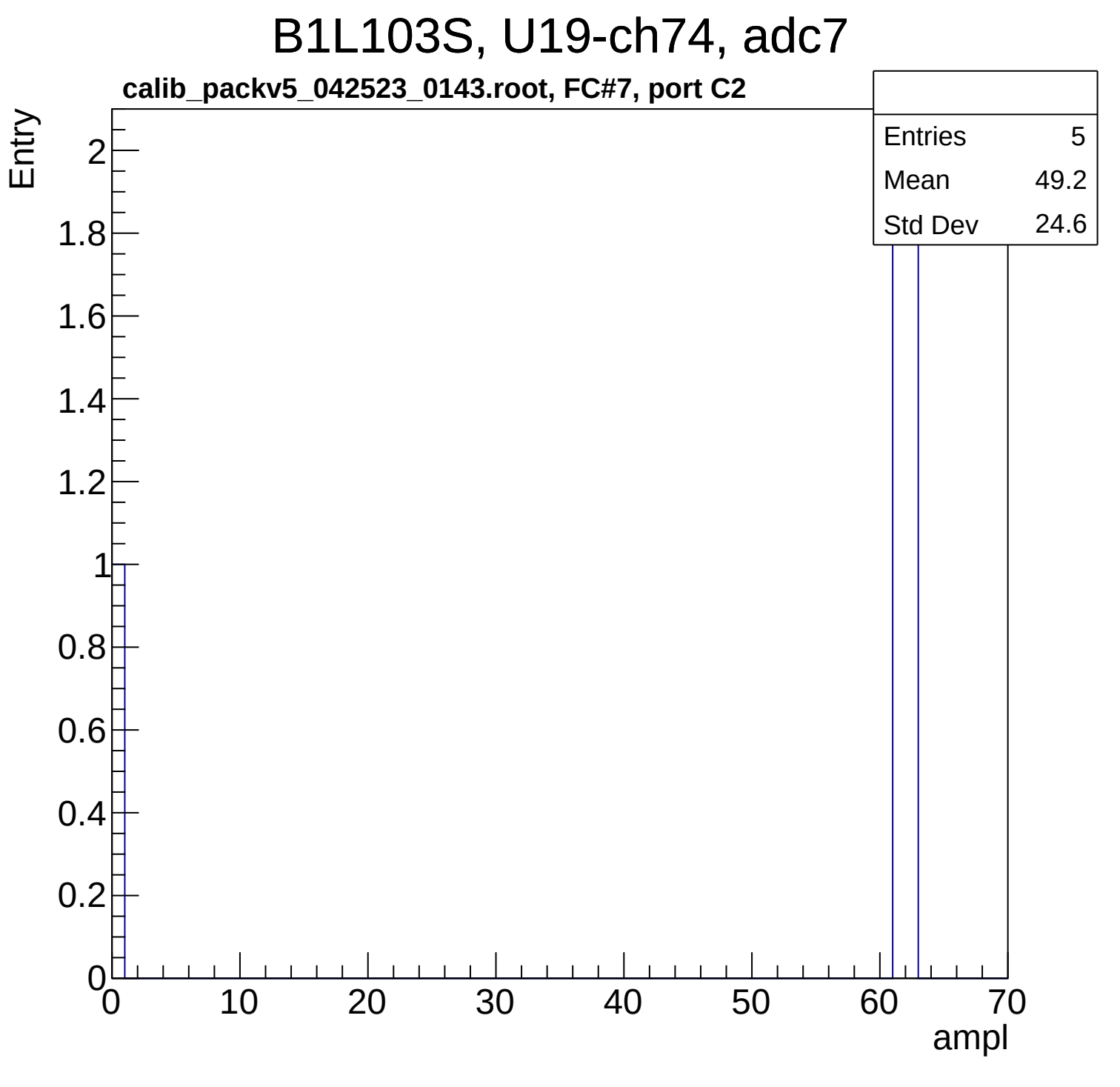
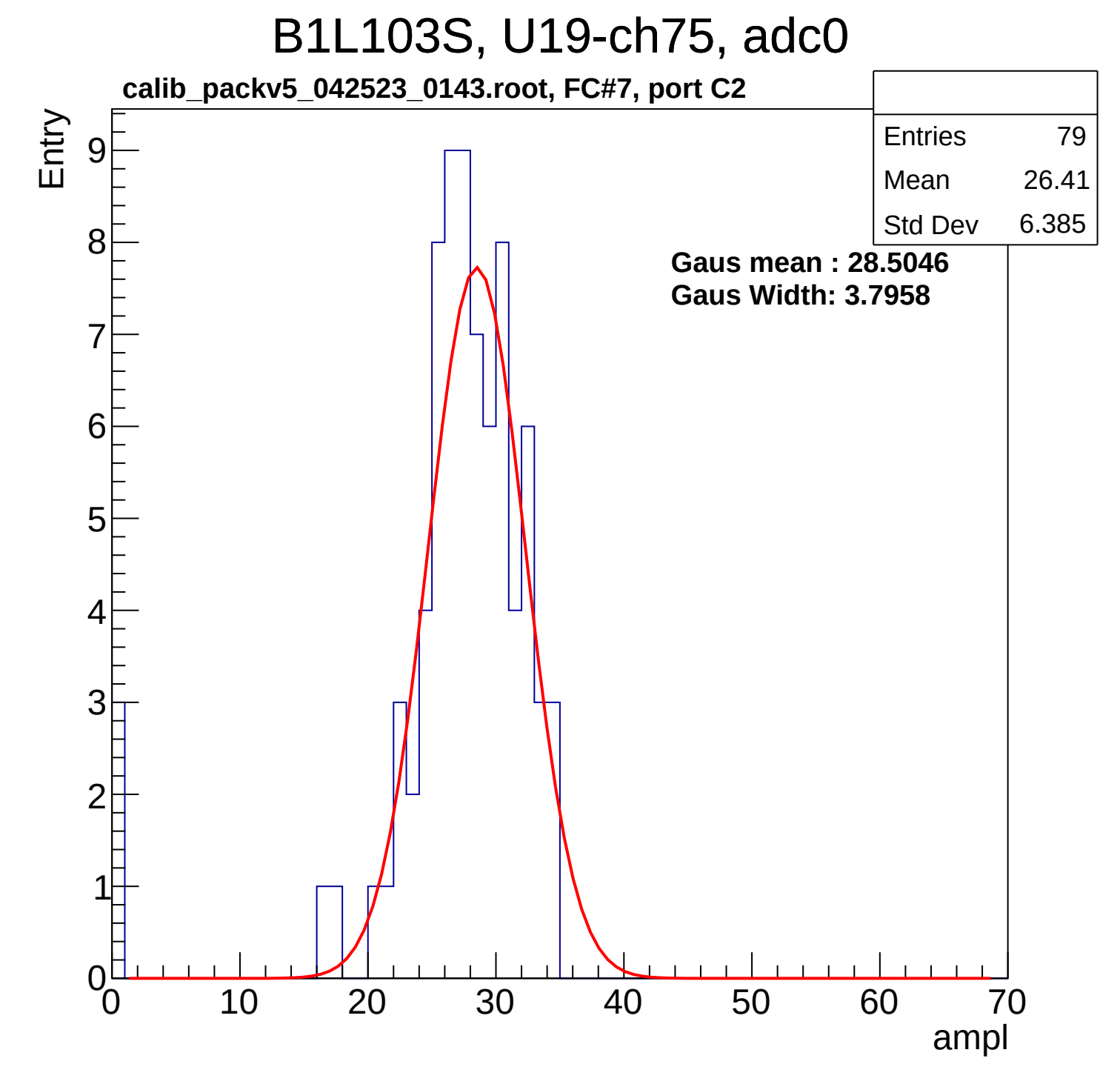

B1L103S, U19-ch74, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 60.75
Std Dev 2.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch74, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.2
Std Dev 24.6
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch75, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 26.41
Std Dev 6.385
Gaus mean : 28.5046
Gaus Width: 3.7958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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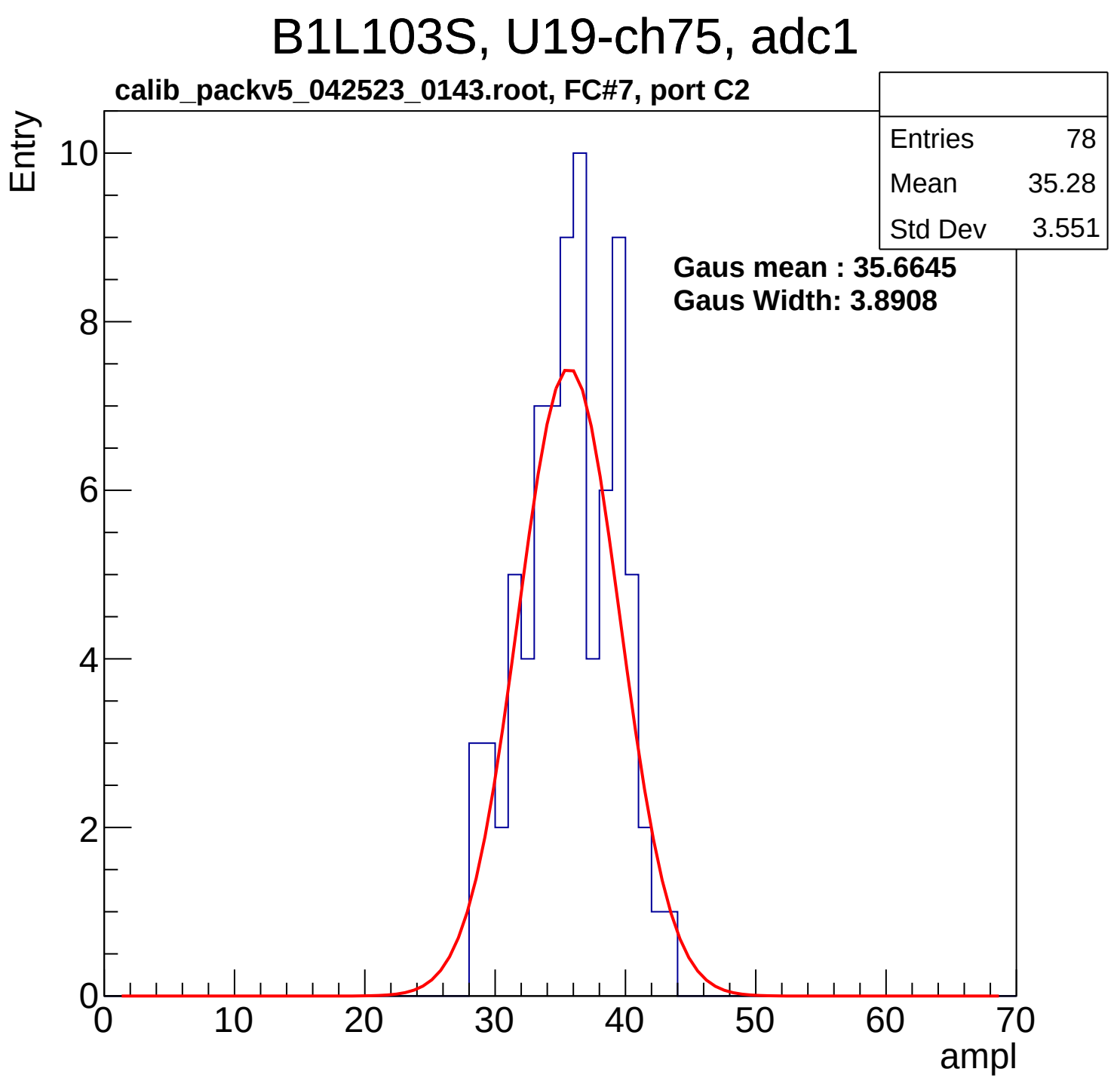
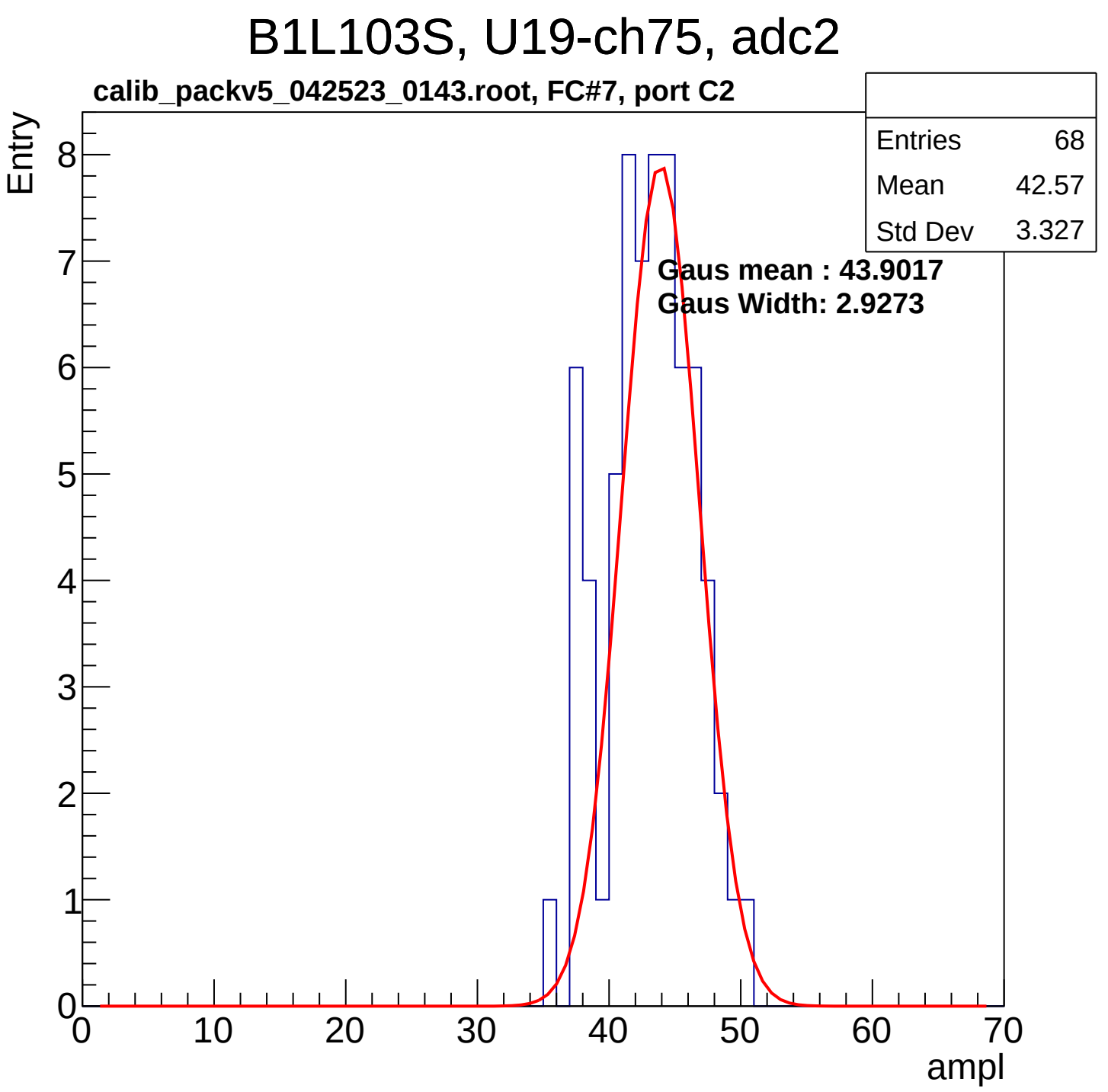
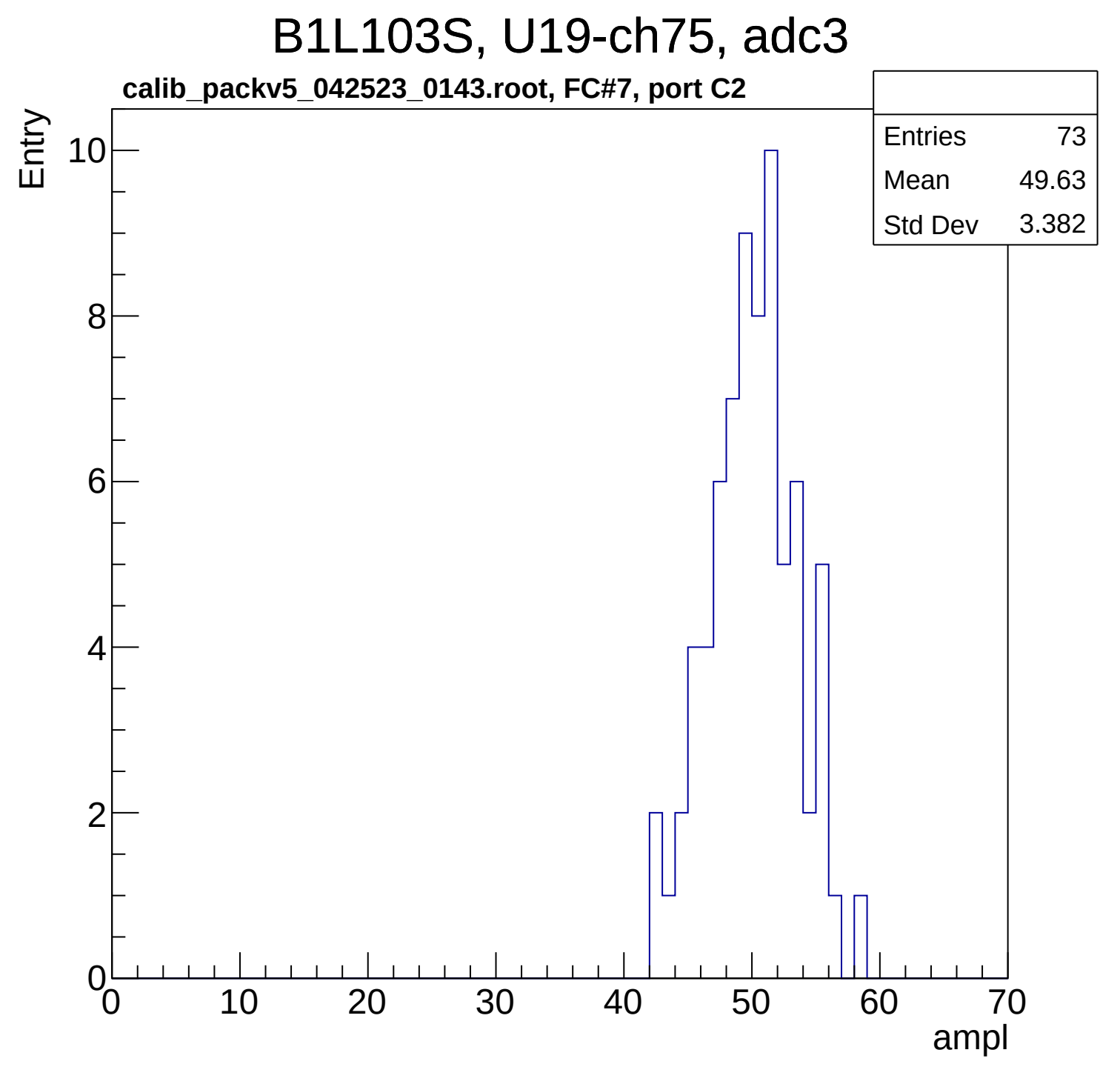
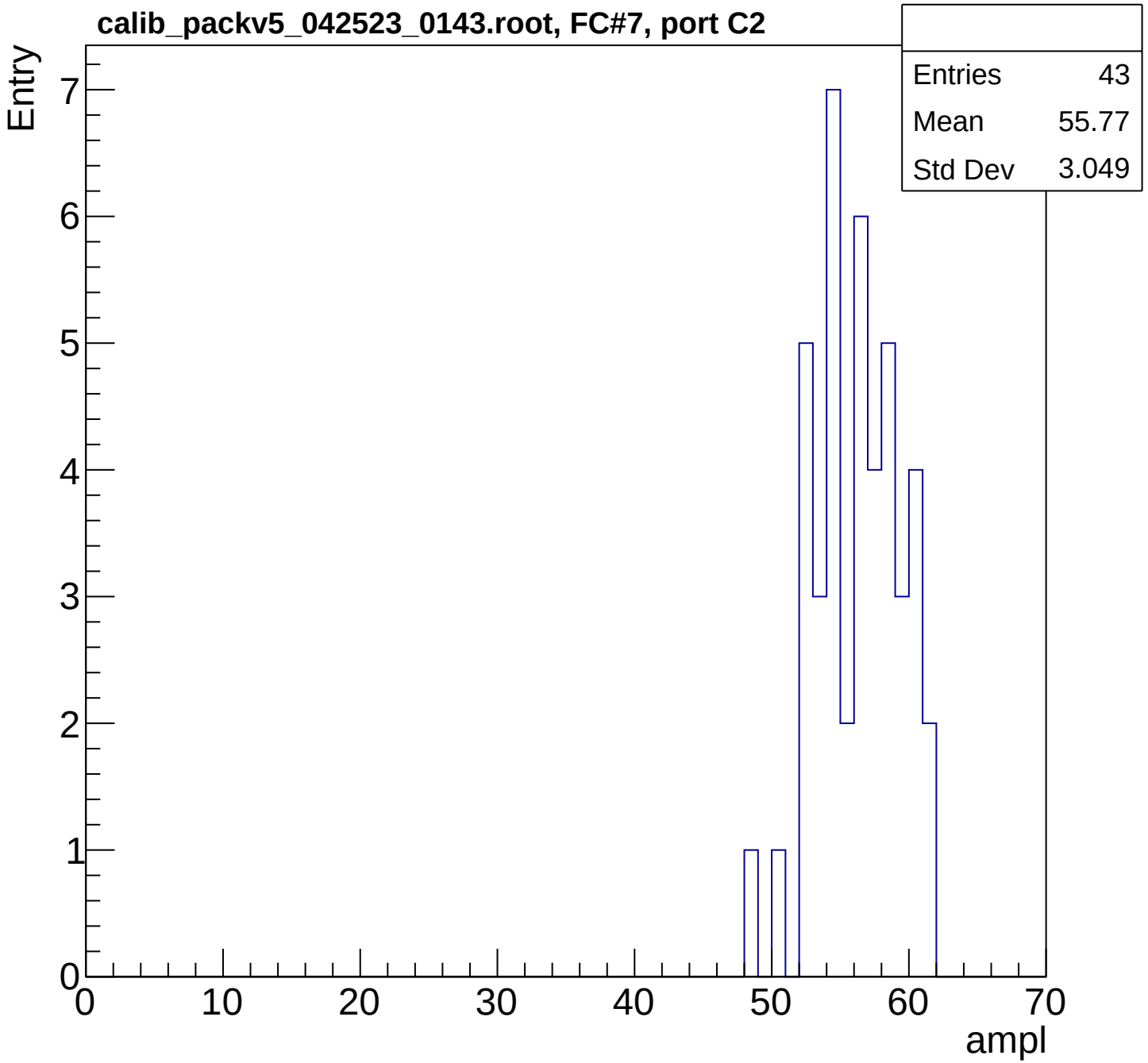

B1L103S, U19-ch75, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 35.28
Std Dev 3.551
Gaus mean : 35.6645
Gaus Width: 3.8908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch75, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 42.57
Std Dev 3.327
Gaus mean : 43.9017
Gaus Width: 2.9273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch75, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.63
Std Dev 3.382
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch75, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 55.77
Std Dev 3.049
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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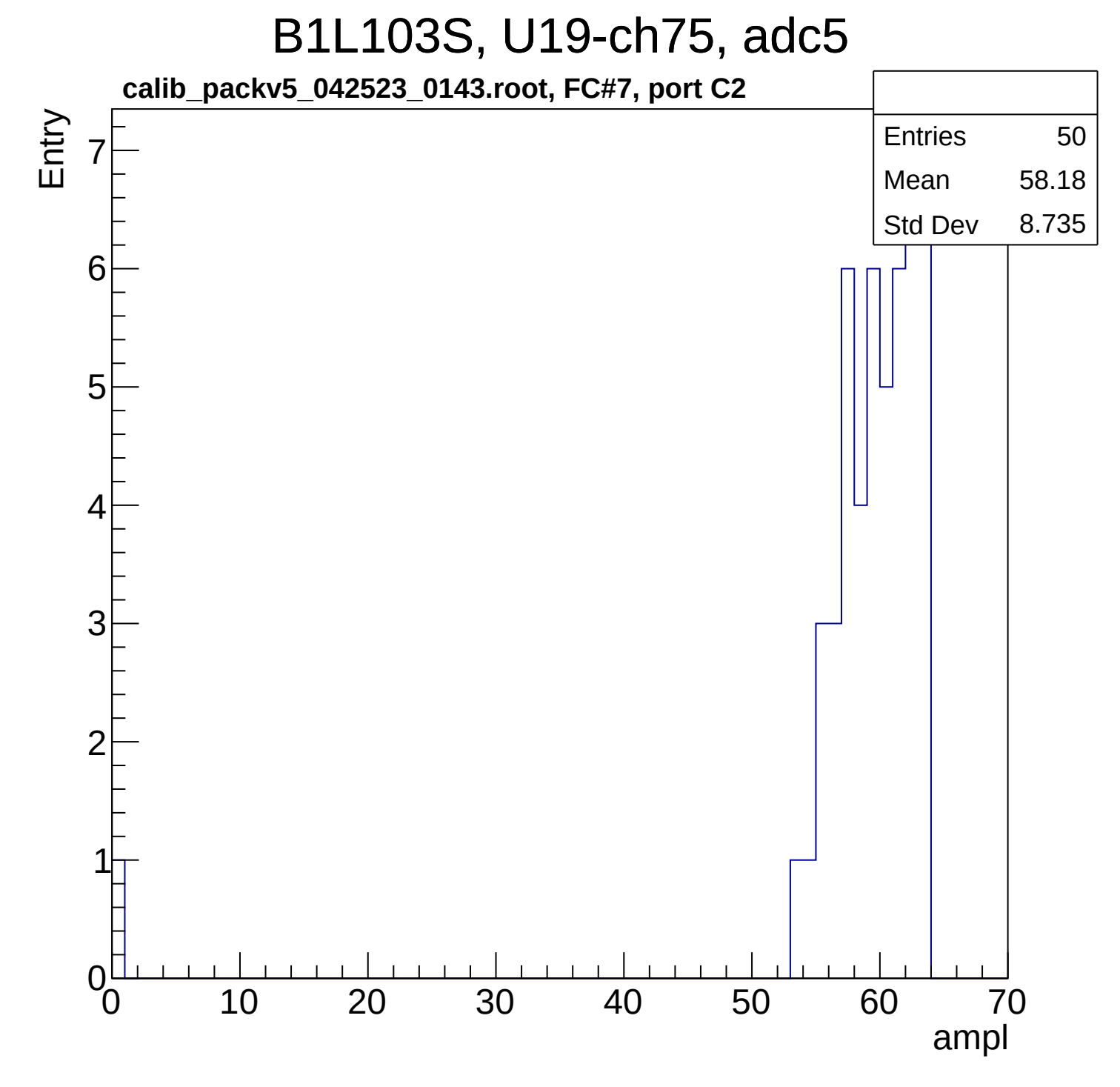
B1L103S, U19-ch75, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.18
Std Dev 8.735
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
50
60
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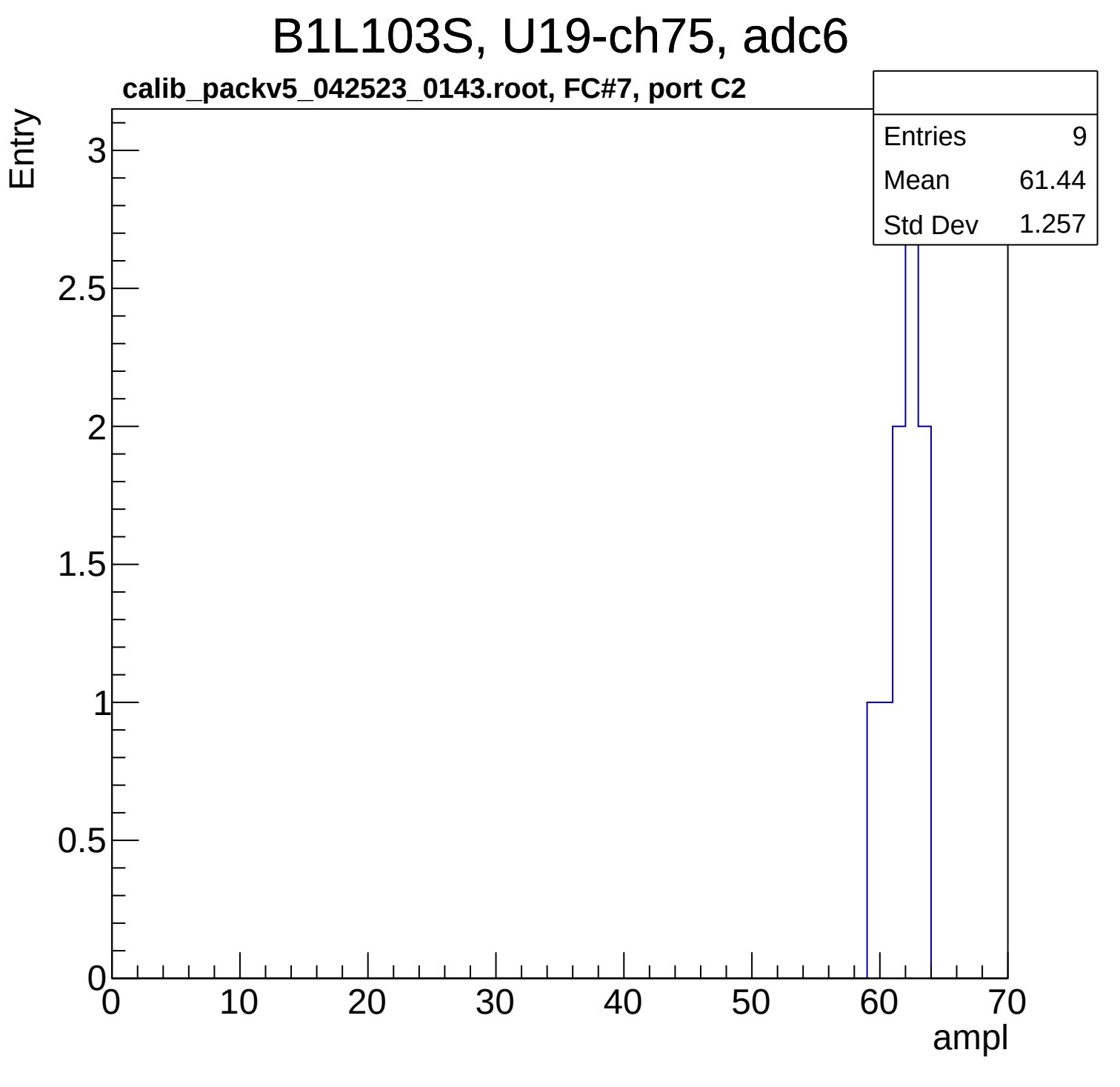
B1L103S, U19-ch75, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.44
Std Dev 1.257
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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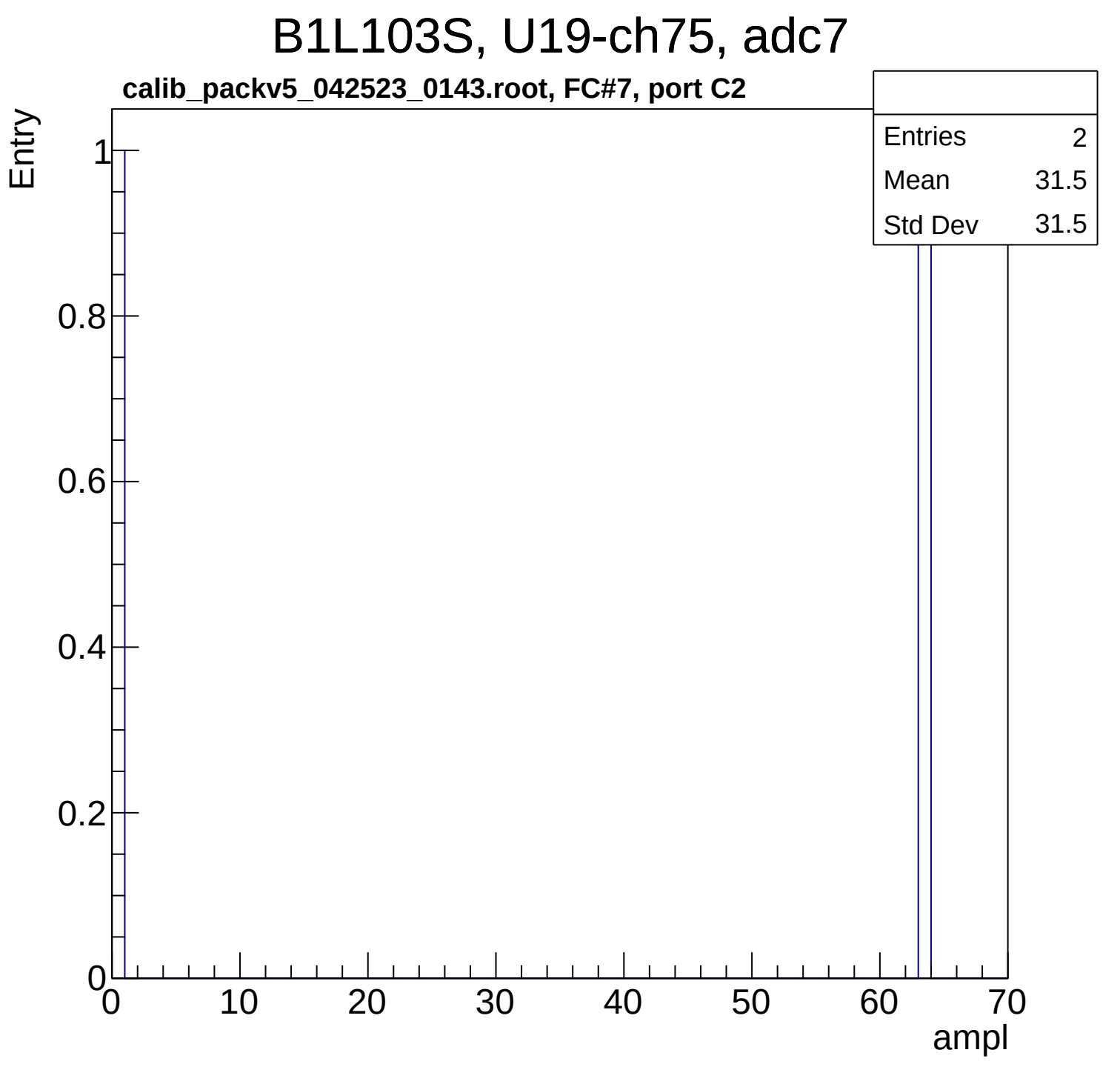
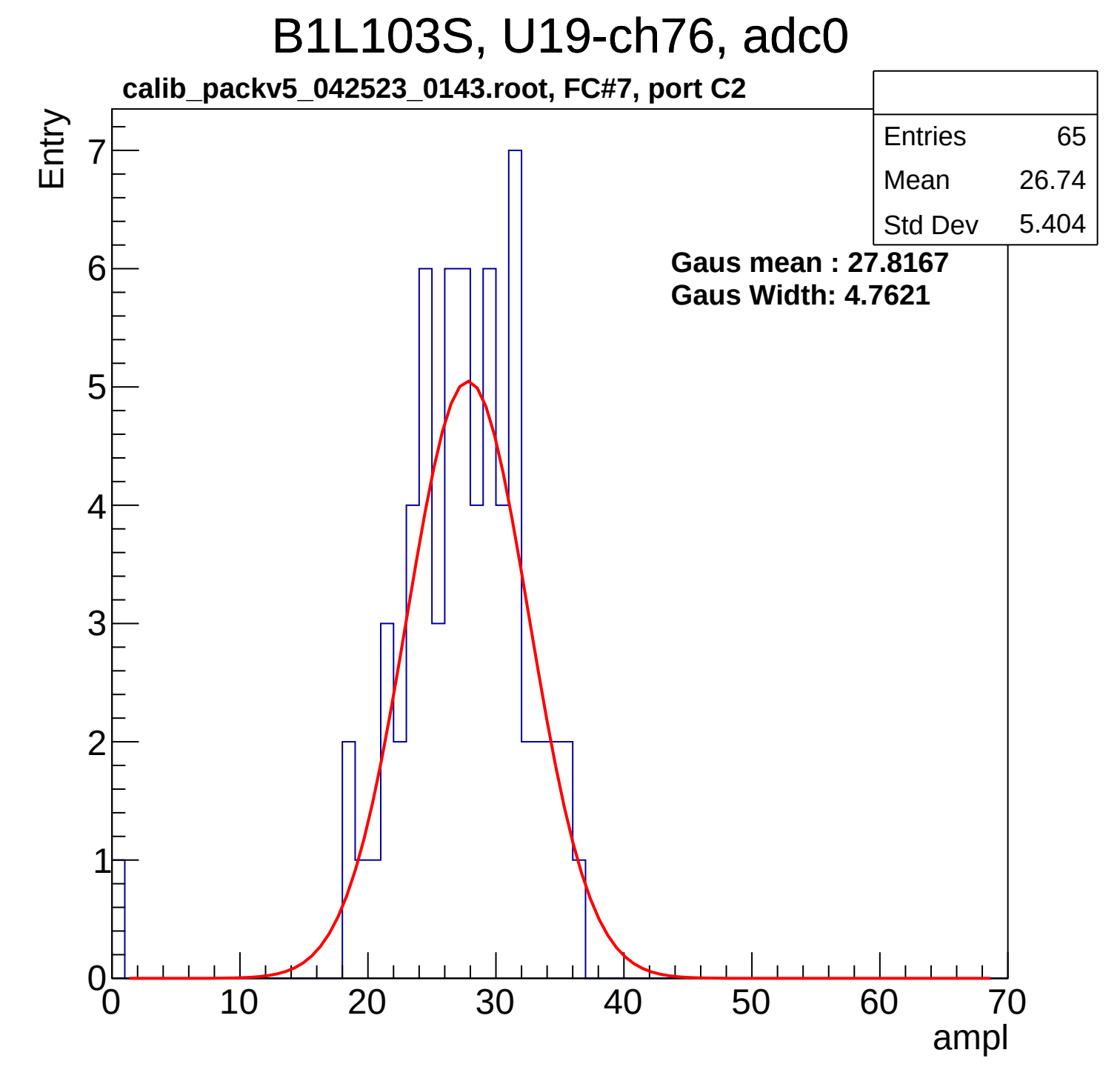
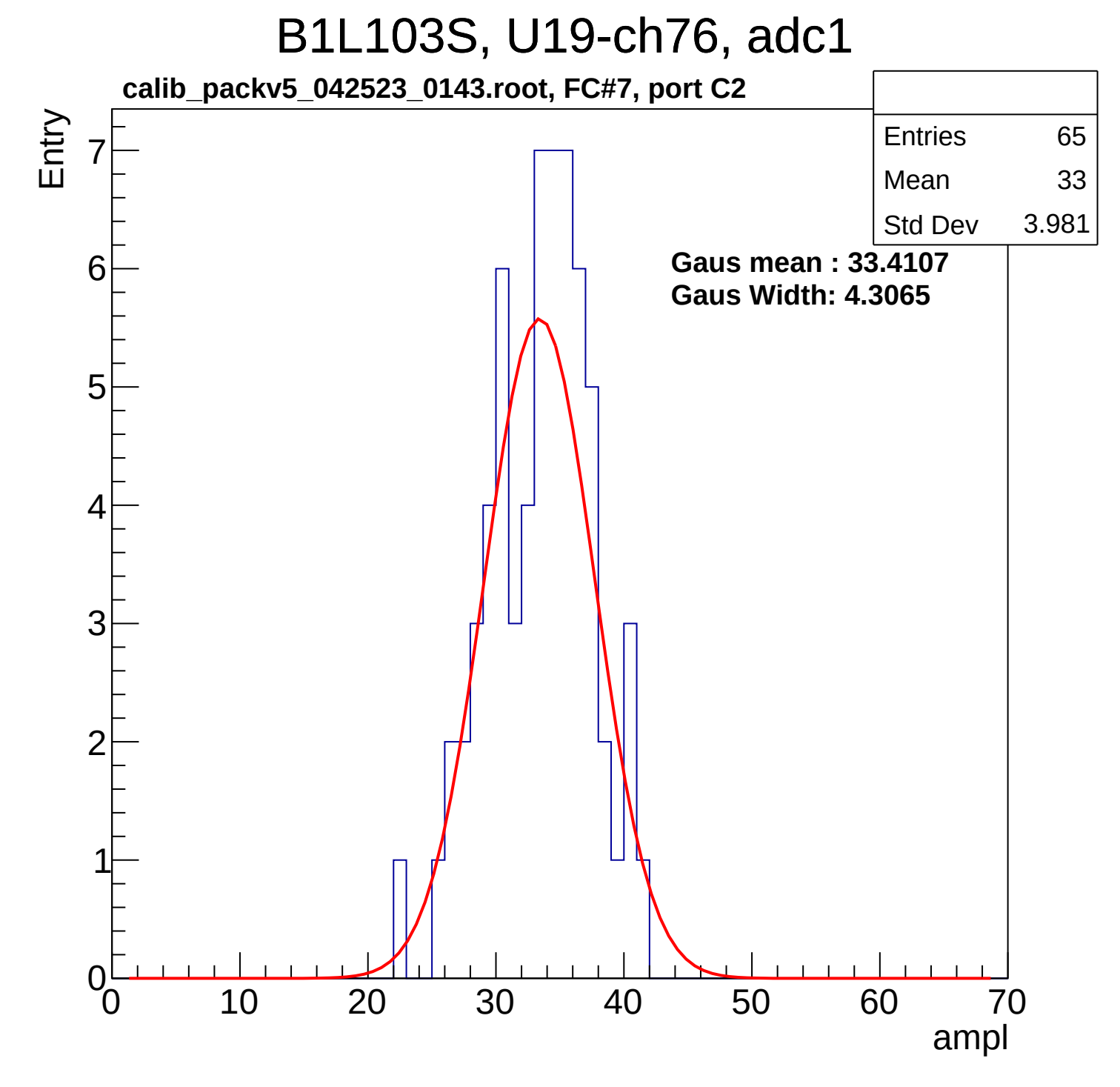
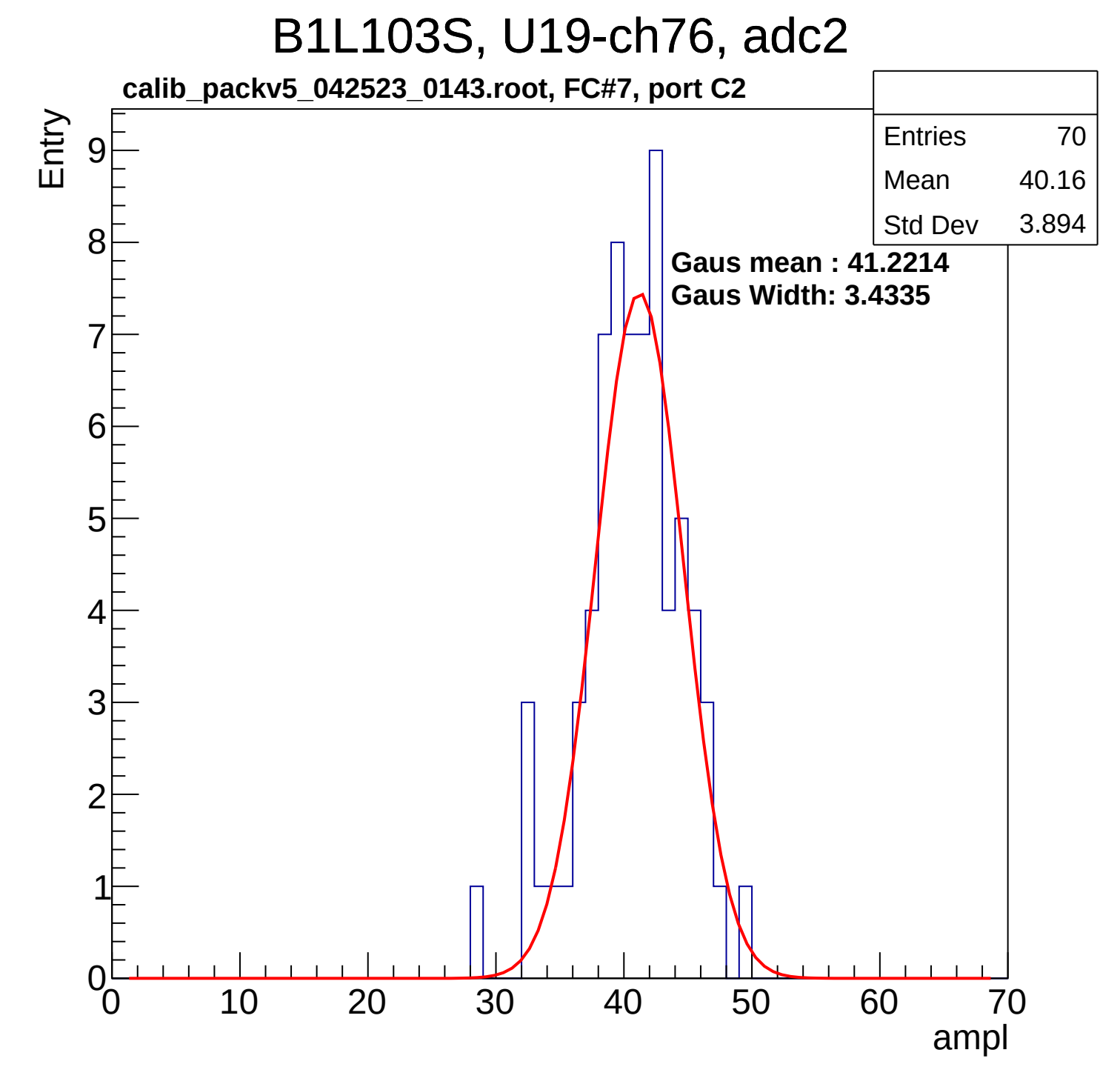
40
50
60
70


B1L103S, U19-ch75, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch76, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 26.74
Std Dev 5.404
Gaus mean : 27.8167
Gaus Width: 4.7621
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch76, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 33
Std Dev 3.981
Gaus mean : 33.4107
Gaus Width: 4.3065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch76, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 40.16
Std Dev 3.894
Gaus mean : 41.2214
Gaus Width: 3.4335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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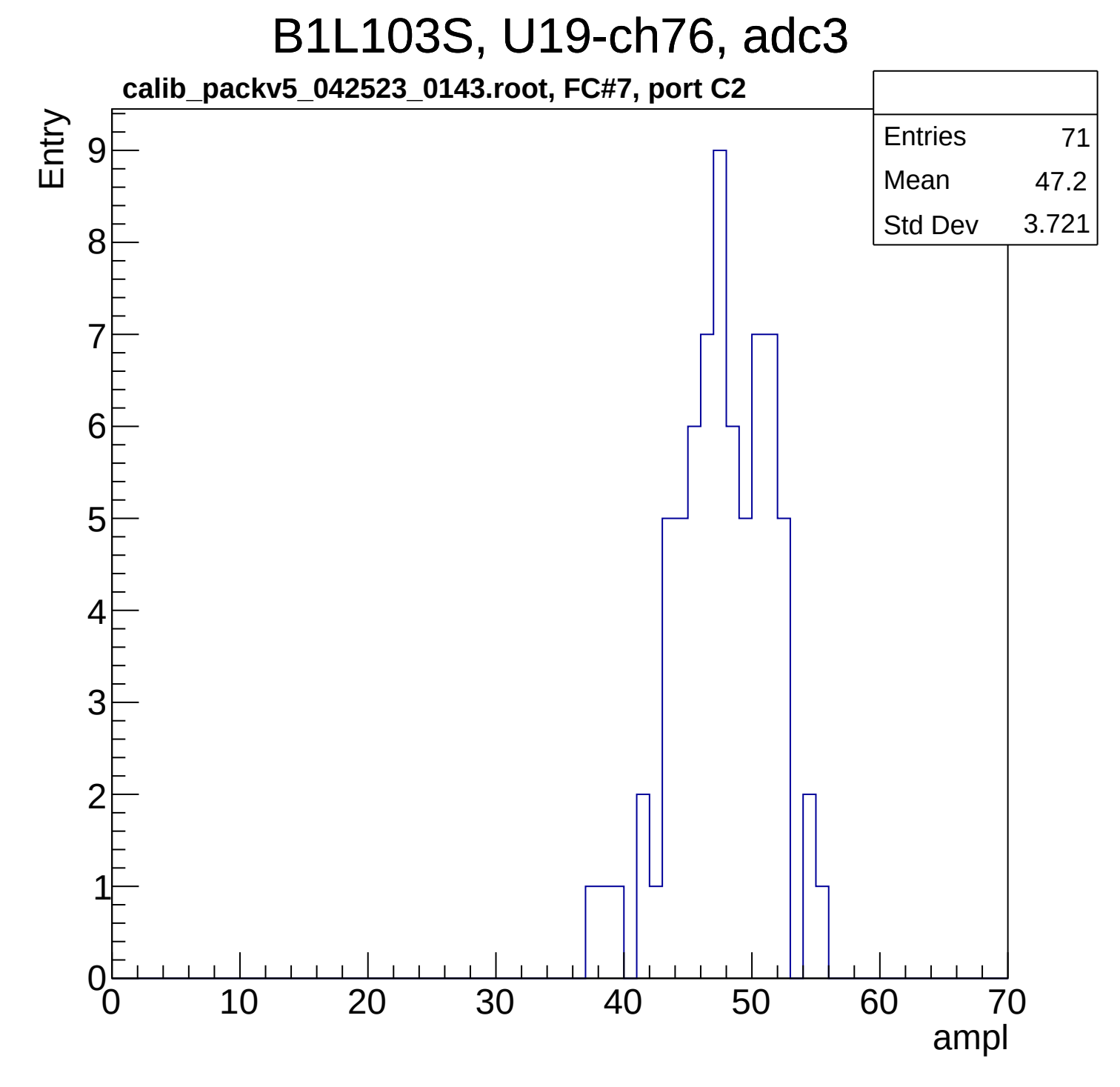
B1L103S, U19-ch76, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 47.2
Std Dev 3.721
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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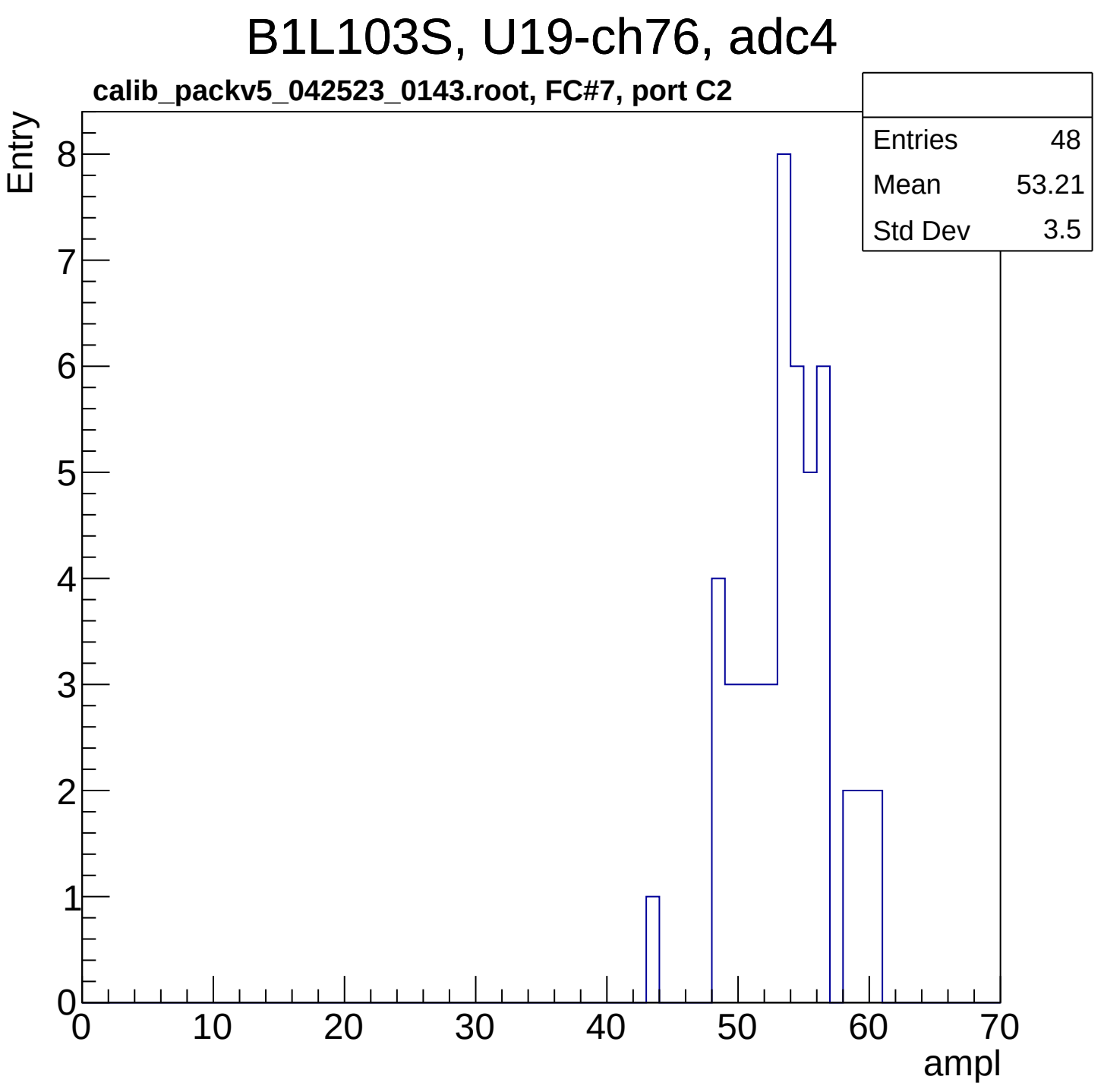
B1L103S, U19-ch76, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 53.21
Std Dev 3.5
Entry
ampl
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1
2
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5
6
7
8
0
10
20
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40
50
60
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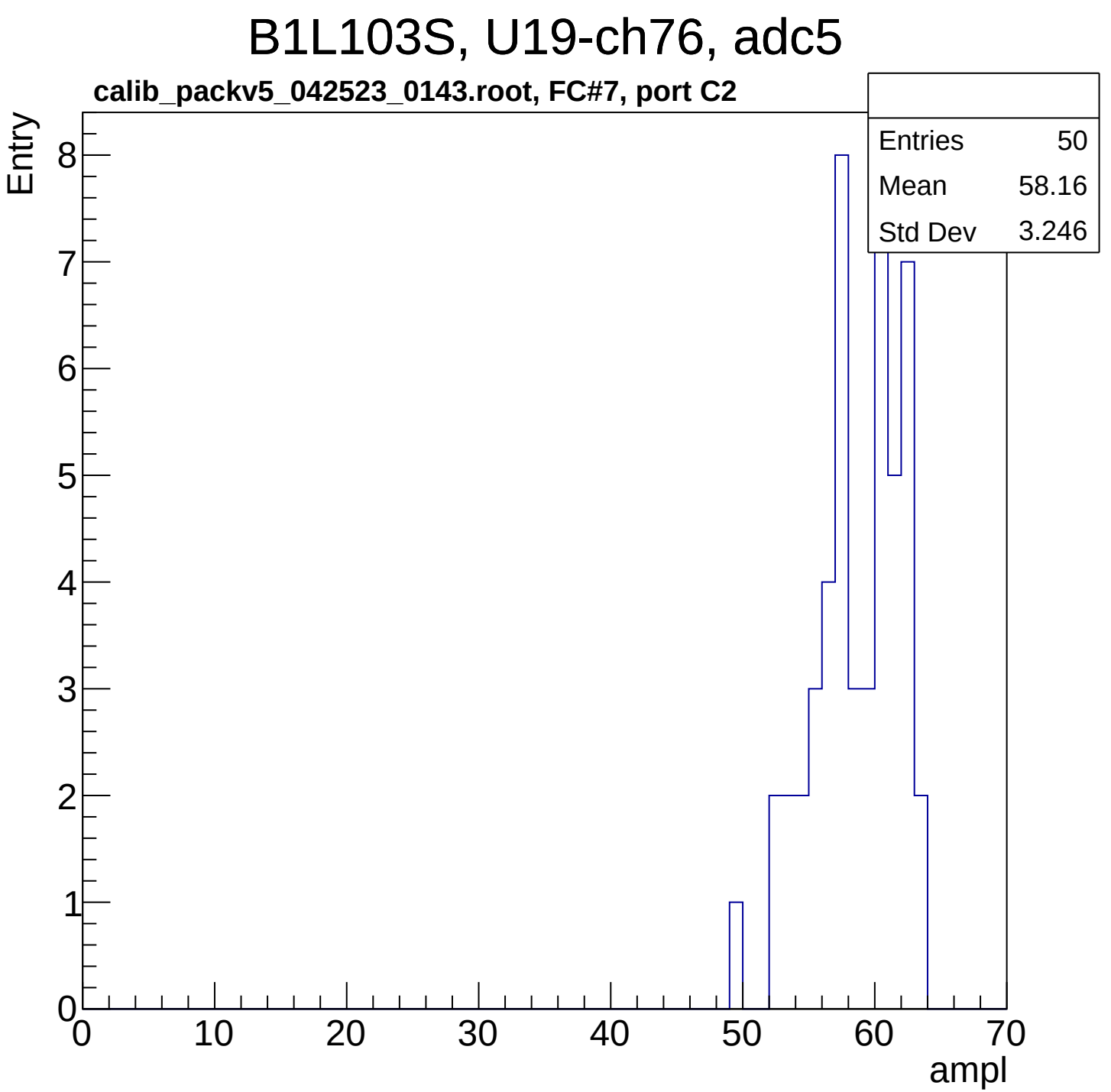
B1L103S, U19-ch76, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.16
Std Dev 3.246
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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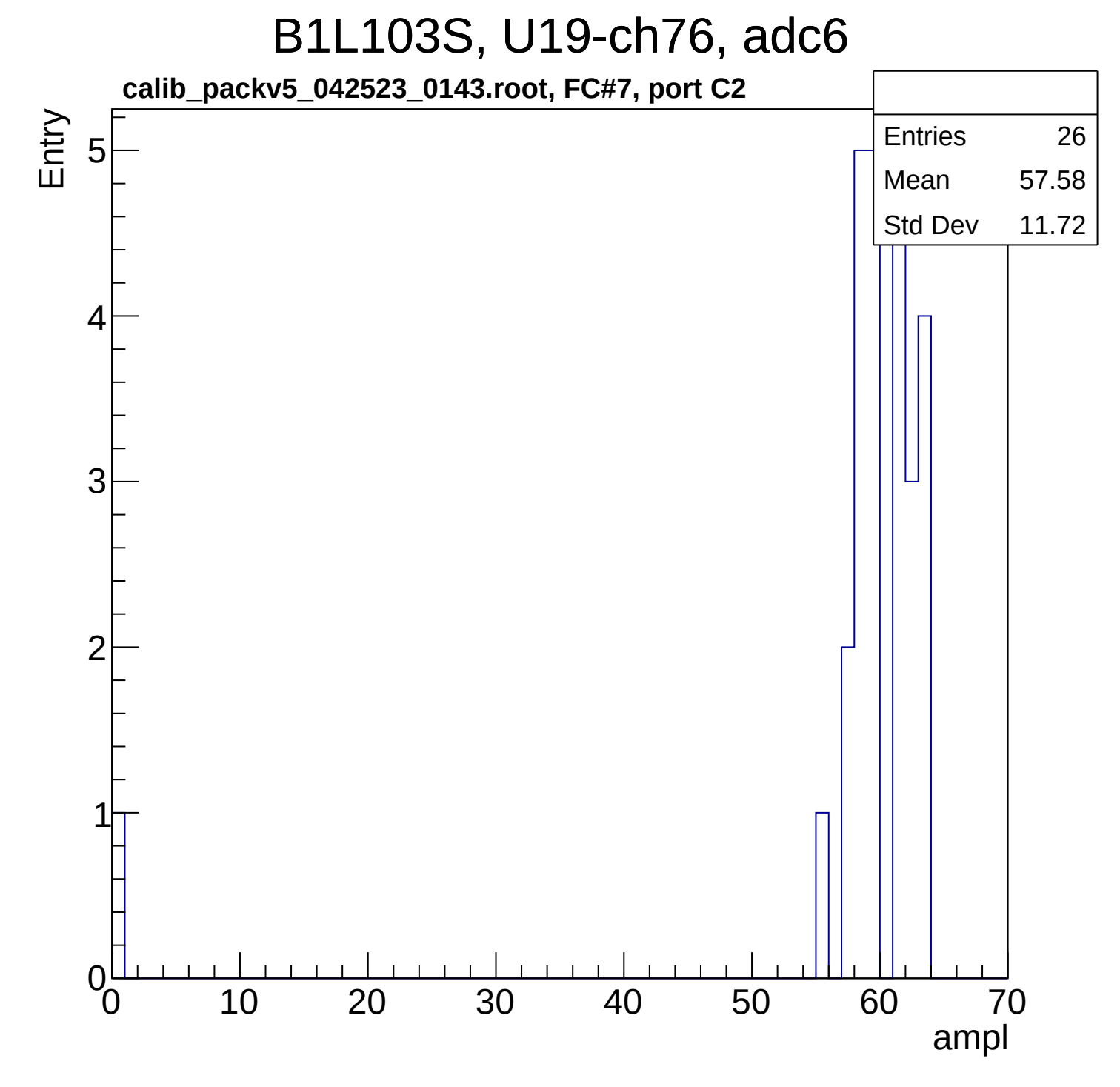
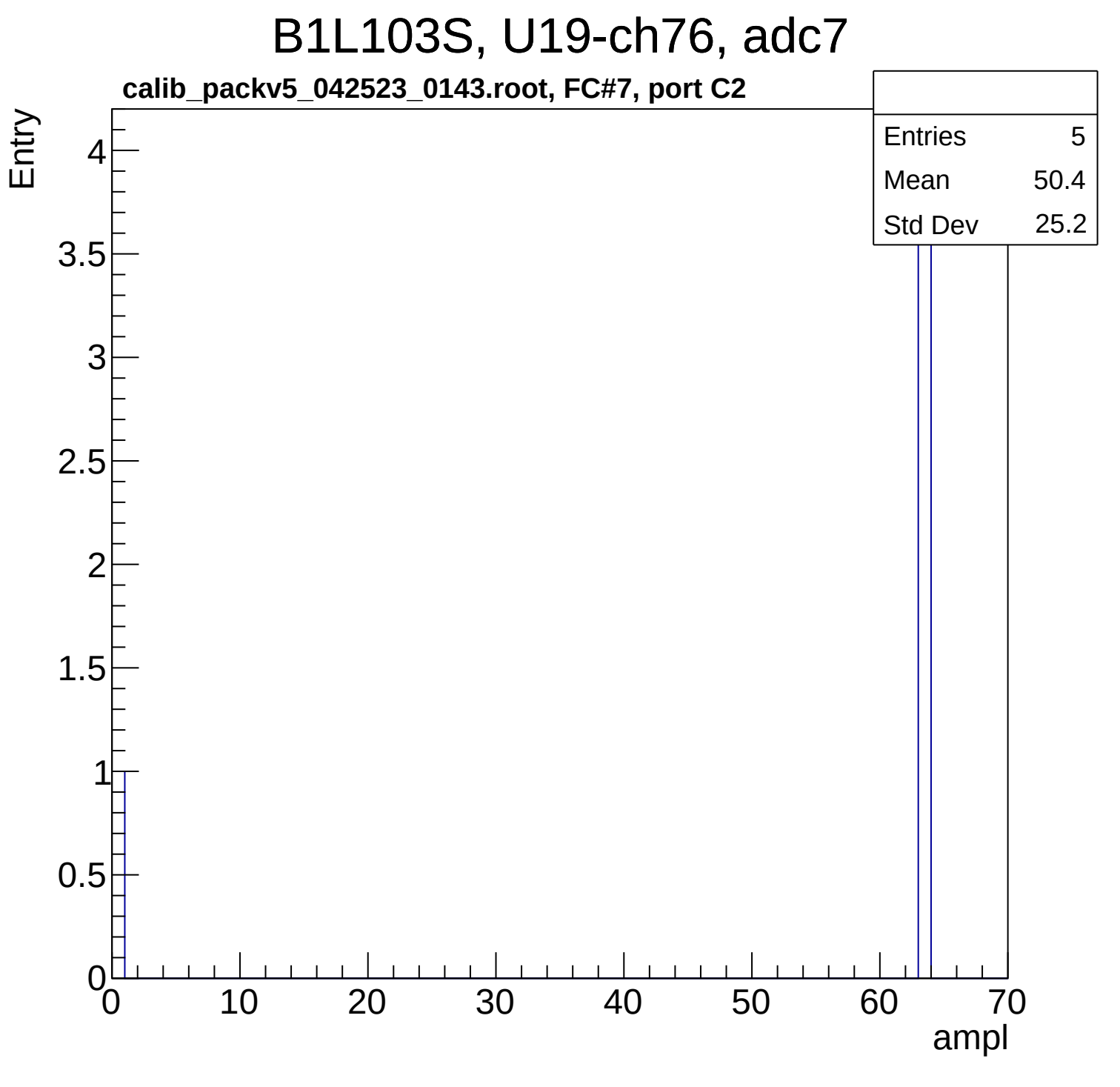

B1L103S, U19-ch76, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 57.58
Std Dev 11.72
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch76, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.4
Std Dev 25.2
Entry
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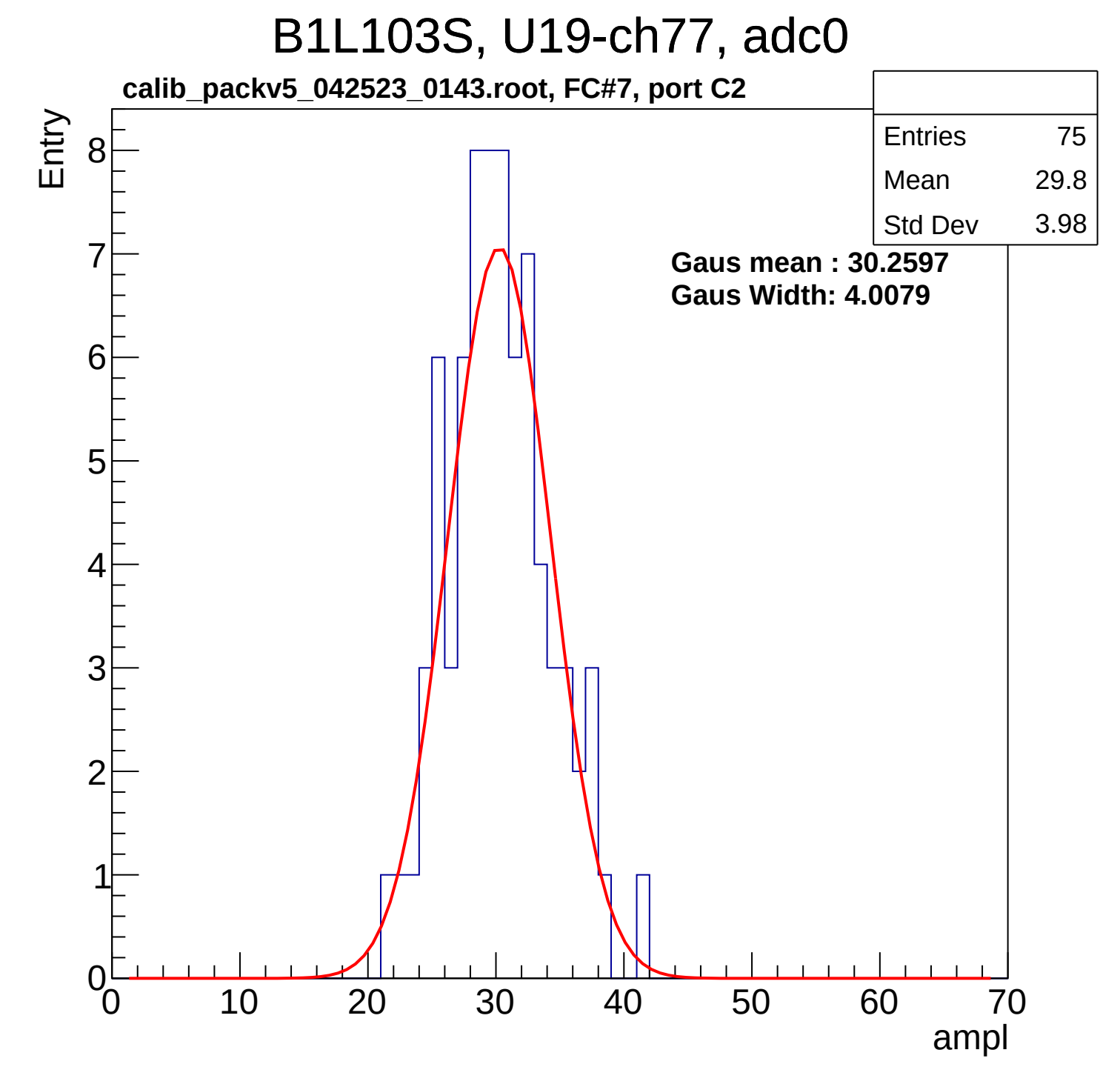
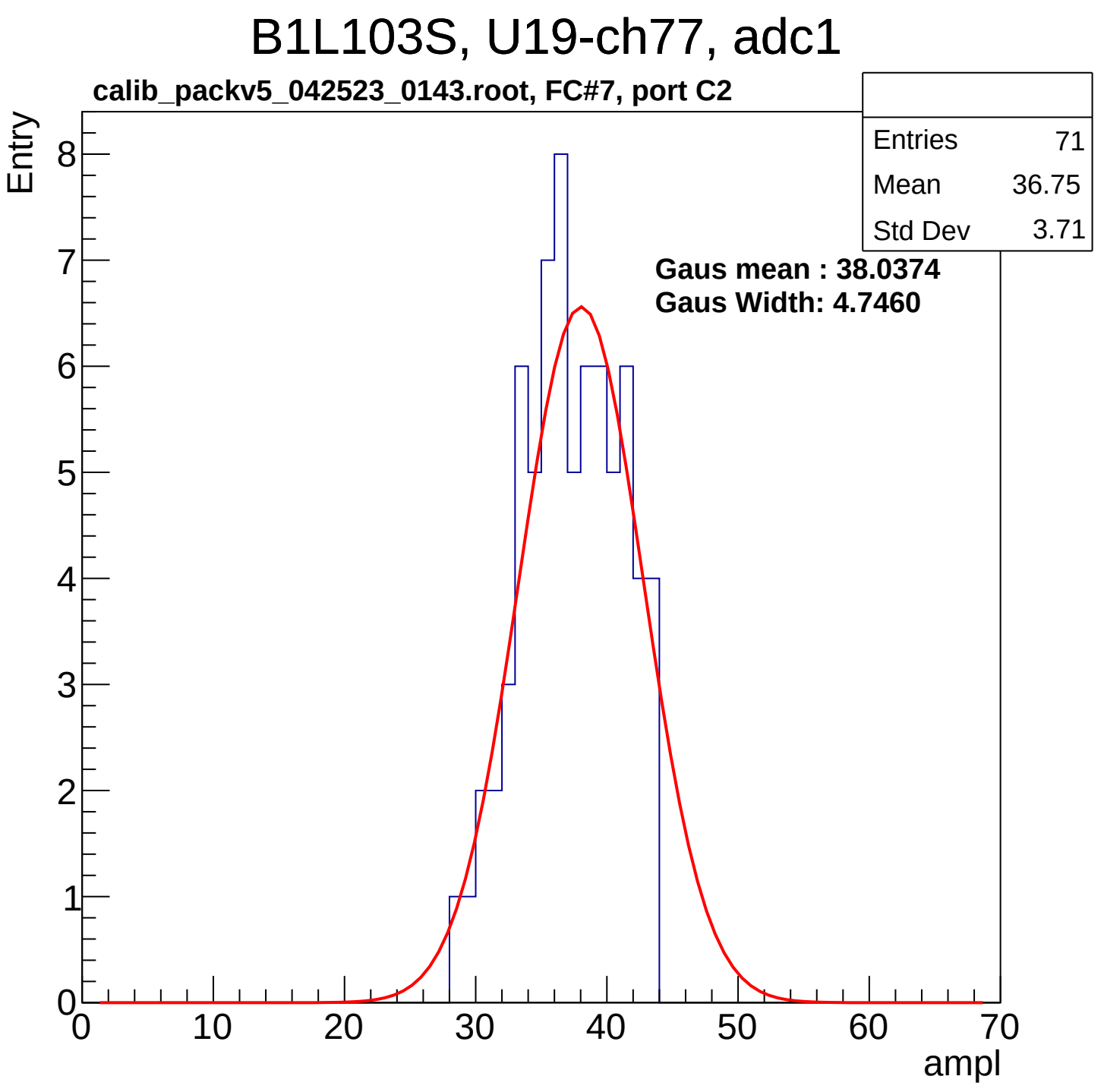
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch77, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.8
Std Dev 3.98
Gaus mean : 30.2597
Gaus Width: 4.0079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch77, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.75
Std Dev 3.71
Gaus mean : 38.0374
Gaus Width: 4.7460
Entry
ampl
0
1
2
3
4
5
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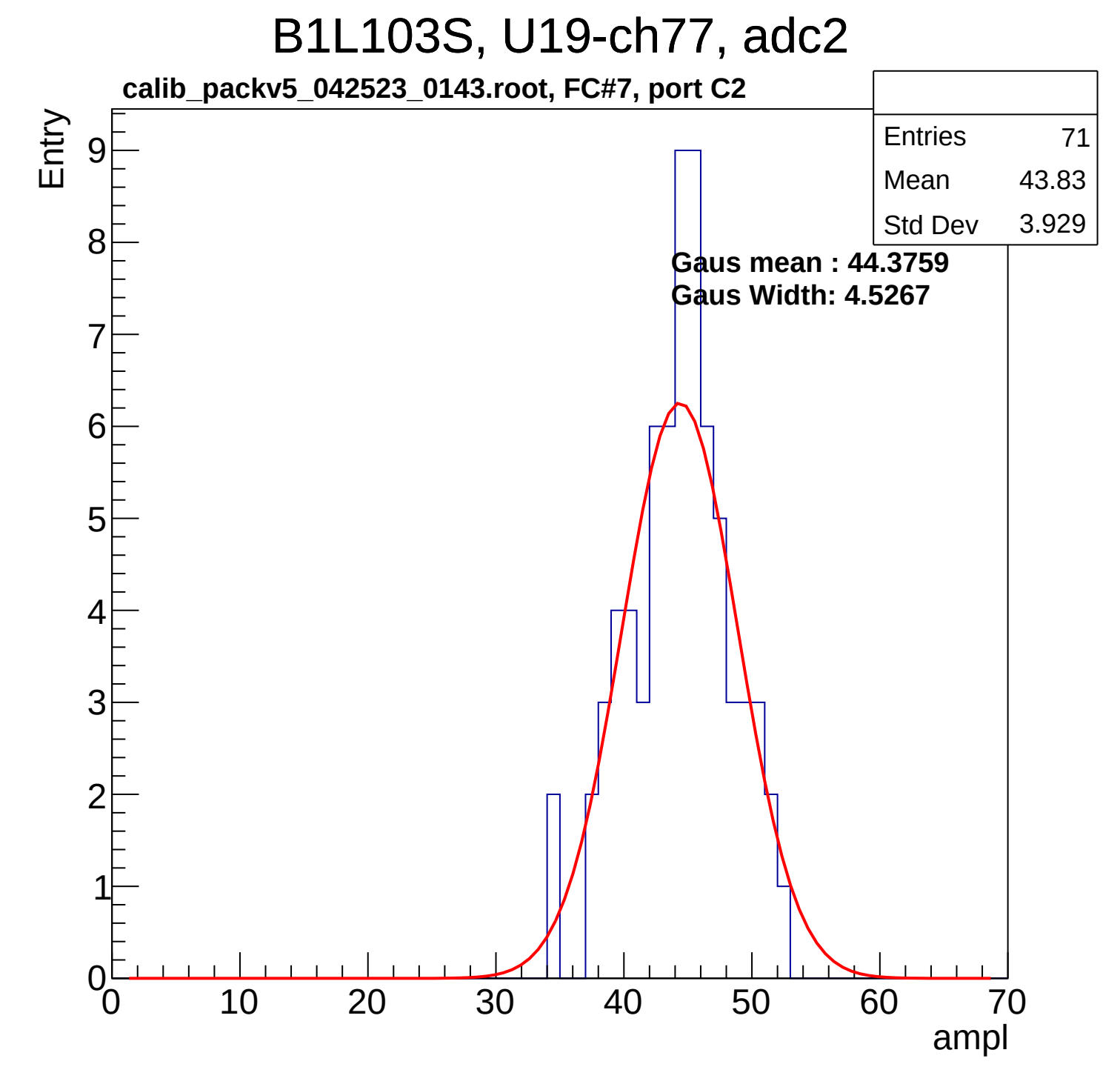
B1L103S, U19-ch77, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 43.83
Std Dev 3.929
Gaus mean : 44.3759
Gaus Width: 4.5267
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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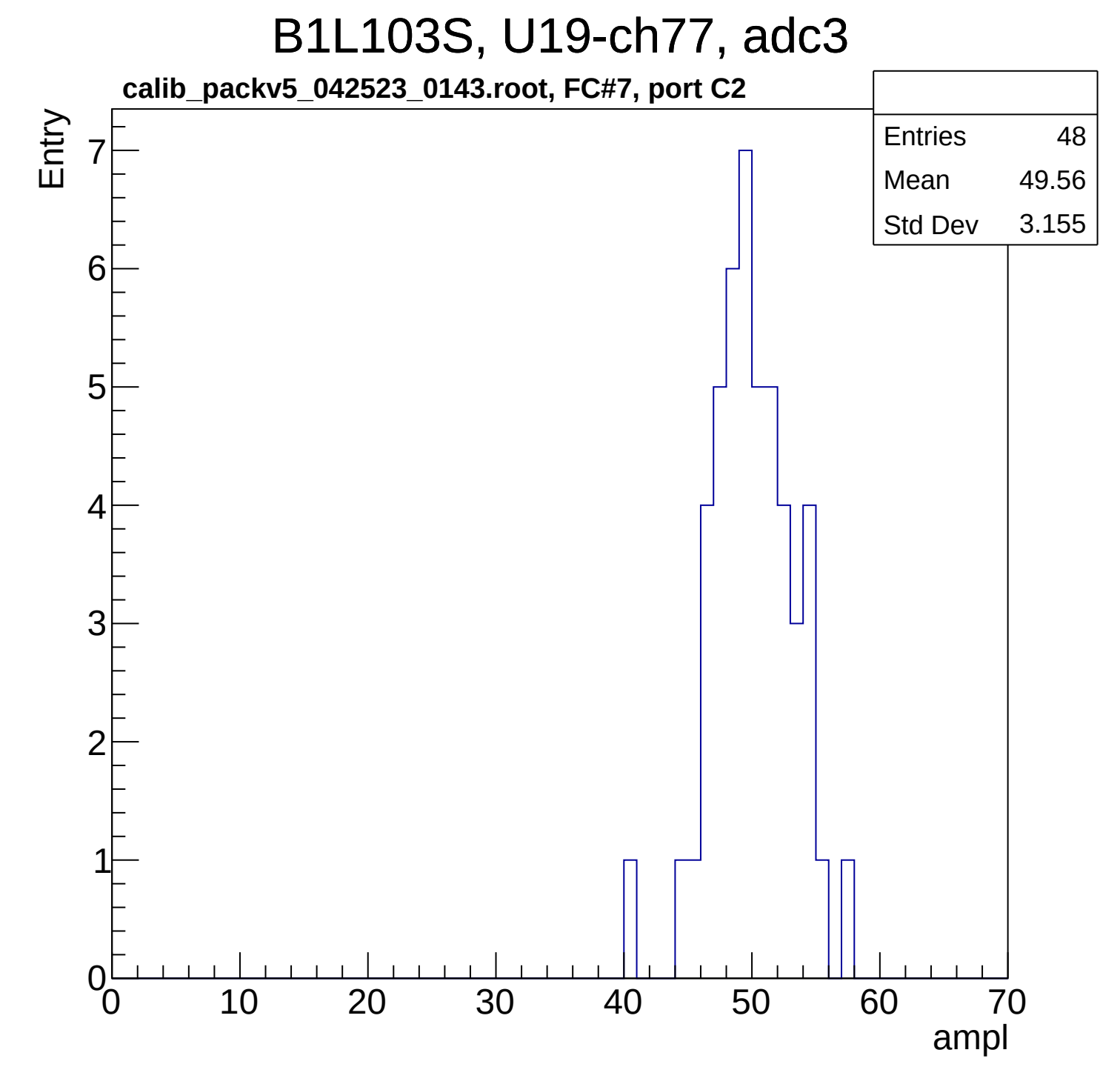
B1L103S, U19-ch77, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 49.56
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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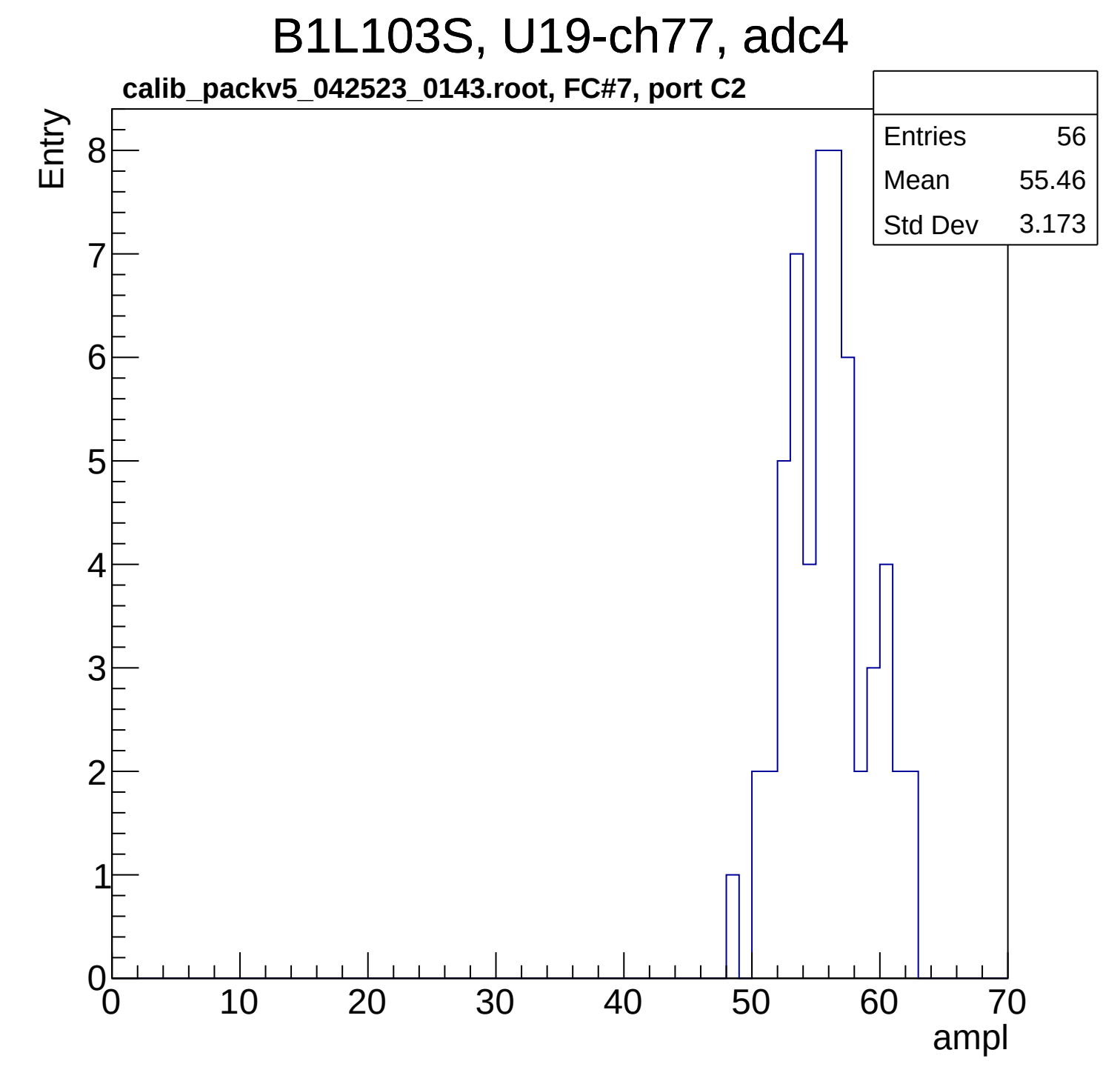
B1L103S, U19-ch77, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55.46
Std Dev 3.173
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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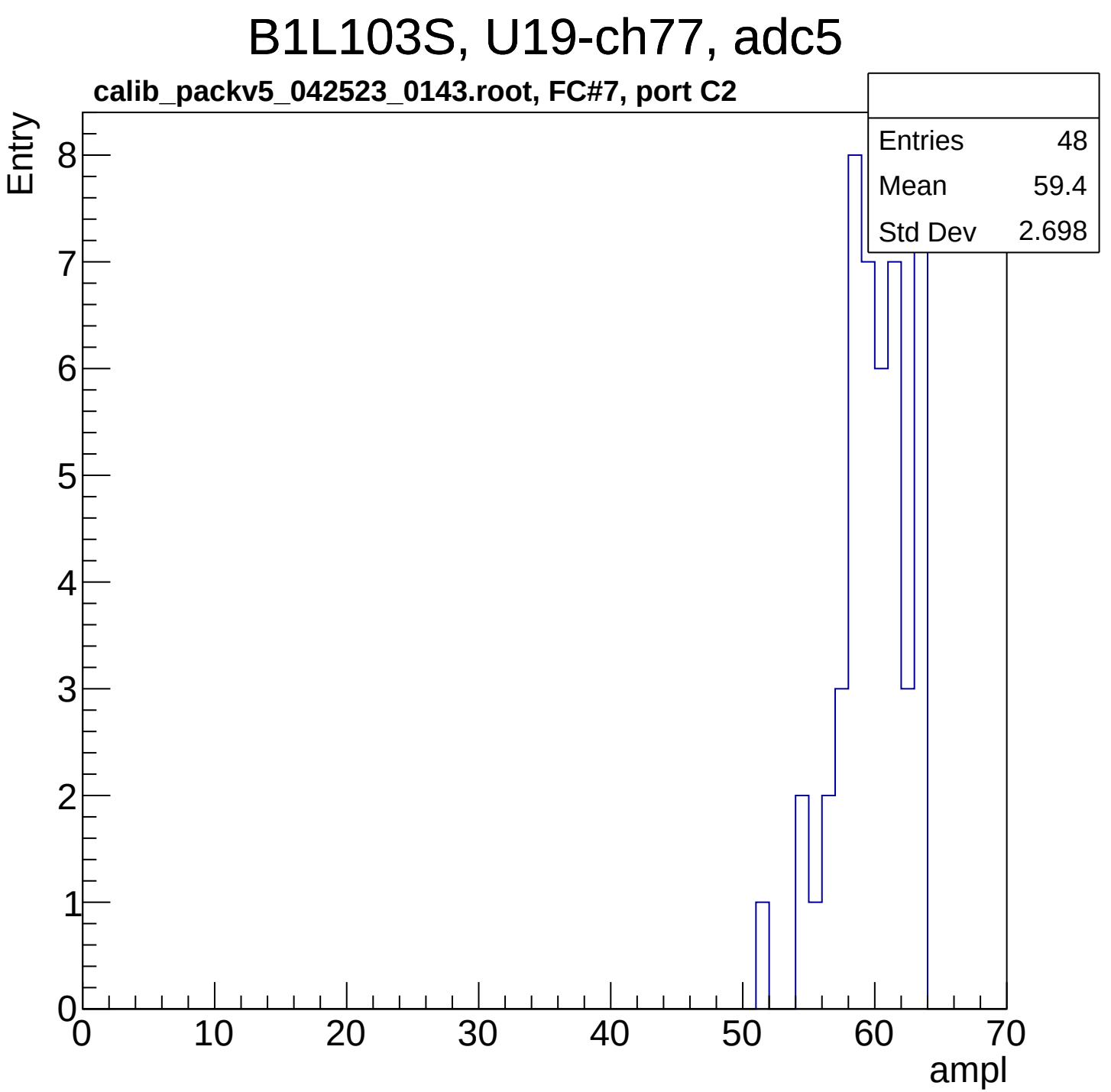
B1L103S, U19-ch77, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.4
Std Dev 2.698
Entry
ampl
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1
2
3
4
5
6
7
8
0
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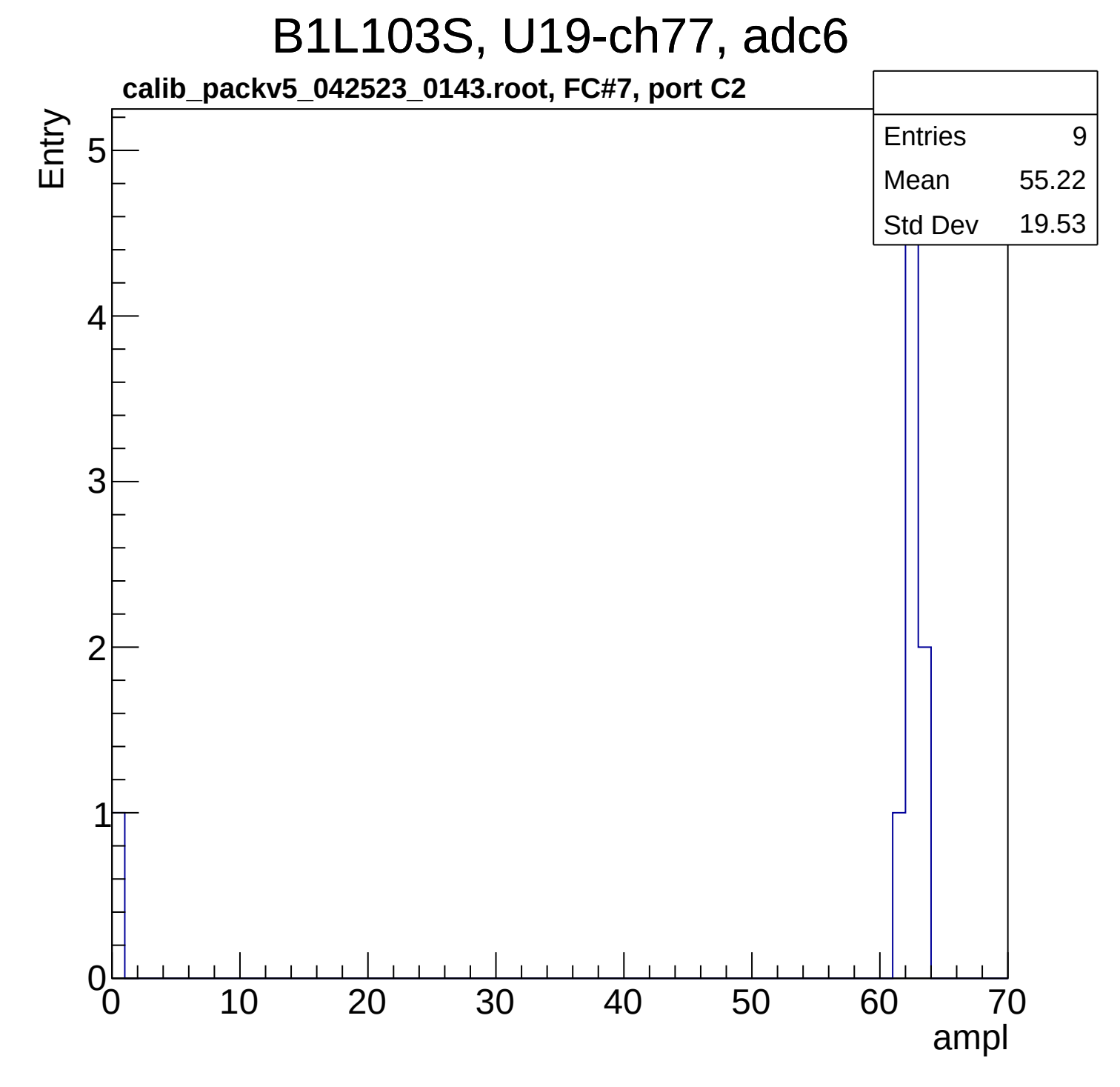
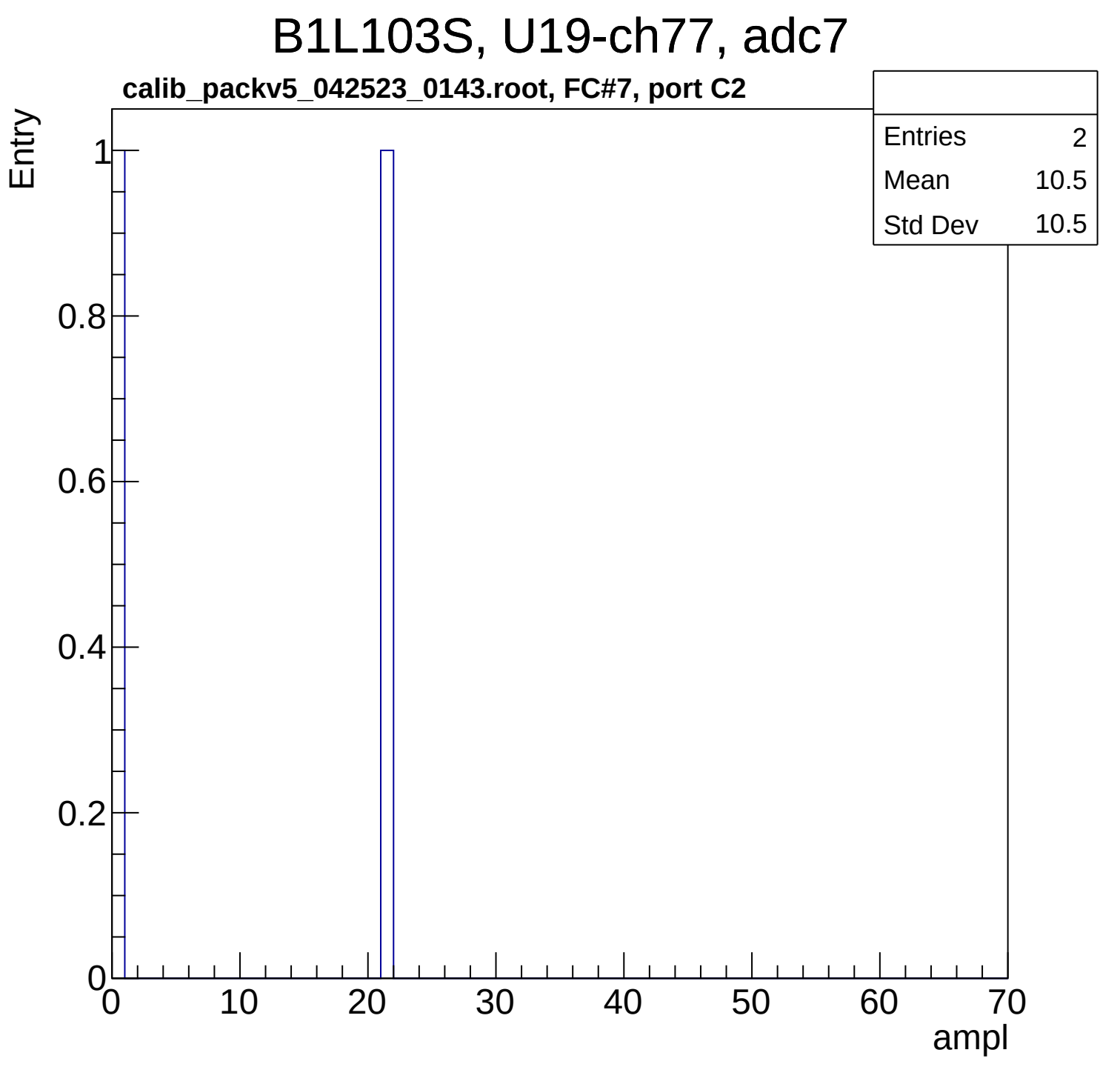
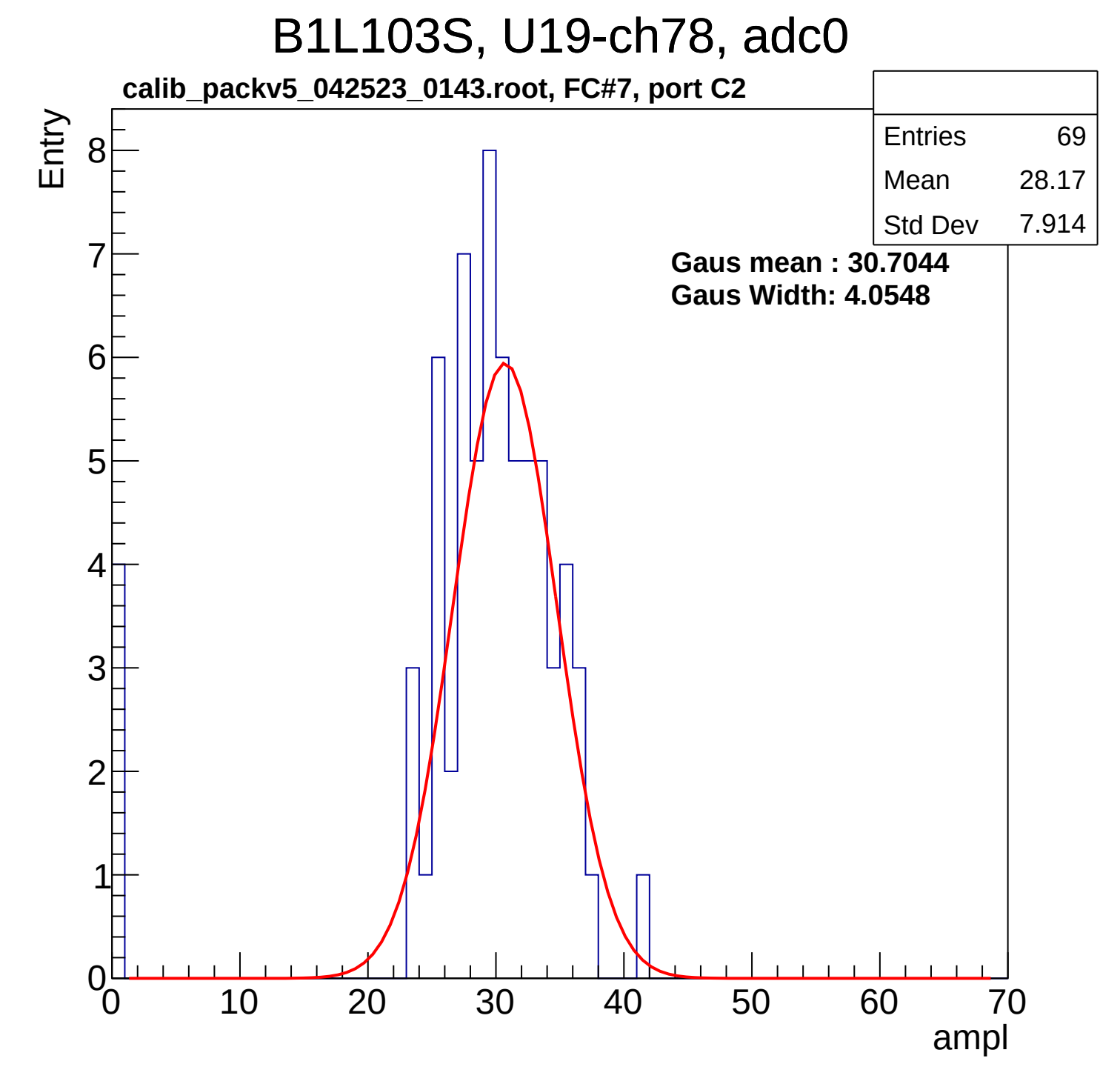

B1L103S, U19-ch77, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 55.22
Std Dev 19.53
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch77, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch78, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.17
Std Dev 7.914
Gaus mean : 30.7044
Gaus Width: 4.0548
Entry
ampl
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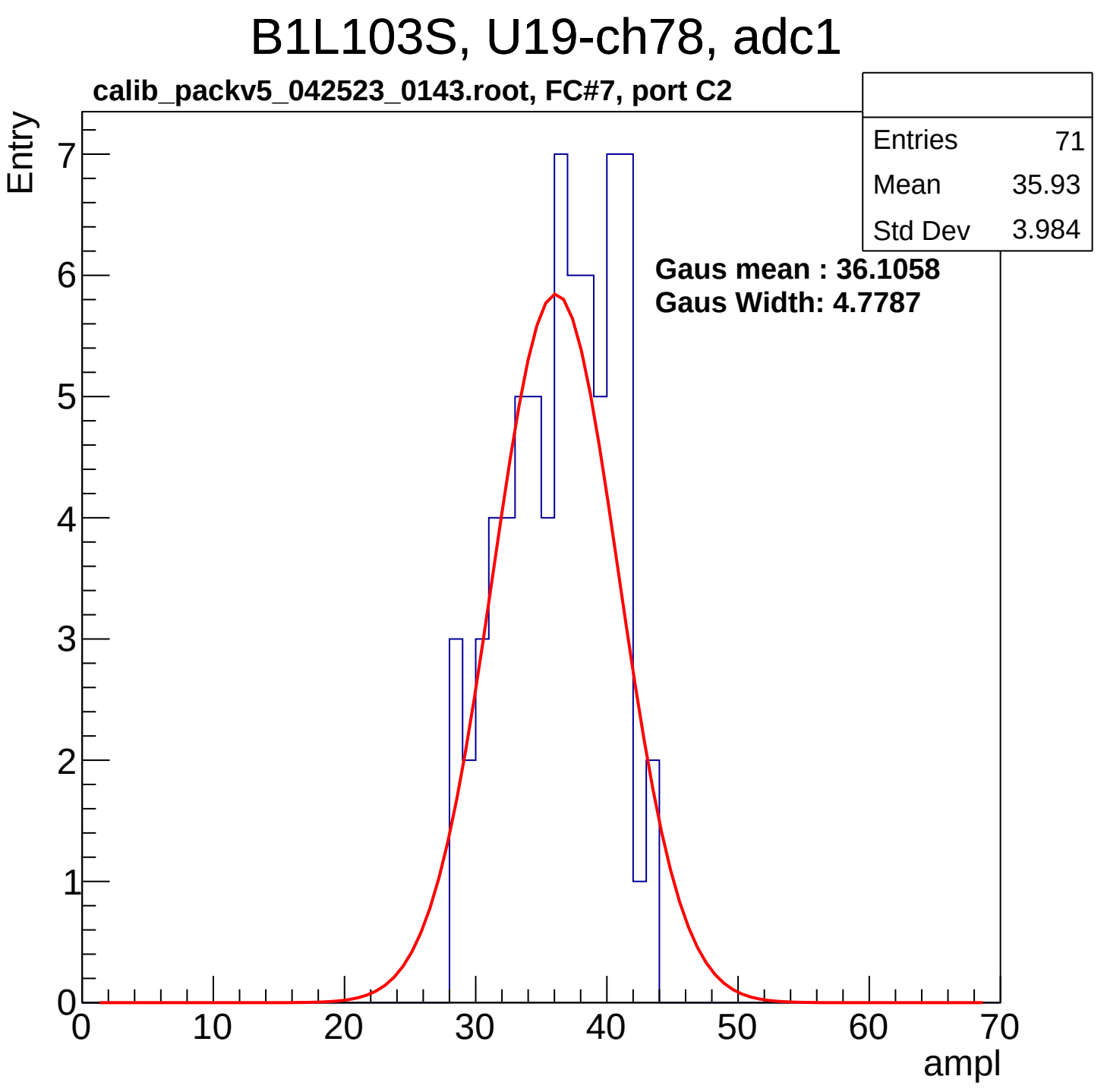
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch78, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 35.93
Std Dev 3.984
Gaus mean : 36.1058
Gaus Width: 4.7787
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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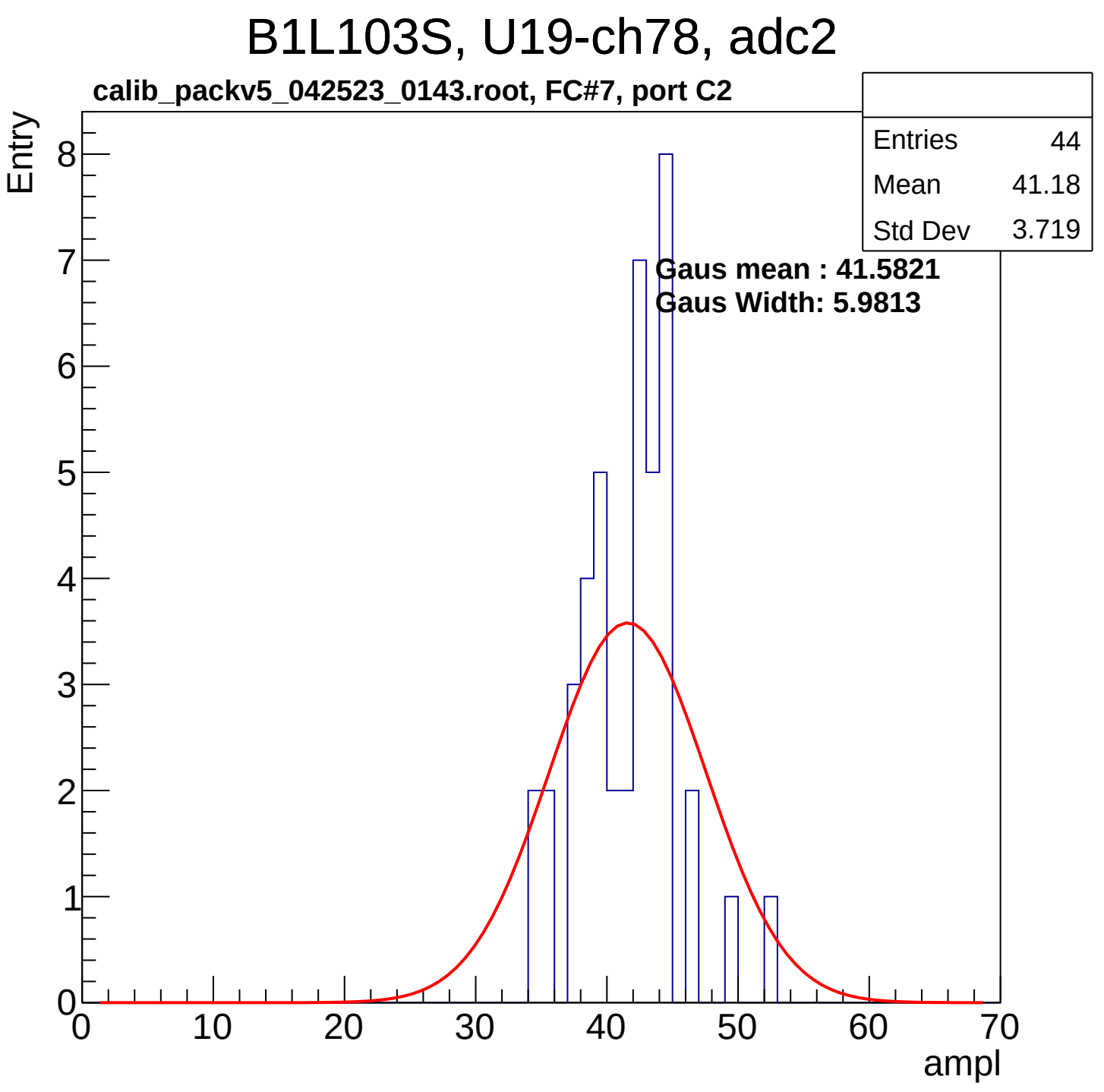
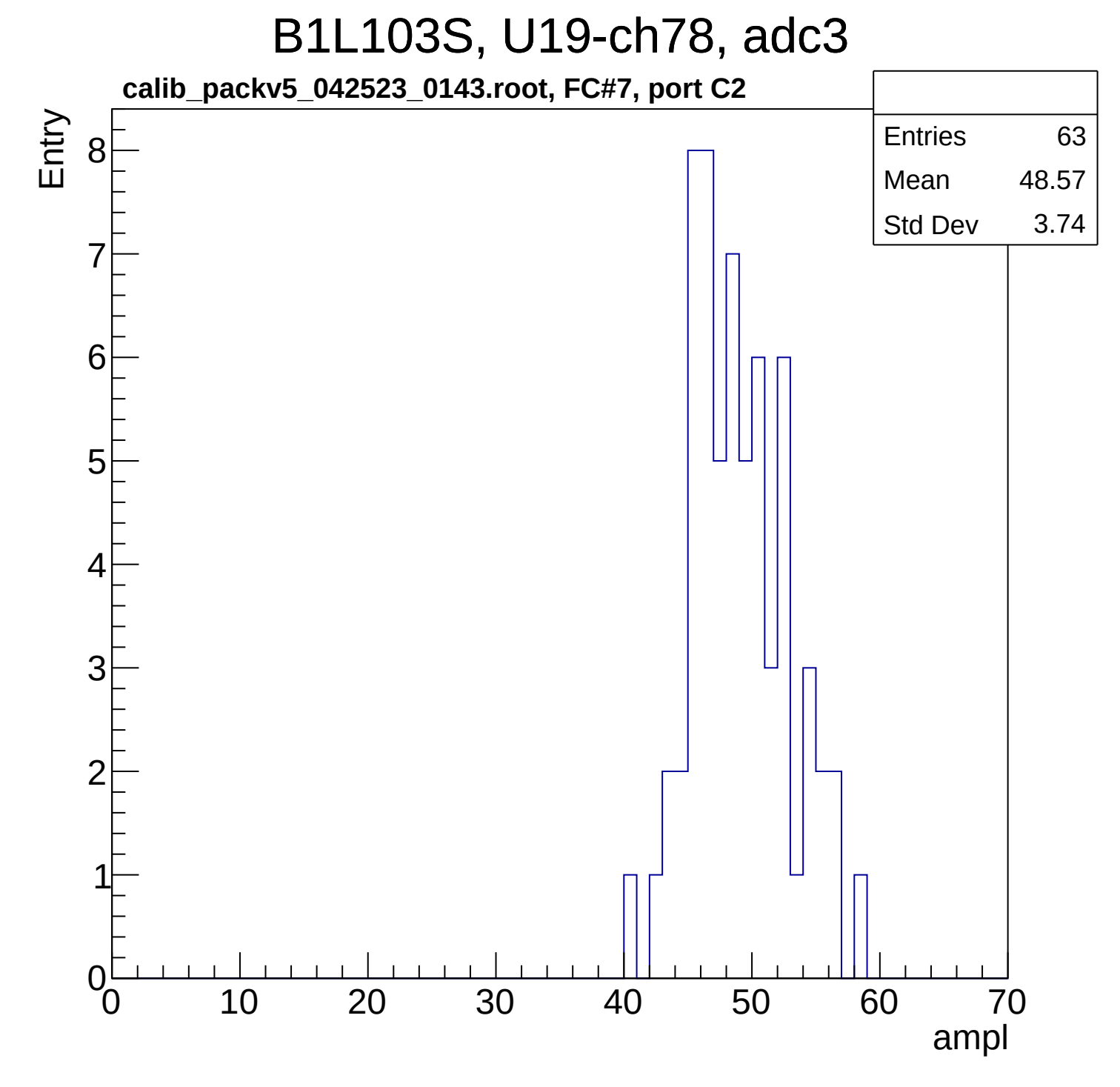

B1L103S, U19-ch78, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 41.18
Std Dev 3.719
Gaus mean : 41.5821
Gaus Width: 5.9813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch78, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.57
Std Dev 3.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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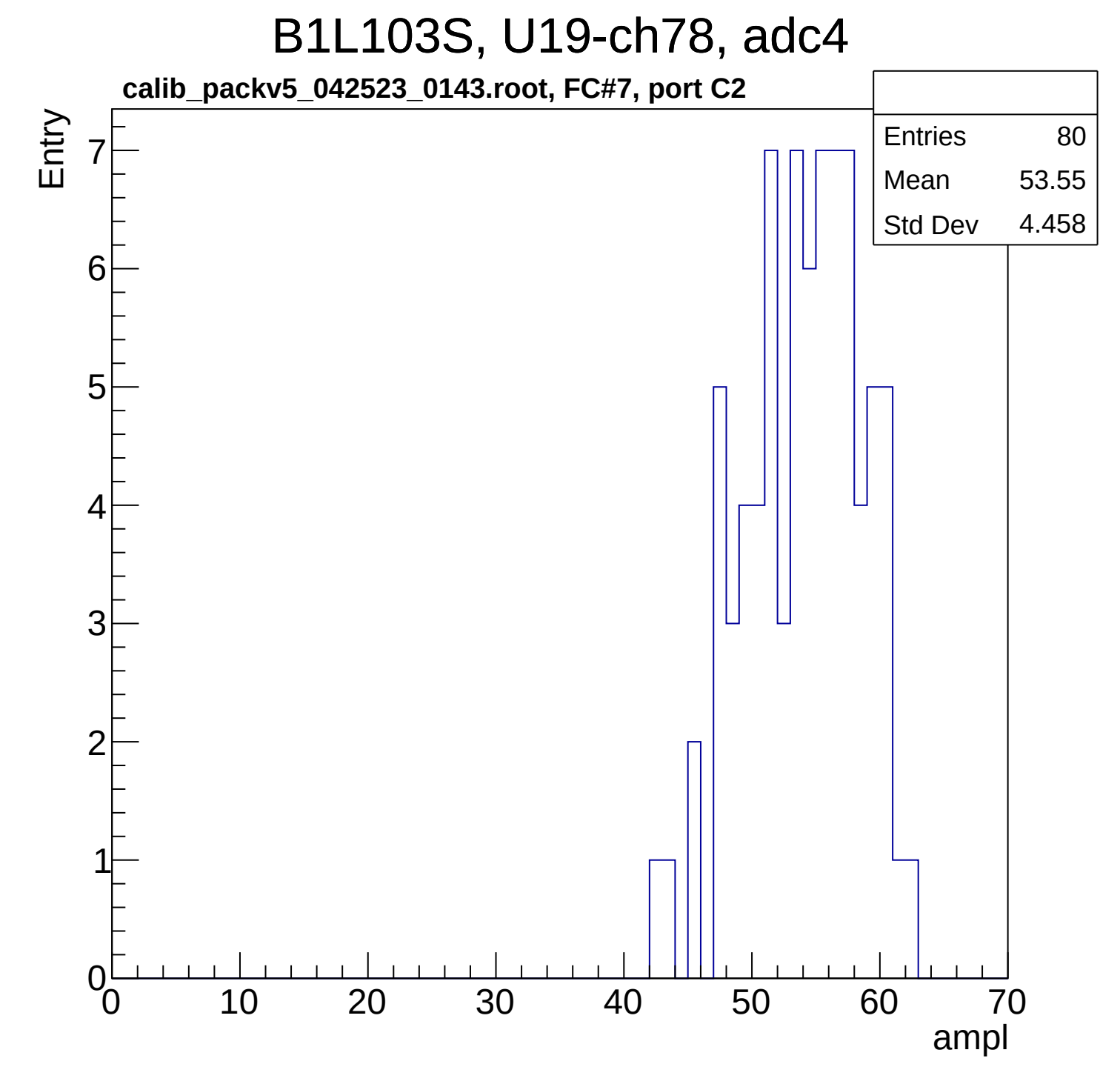
B1L103S, U19-ch78, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 53.55
Std Dev 4.458
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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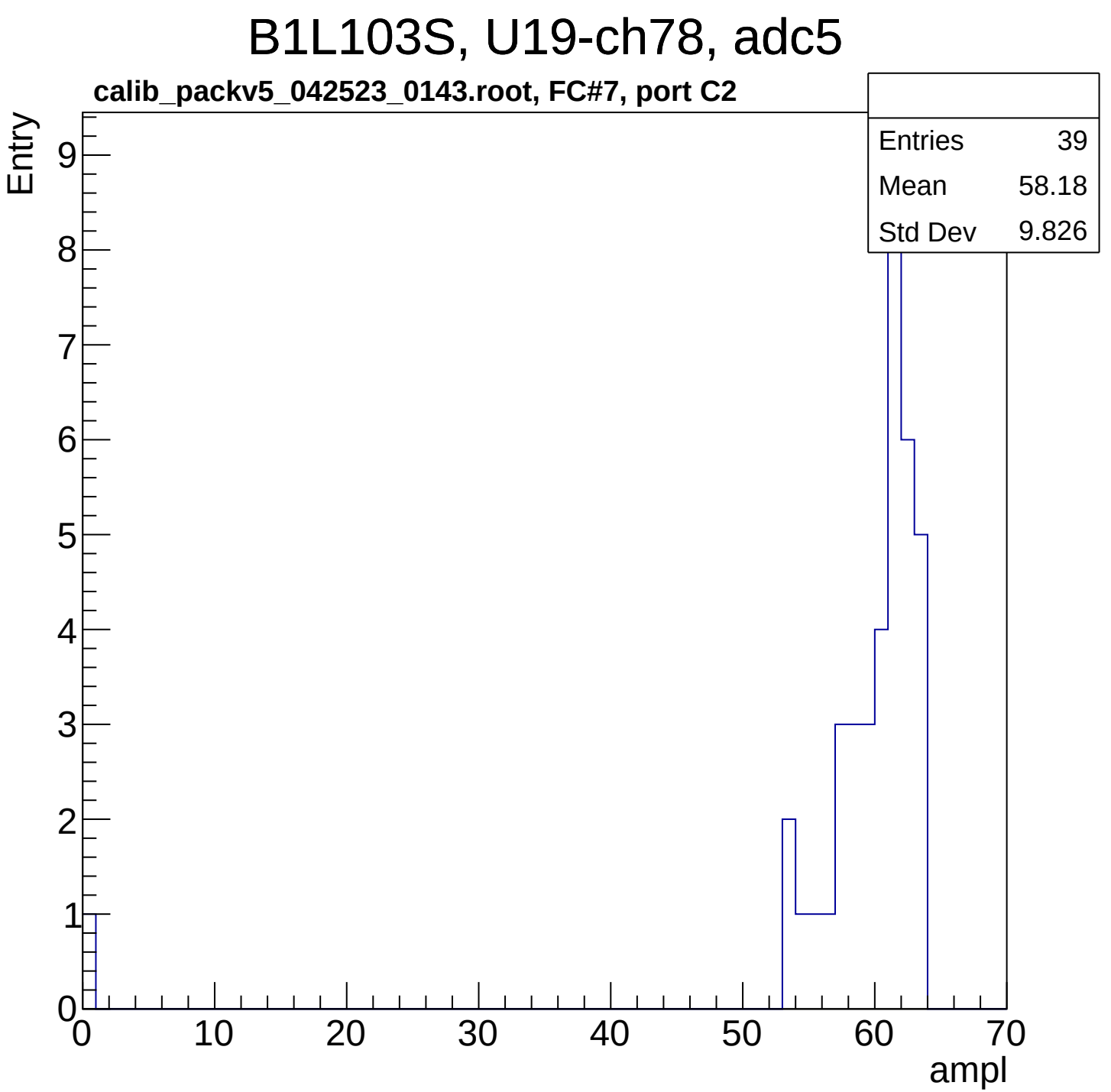
B1L103S, U19-ch78, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.18
Std Dev 9.826
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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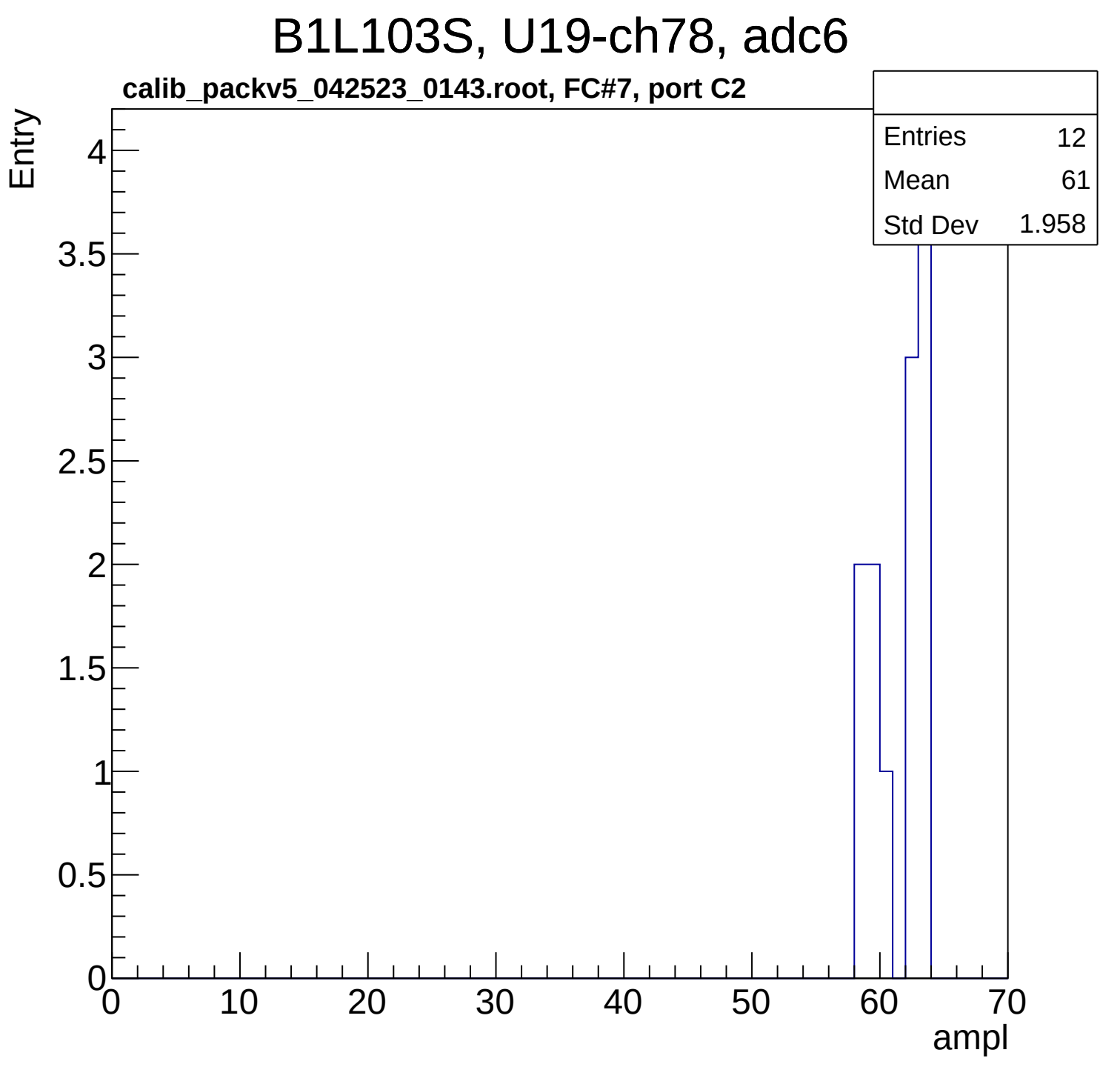
60
70


B1L103S, U19-ch78, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61
Std Dev 1.958
Entry
ampl
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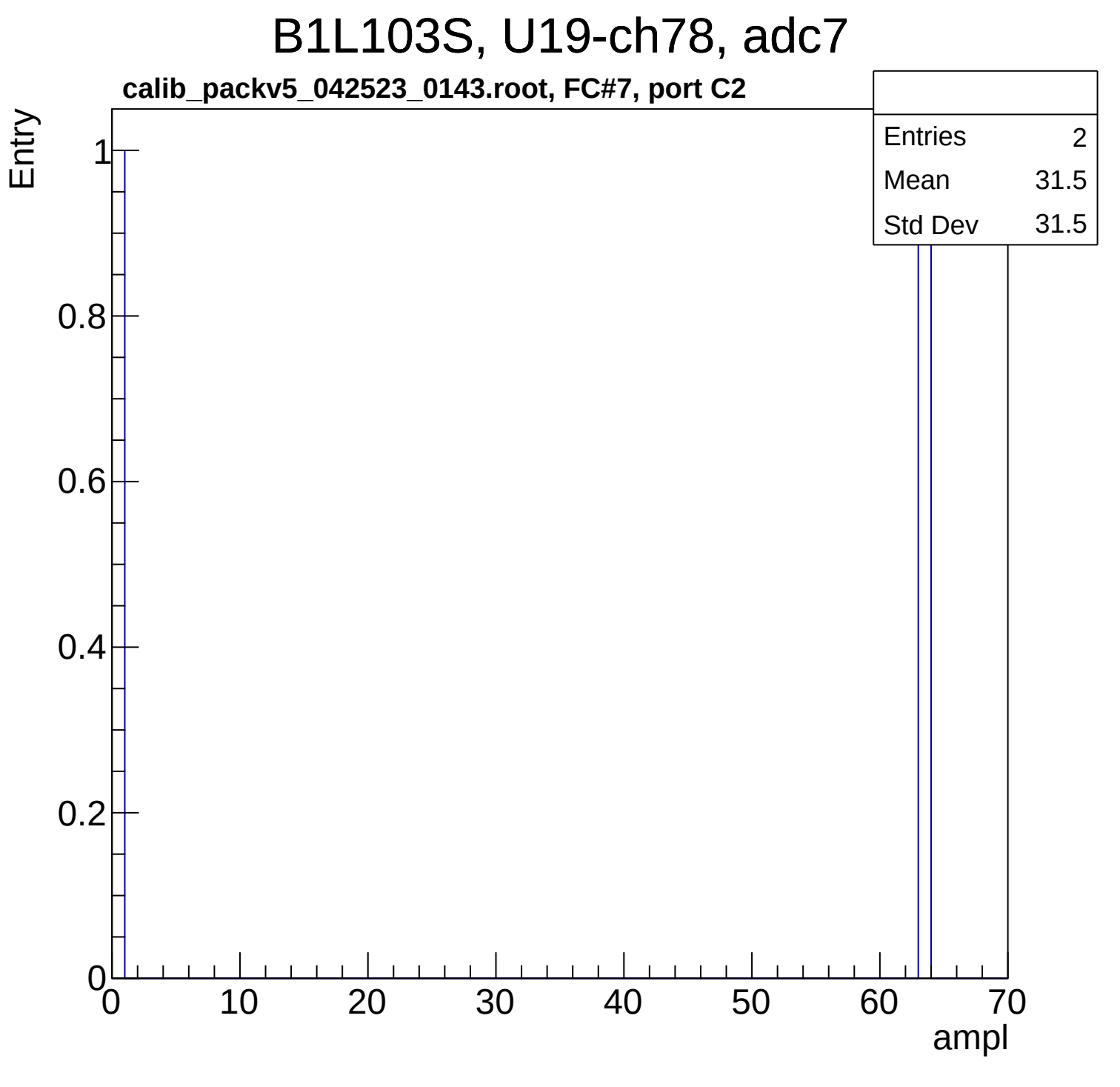
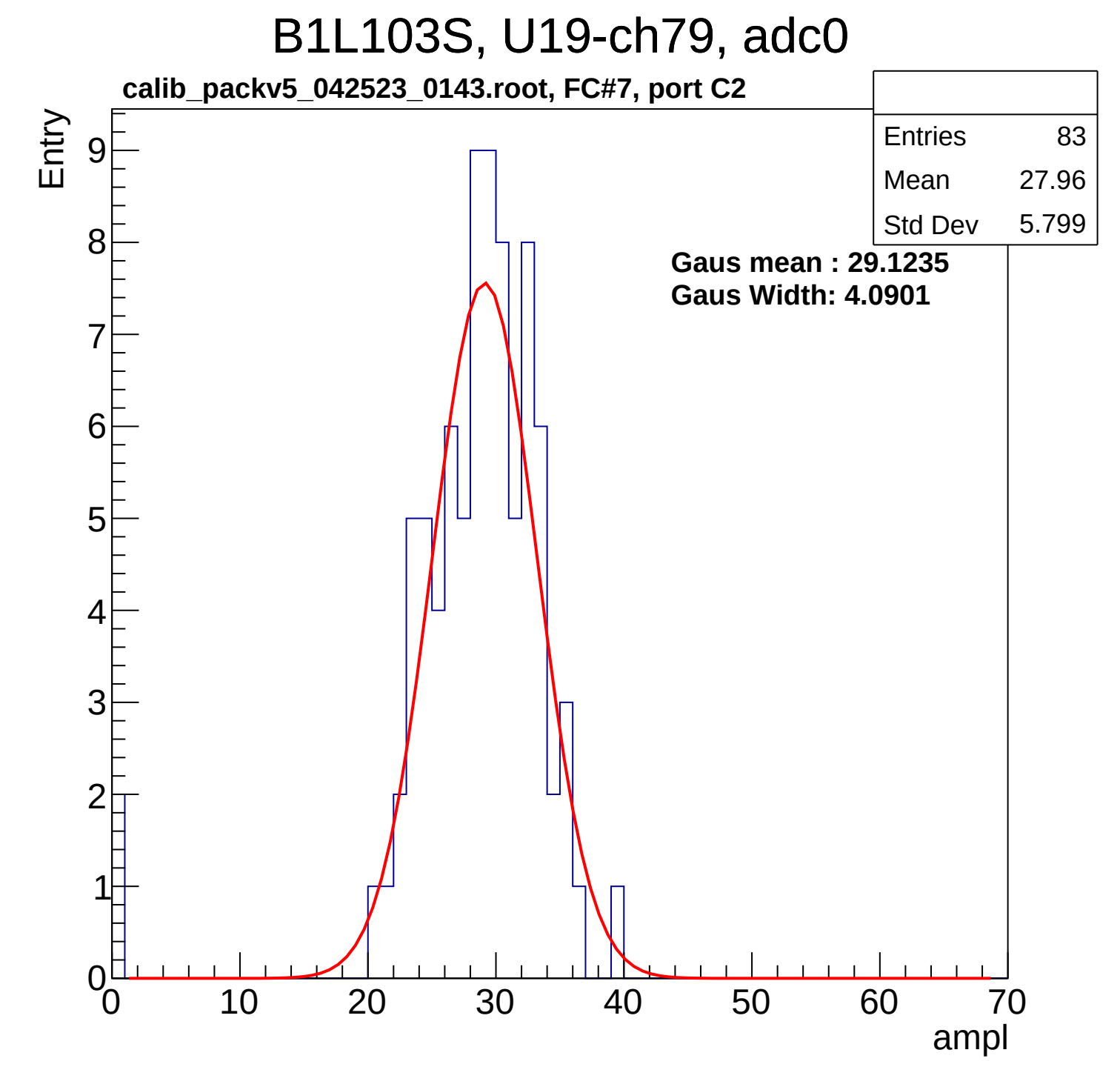
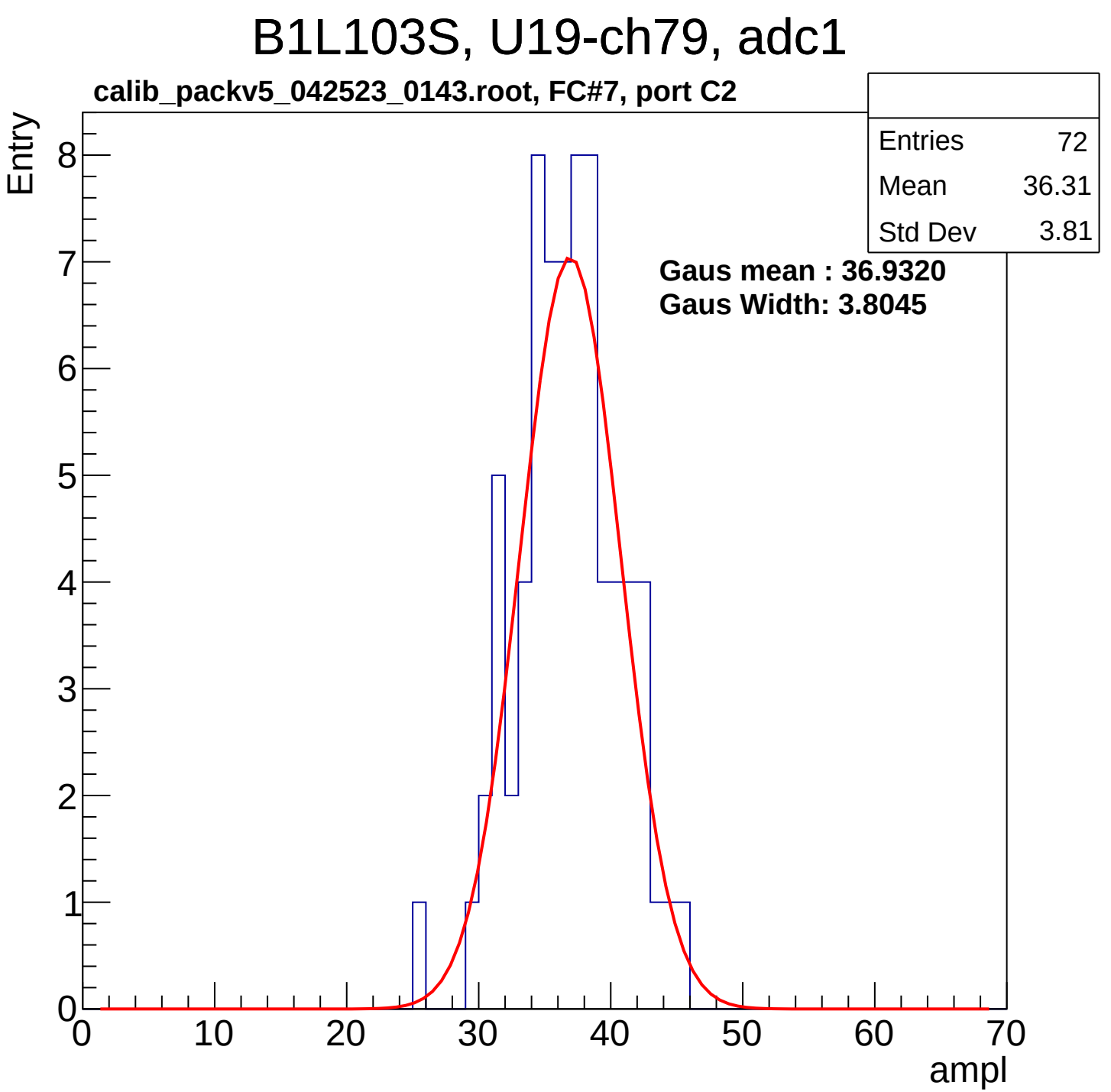
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U19-ch78, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch79, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 27.96
Std Dev 5.799
Gaus mean : 29.1235
Gaus Width: 4.0901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch79, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.31
Std Dev 3.81
Gaus mean : 36.9320
Gaus Width: 3.8045
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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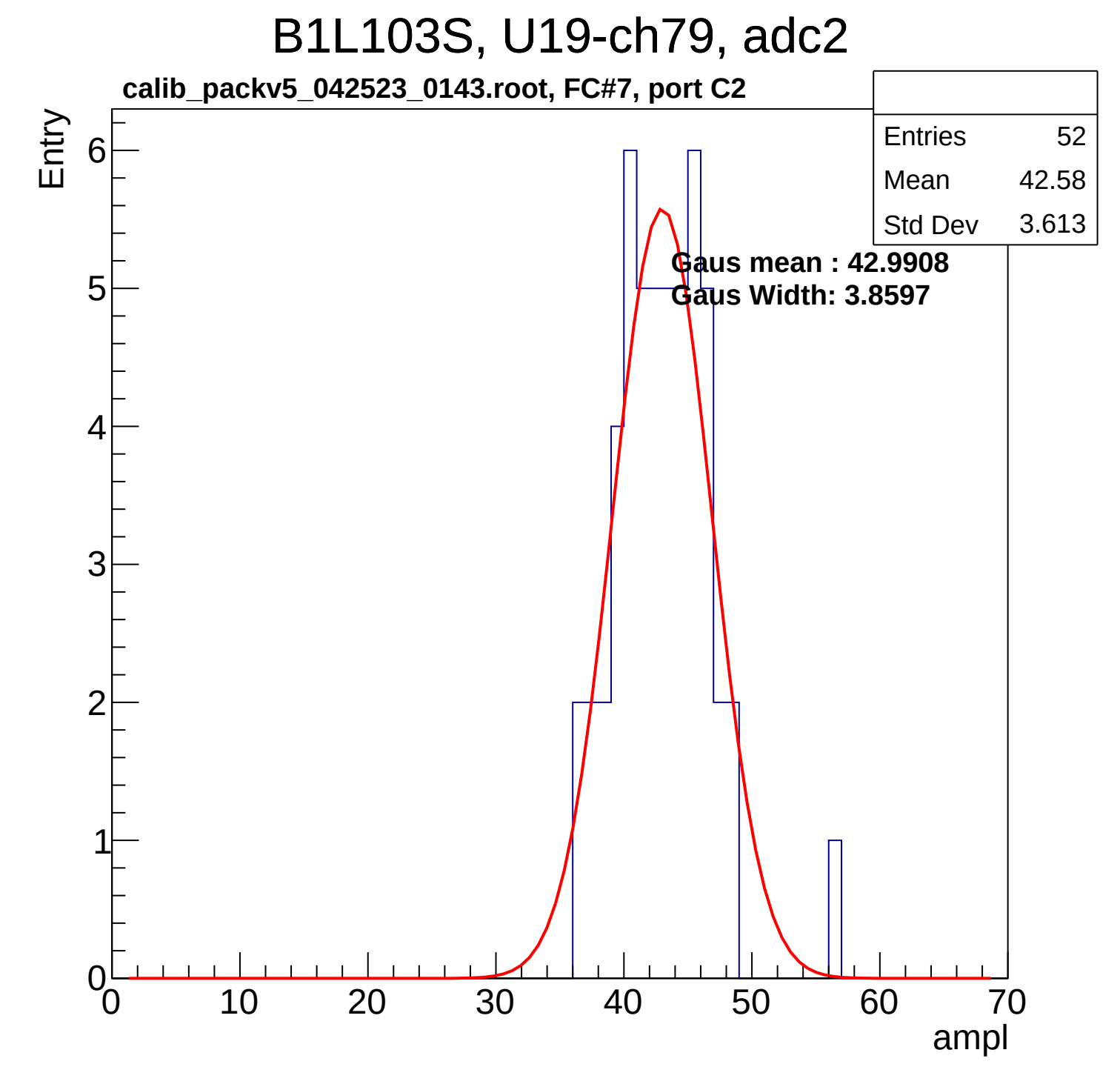
B1L103S, U19-ch79, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 42.58
Std Dev 3.613
Gaus mean : 42.9908
Gaus Width: 3.8597
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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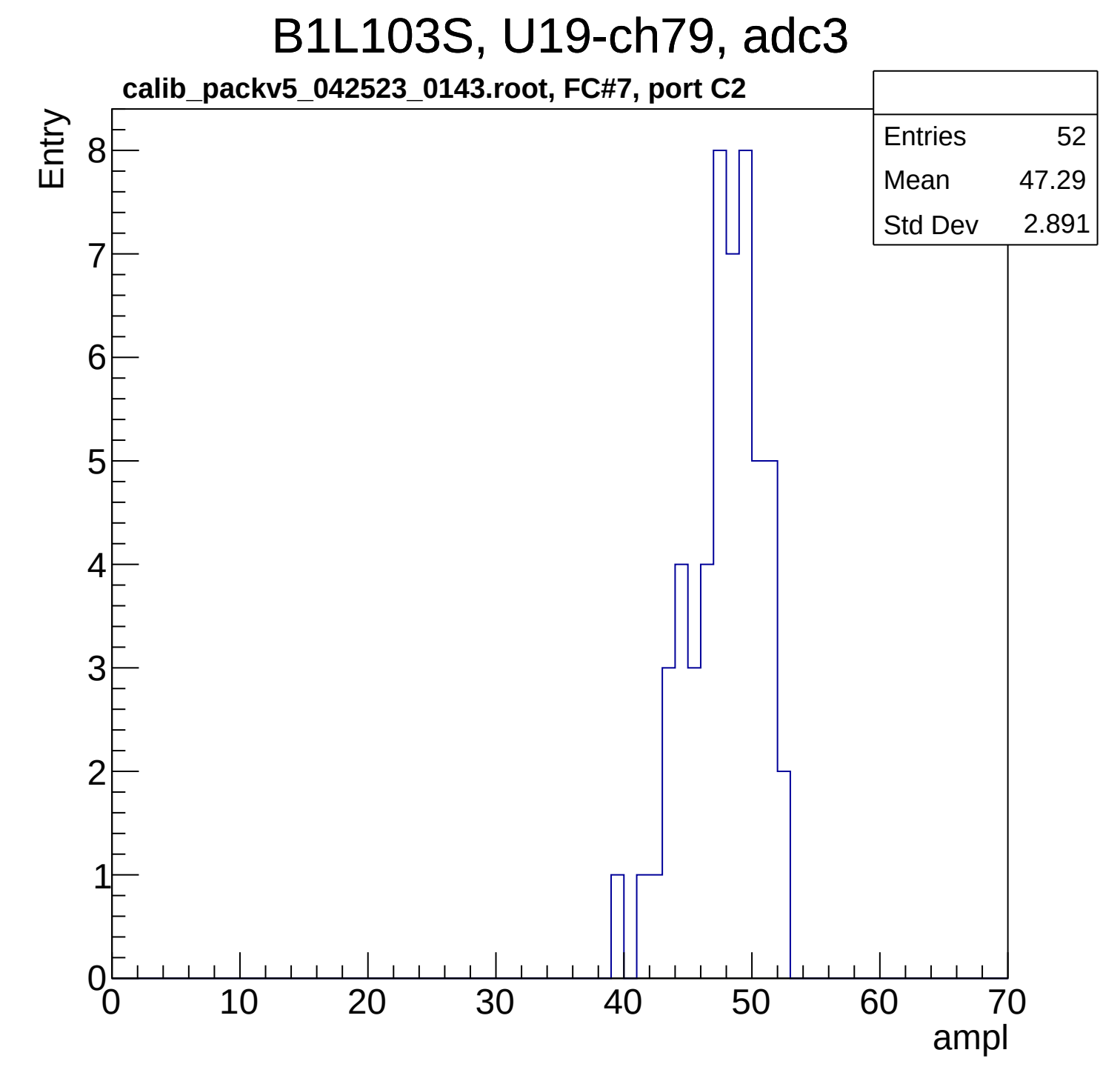
B1L103S, U19-ch79, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 47.29
Std Dev 2.891
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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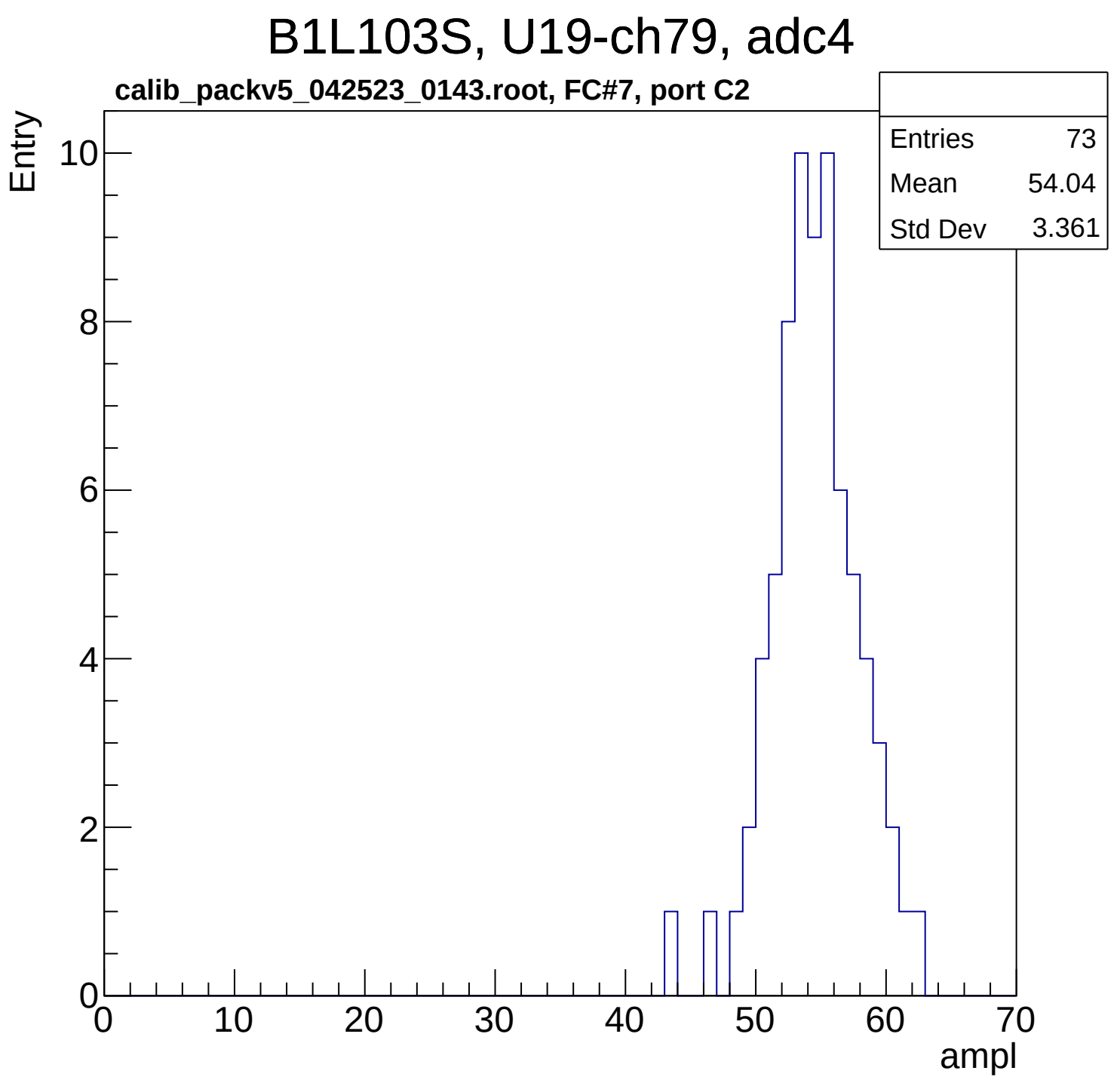
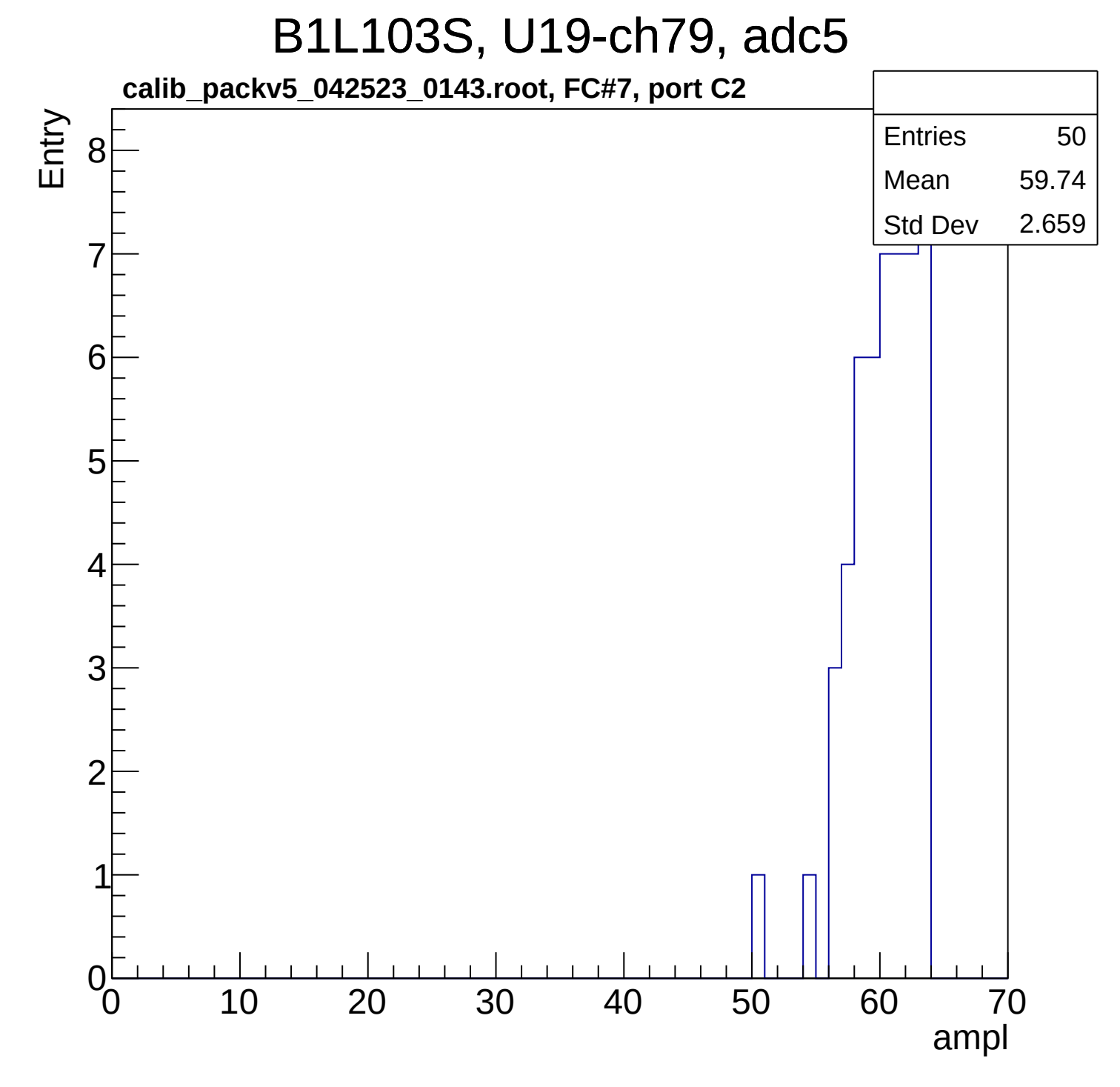

B1L103S, U19-ch79, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 54.04
Std Dev 3.361
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch79, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.74
Std Dev 2.659
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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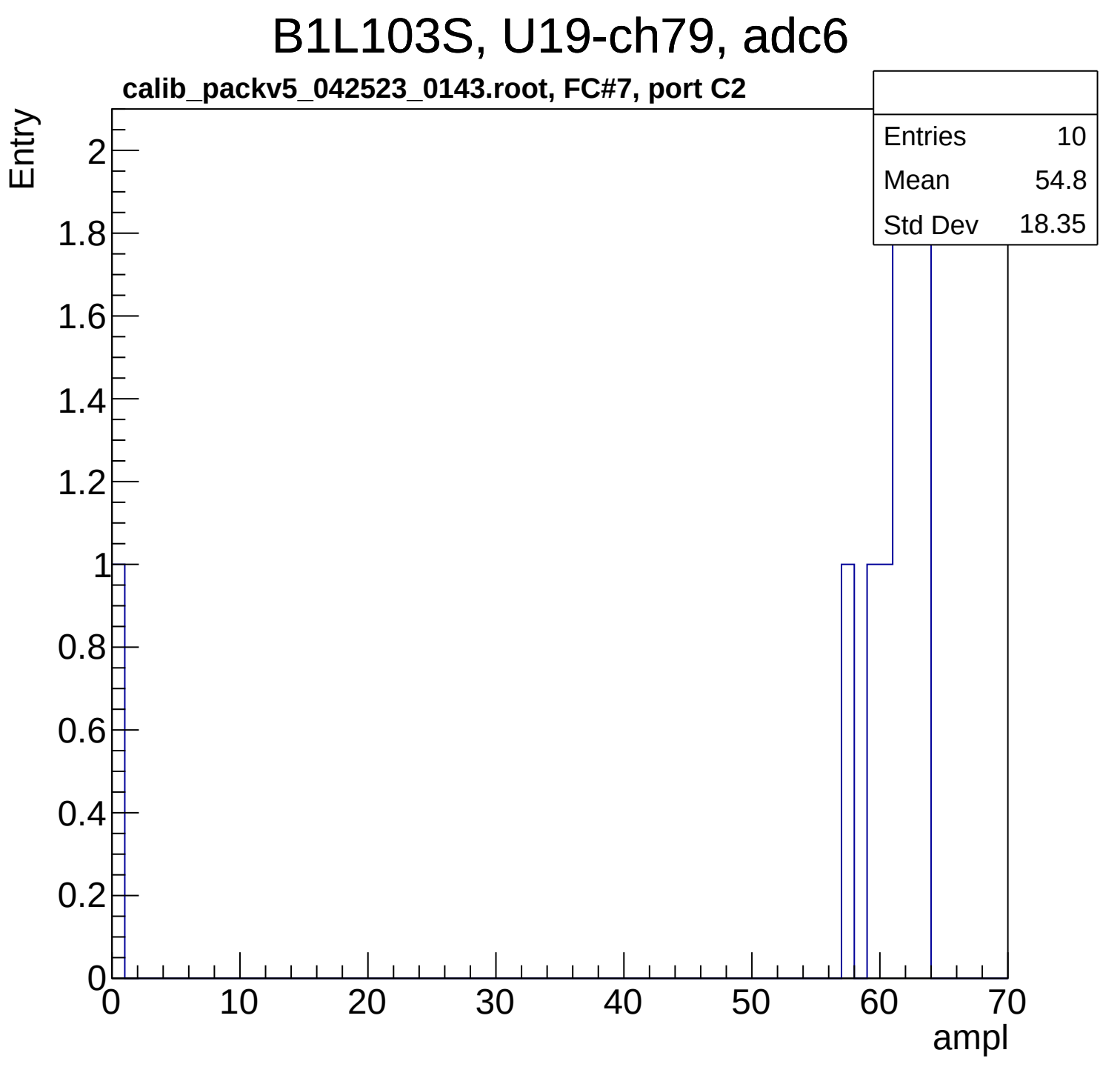
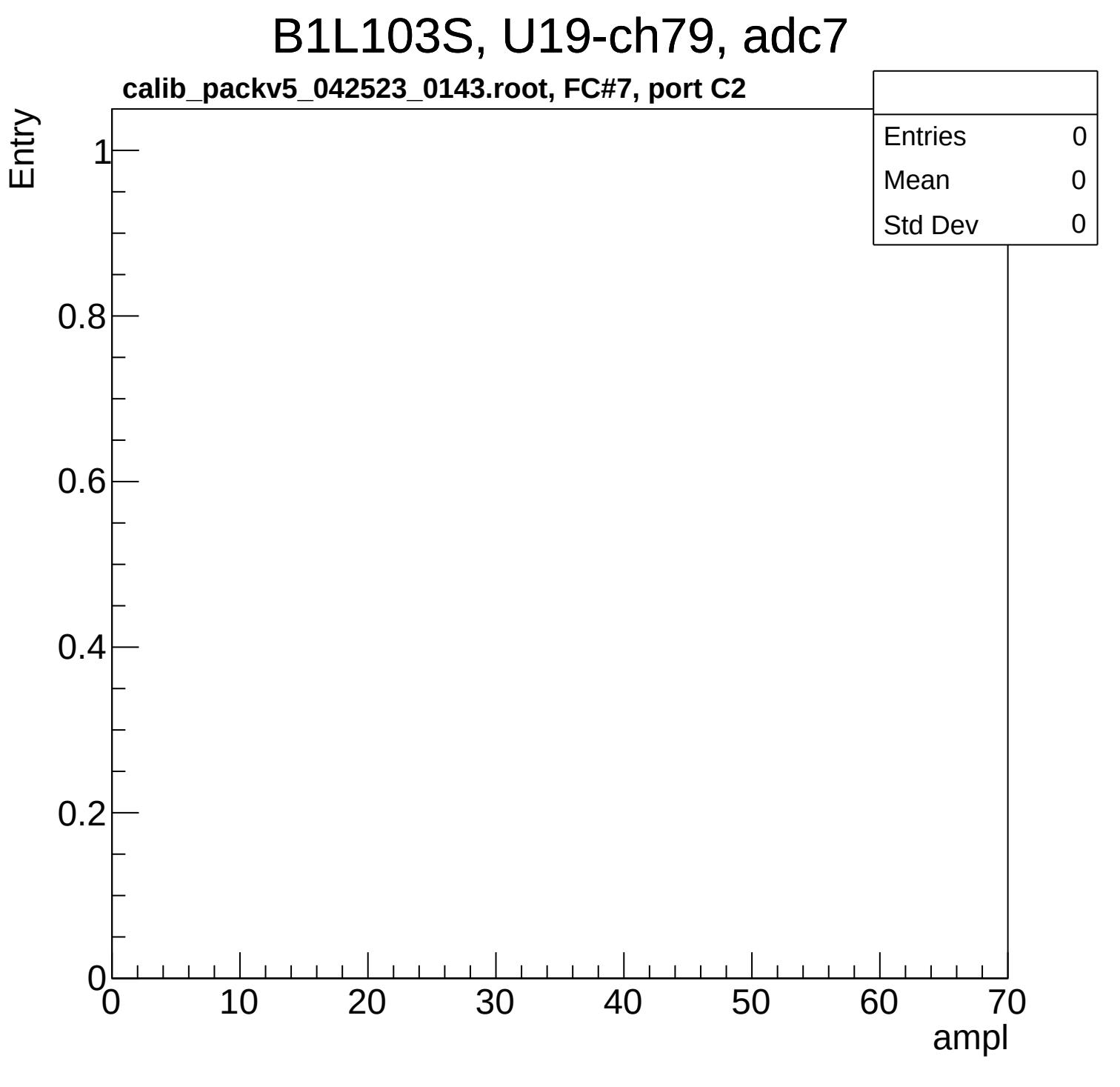
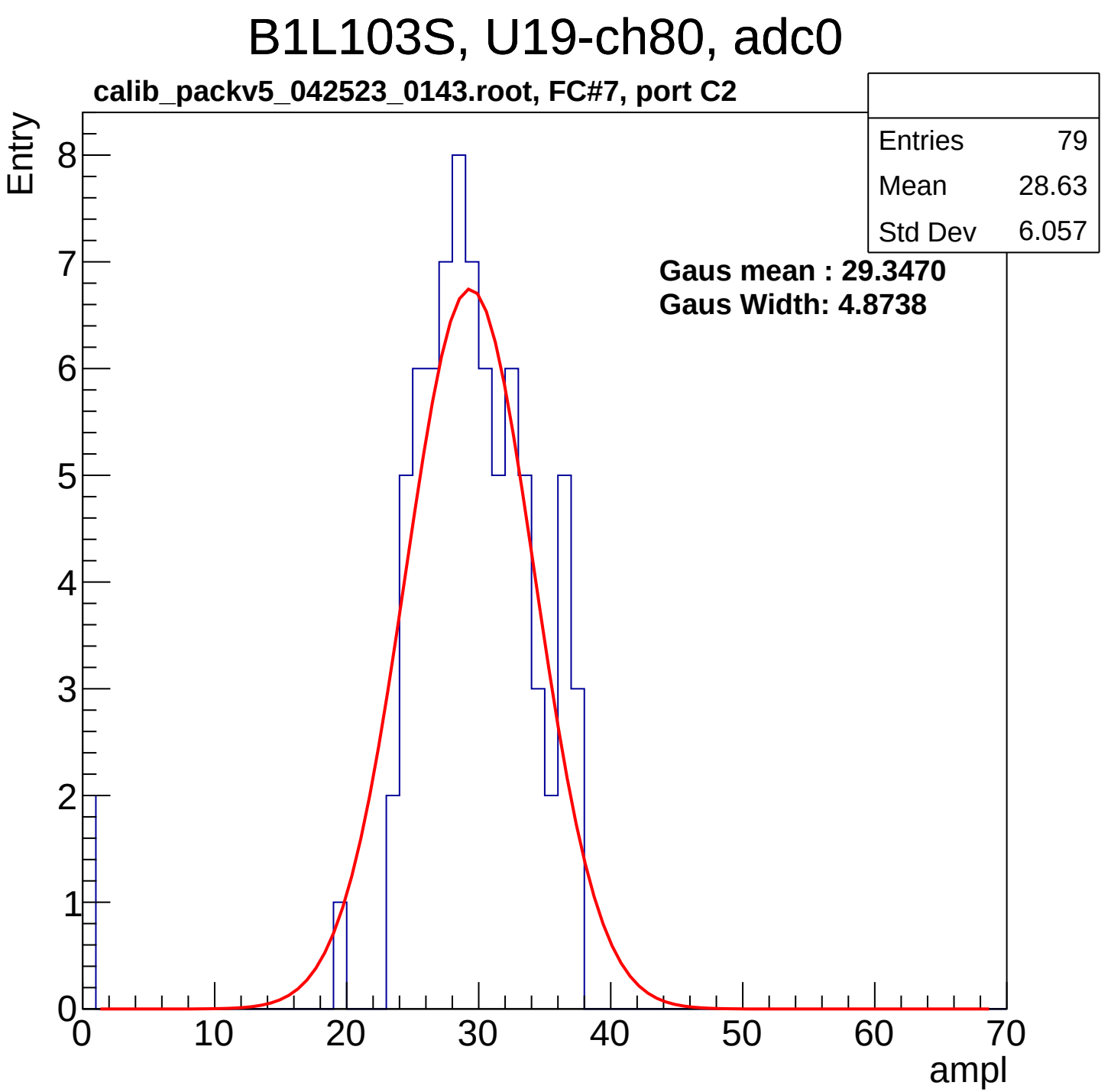
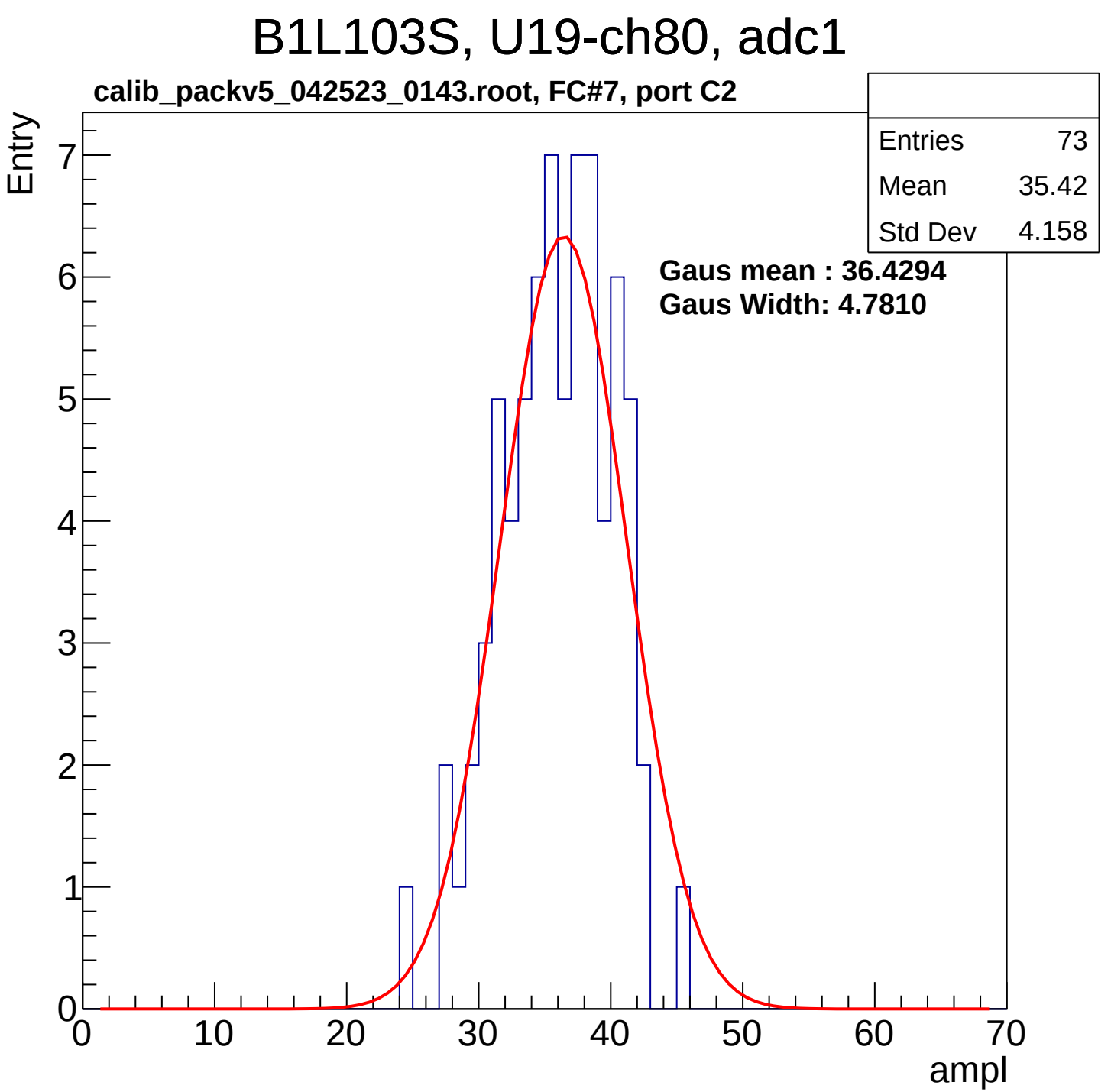
40
50
60
70


B1L103S, U19-ch79, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 54.8
Std Dev 18.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch79, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch80, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 28.63
Std Dev 6.057
Gaus mean : 29.3470
Gaus Width: 4.8738
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch80, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 35.42
Std Dev 4.158
Gaus mean : 36.4294
Gaus Width: 4.7810
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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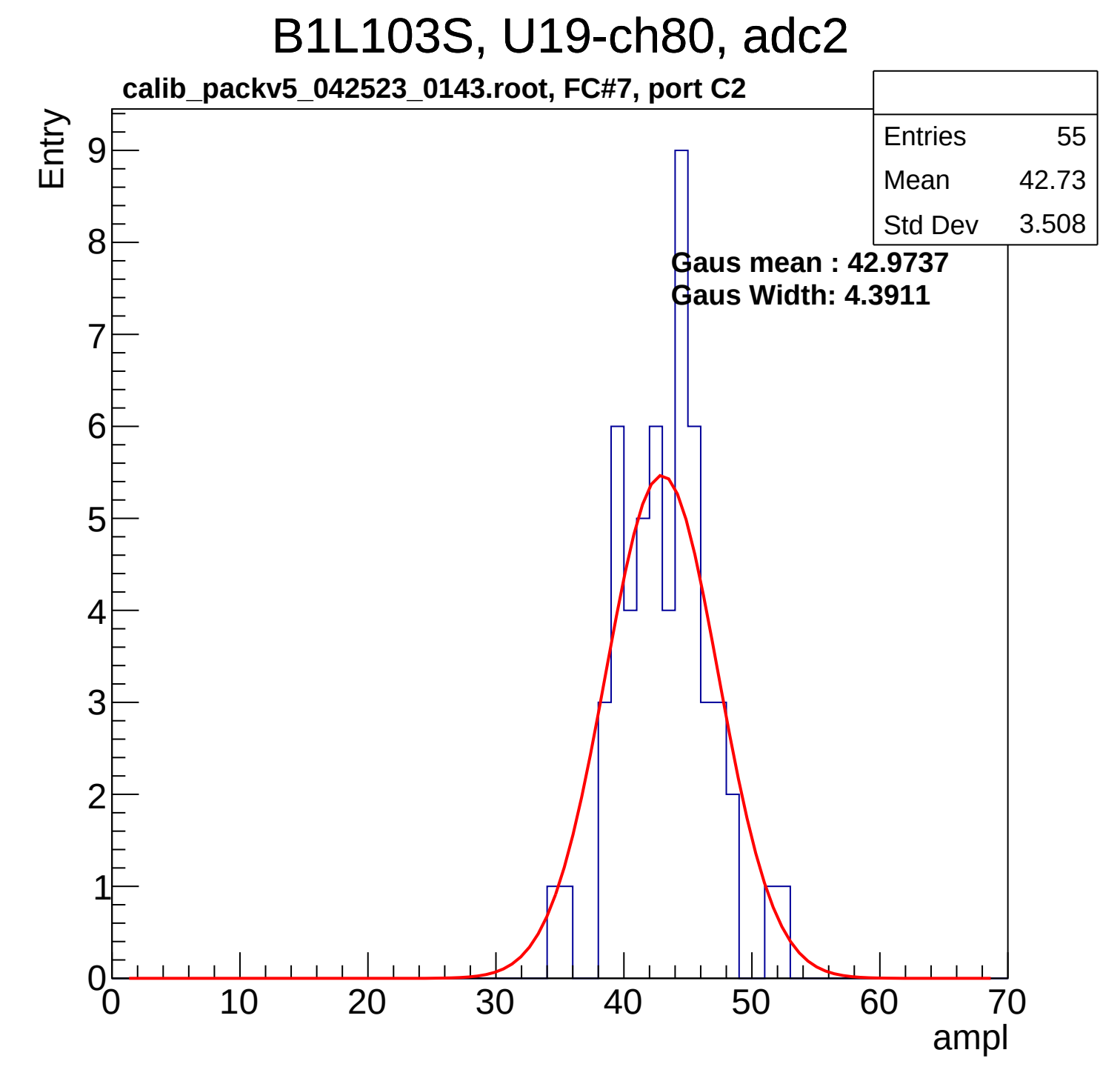
B1L103S, U19-ch80, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 42.73
Std Dev 3.508
Gaus mean : 42.9737
Gaus Width: 4.3911
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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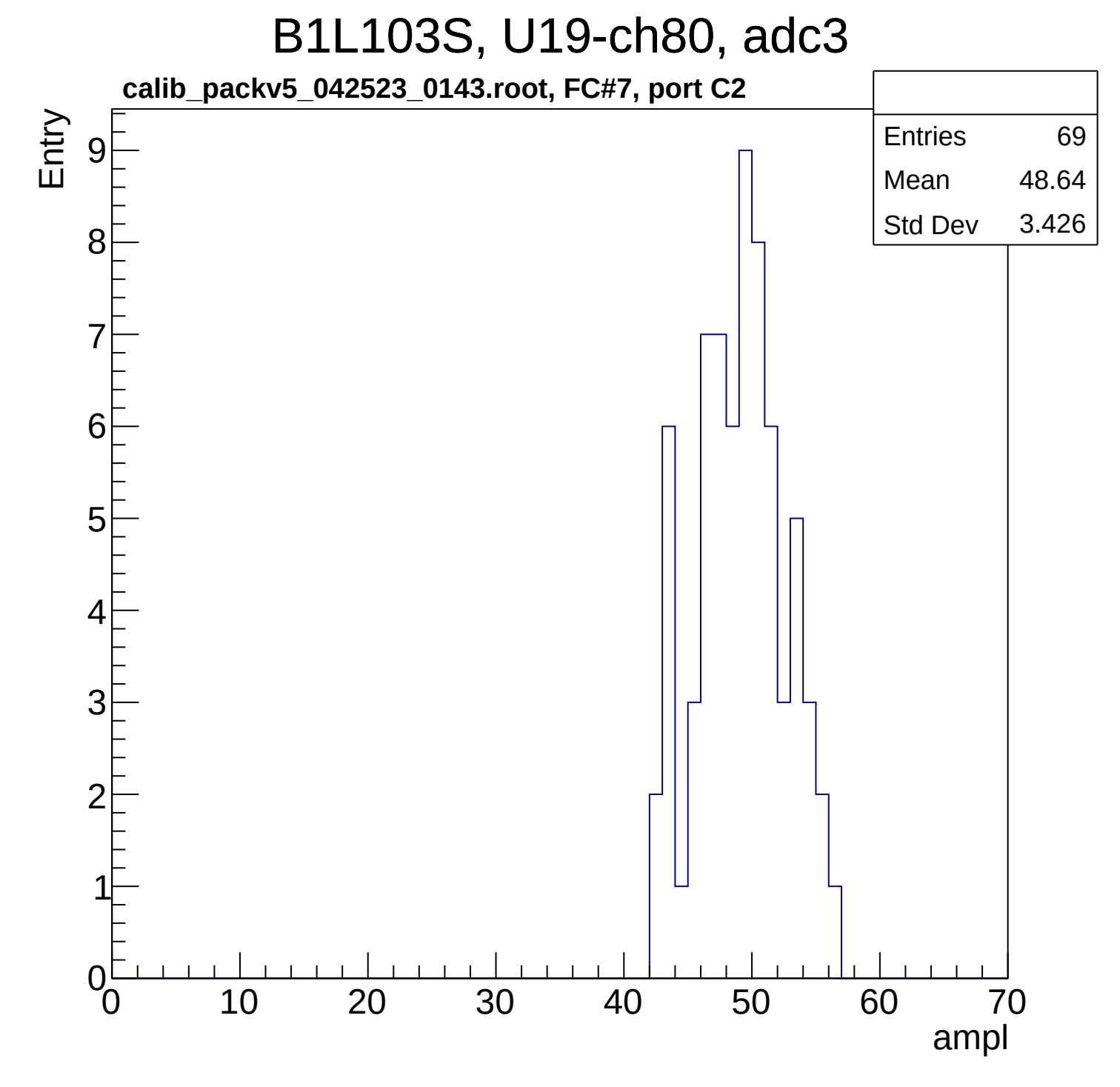
B1L103S, U19-ch80, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 48.64
Std Dev 3.426
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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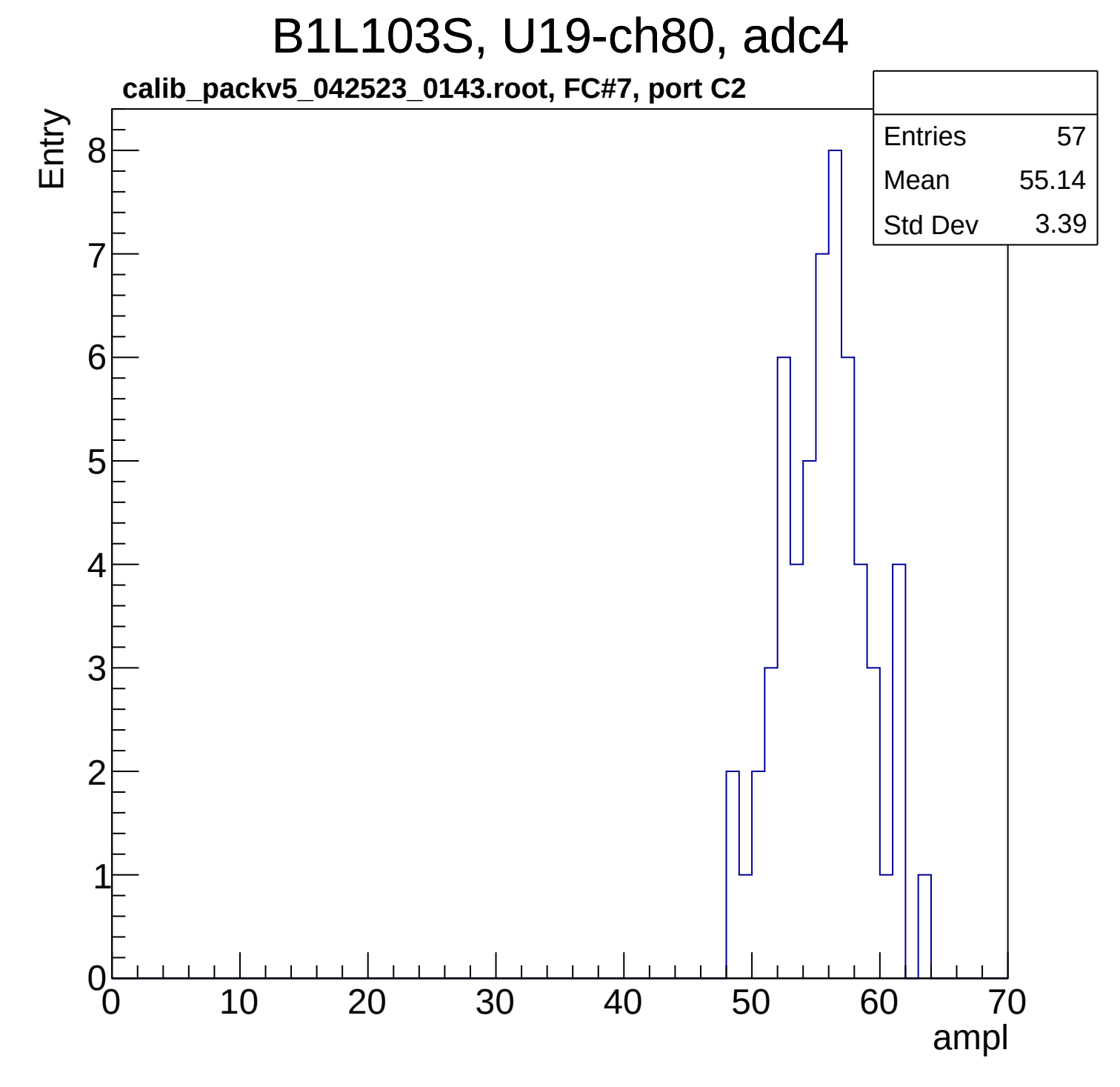
B1L103S, U19-ch80, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.14
Std Dev 3.39
Entry
ampl
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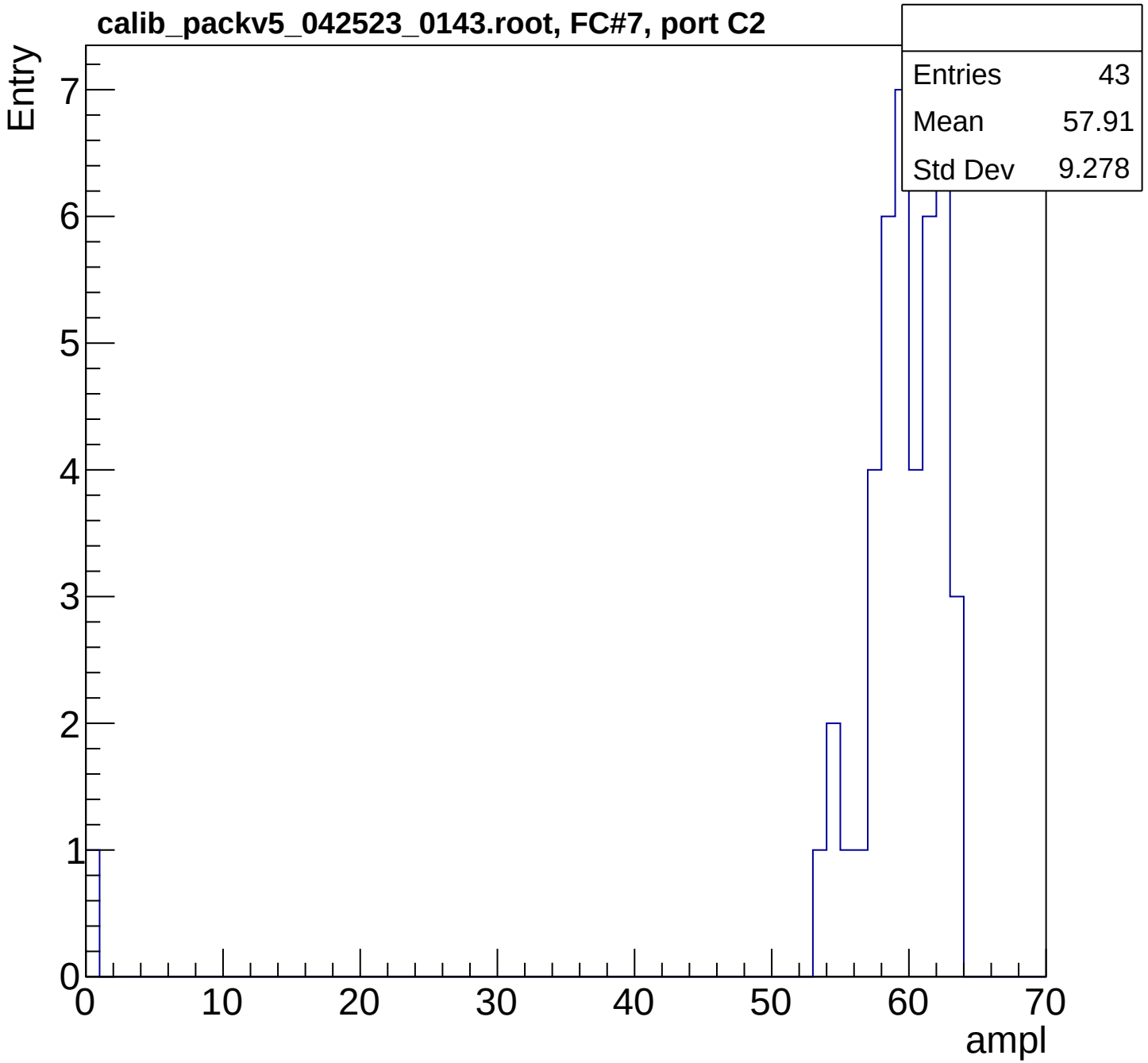
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch80, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 57.91
Std Dev 9.278
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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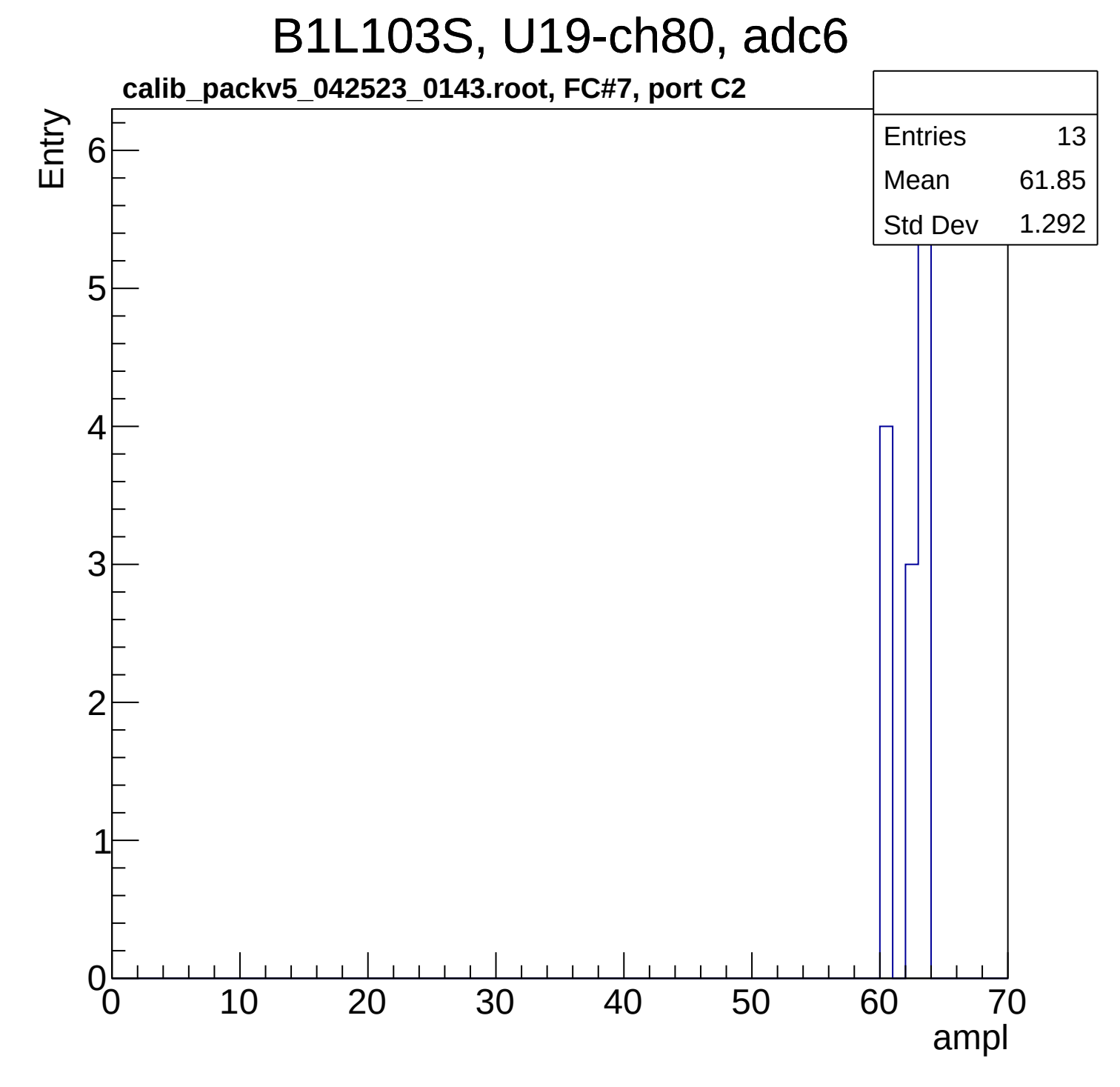
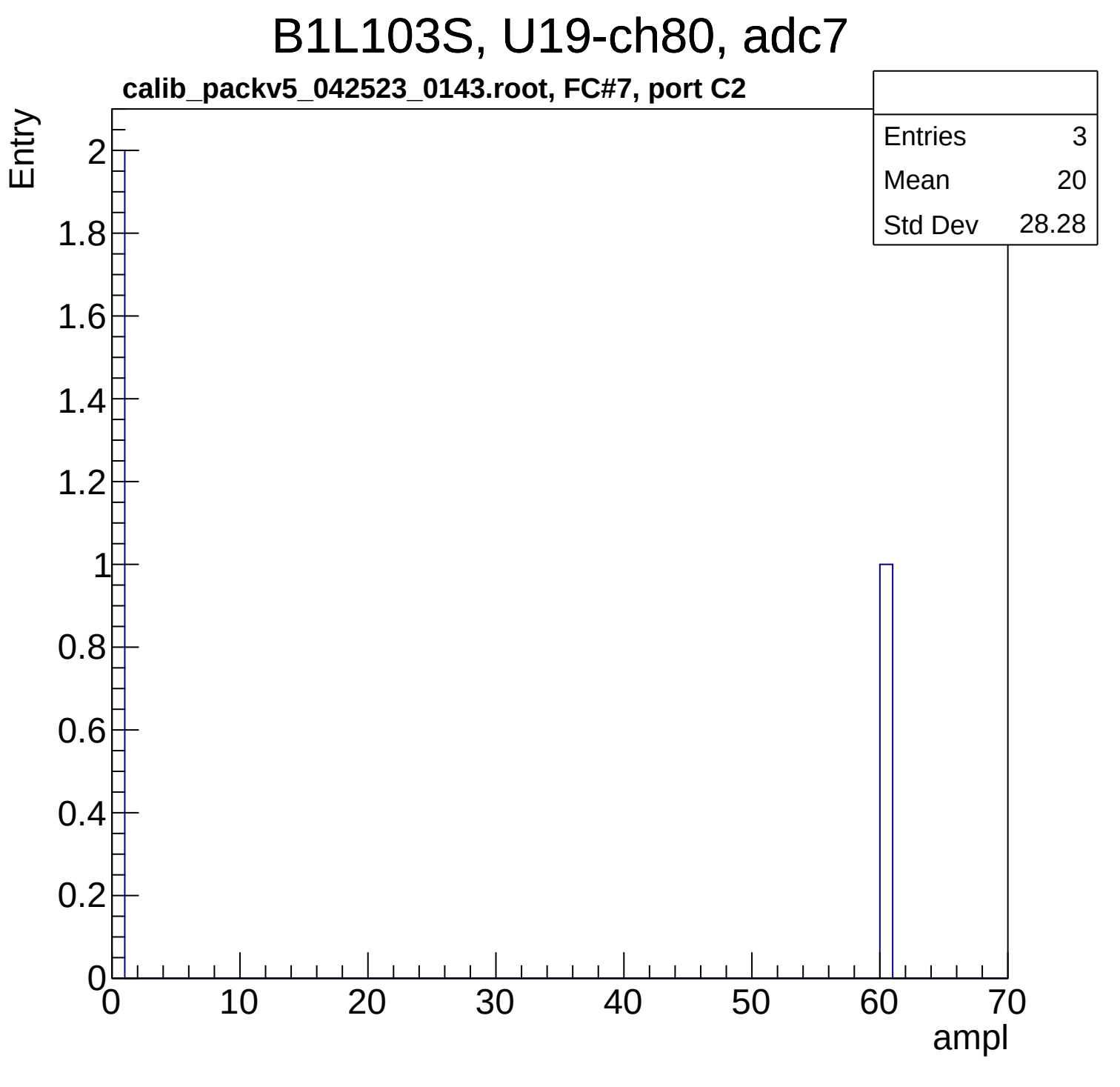

B1L103S, U19-ch80, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.85
Std Dev 1.292
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch80, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20
Std Dev 28.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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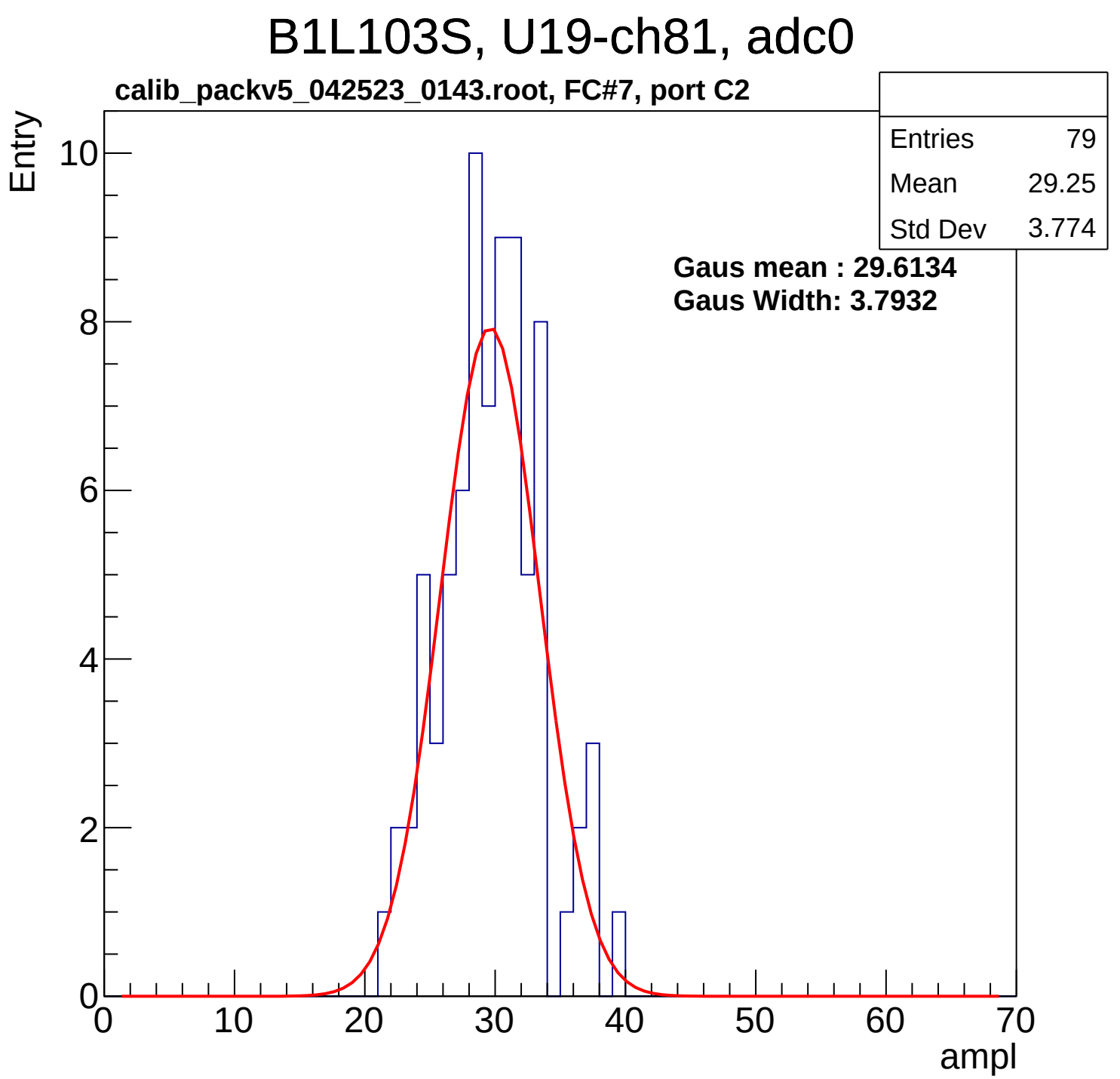
B1L103S, U19-ch81, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.25
Std Dev 3.774
Gaus mean : 29.6134
Gaus Width: 3.7932
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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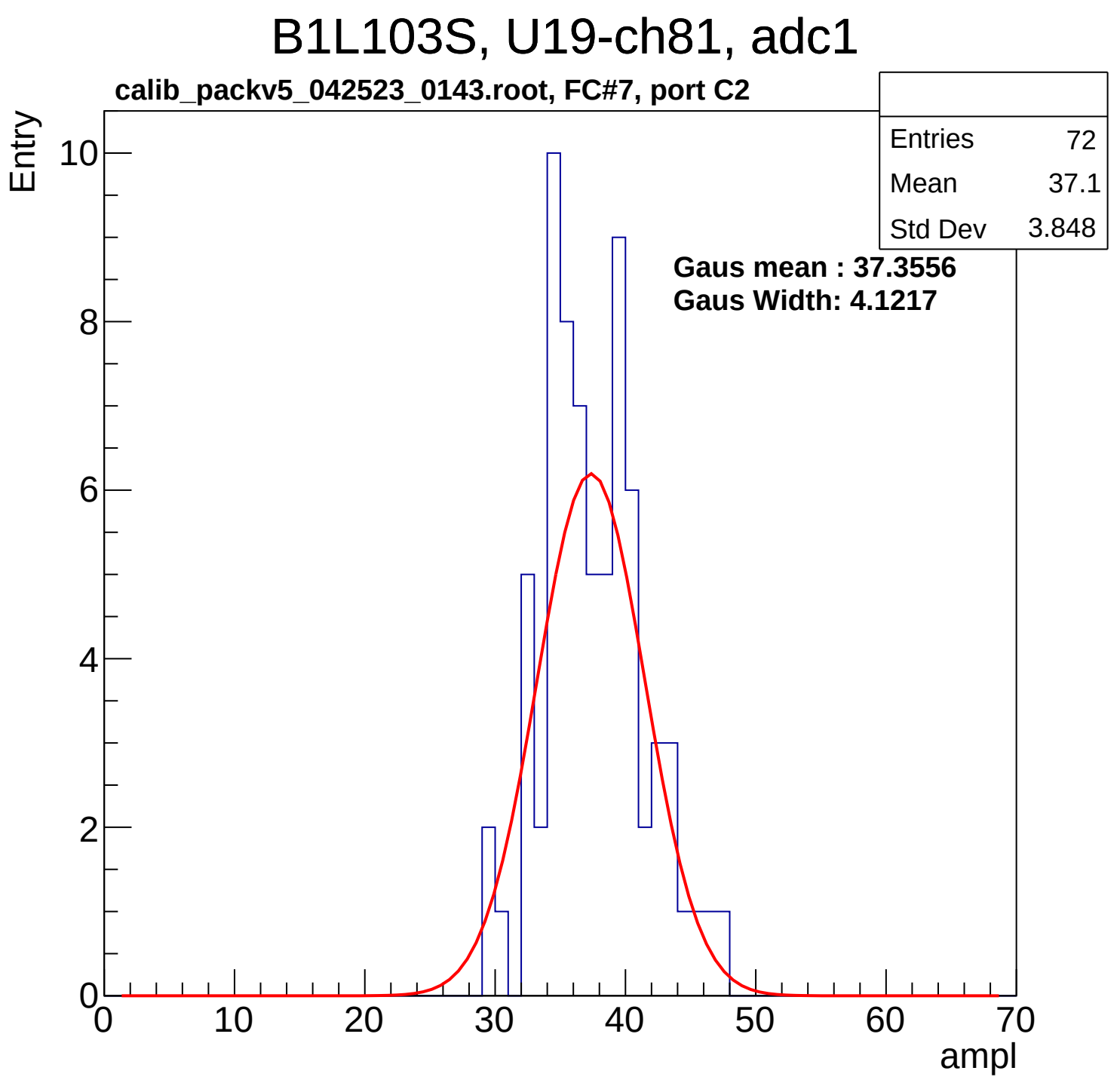
B1L103S, U19-ch81, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.1
Std Dev 3.848
Gaus mean : 37.3556
Gaus Width: 4.1217
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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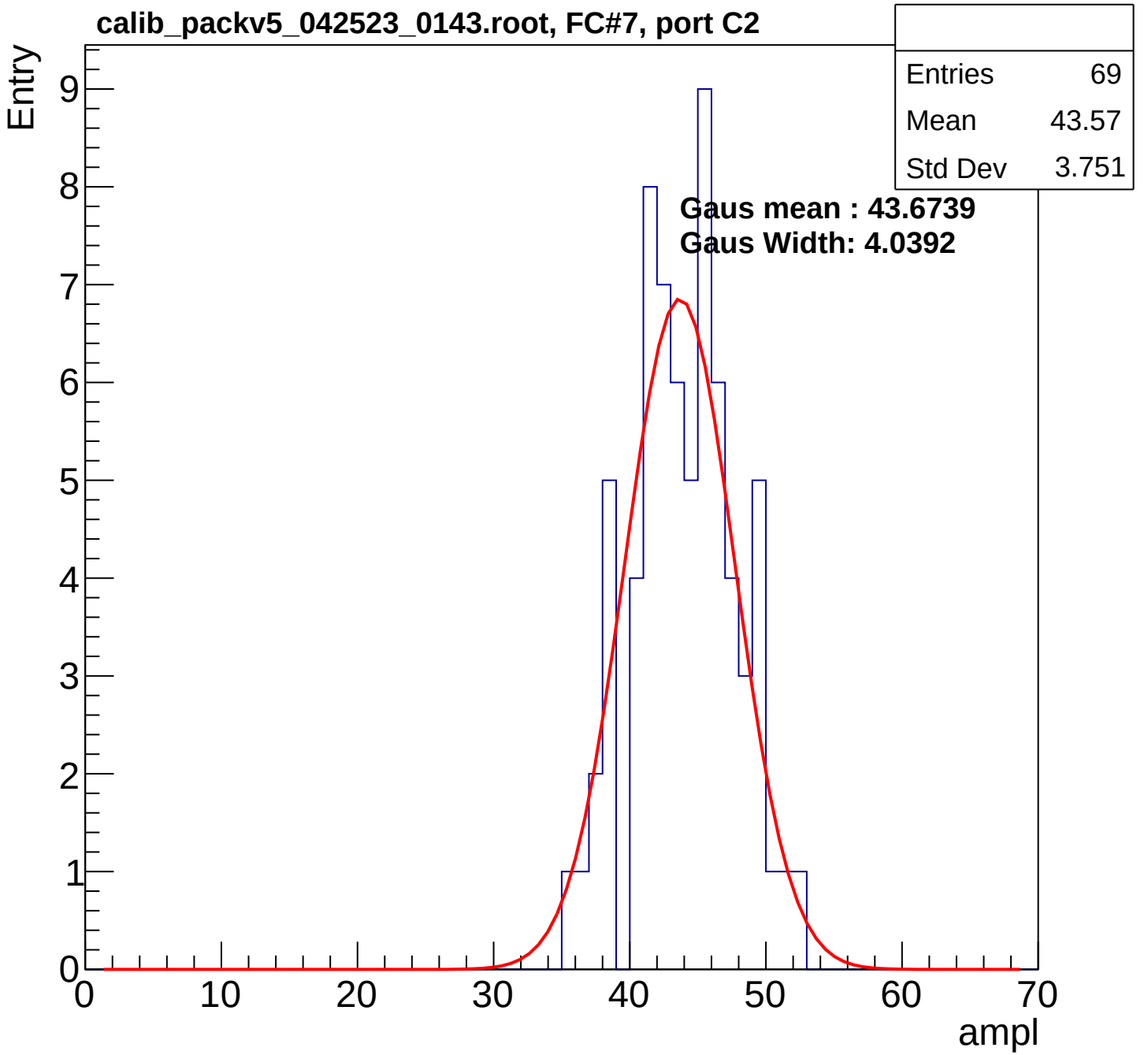
B1L103S, U19-ch81, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 43.57
Std Dev 3.751
Gaus mean : 43.6739
Gaus Width: 4.0392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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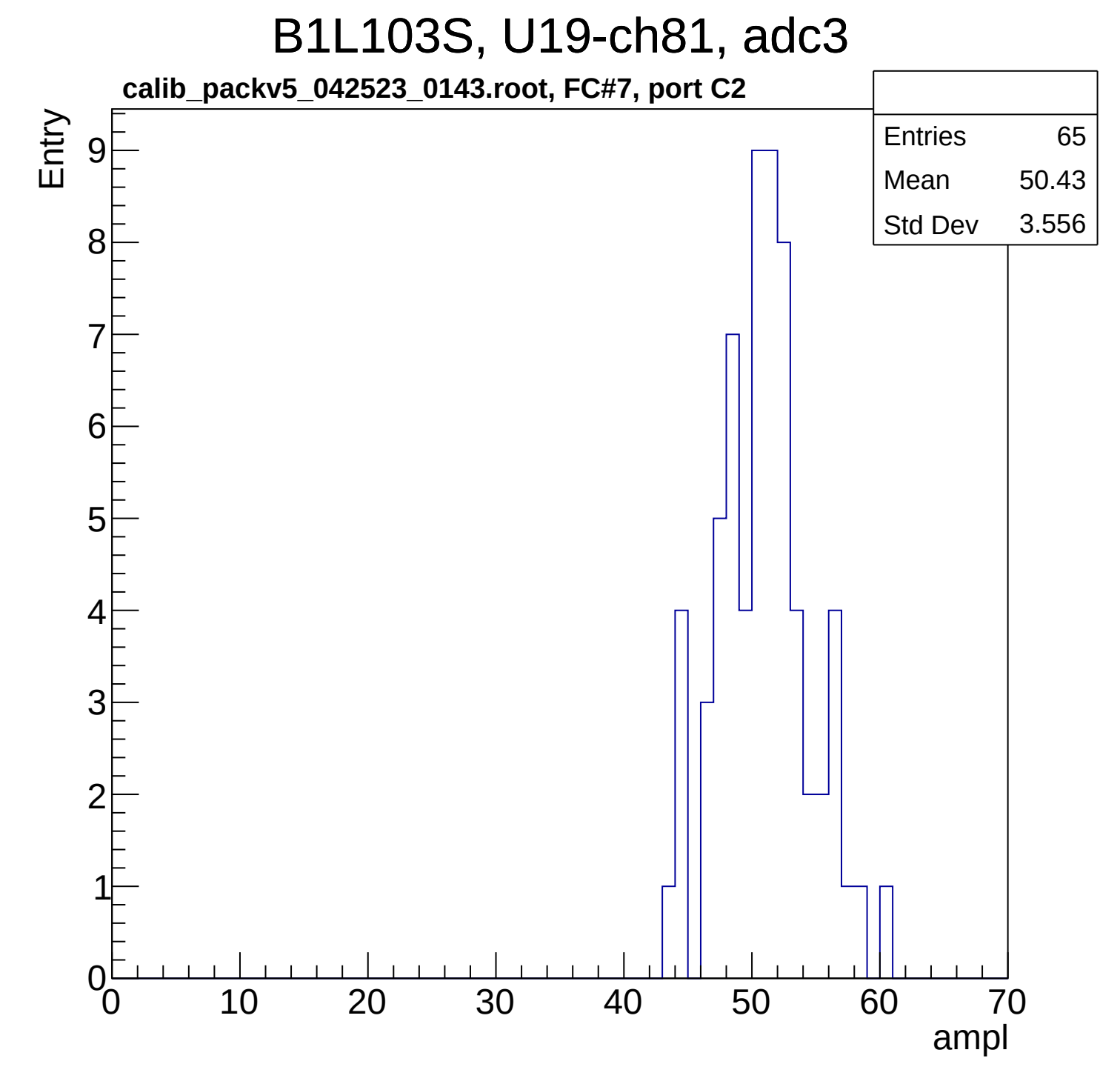
B1L103S, U19-ch81, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 50.43
Std Dev 3.556
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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50
60
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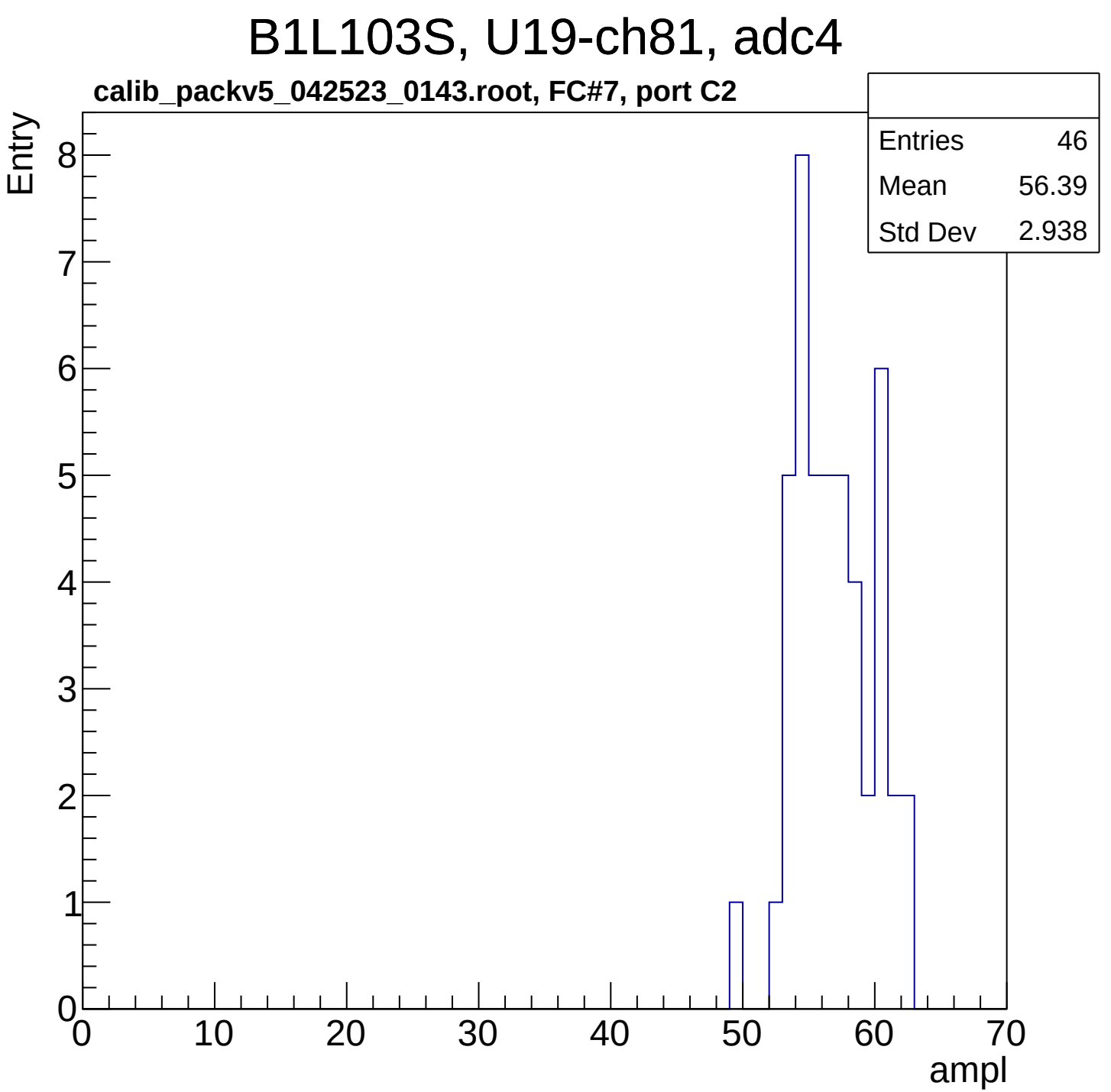
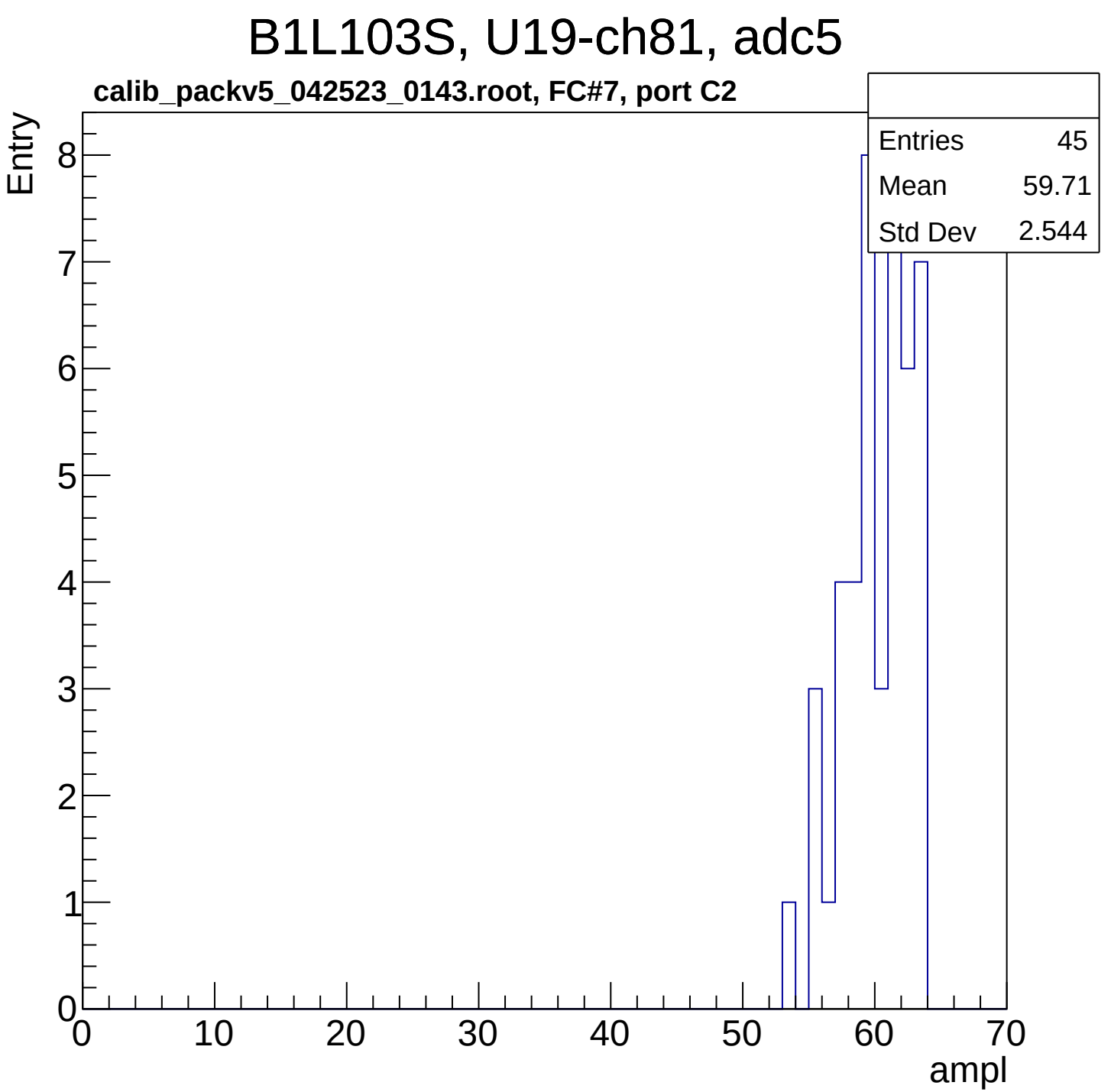

B1L103S, U19-ch81, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.39
Std Dev 2.938
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch81, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.71
Std Dev 2.544
Entry
ampl
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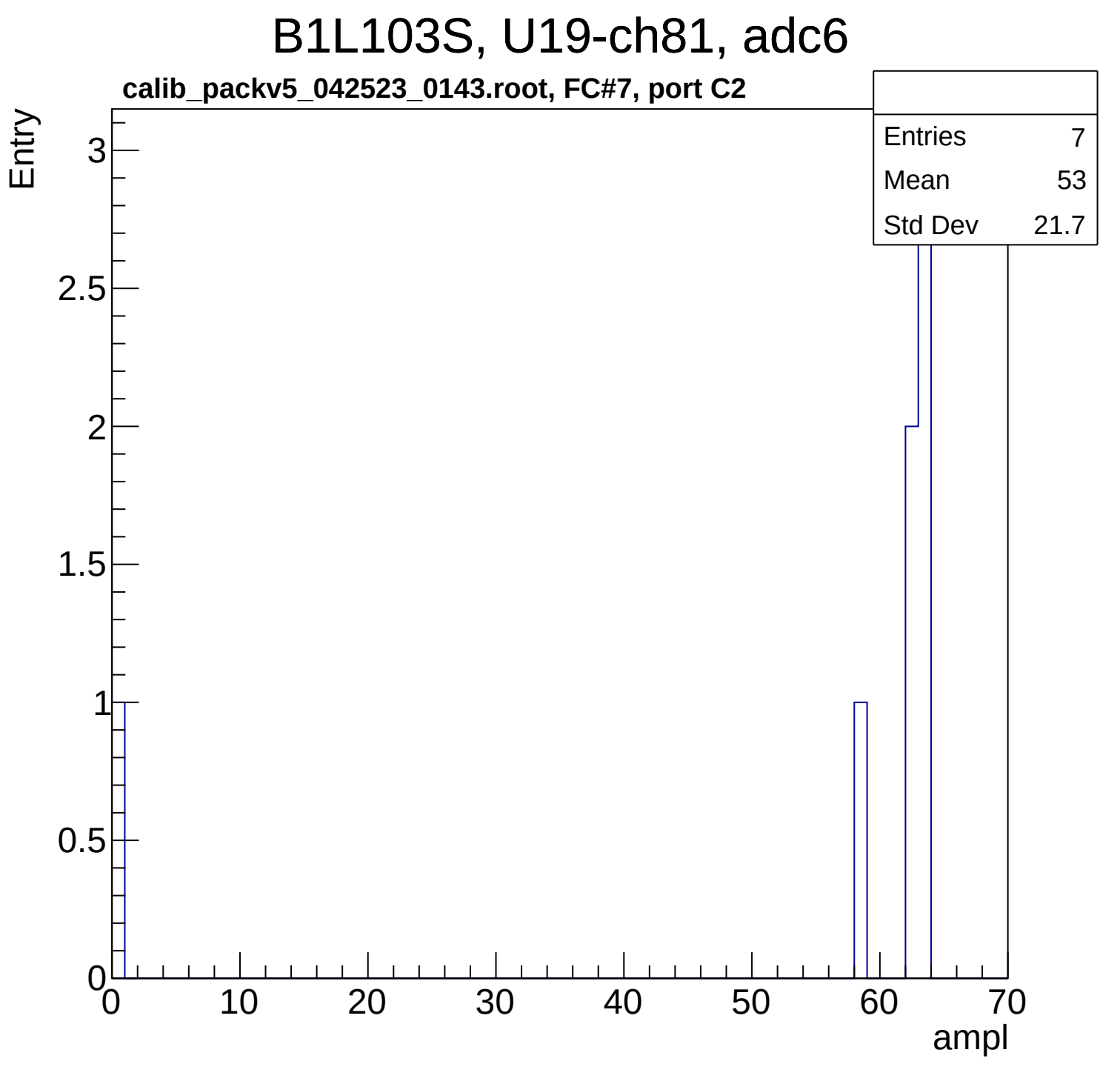
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch81, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 53
Std Dev 21.7
Entry
ampl
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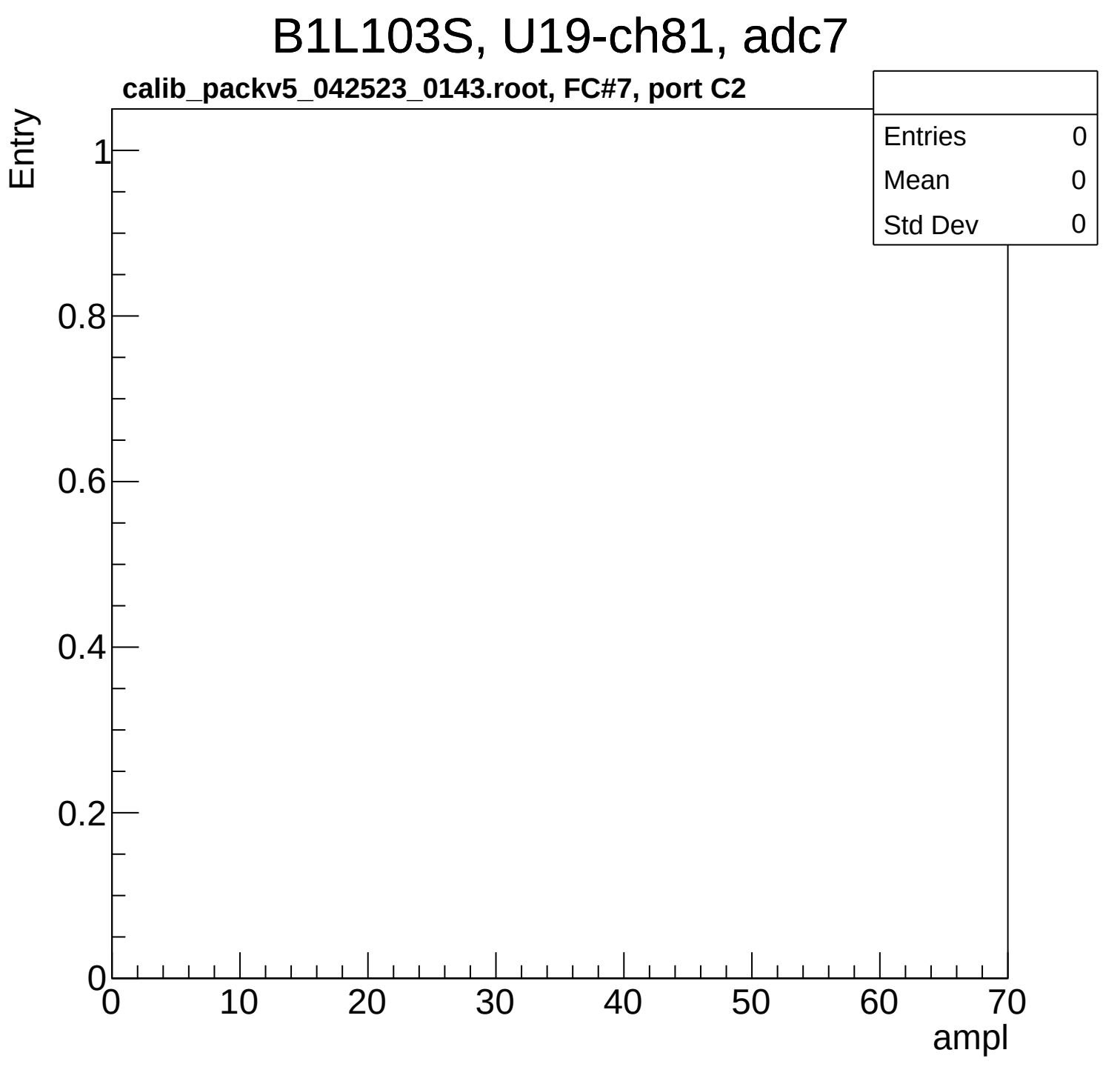
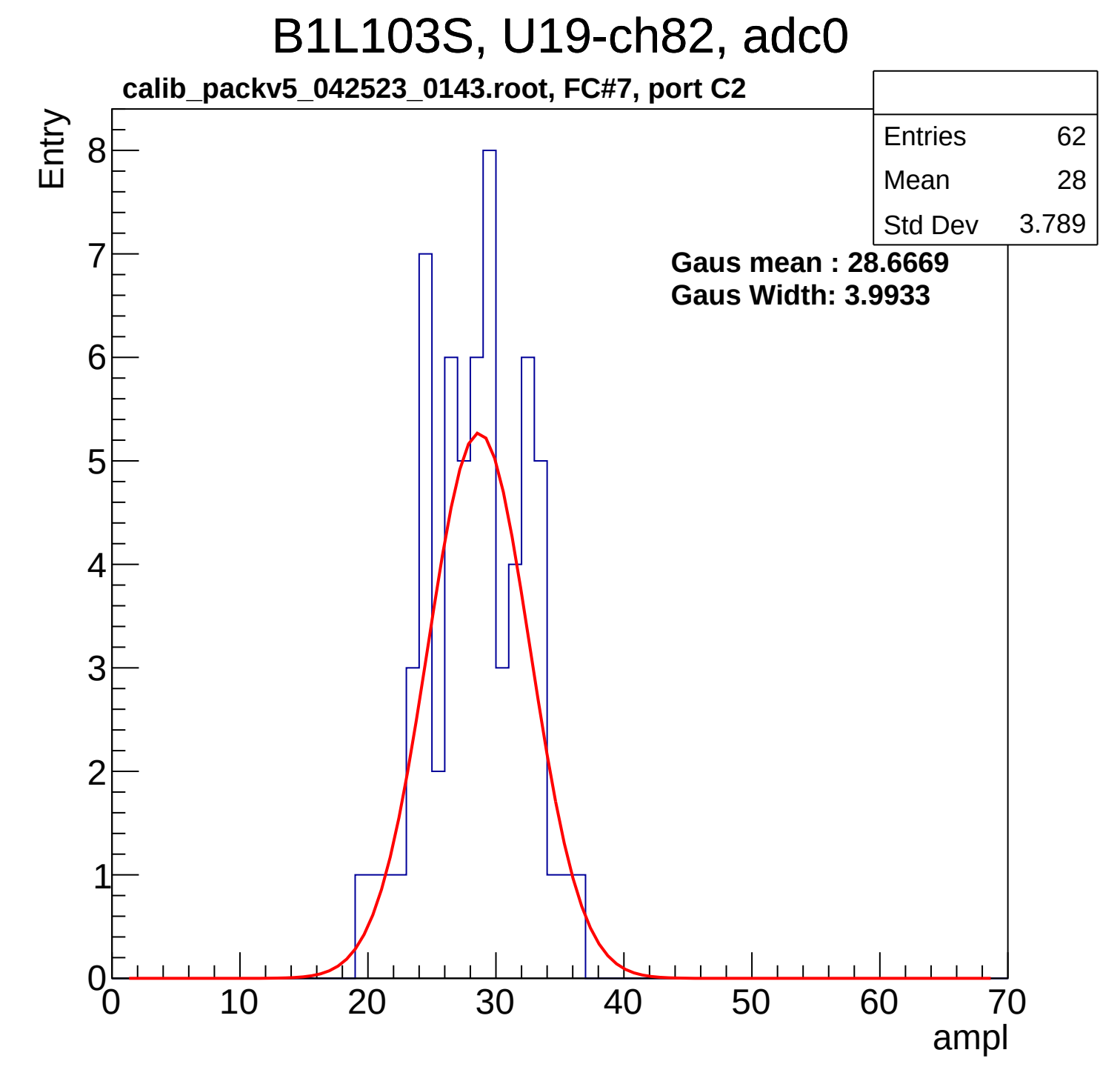
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch81, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch82, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 28
Std Dev 3.789
Gaus mean : 28.6669
Gaus Width: 3.9933
Entry
ampl
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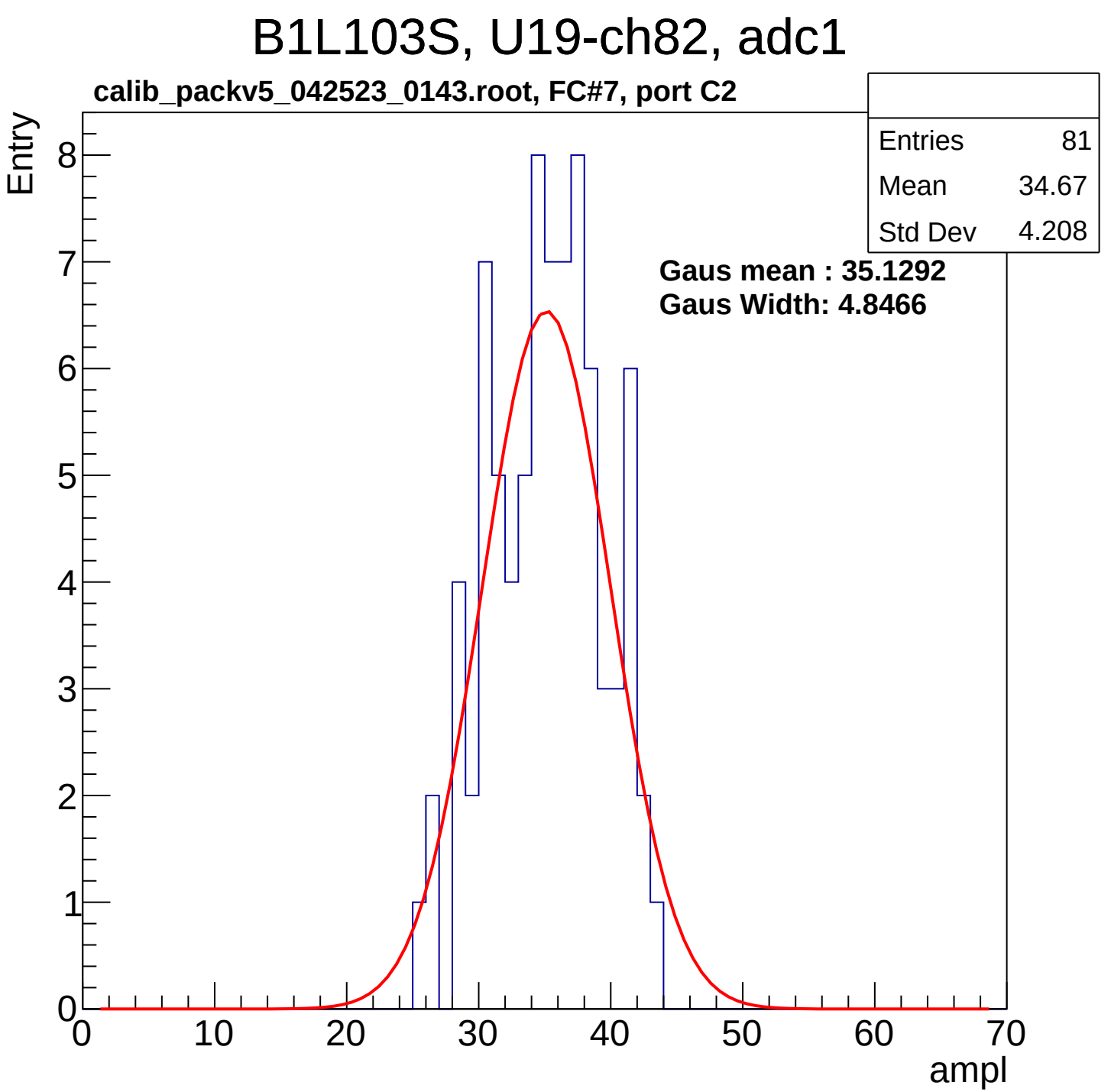
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch82, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 34.67
Std Dev 4.208
Gaus mean : 35.1292
Gaus Width: 4.8466
Entry
ampl
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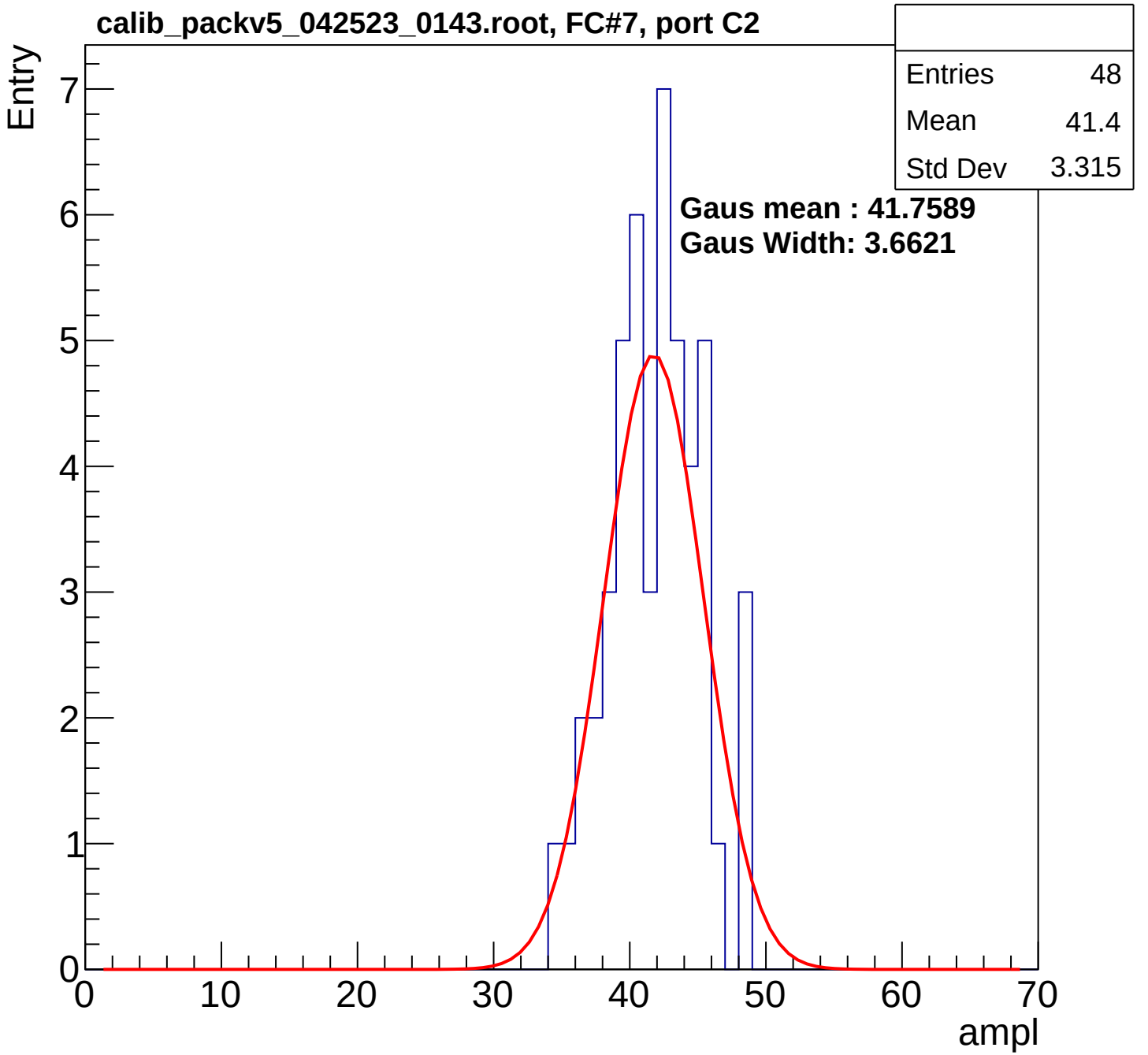
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch82, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 41.4
Std Dev 3.315
Gaus mean : 41.7589
Gaus Width: 3.6621
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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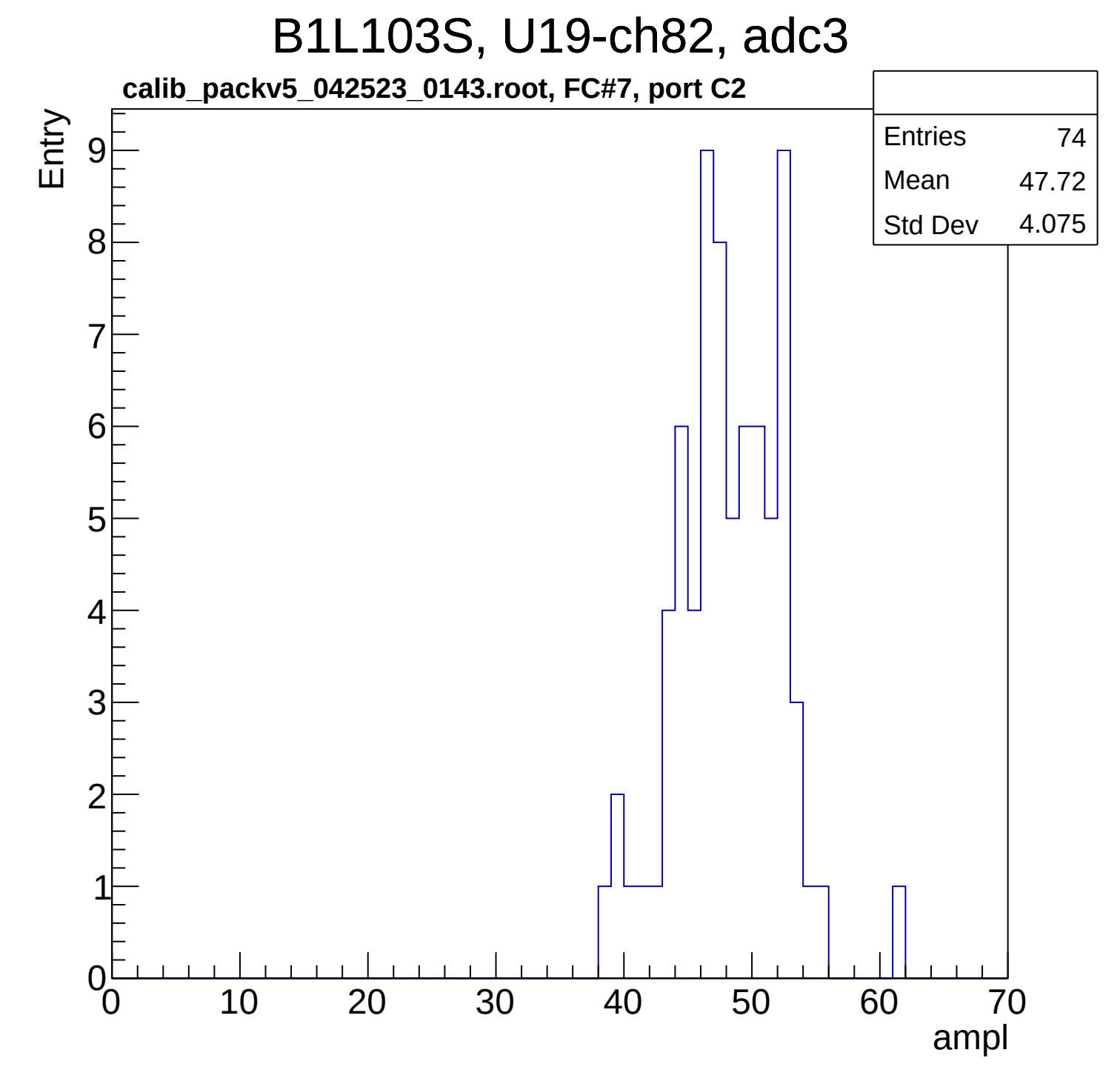
B1L103S, U19-ch82, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 47.72
Std Dev 4.075
Entry
ampl
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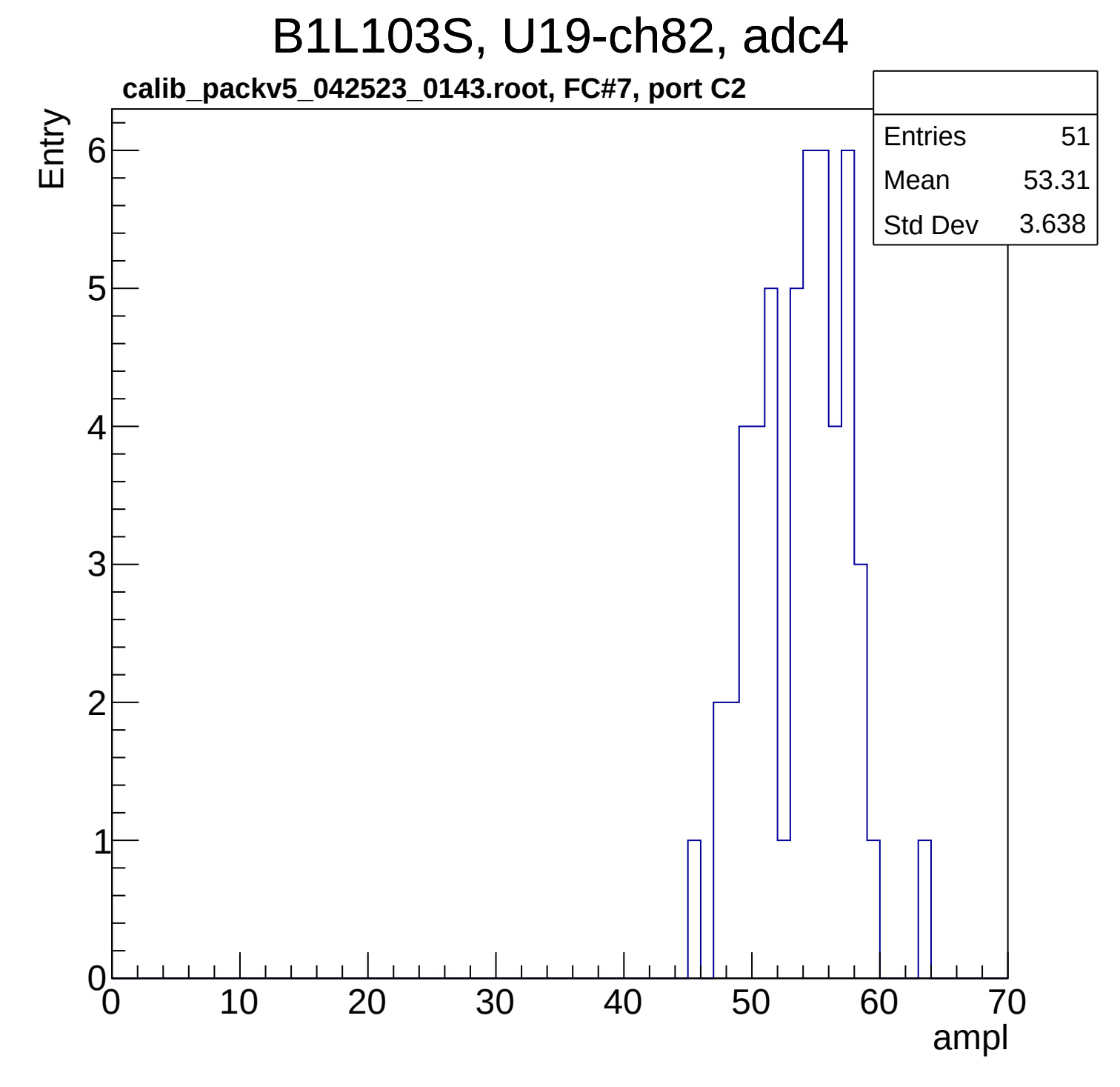
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch82, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 53.31
Std Dev 3.638
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
50
60
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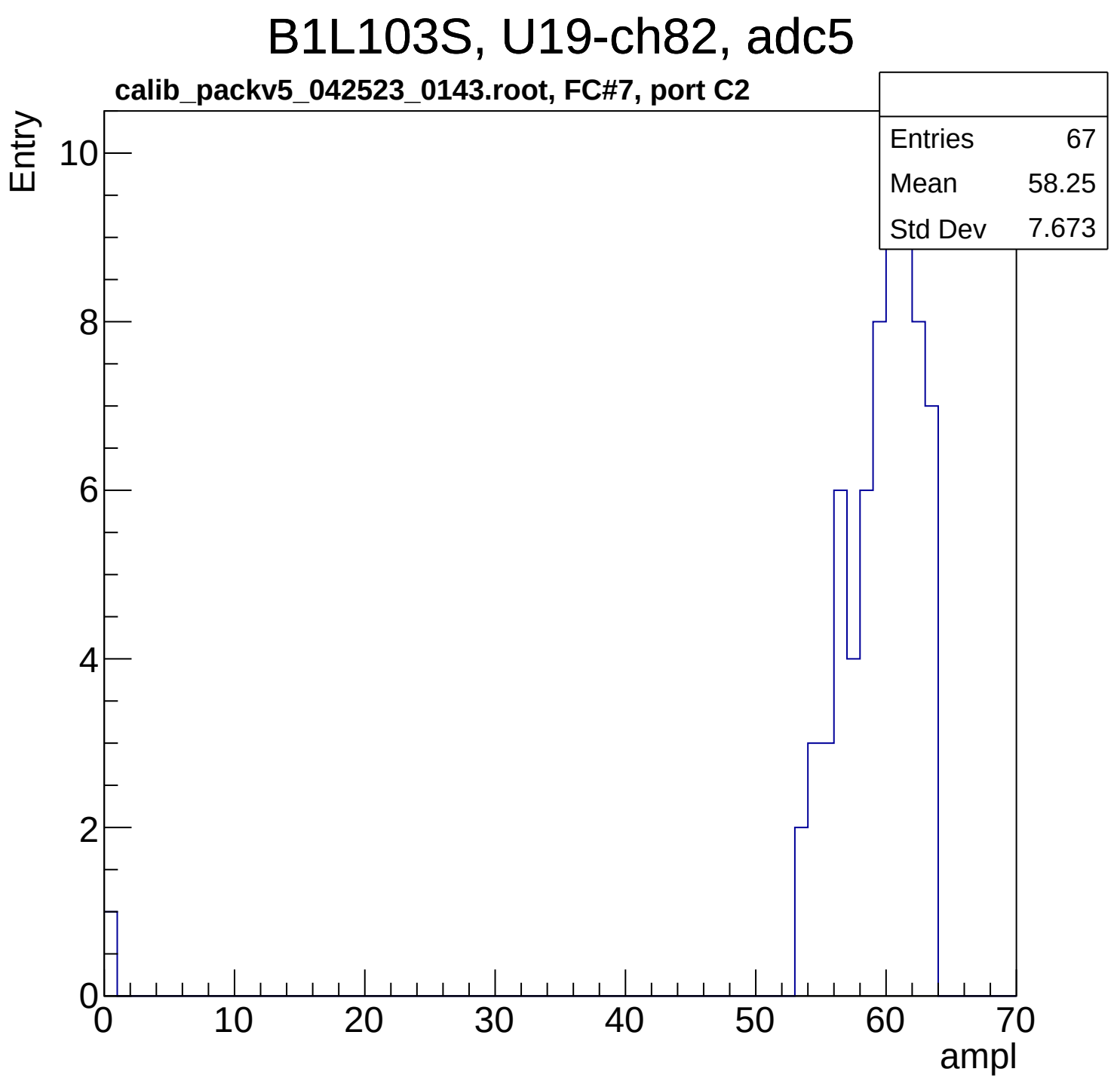
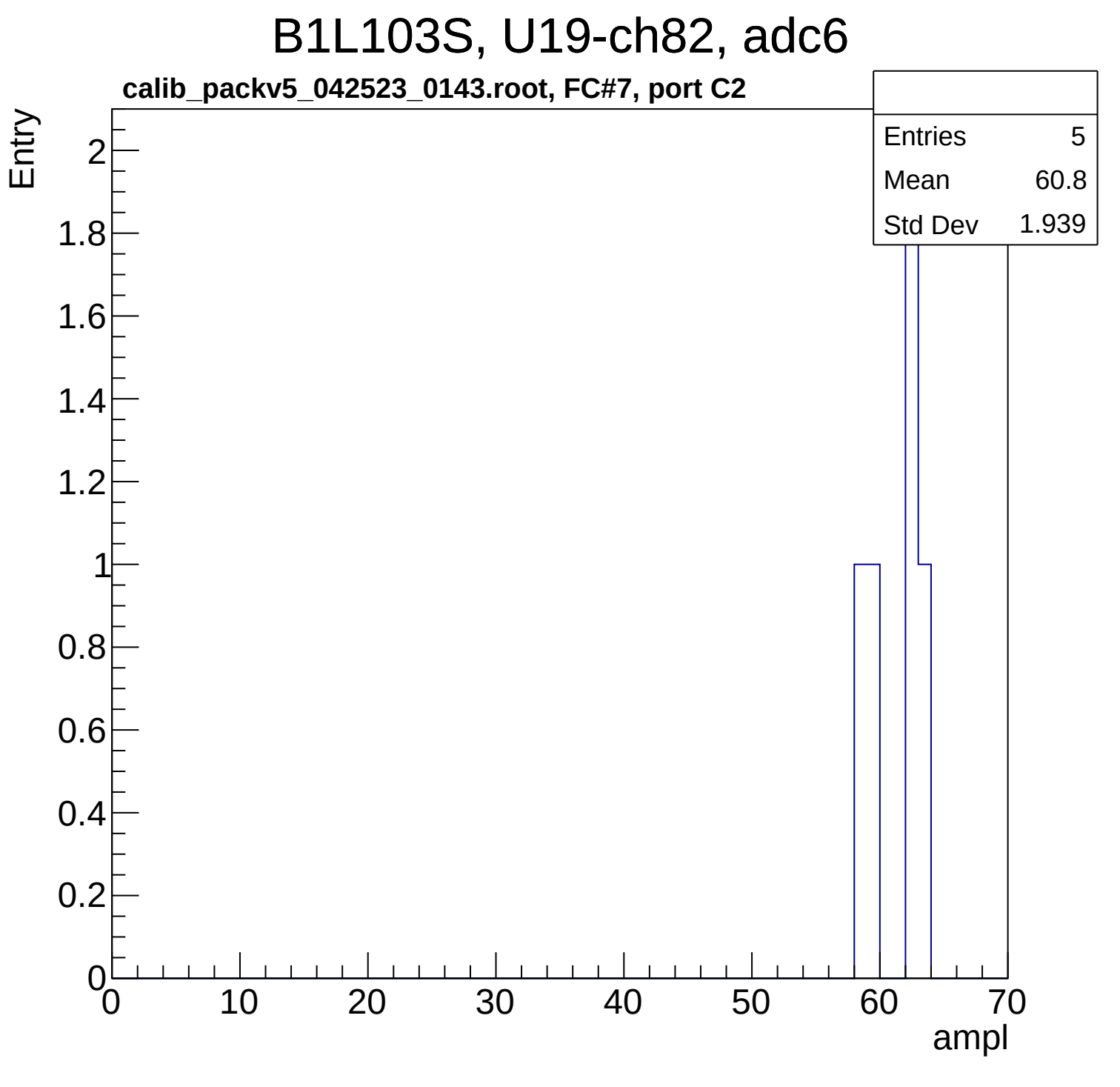
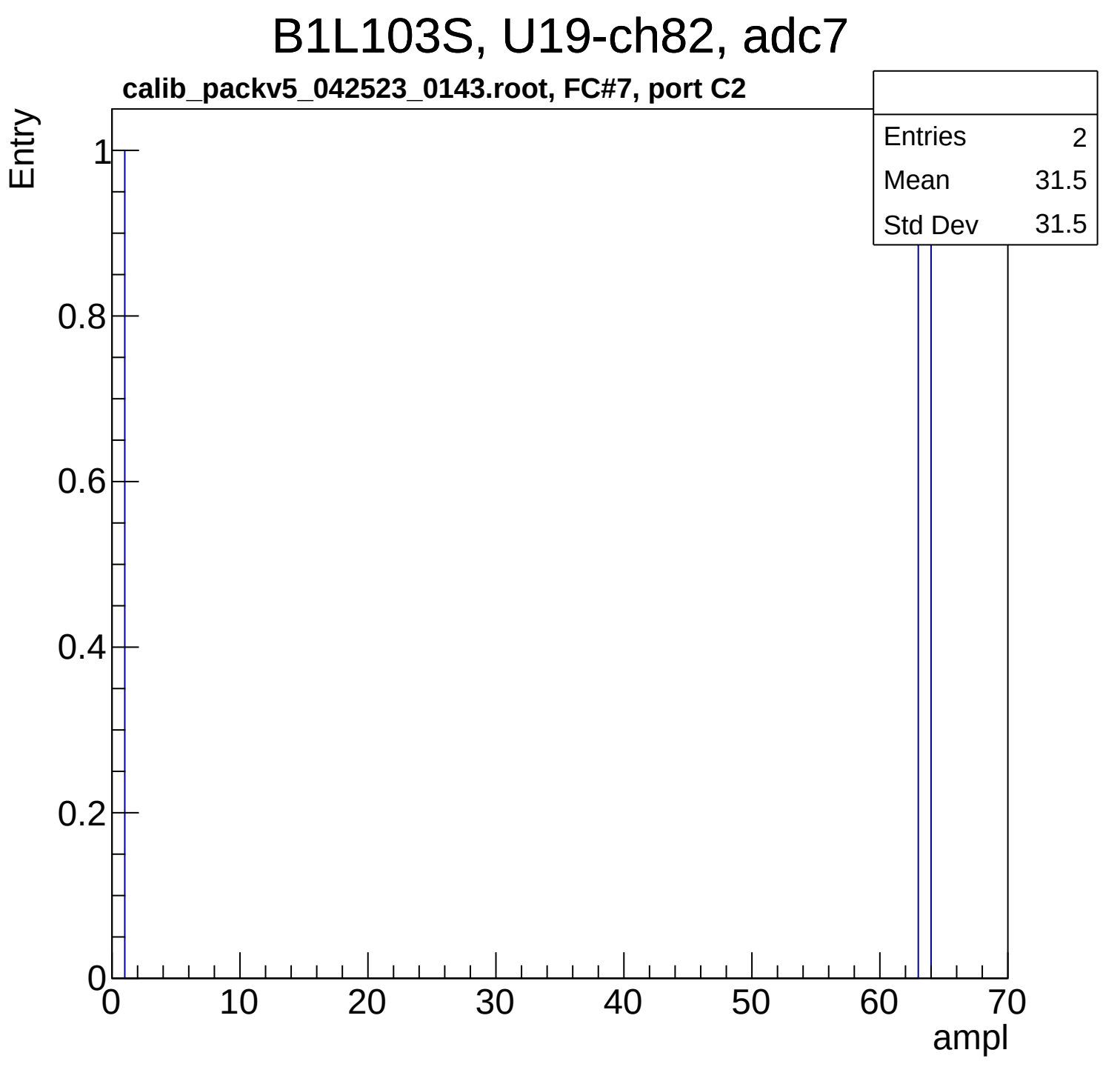
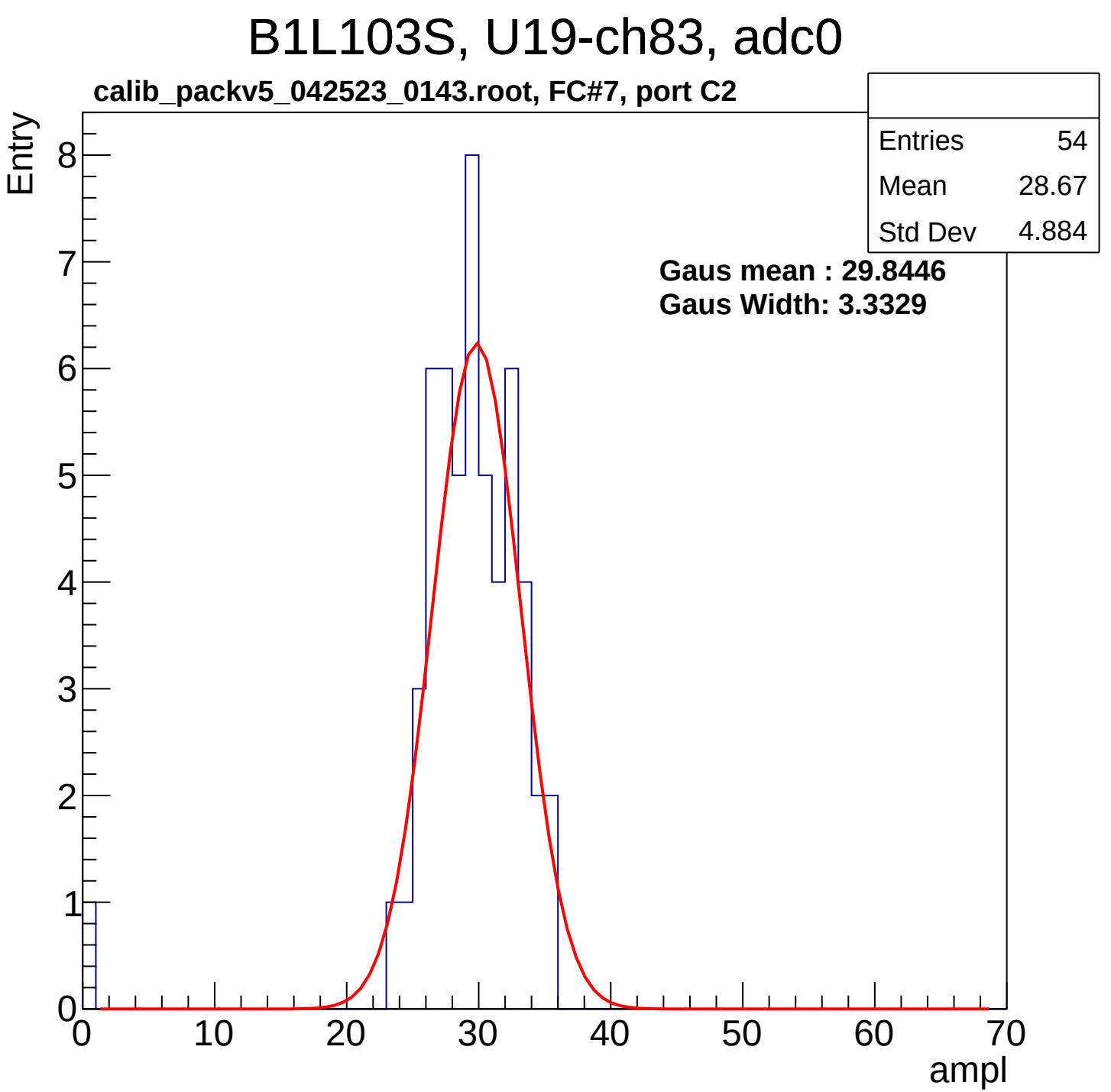

B1L103S, U19-ch82, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 58.25
Std Dev 7.673
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch82, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 60.8
Std Dev 1.939
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch82, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch83, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 28.67
Std Dev 4.884
Gaus mean : 29.8446
Gaus Width: 3.3329
Entry
ampl
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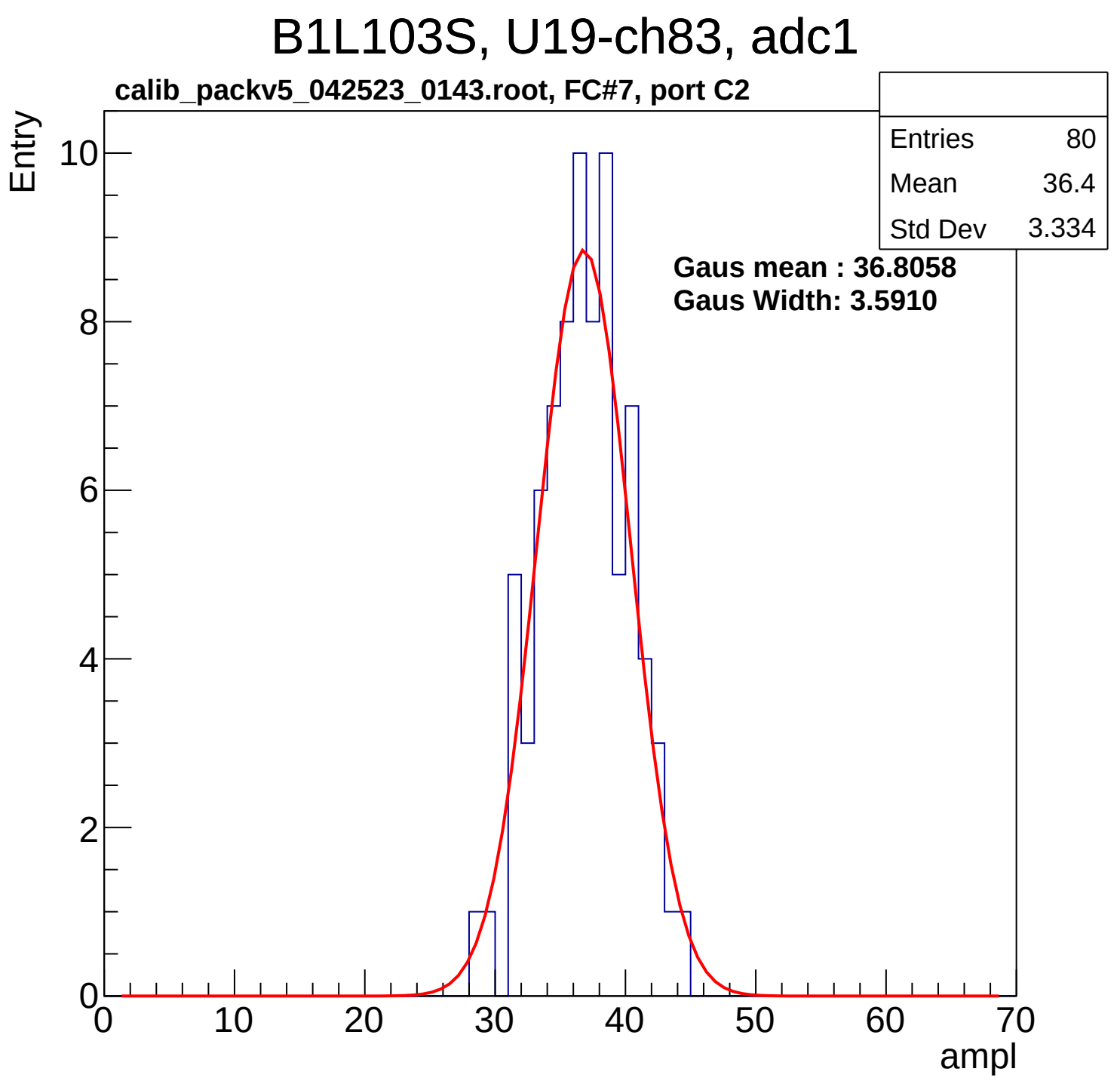
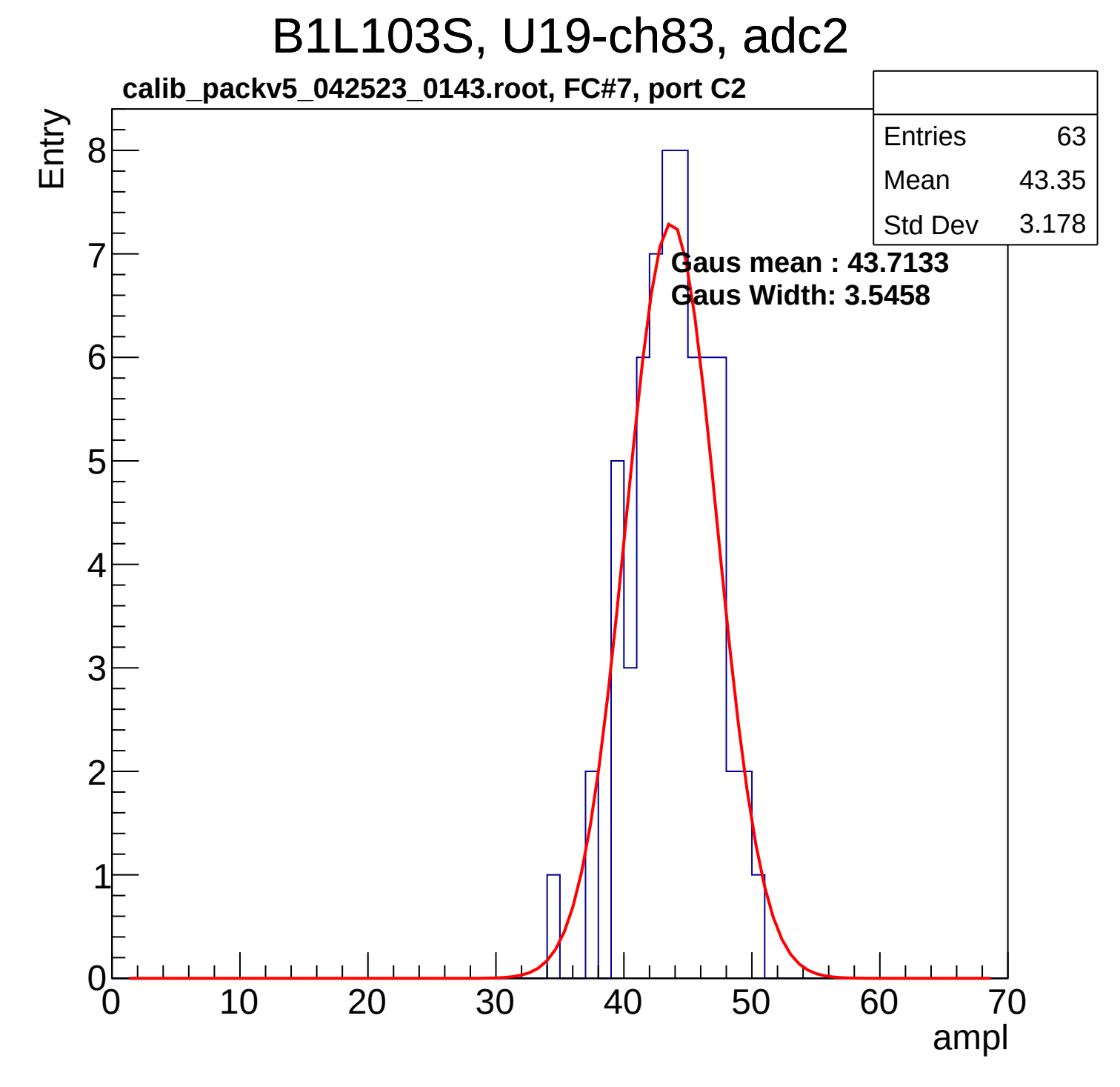
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch83, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 36.4
Std Dev 3.334
Gaus mean : 36.8058
Gaus Width: 3.5910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch83, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.35
Std Dev 3.178
Gaus mean : 43.7133
Gaus Width: 3.5458
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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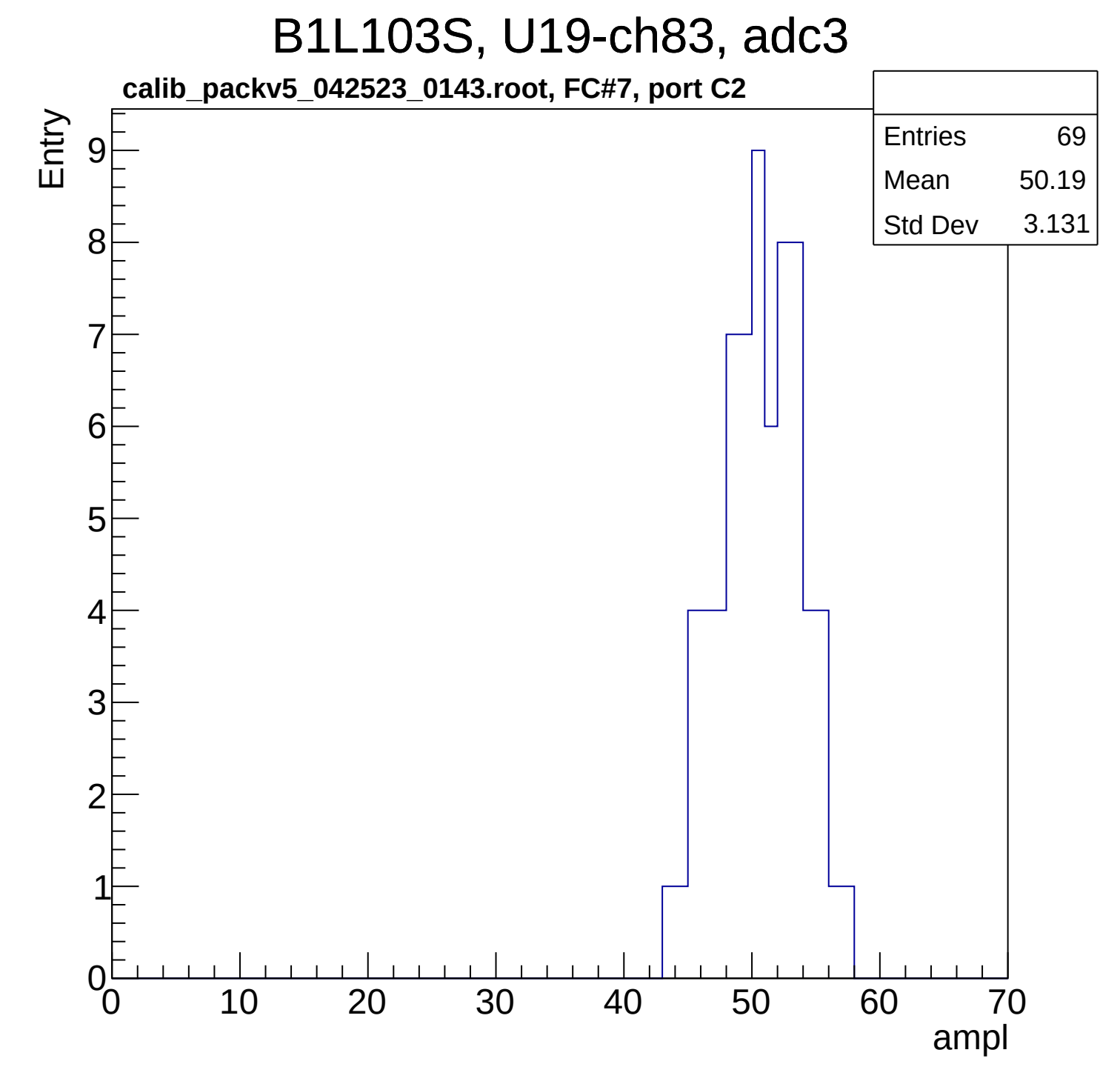
B1L103S, U19-ch83, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.19
Std Dev 3.131
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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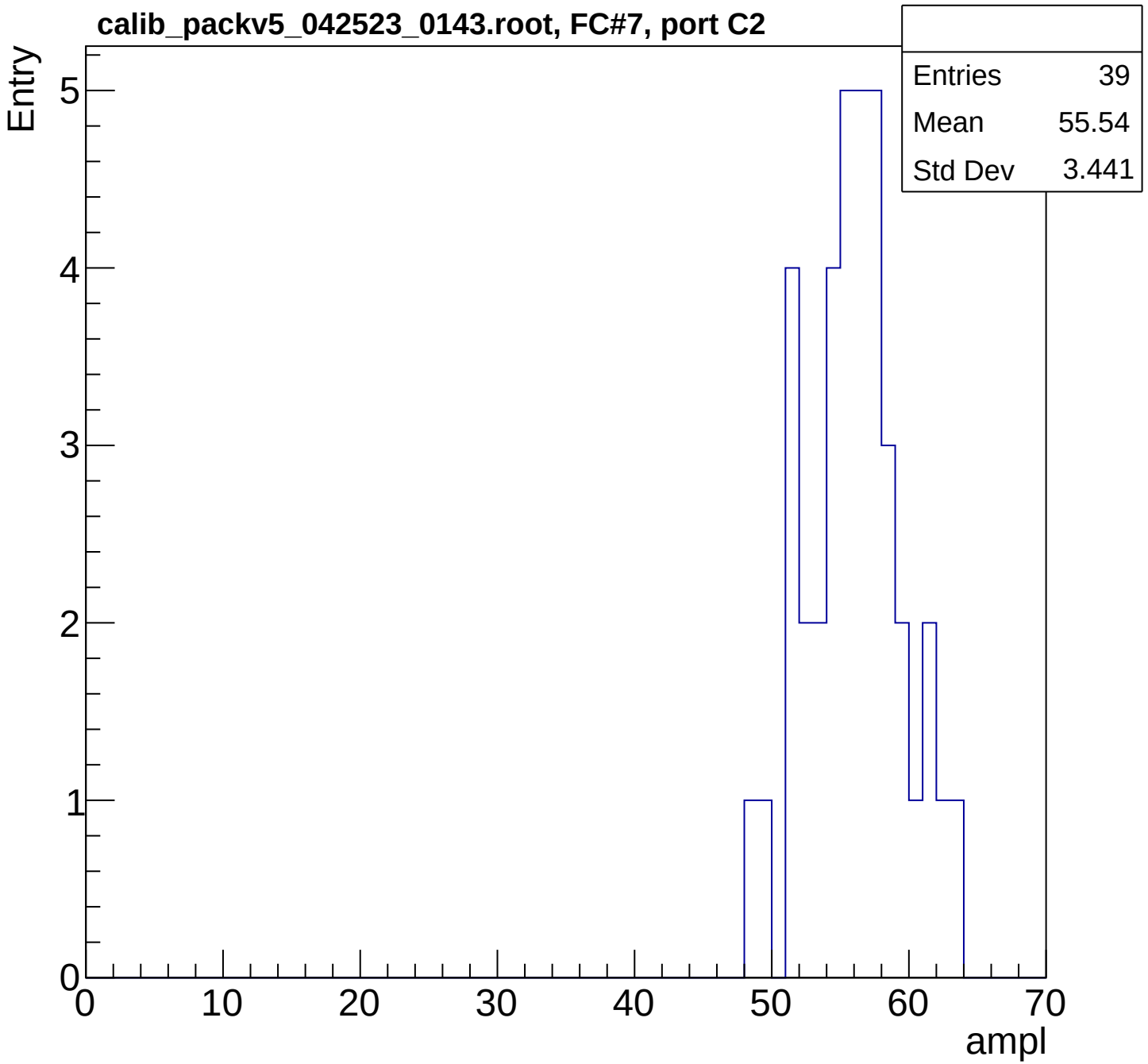
B1L103S, U19-ch83, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 55.54
Std Dev 3.441
Entry
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1
2
3
4
5
0
10
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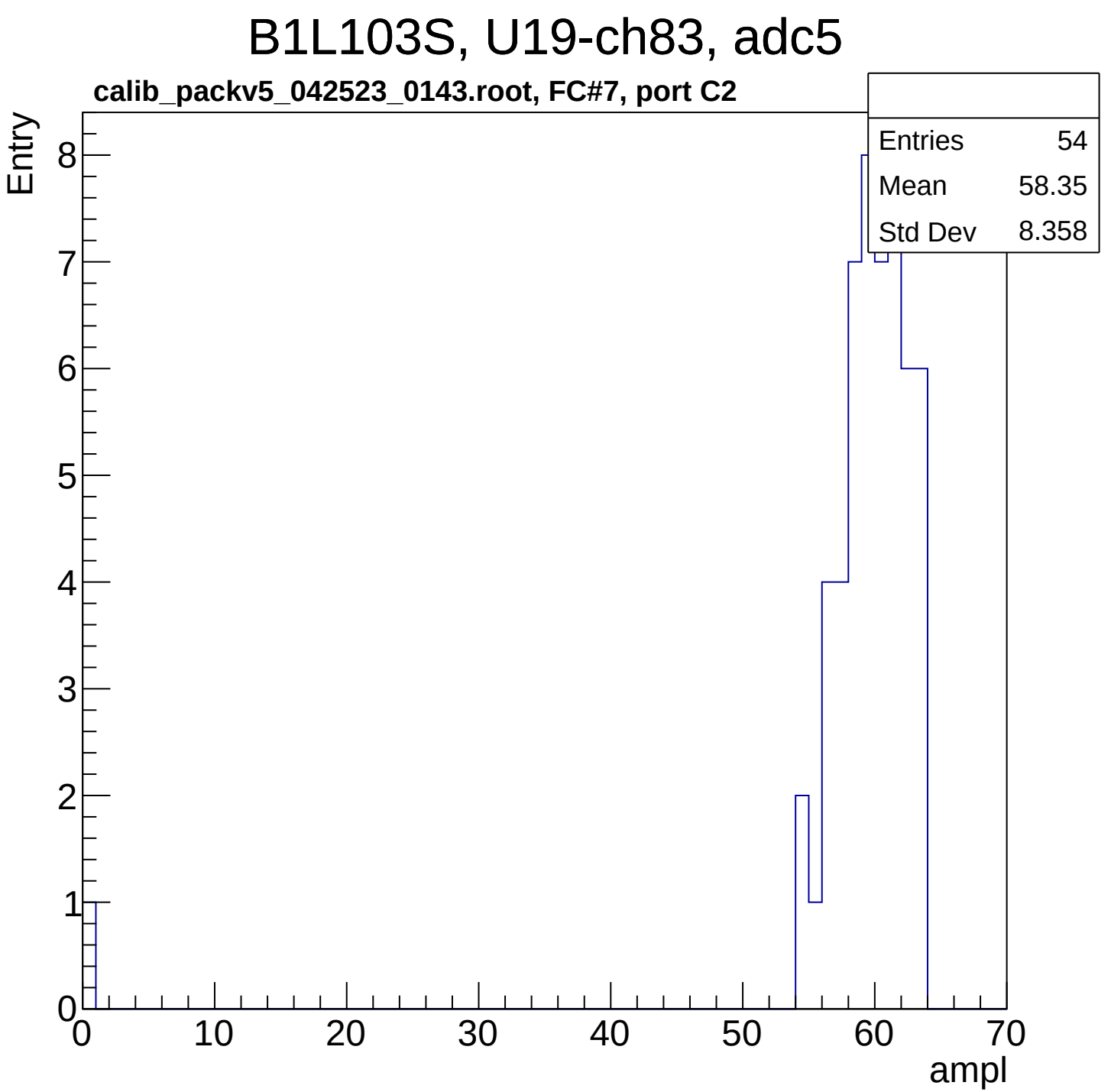
30
40
50
60
70
ampl


B1L103S, U19-ch83, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.35
Std Dev 8.358
Entry
ampl
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5
6
7
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0
10
20
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60
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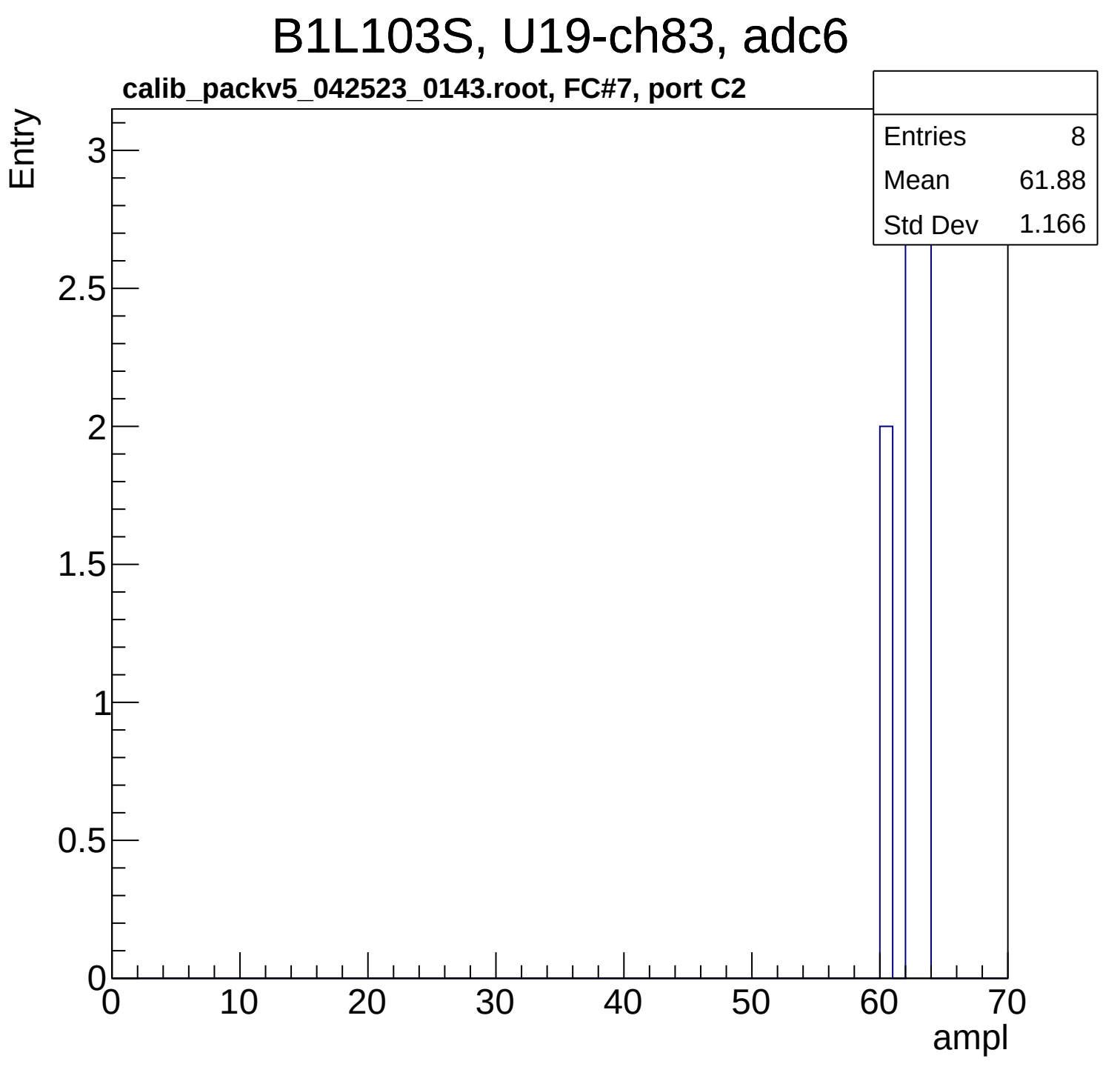
B1L103S, U19-ch83, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.88
Std Dev 1.166
Entry
ampl
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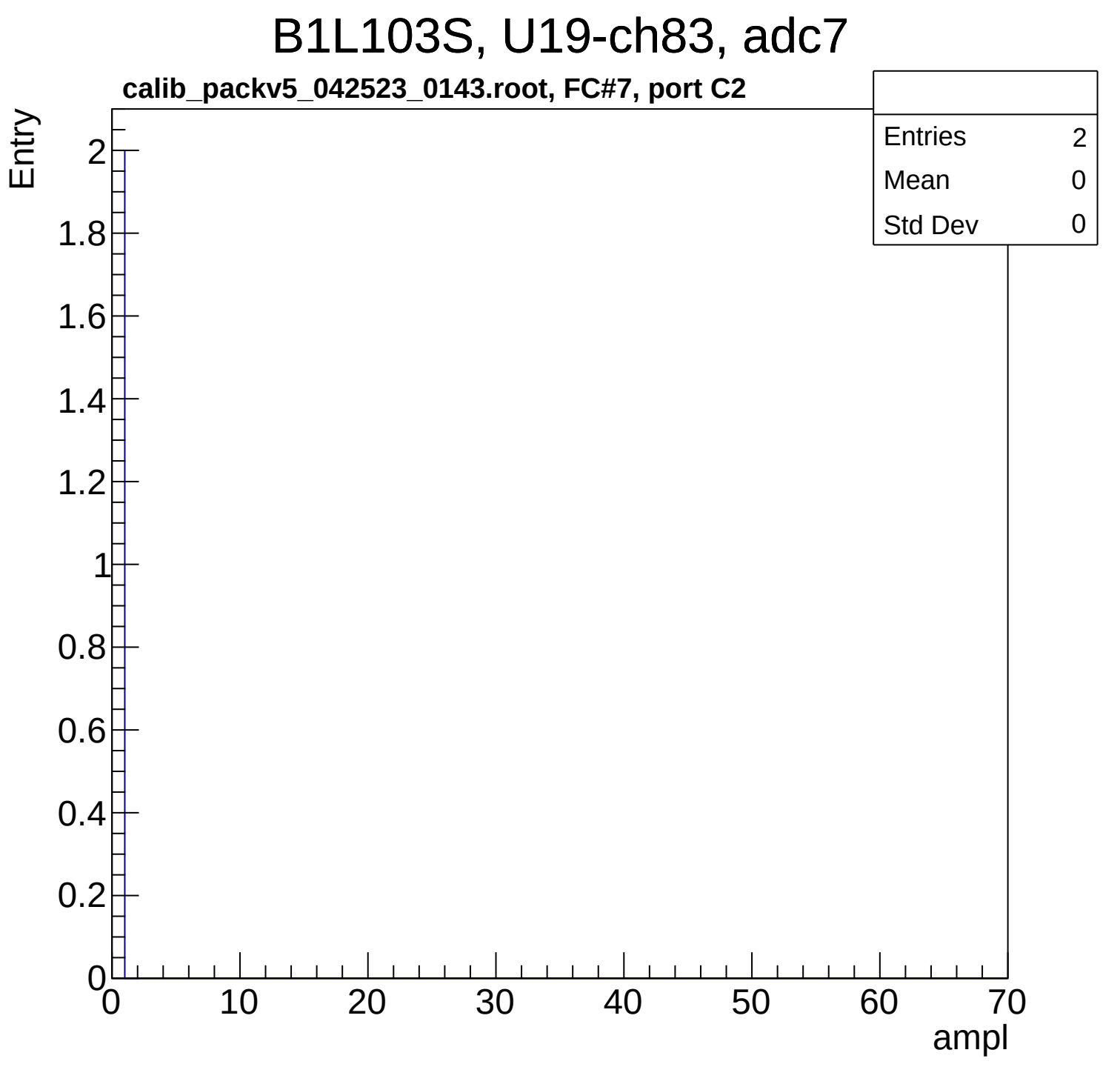
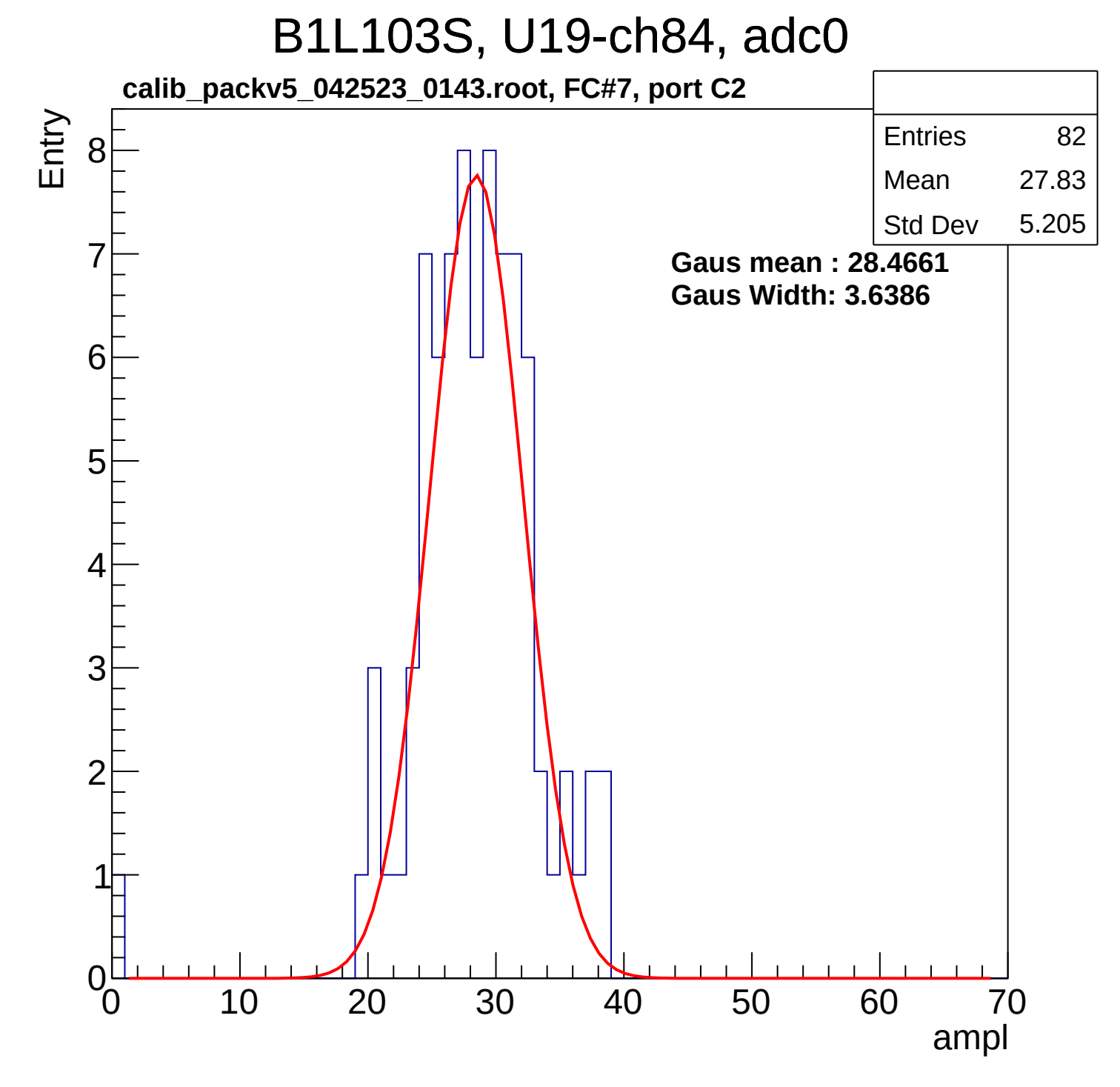
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch83, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch84, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 27.83
Std Dev 5.205
Gaus mean : 28.4661
Gaus Width: 3.6386
Entry
ampl
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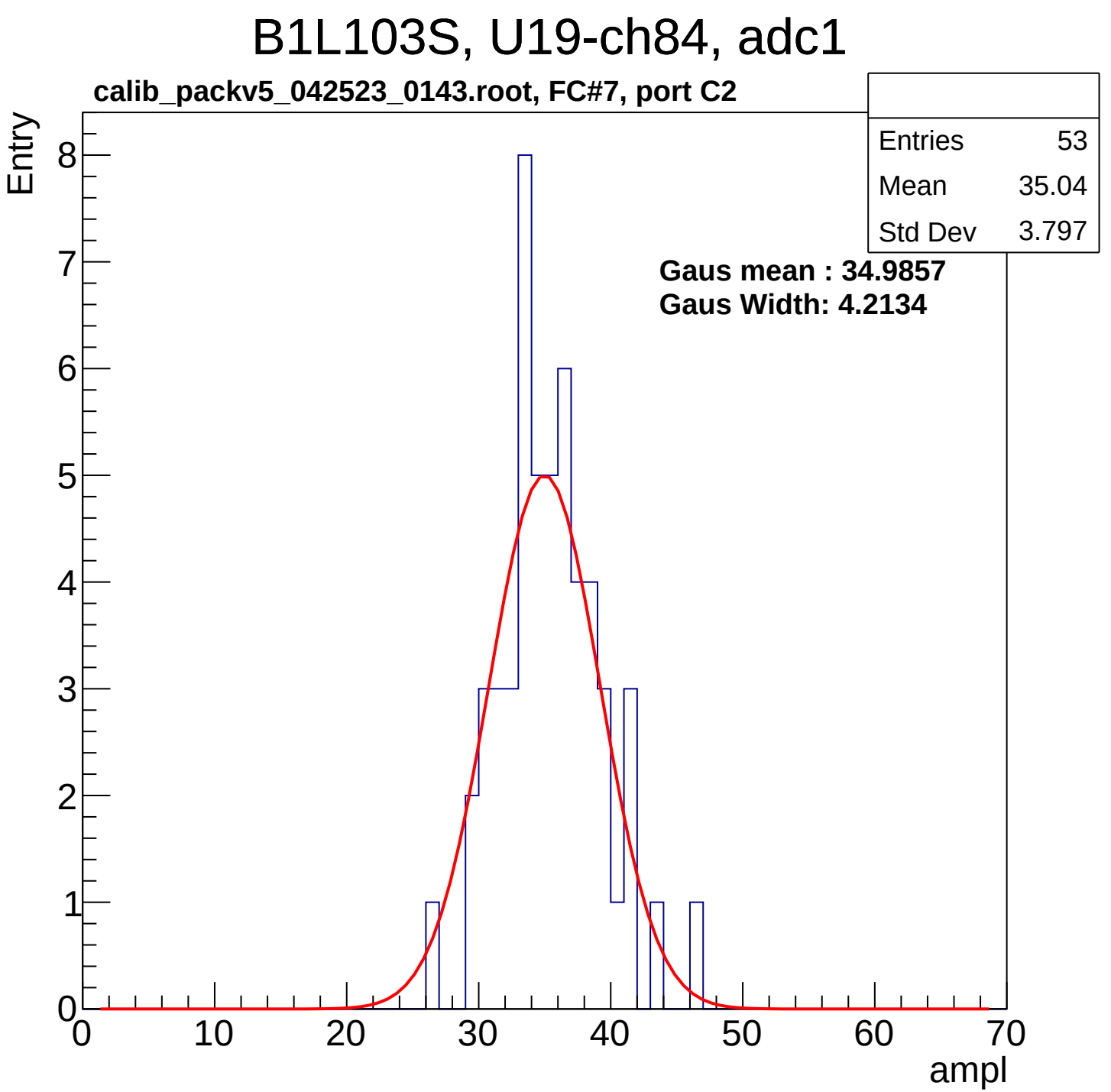
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch84, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 35.04
Std Dev 3.797
Gaus mean : 34.9857
Gaus Width: 4.2134
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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30
40
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60
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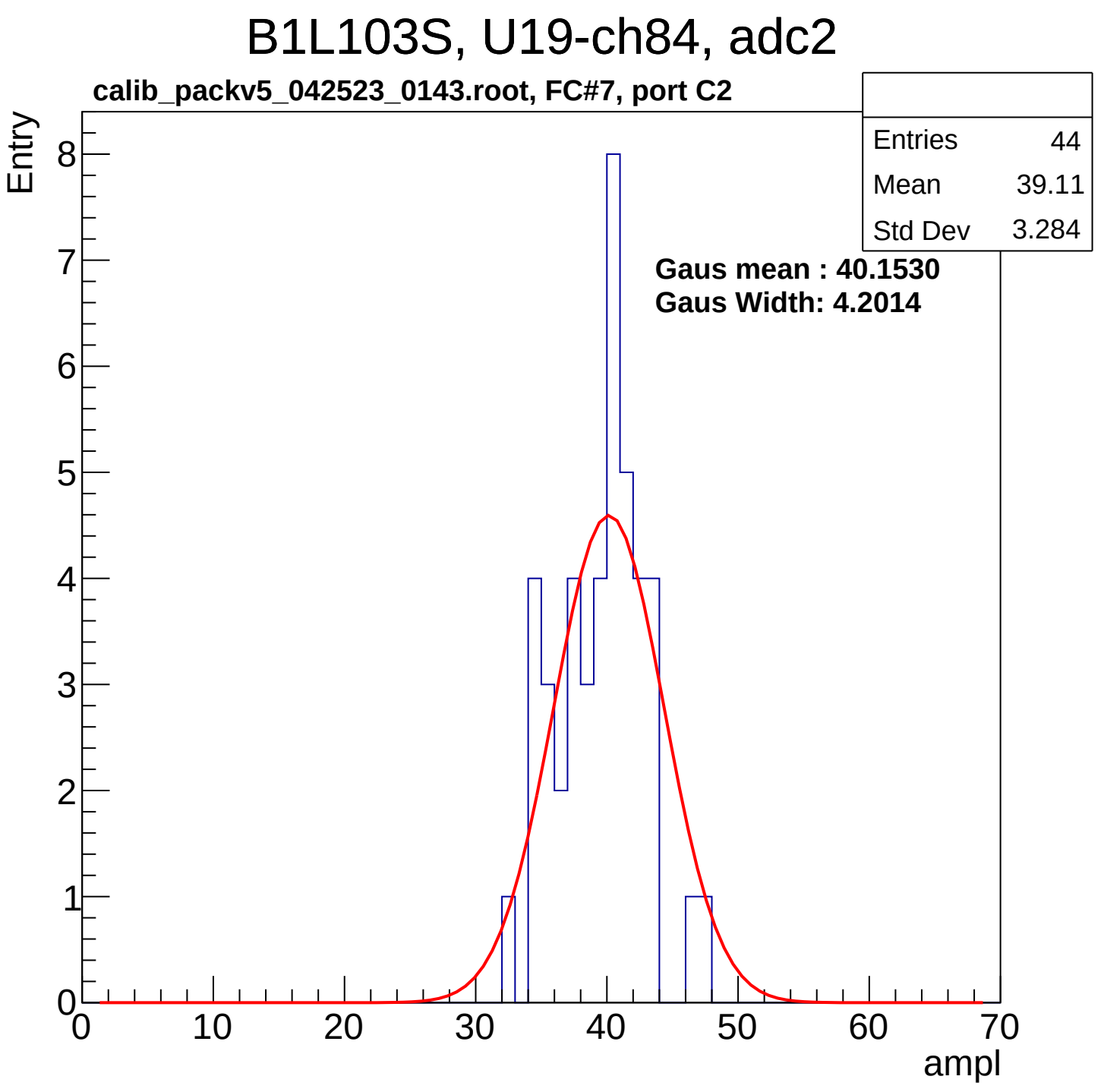
B1L103S, U19-ch84, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 39.11
Std Dev 3.284
Gaus mean : 40.1530
Gaus Width: 4.2014
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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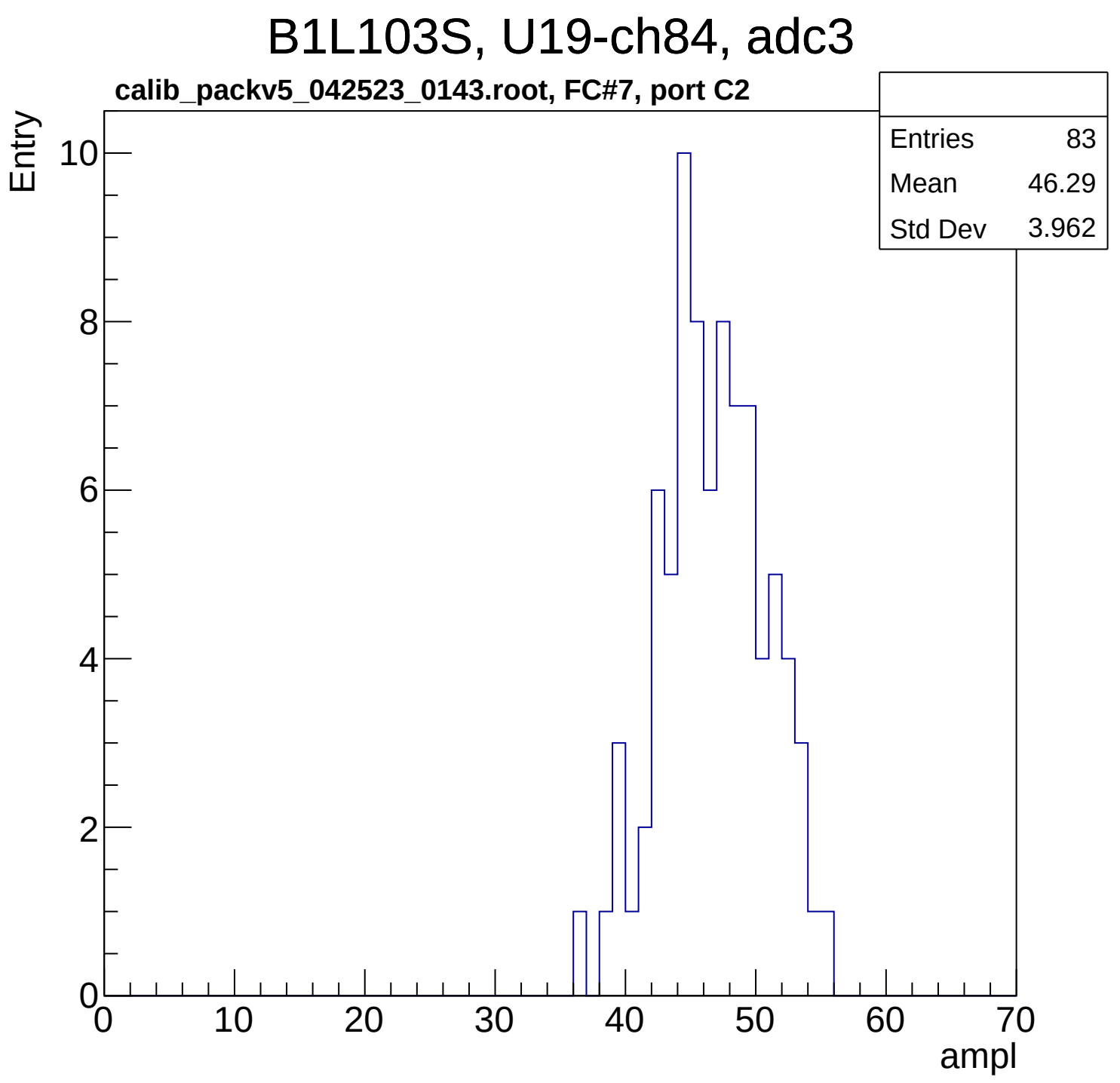
B1L103S, U19-ch84, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 46.29
Std Dev 3.962
Entry
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6
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10
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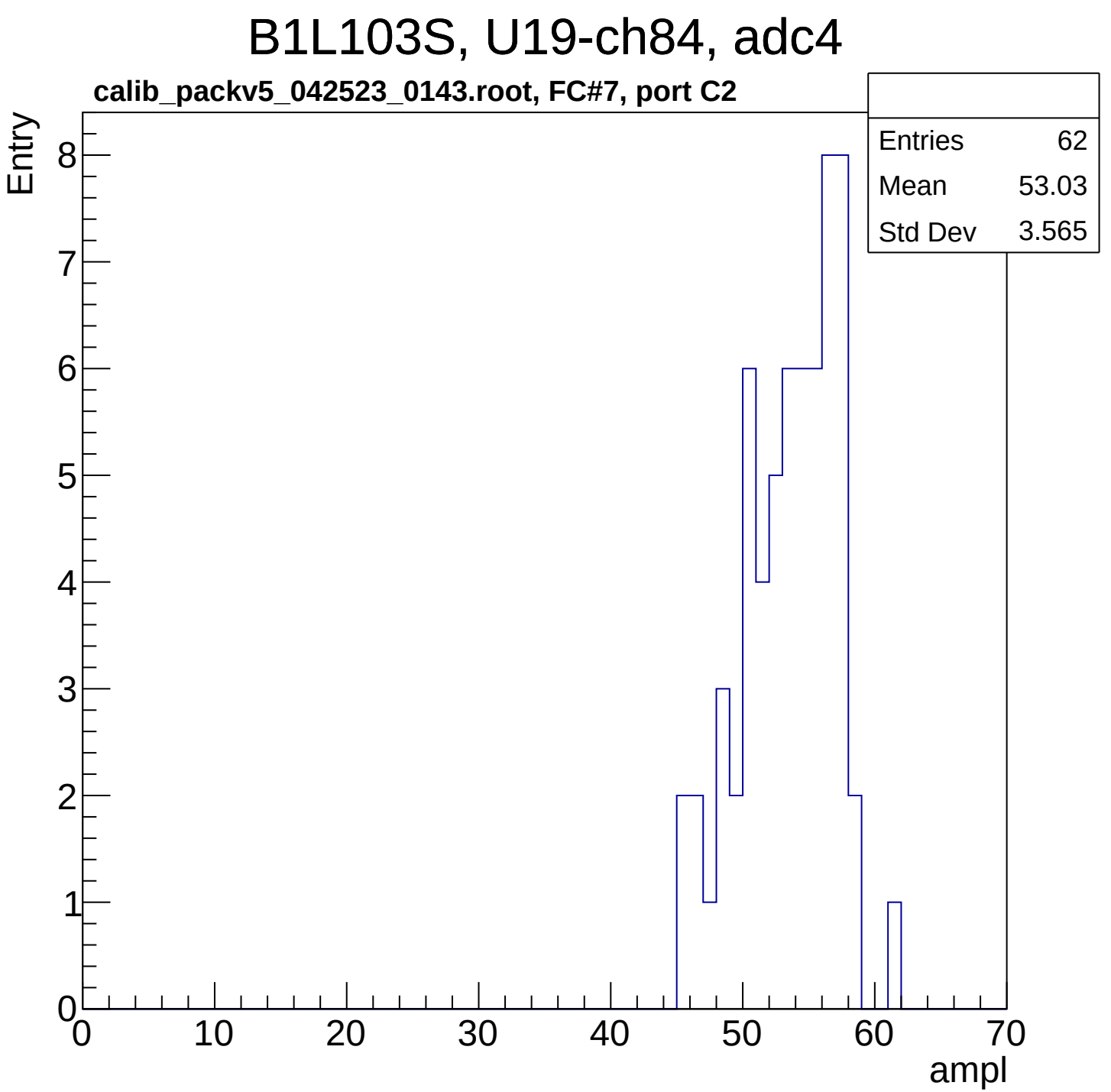
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch84, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 53.03
Std Dev 3.565
Entry
ampl
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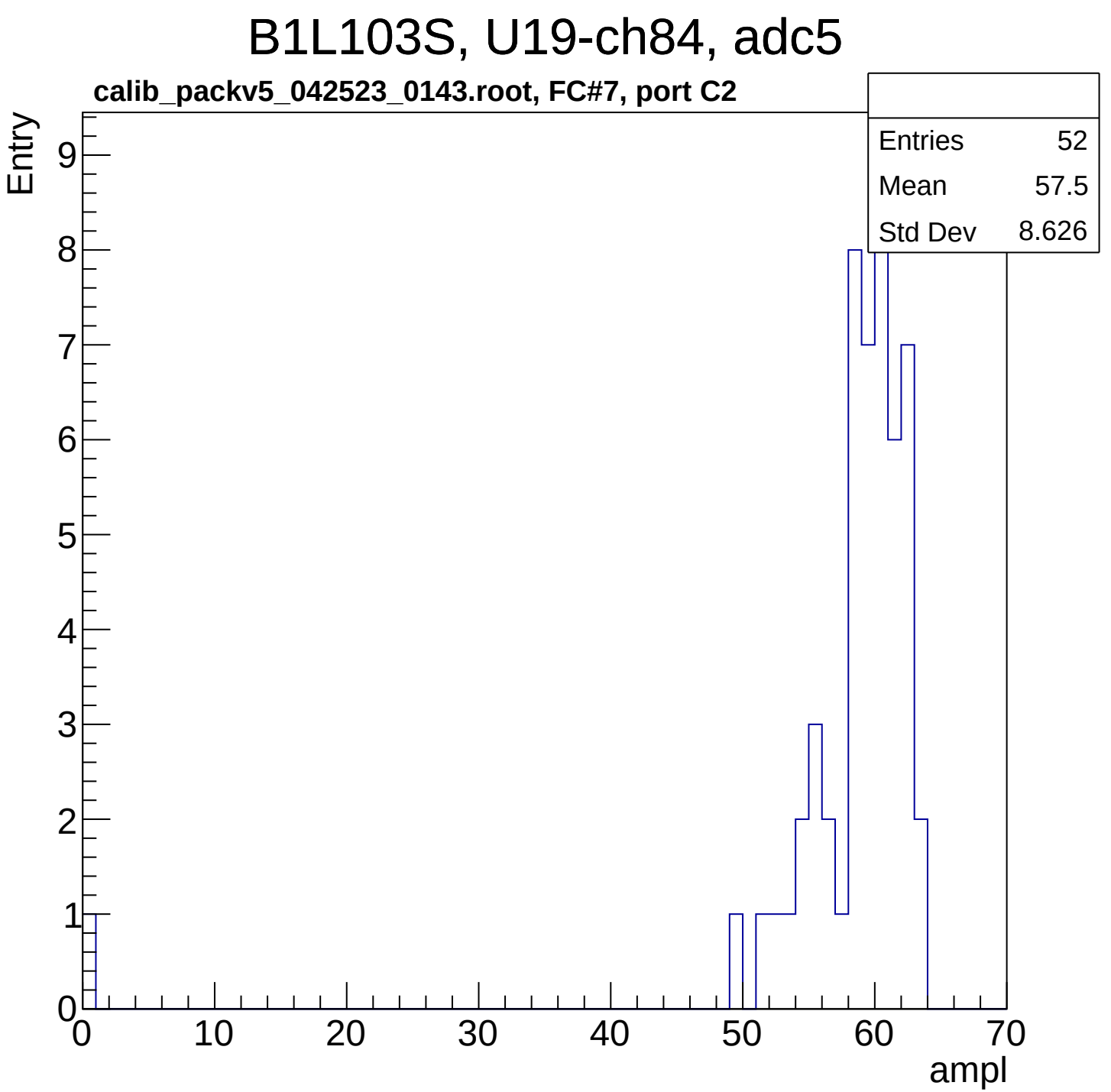
B1L103S, U19-ch84, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.5
Std Dev 8.626
Entry
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7
8
9
0
10
20
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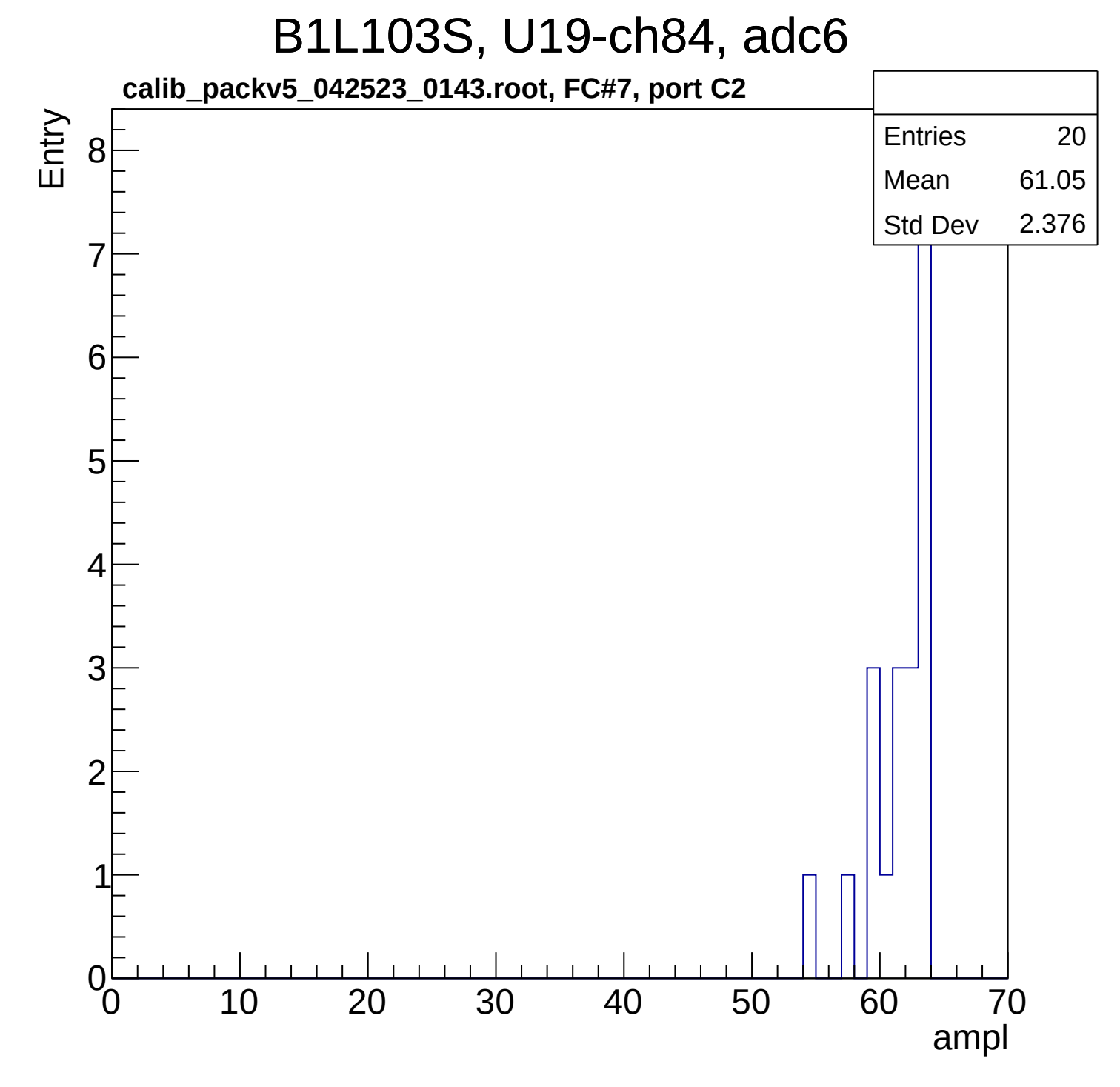
50
60
70
ampl


B1L103S, U19-ch84, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 20
Mean 61.05
Std Dev 2.376
Entry
ampl
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3
4
5
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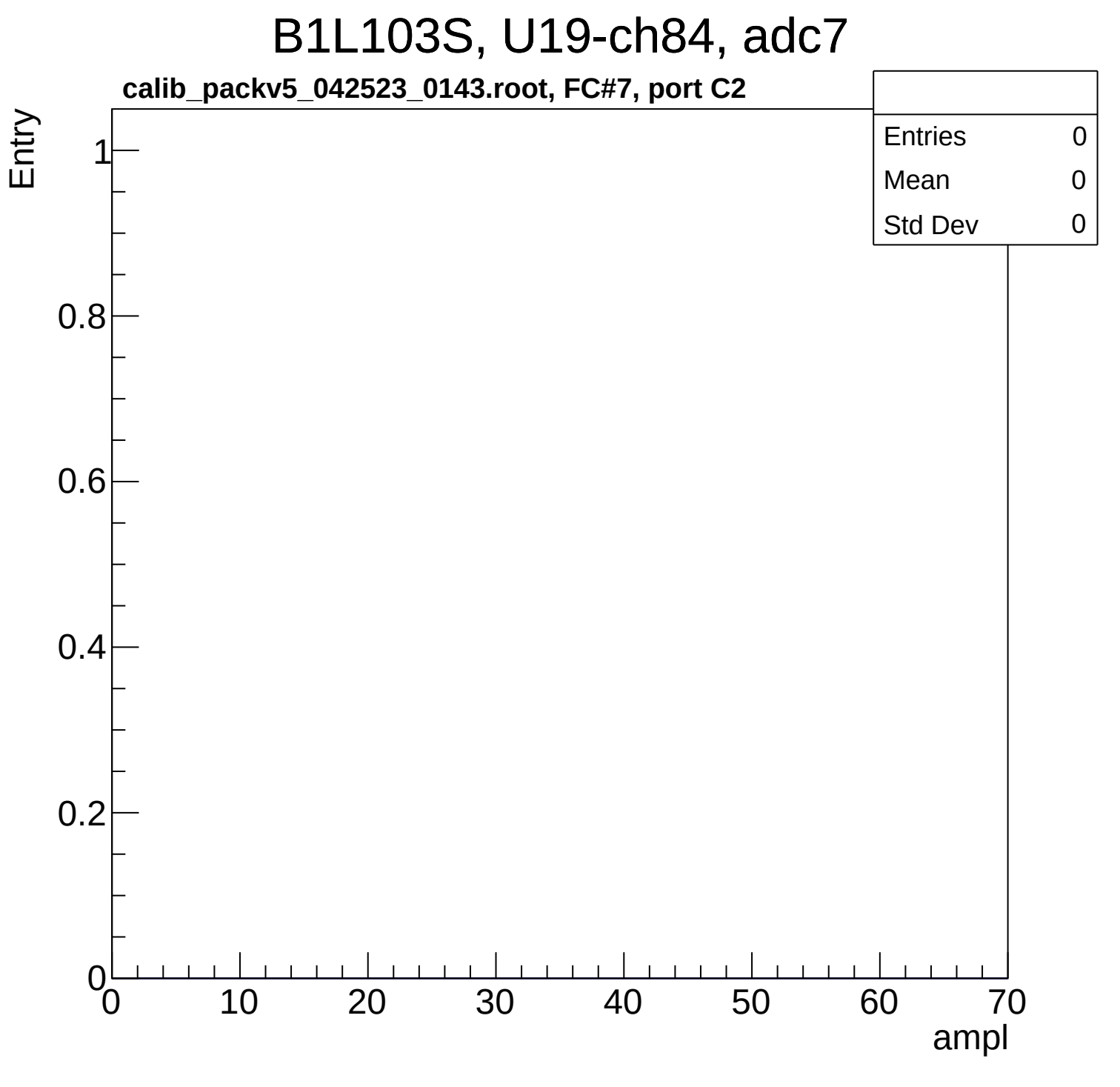
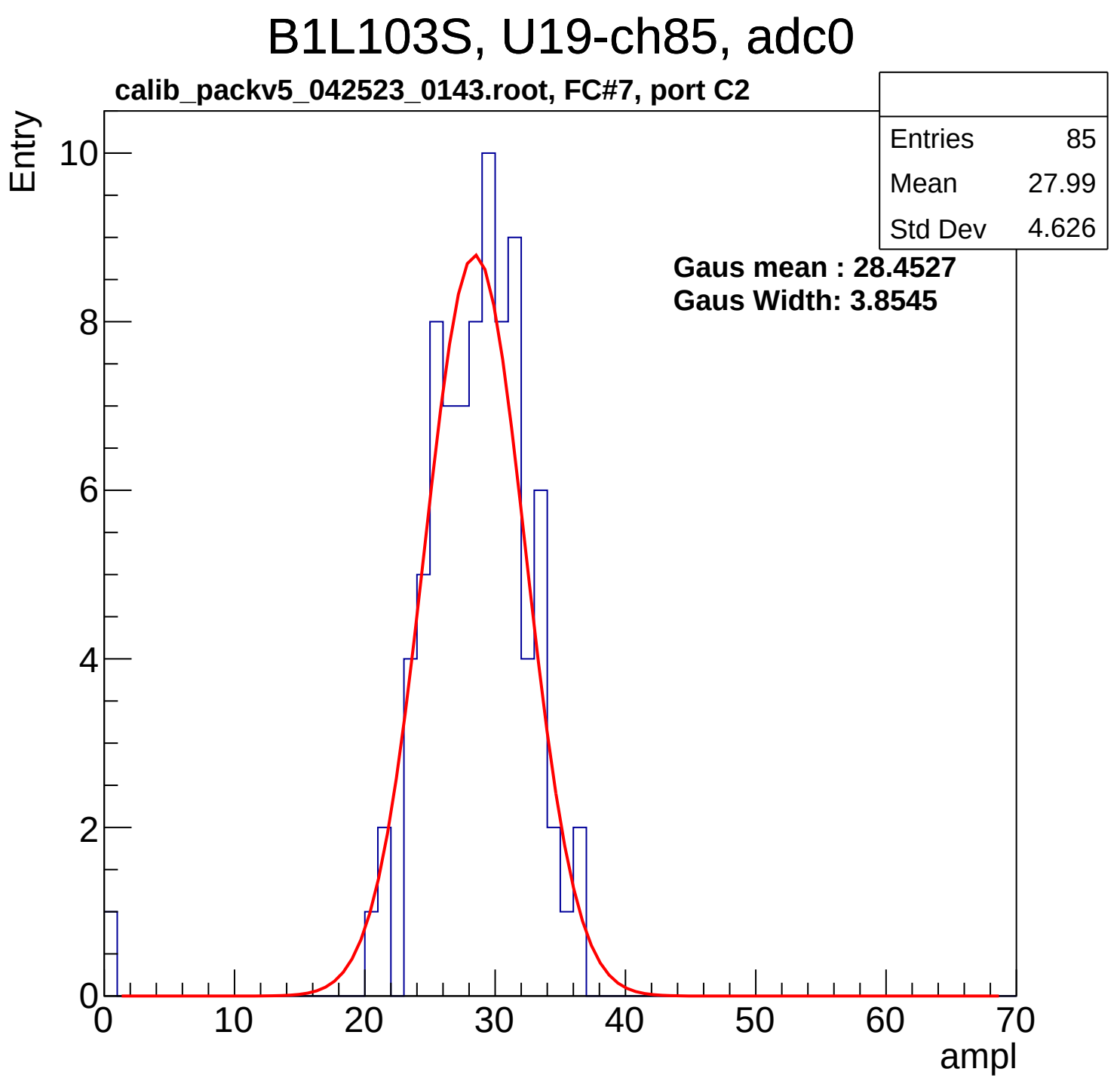

B1L103S, U19-ch84, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch85, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 27.99
Std Dev 4.626
Gaus mean : 28.4527
Gaus Width: 3.8545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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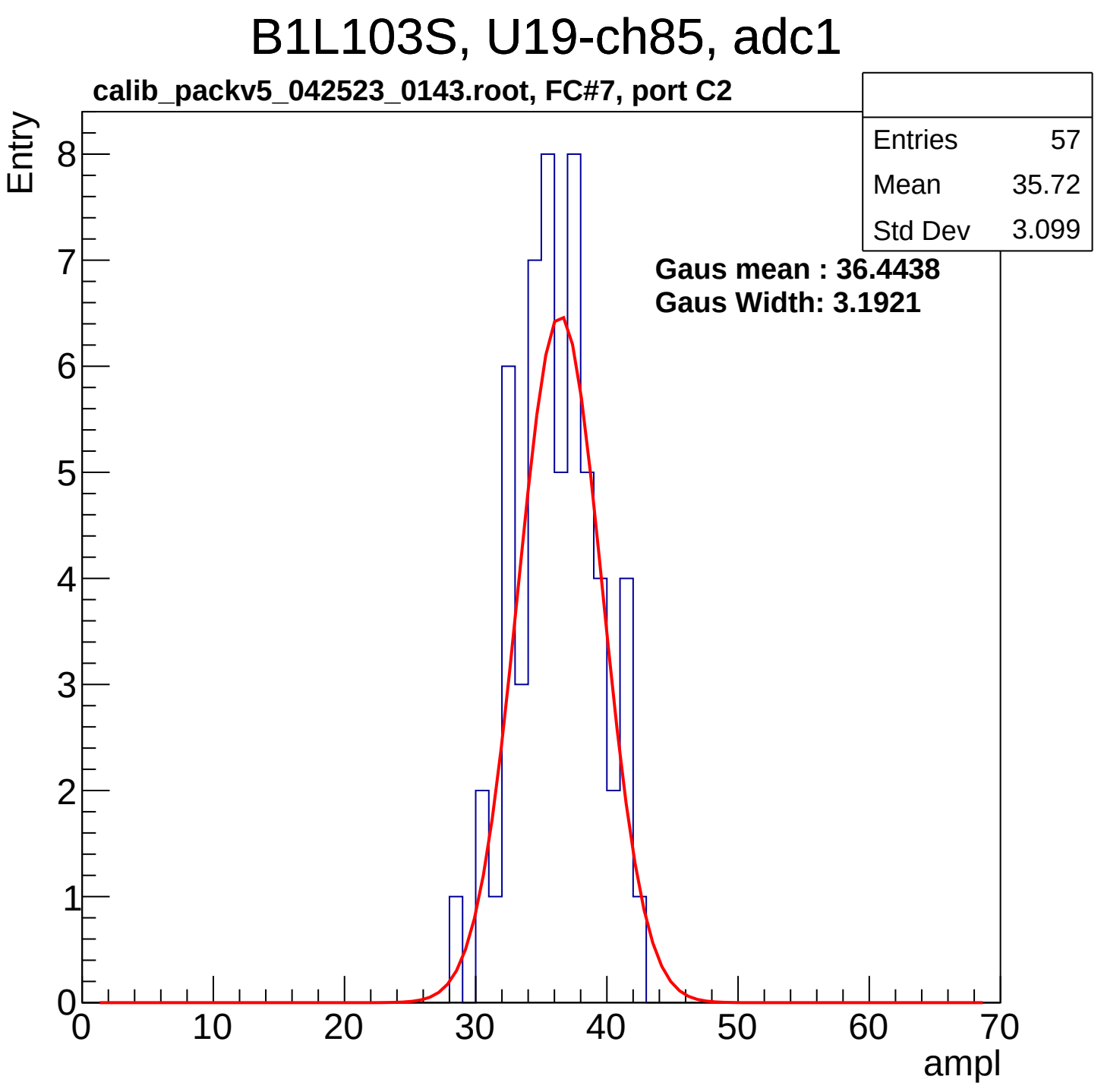
B1L103S, U19-ch85, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 35.72
Std Dev 3.099
Gaus mean : 36.4438
Gaus Width: 3.1921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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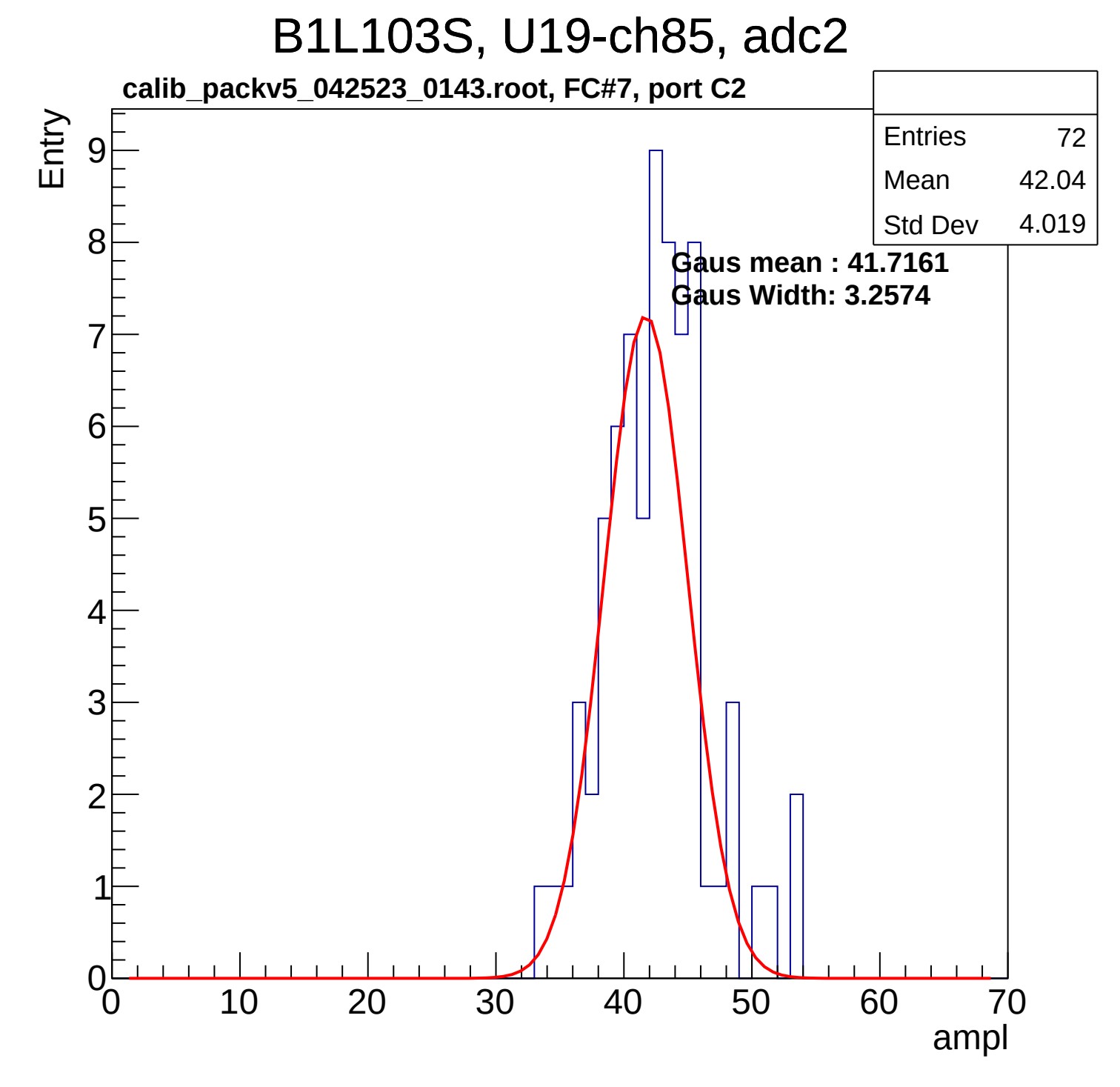
B1L103S, U19-ch85, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 42.04
Std Dev 4.019
Gaus mean : 41.7161
Gaus Width: 3.2574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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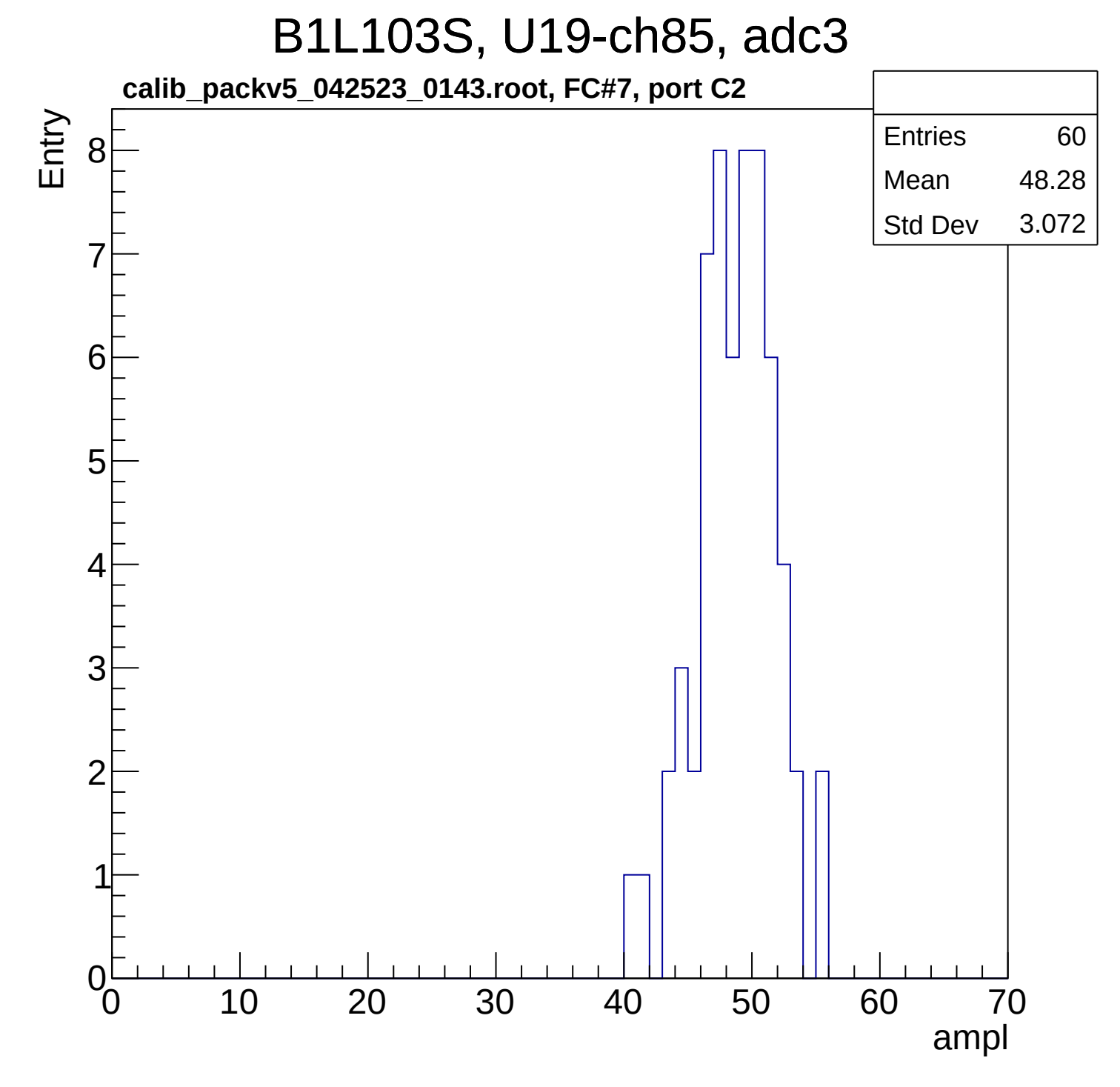
B1L103S, U19-ch85, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.28
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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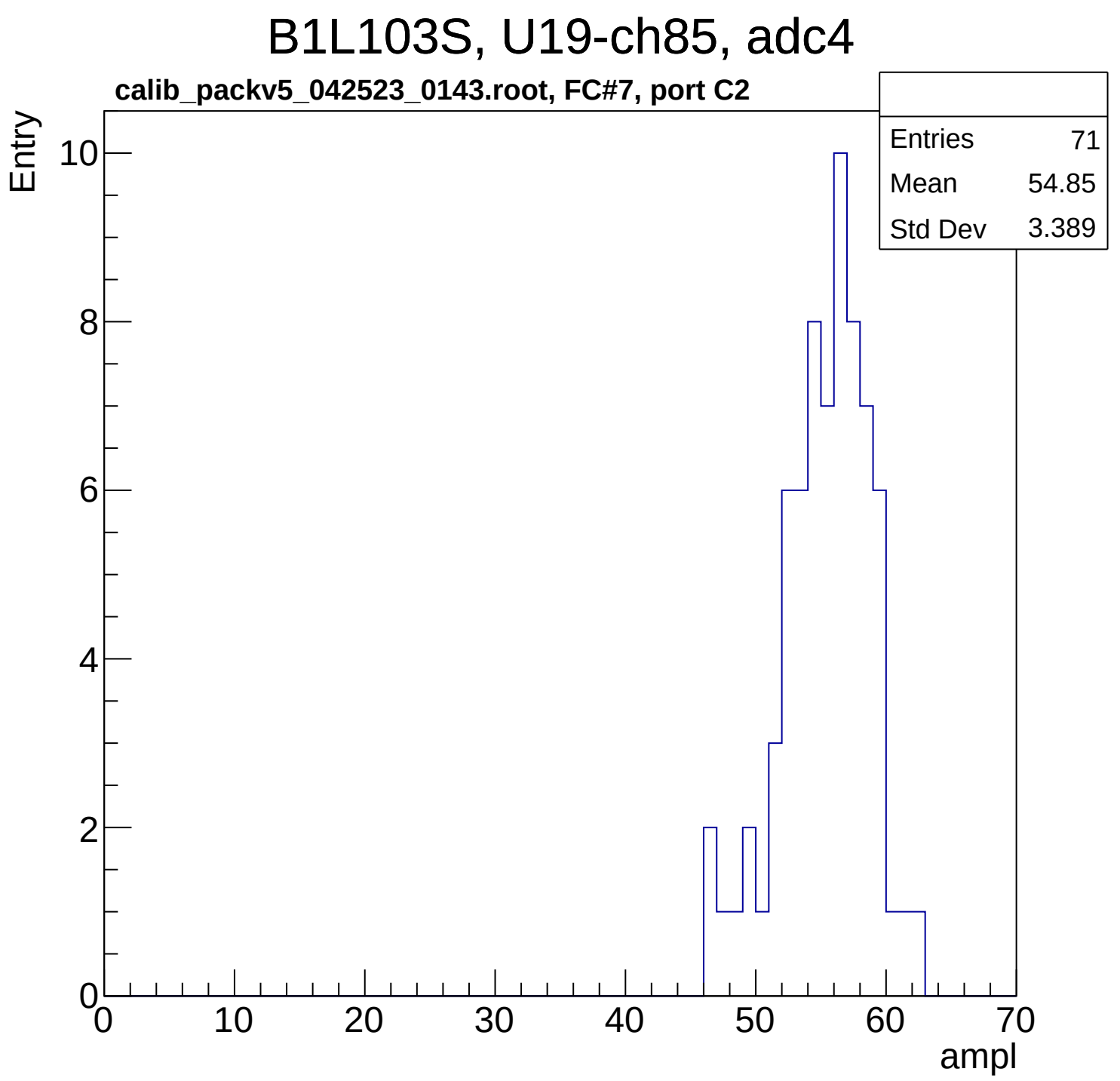
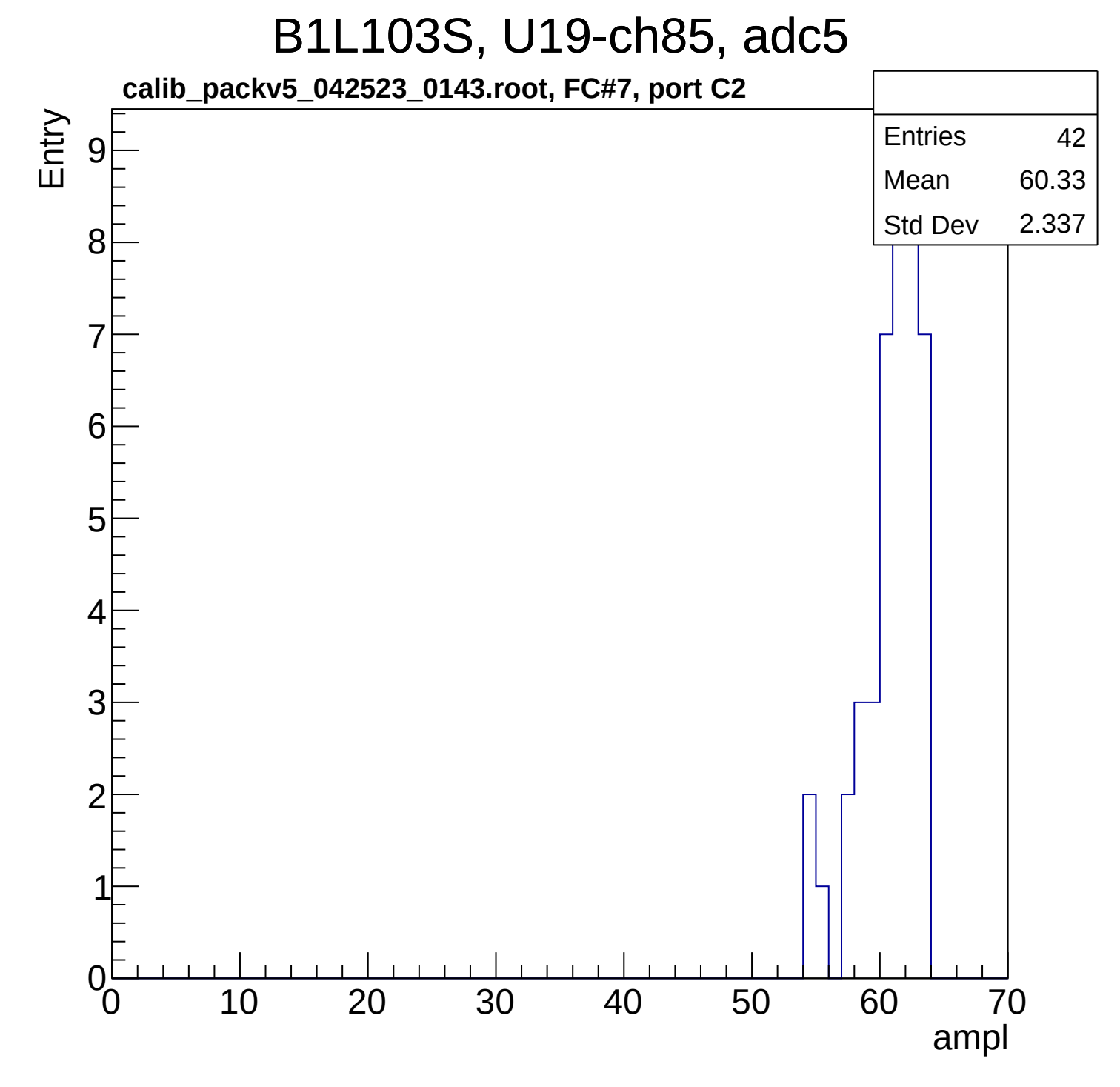

B1L103S, U19-ch85, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 54.85
Std Dev 3.389
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch85, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 60.33
Std Dev 2.337
Entry
ampl
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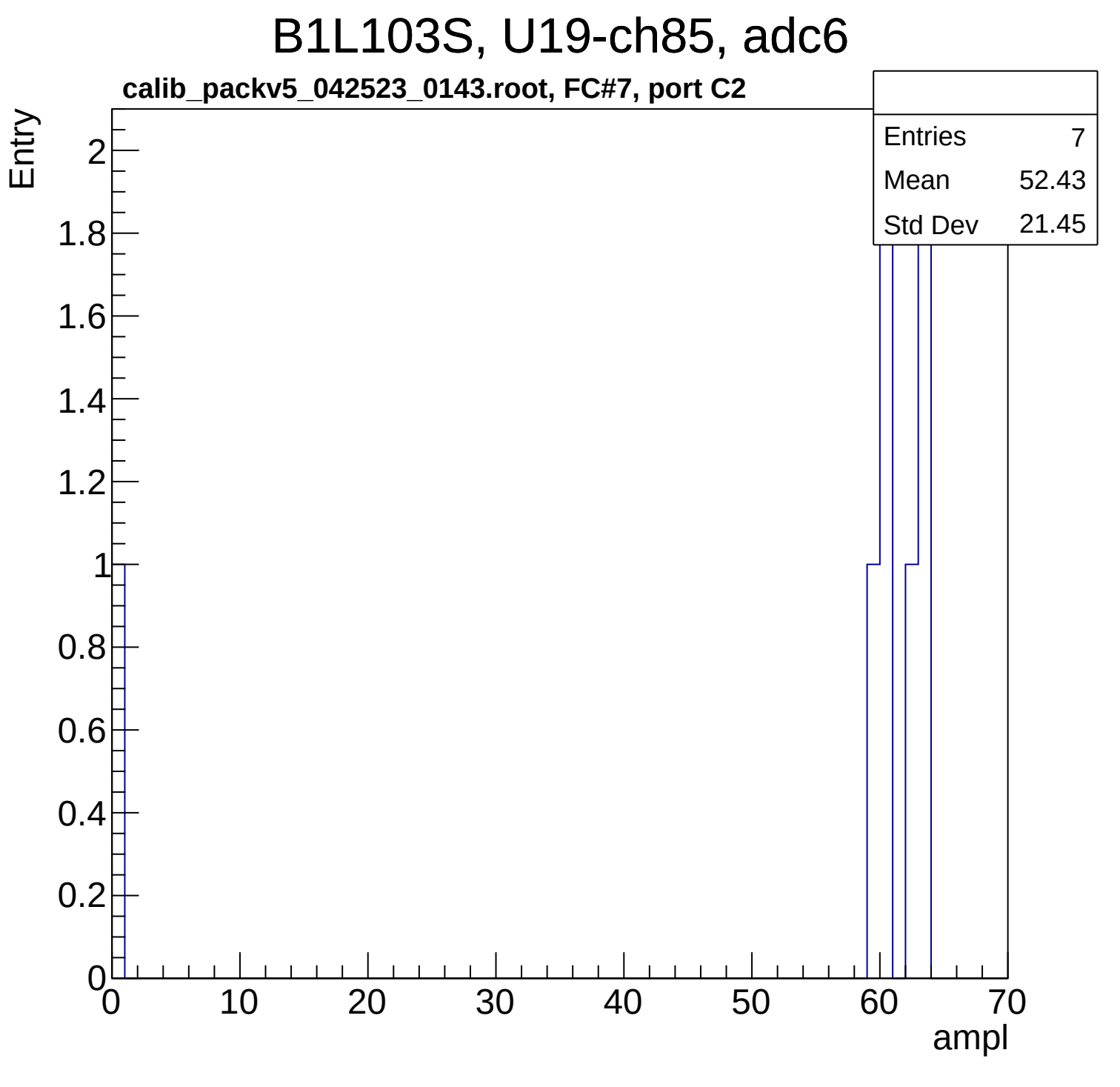
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch85, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 52.43
Std Dev 21.45
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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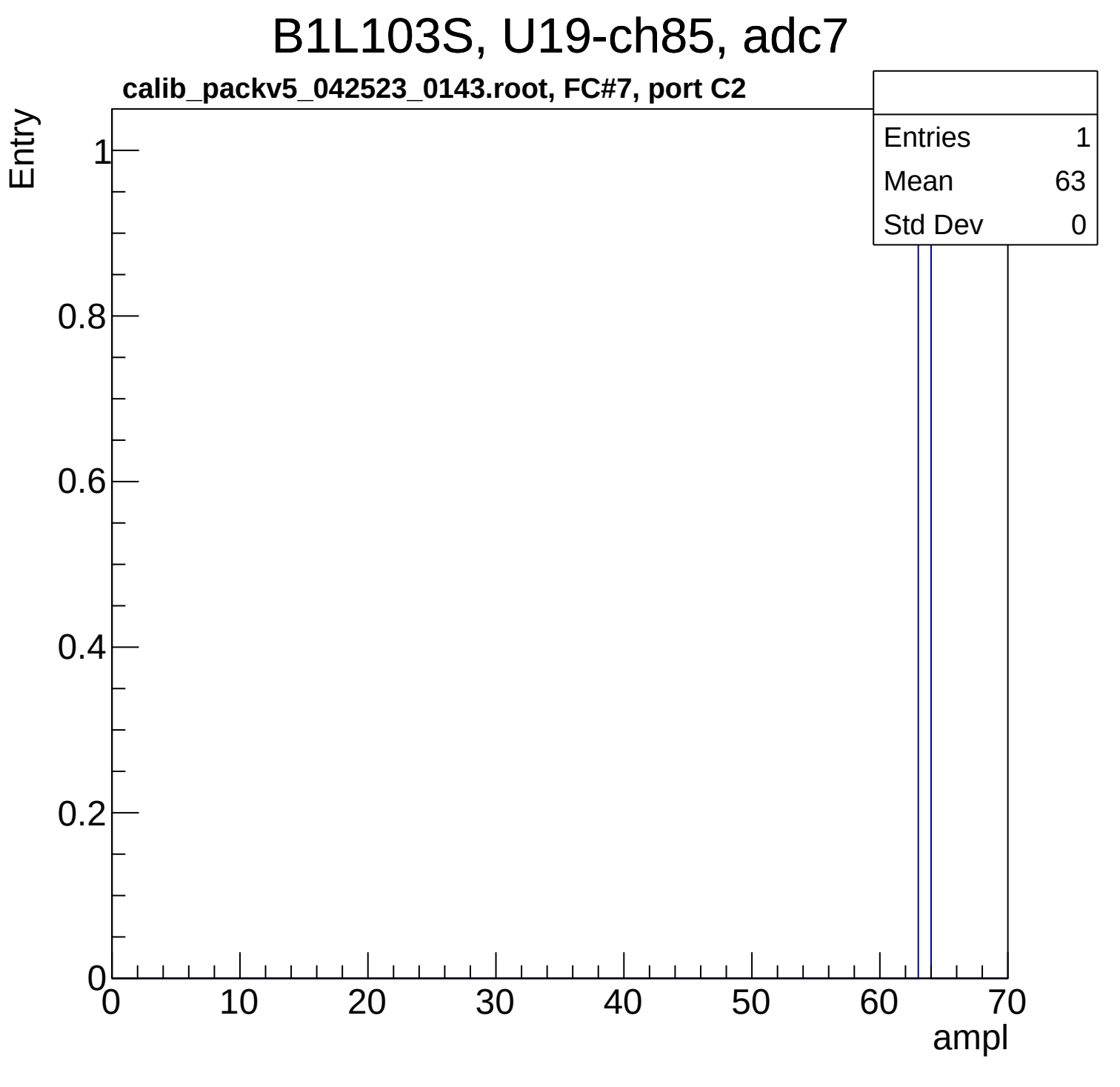
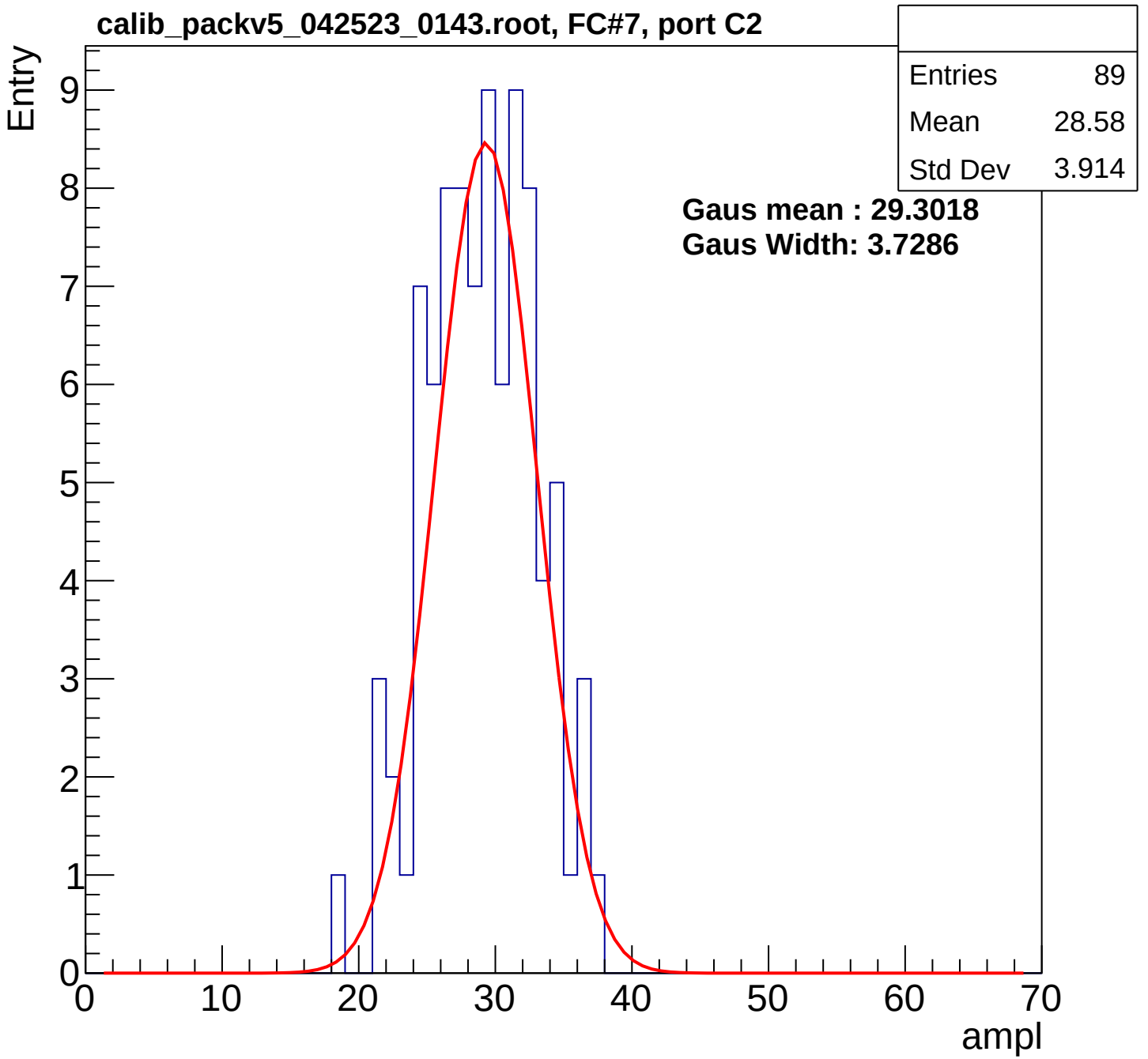
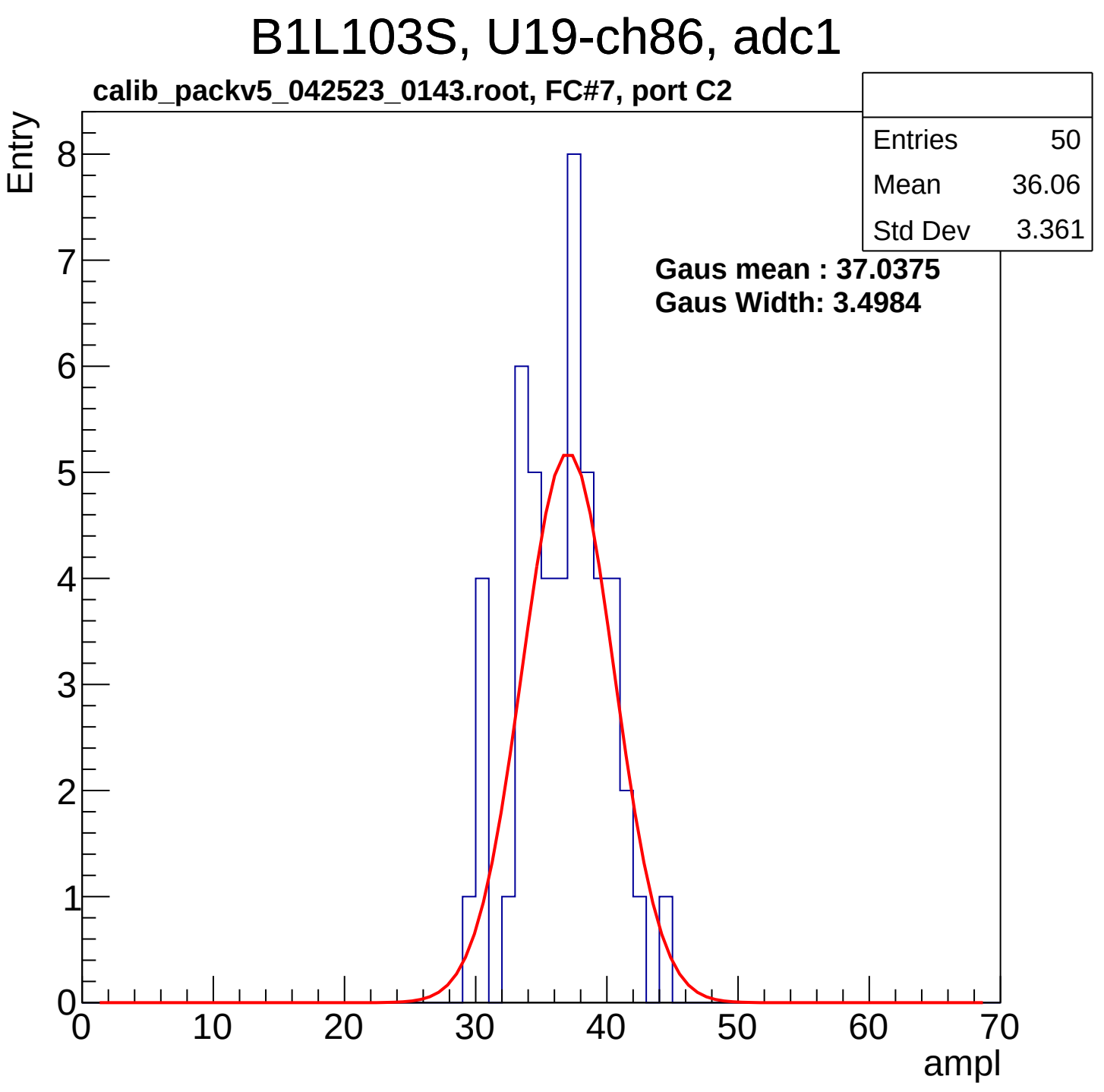

B1L103S, U19-ch85, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch86, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 28.58
Std Dev 3.914
Gaus mean : 29.3018
Gaus Width: 3.7286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch86, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 36.06
Std Dev 3.361
Gaus mean : 37.0375
Gaus Width: 3.4984
Entry
ampl
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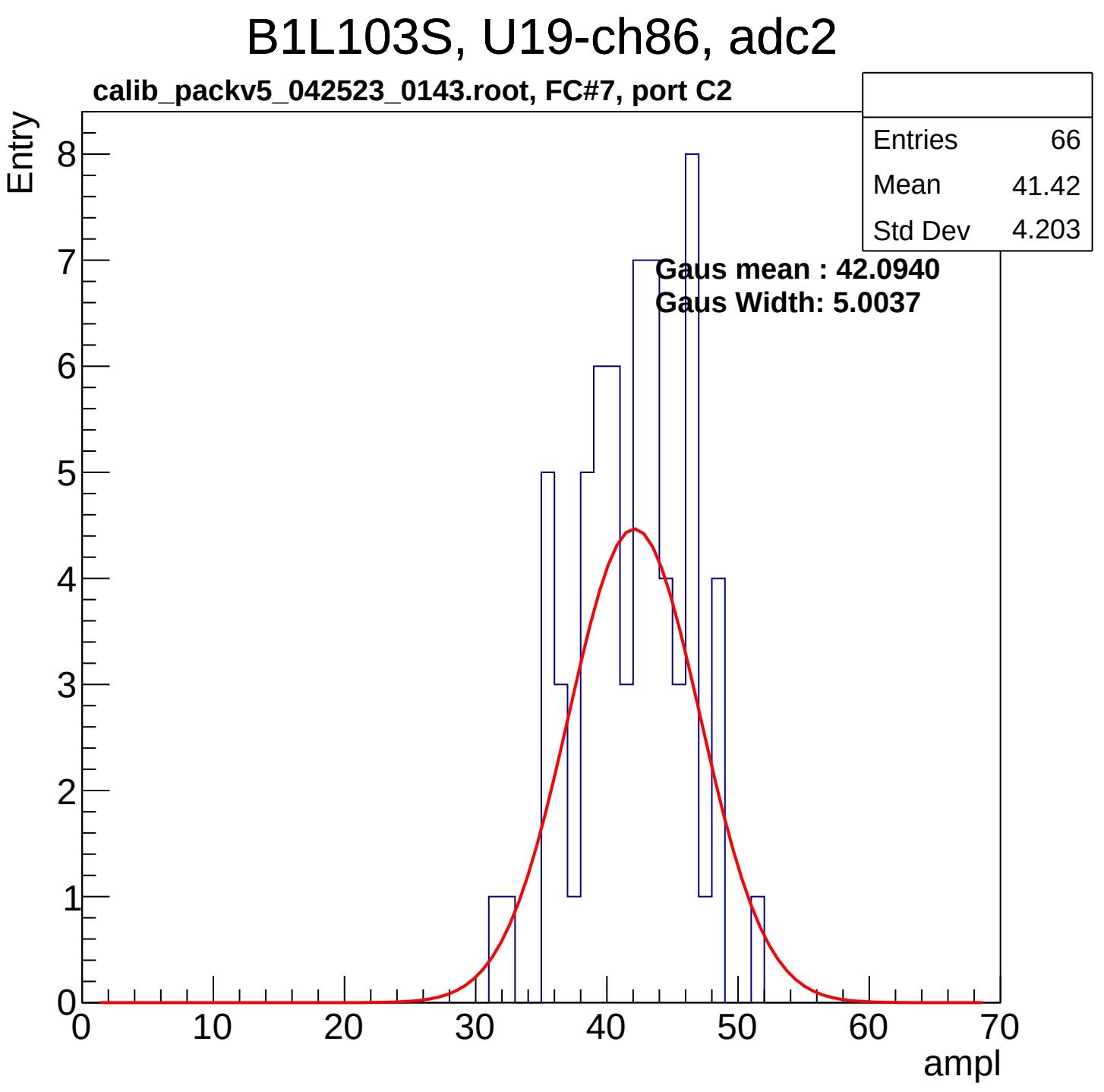
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch86, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 41.42
Std Dev 4.203
Gaus mean : 42.0940
Gaus Width: 5.0037
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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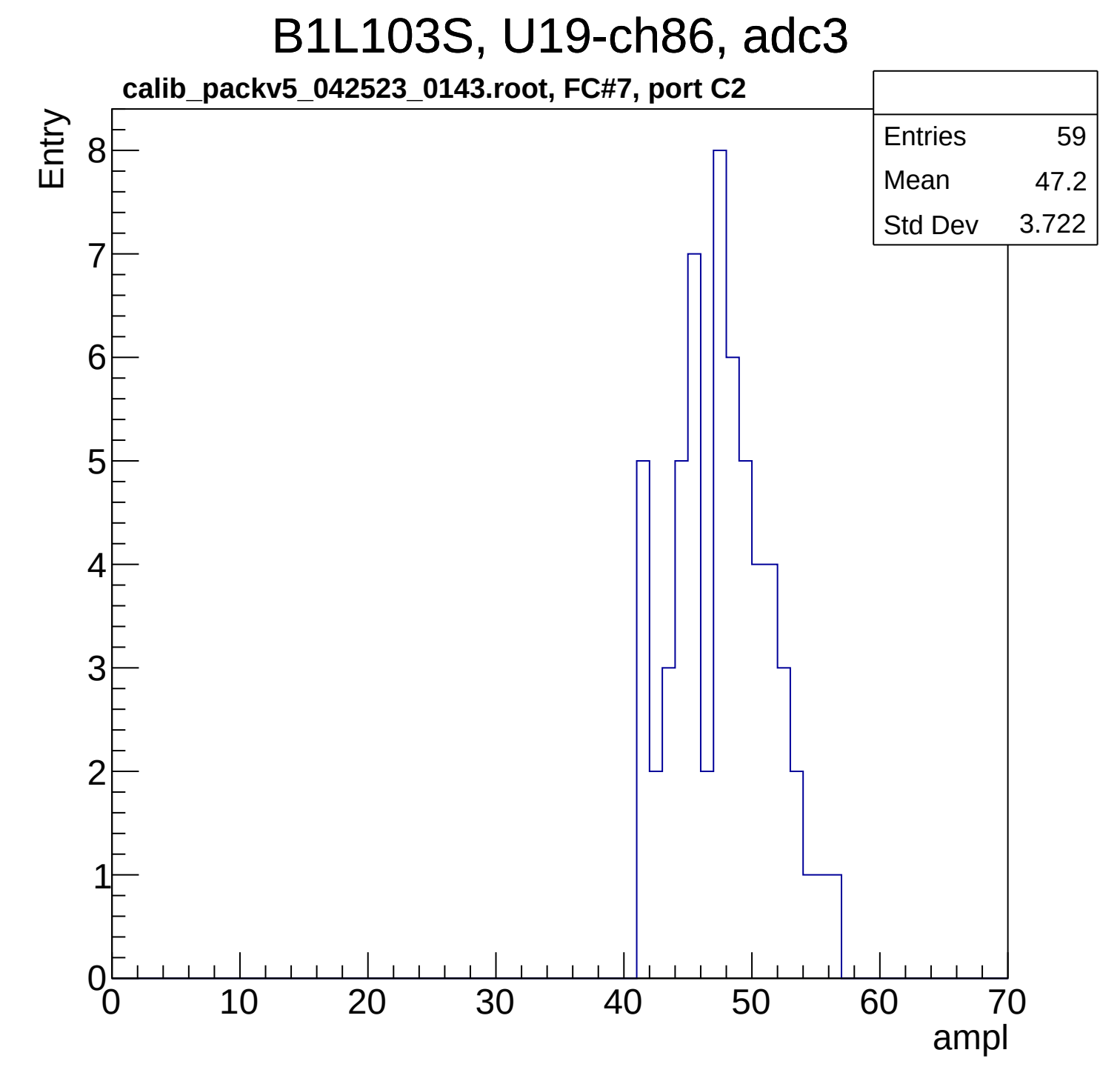
B1L103S, U19-ch86, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 47.2
Std Dev 3.722
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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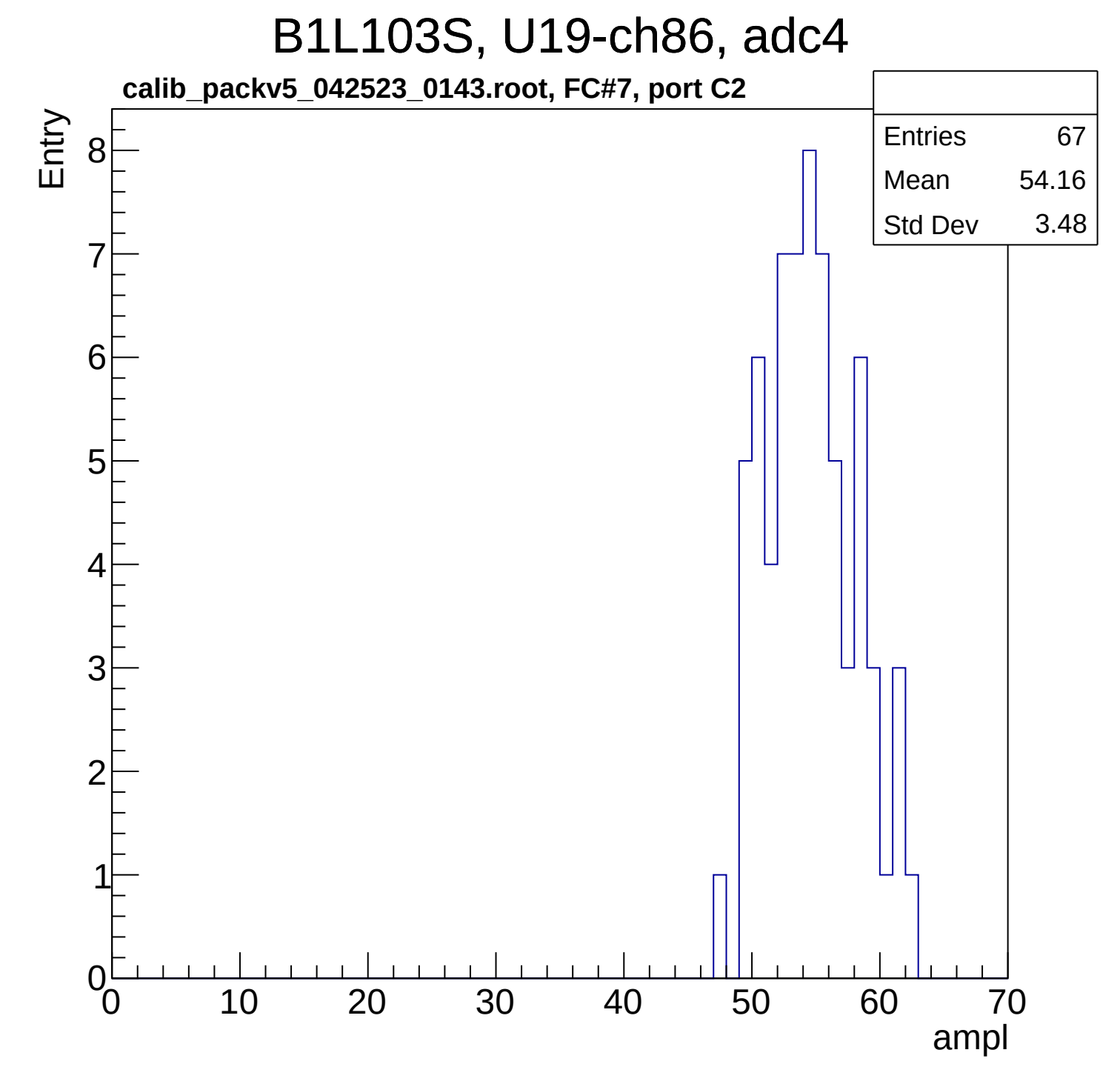
B1L103S, U19-ch86, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.16
Std Dev 3.48
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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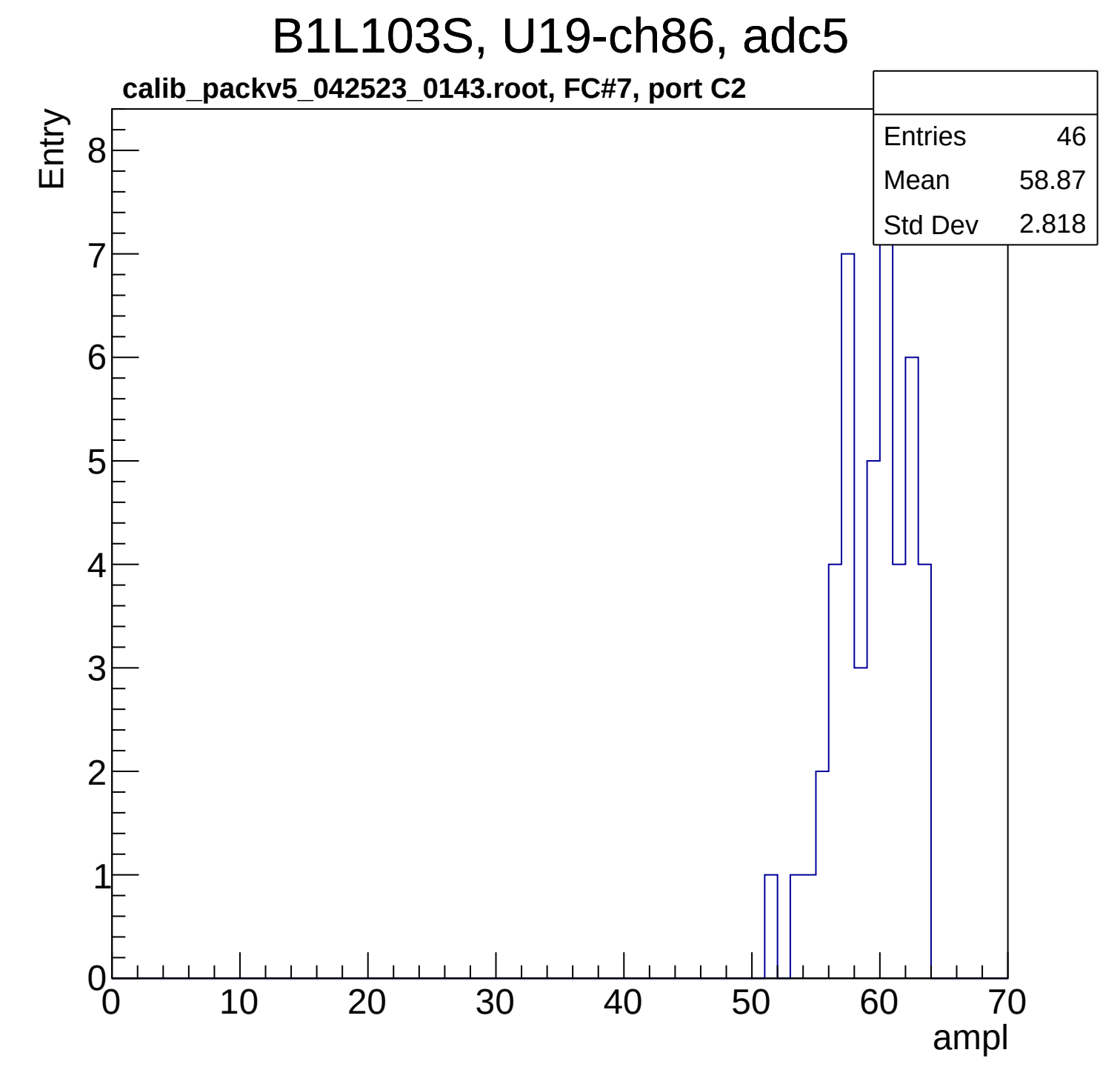
B1L103S, U19-ch86, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.87
Std Dev 2.818
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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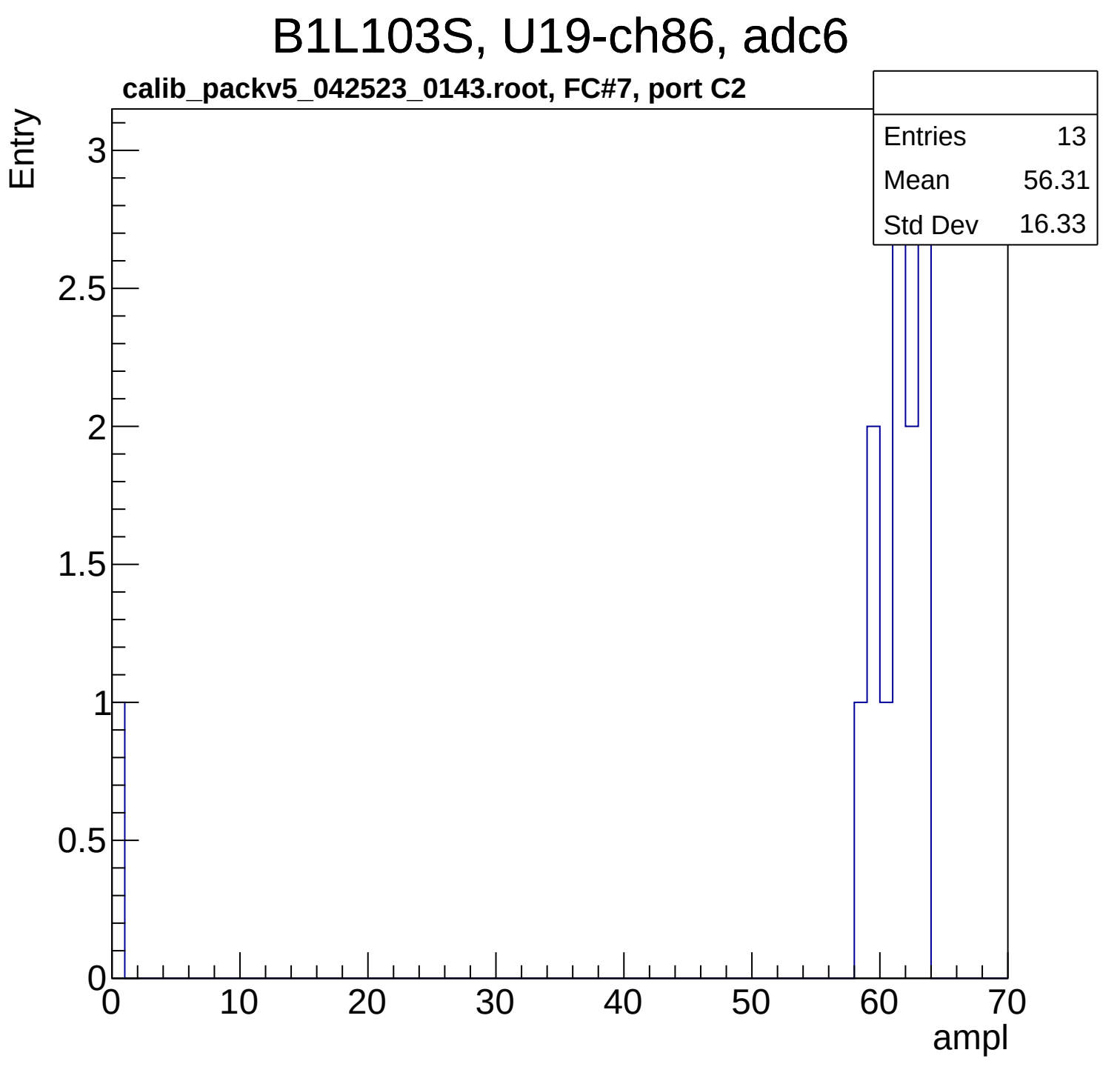
B1L103S, U19-ch86, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.31
Std Dev 16.33
Entry
ampl
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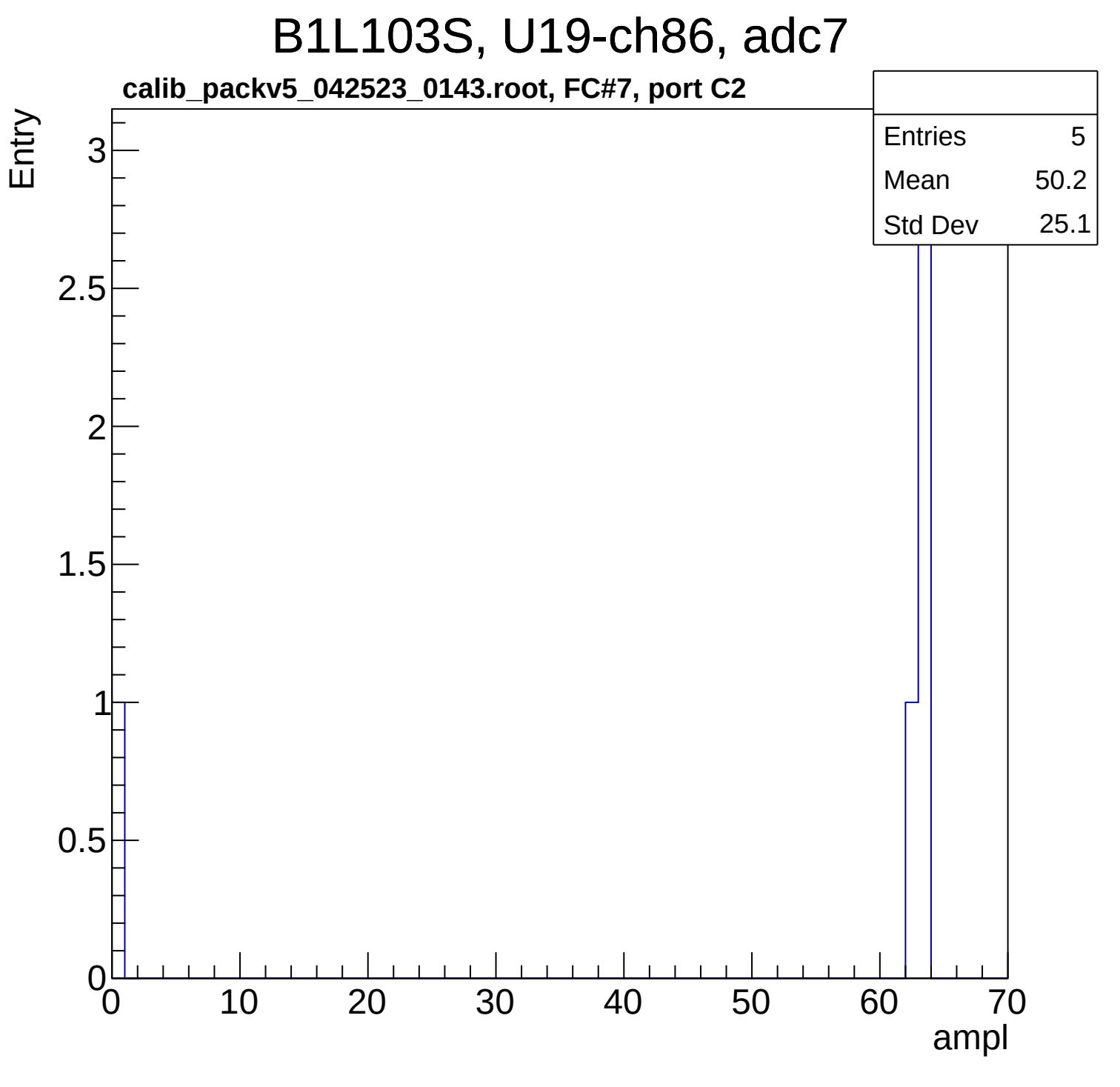
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch86, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
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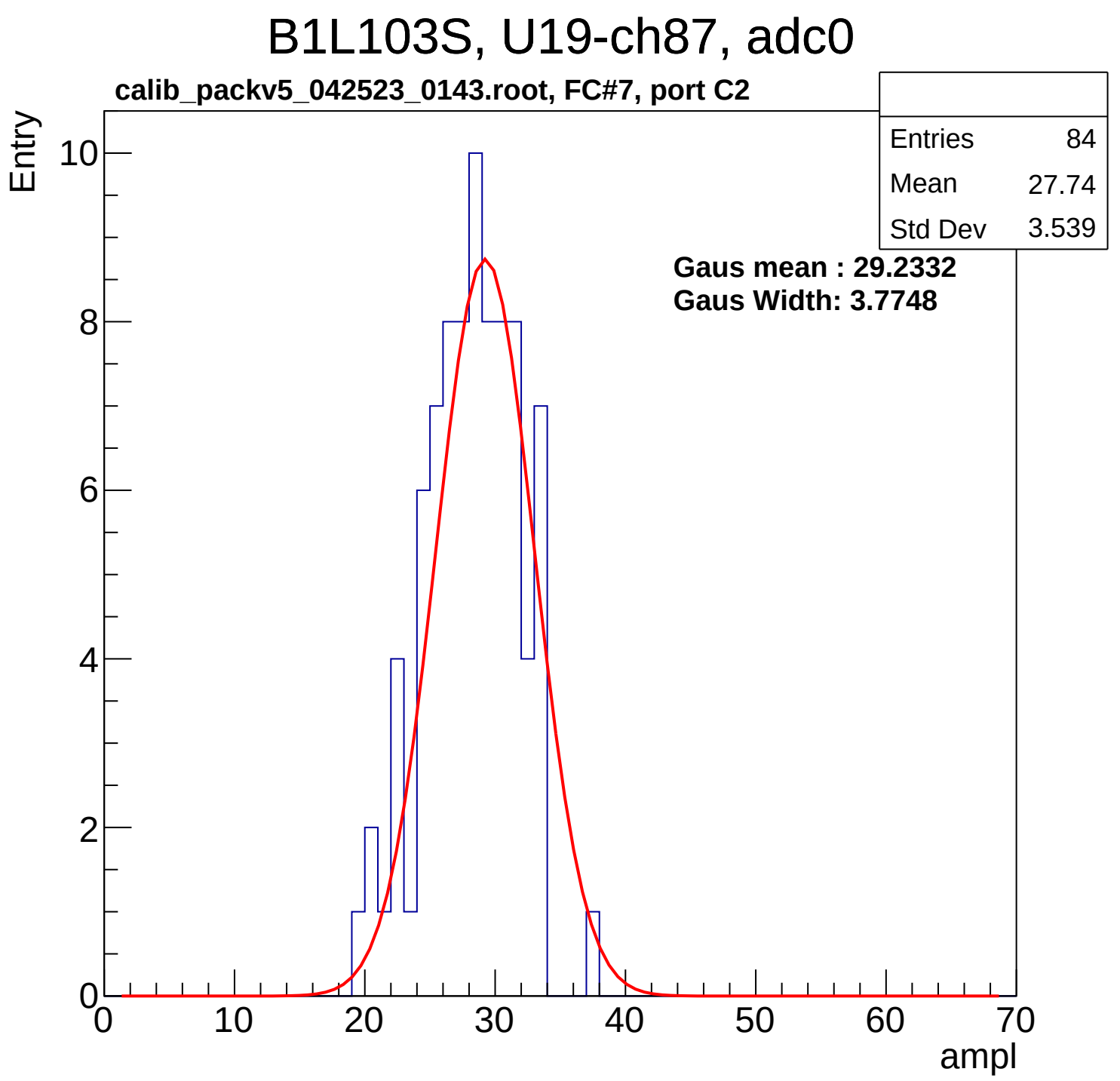
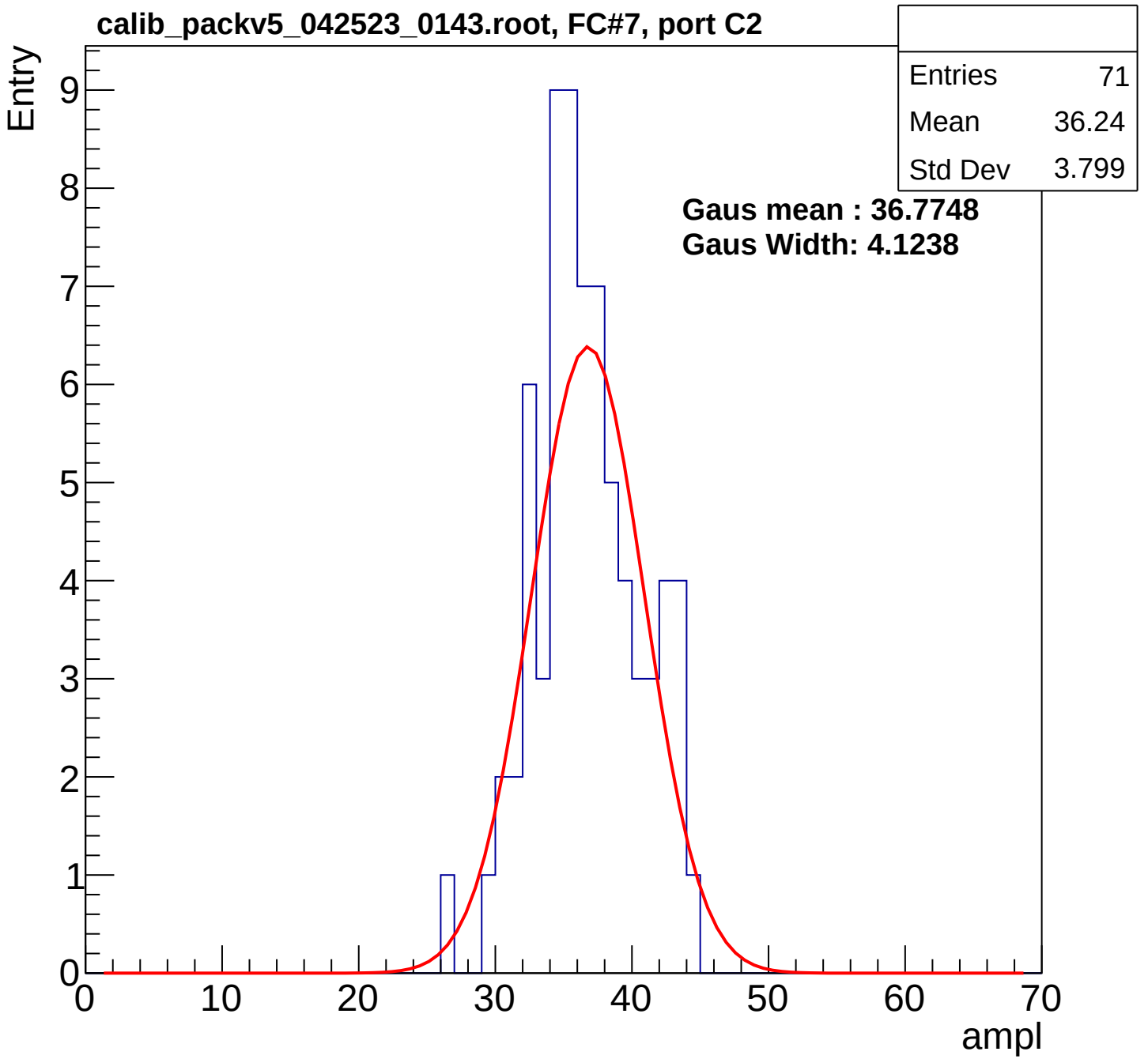
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch87, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 27.74
Std Dev 3.539
Gaus mean : 29.2332
Gaus Width: 3.7748
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch87, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.24
Std Dev 3.799
Gaus mean : 36.7748
Gaus Width: 4.1238
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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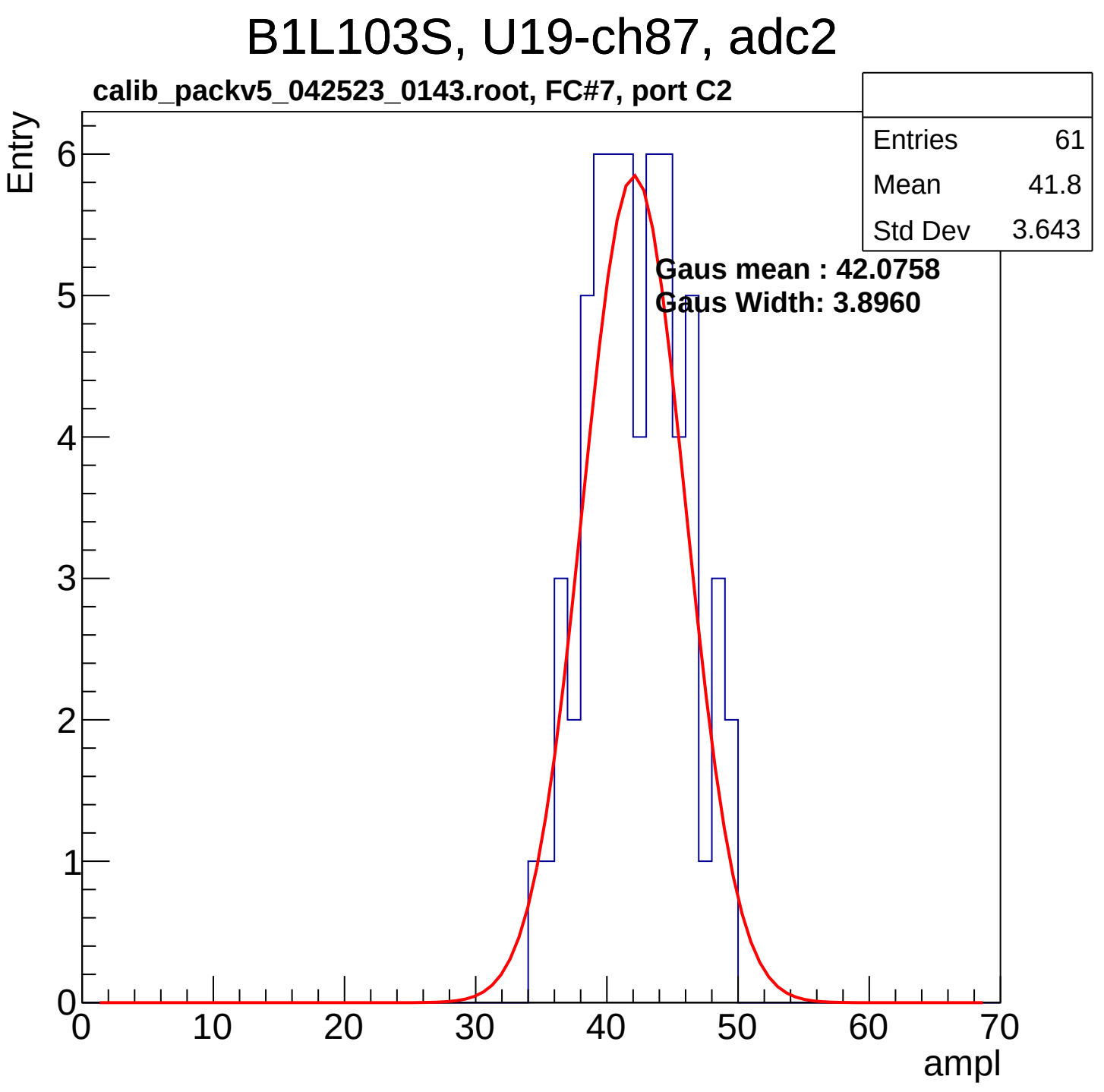
B1L103S, U19-ch87, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.8
Std Dev 3.643
Gaus mean : 42.0758
Gaus Width: 3.8960
Entry
ampl
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1
2
3
4
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6
0
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40
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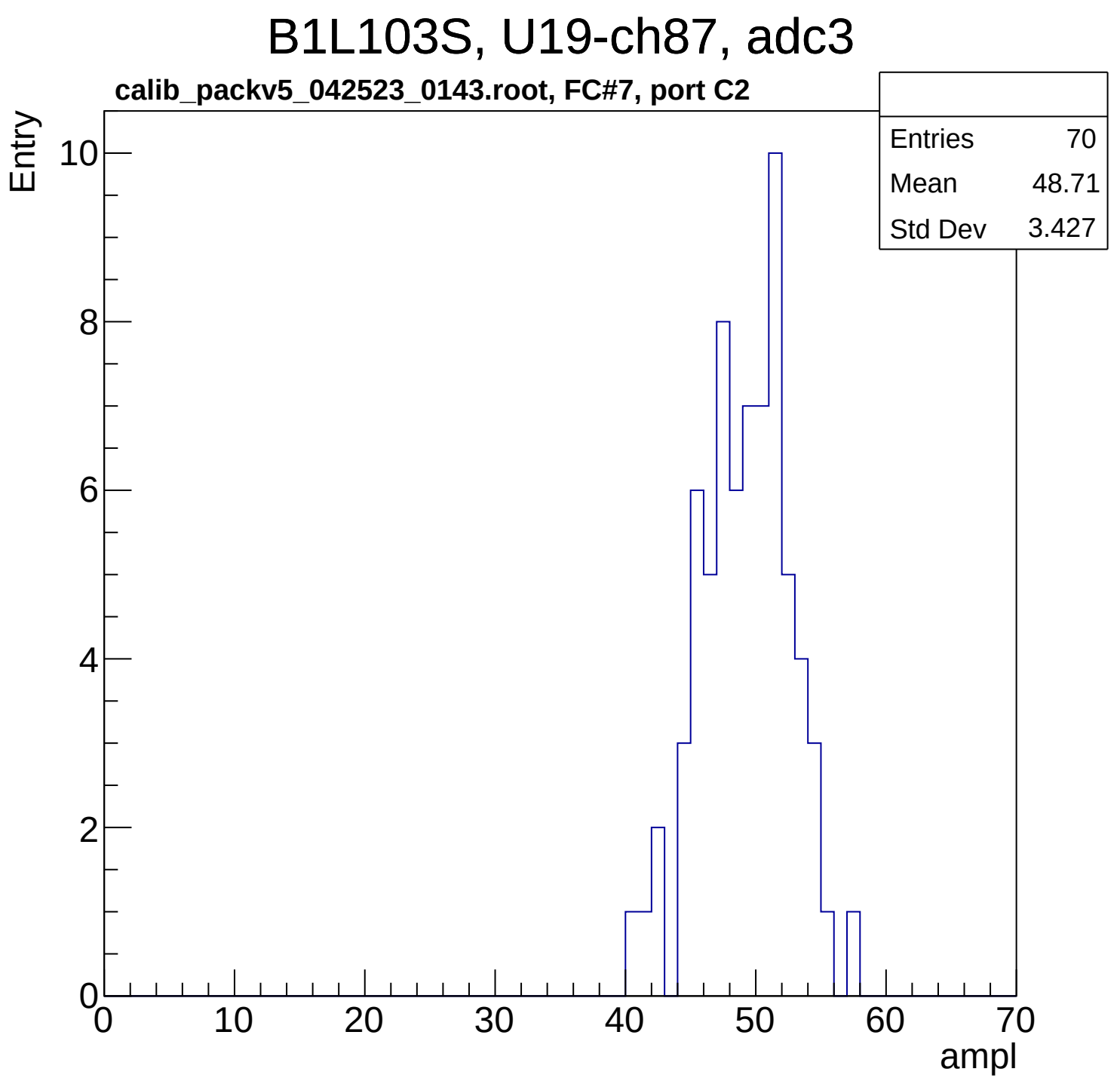
60
70


B1L103S, U19-ch87, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 48.71
Std Dev 3.427
Entry
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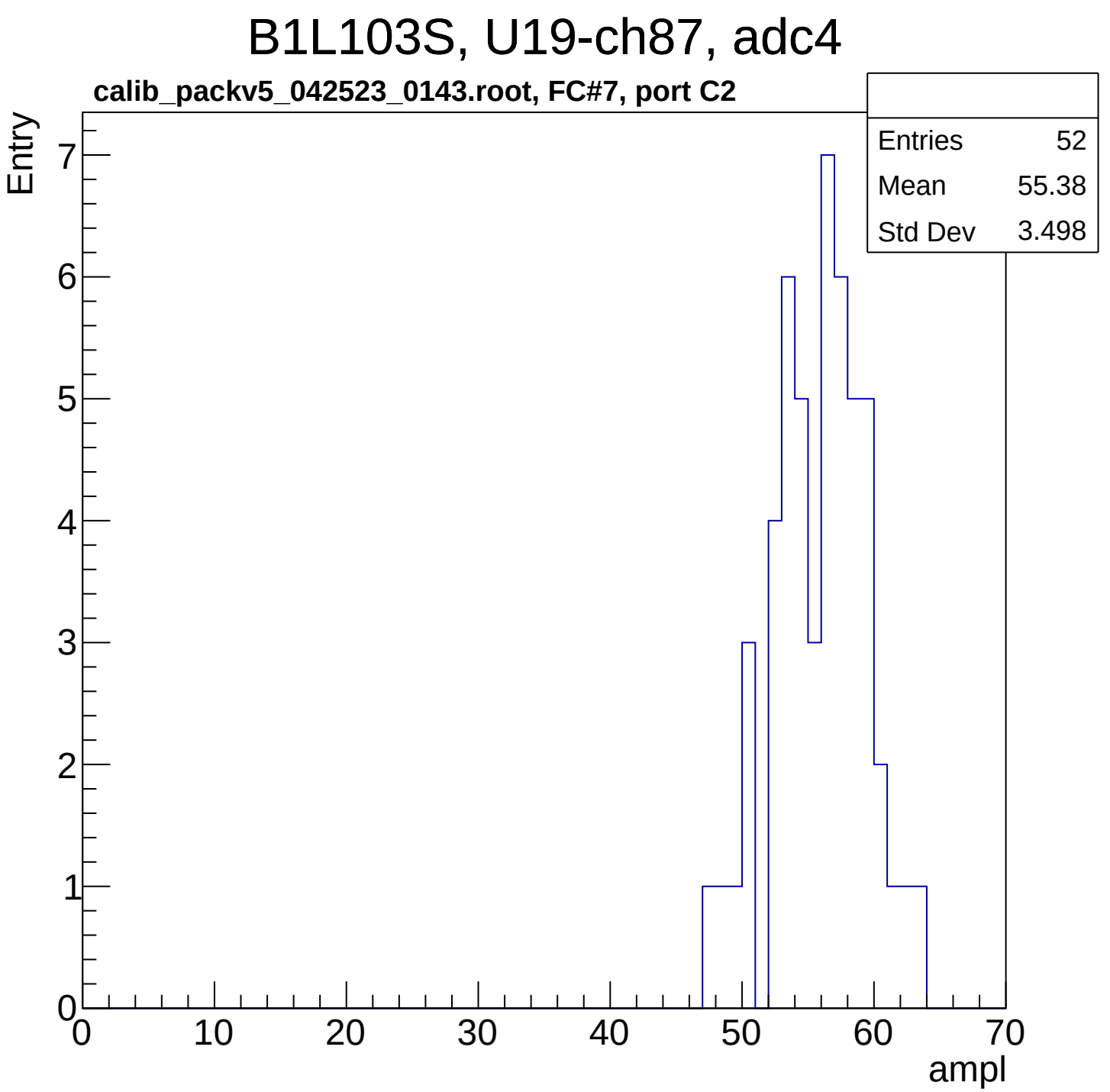
50
60
70
ampl


B1L103S, U19-ch87, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.38
Std Dev 3.498
Entry
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7
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10
20
30
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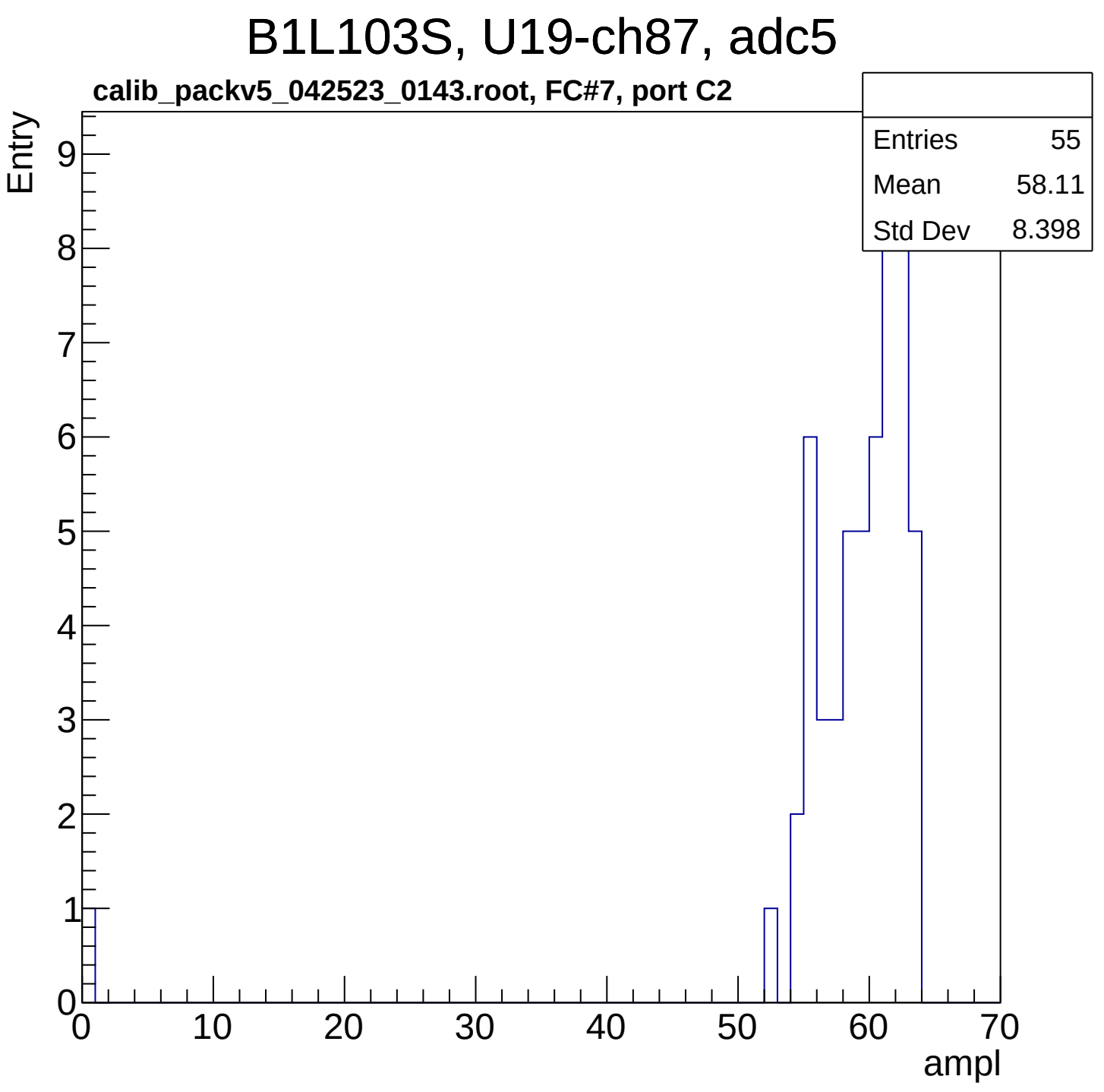
50
60
70
ampl


B1L103S, U19-ch87, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.11
Std Dev 8.398
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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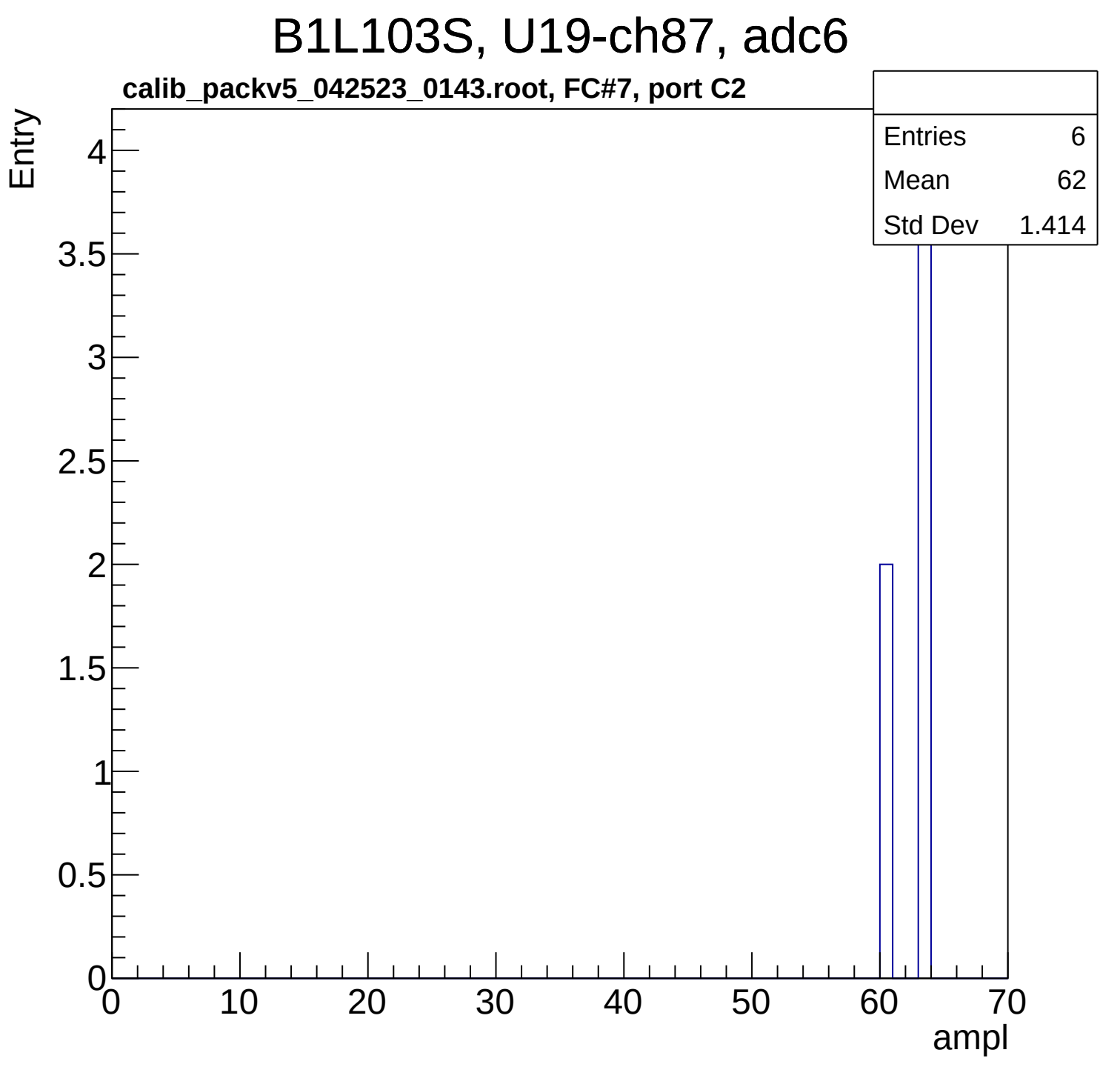
B1L103S, U19-ch87, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62
Std Dev 1.414
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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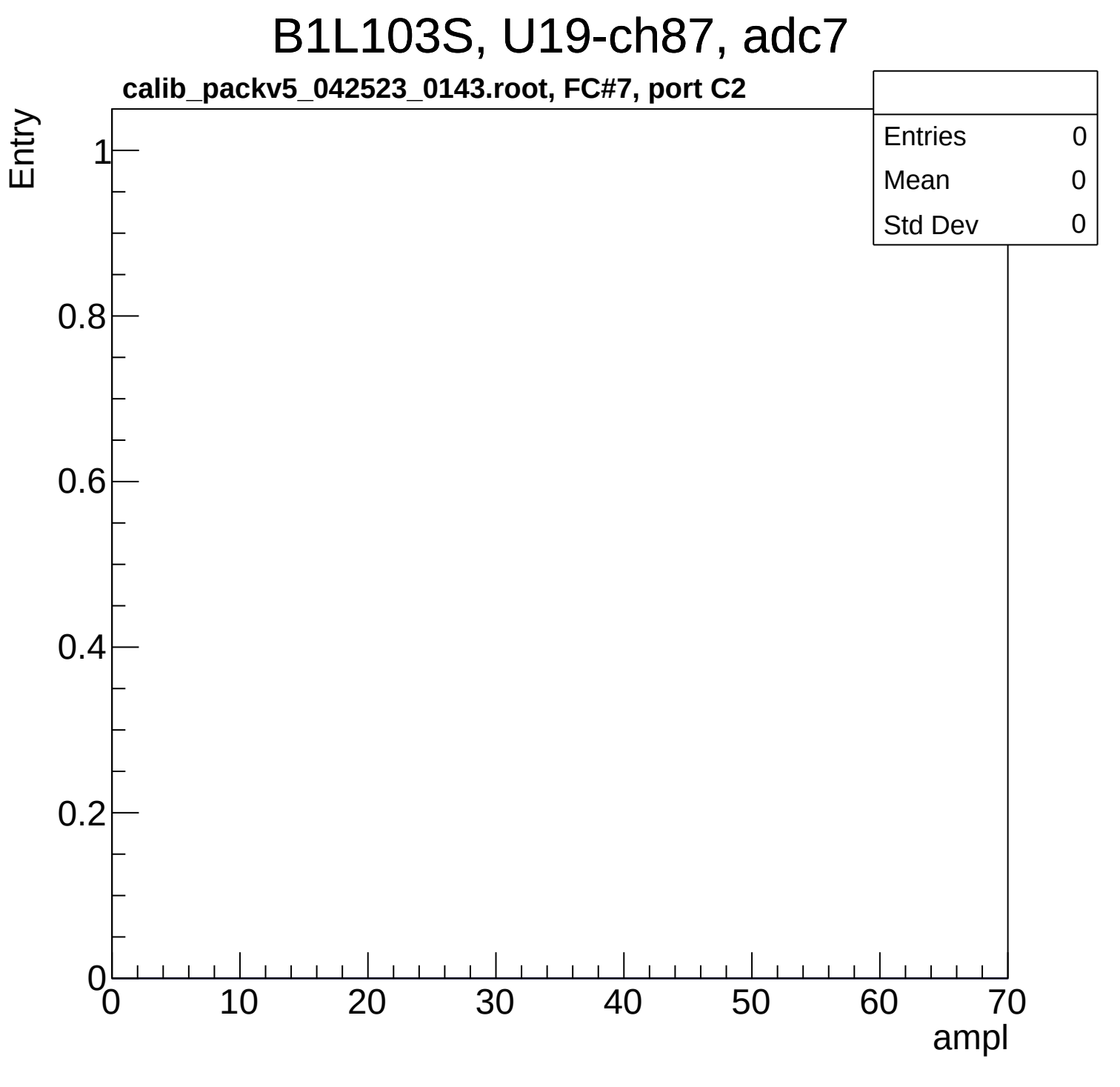
40
50
60
70


B1L103S, U19-ch87, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
20
30
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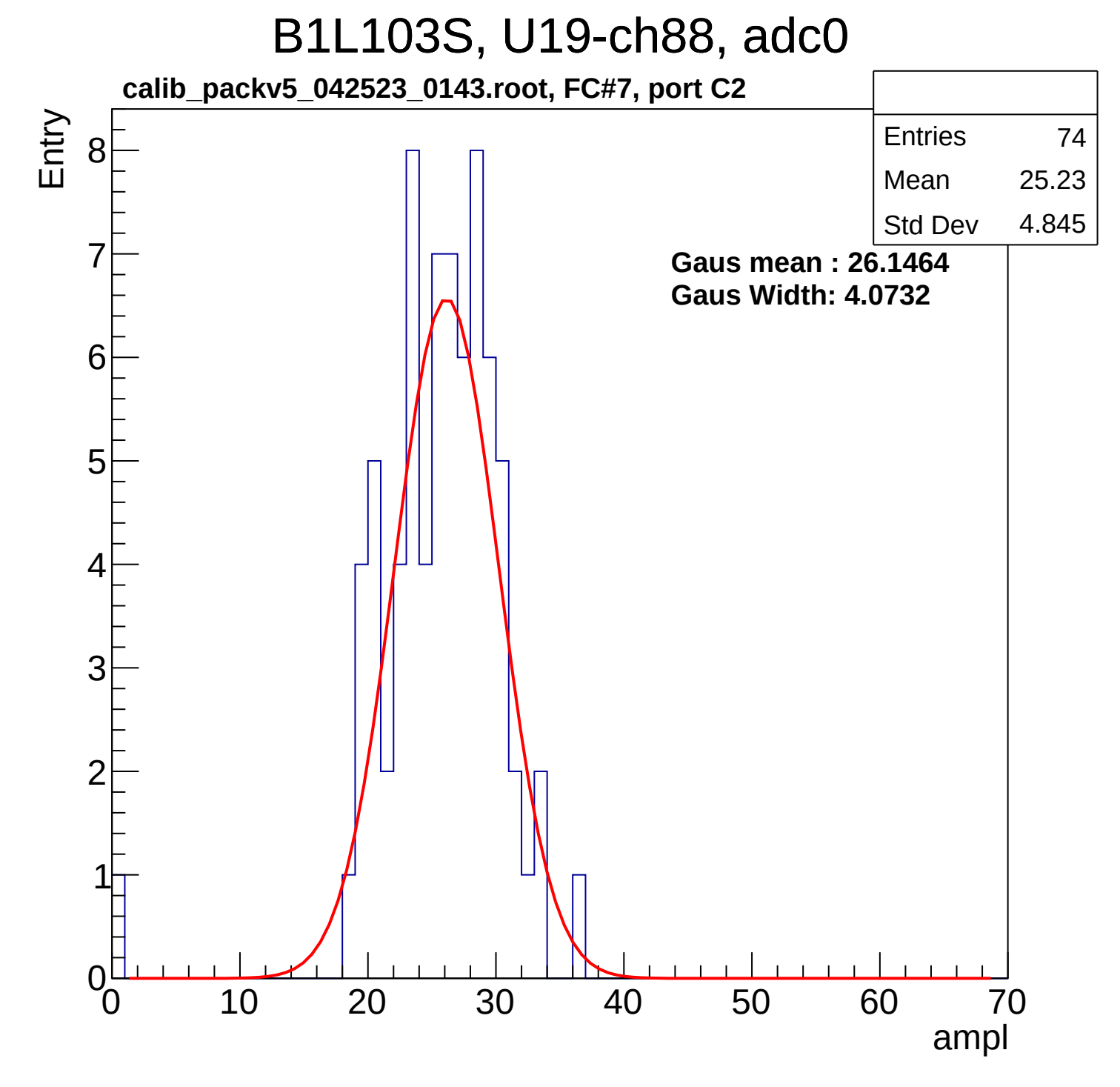
50
60
70
ampl


B1L103S, U19-ch88, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 25.23
Std Dev 4.845
Gaus mean : 26.1464
Gaus Width: 4.0732
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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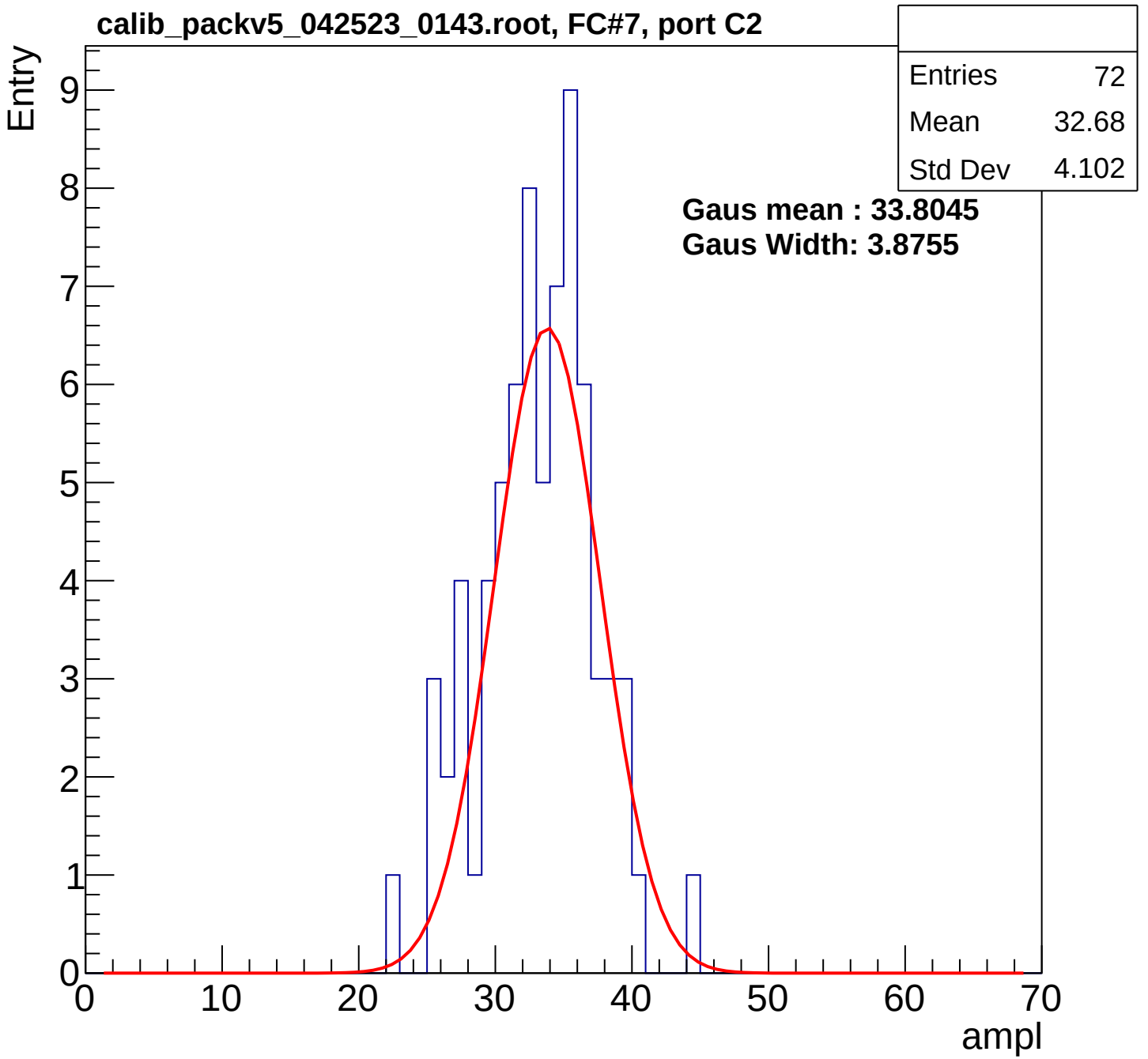
B1L103S, U19-ch88, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 32.68
Std Dev 4.102
Gaus mean : 33.8045
Gaus Width: 3.8755
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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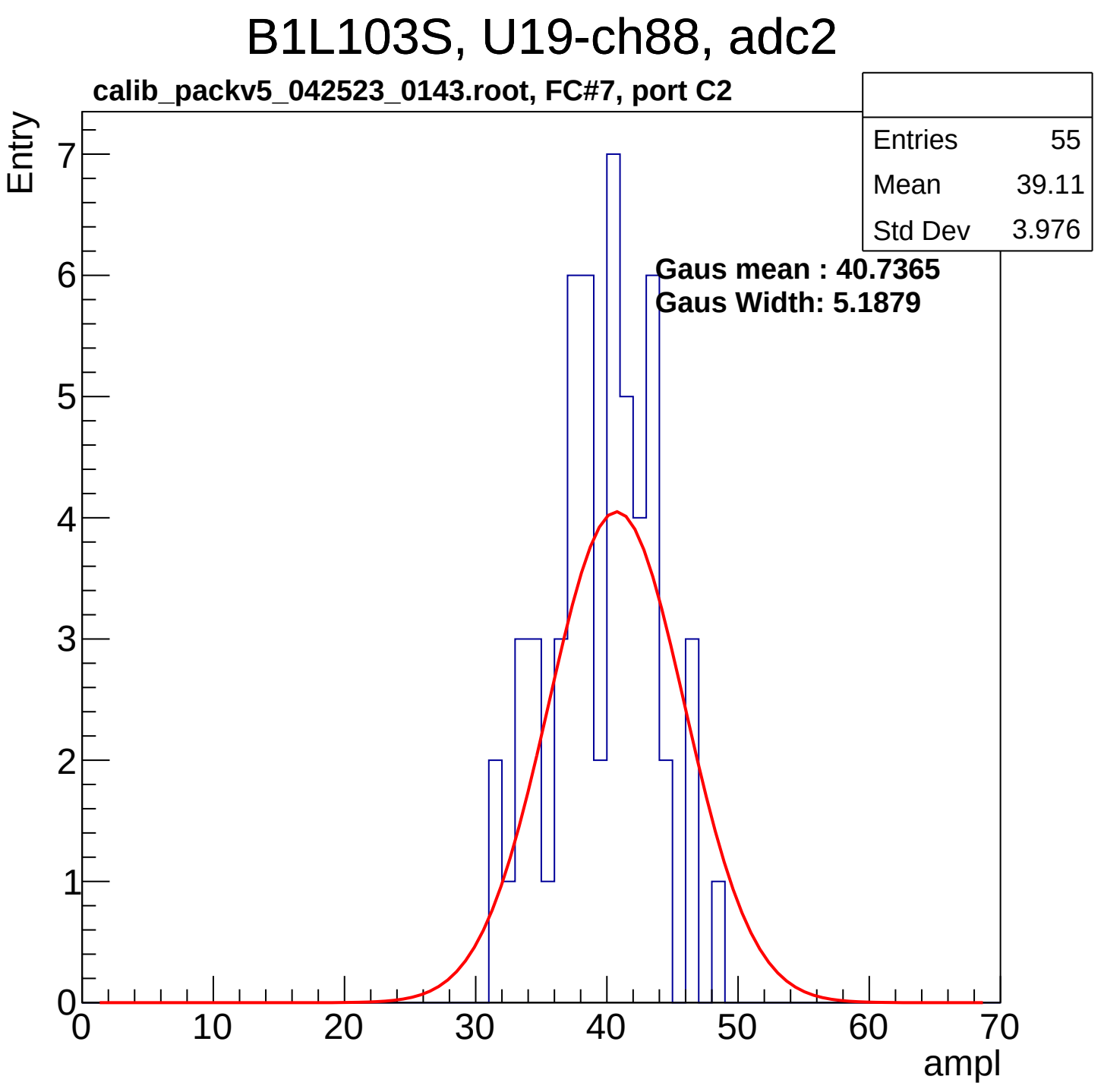
B1L103S, U19-ch88, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 39.11
Std Dev 3.976
Gaus mean : 40.7365
Gaus Width: 5.1879
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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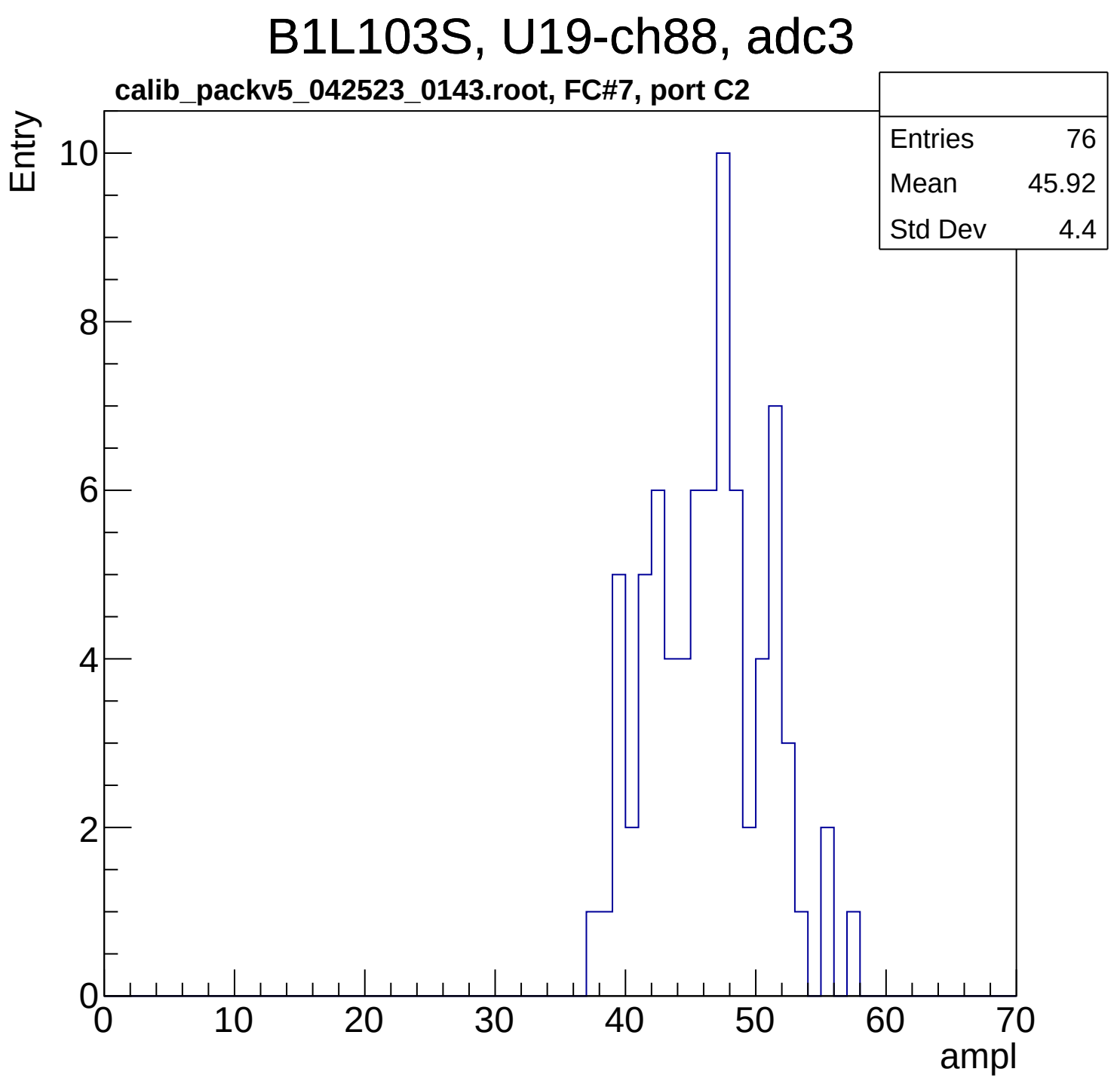
B1L103S, U19-ch88, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 45.92
Std Dev 4.4
Entry
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2
4
6
8
10
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10
20
30
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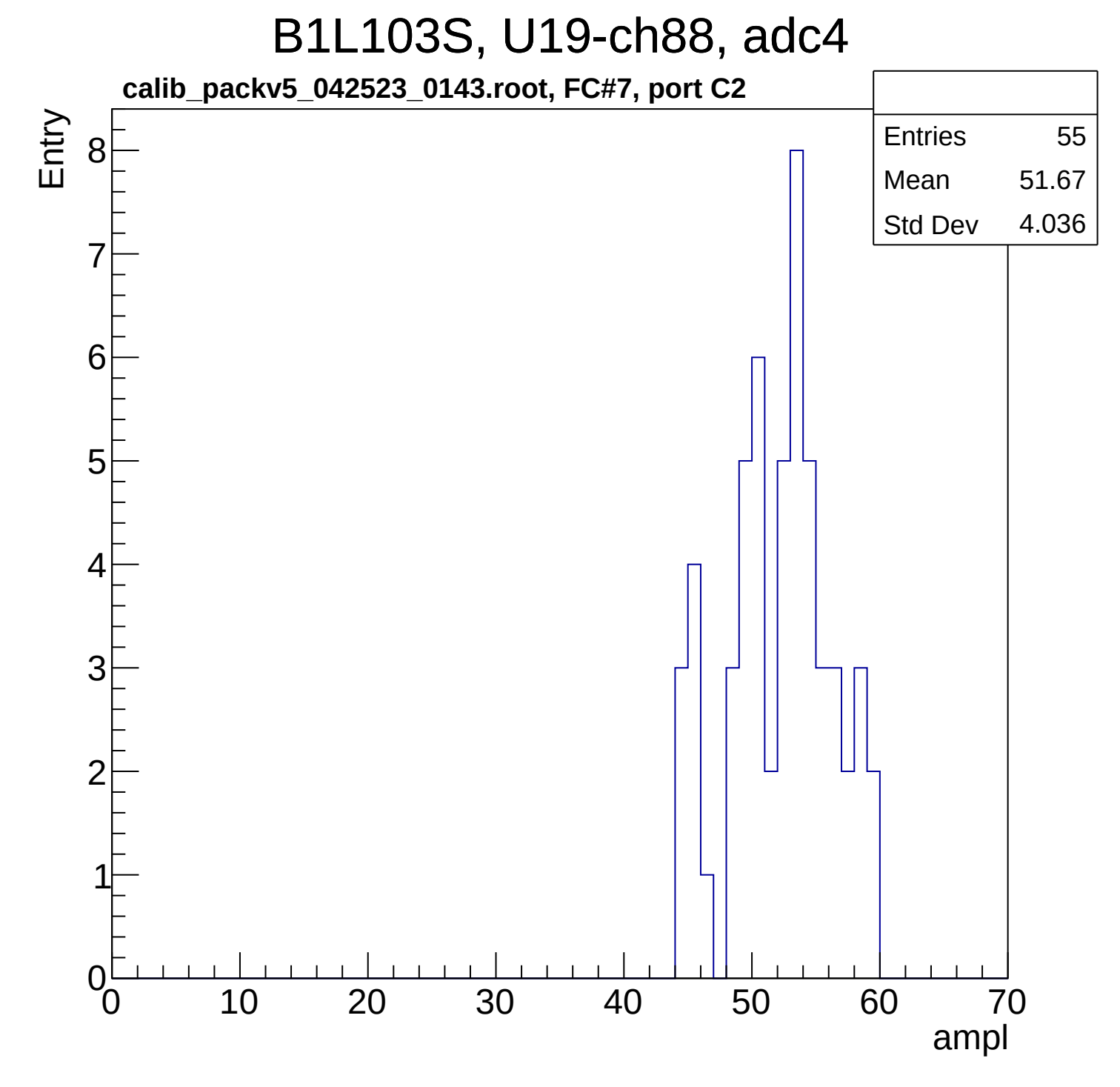
50
60
70
ampl


B1L103S, U19-ch88, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 51.67
Std Dev 4.036
Entry
ampl
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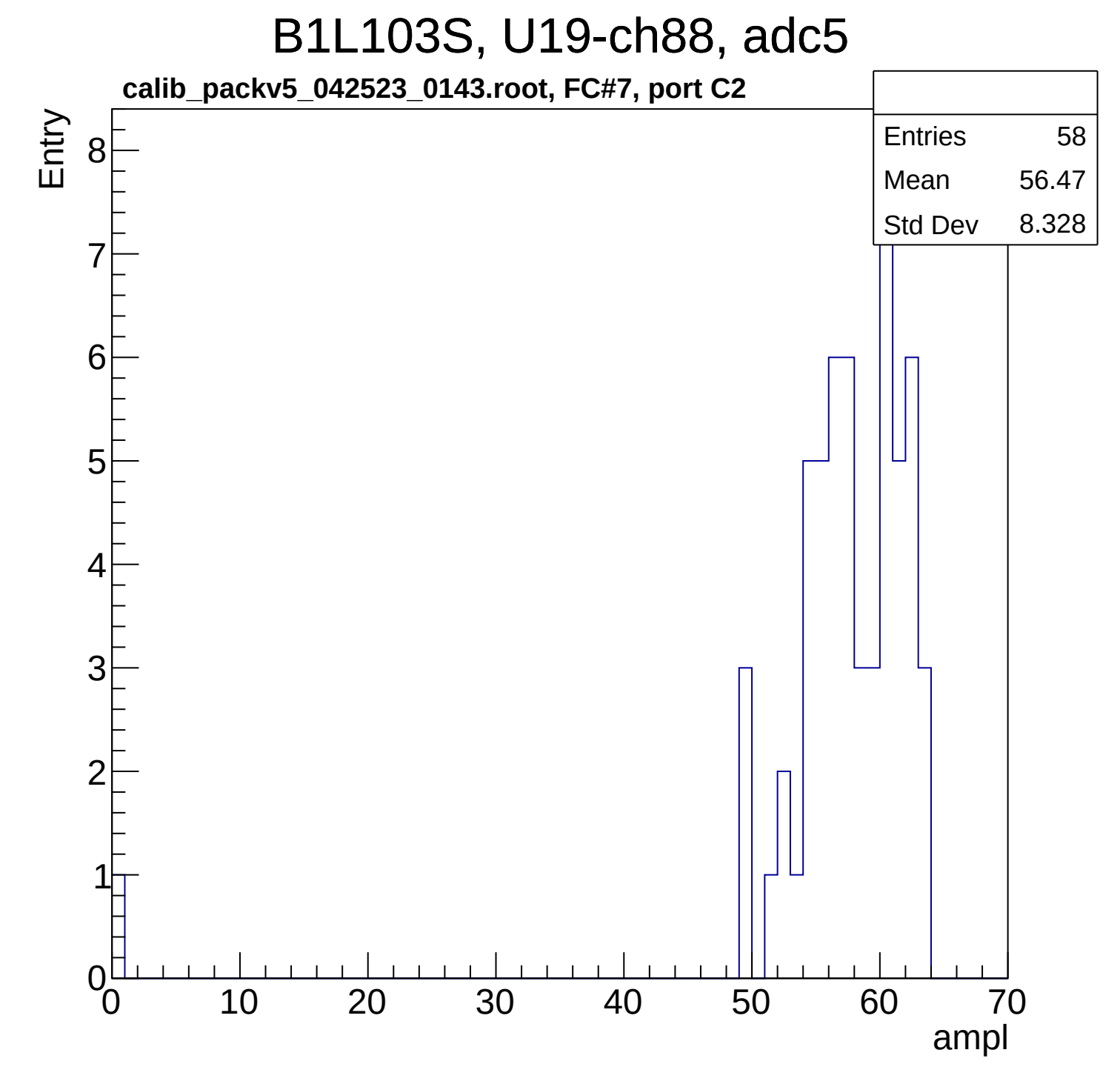
B1L103S, U19-ch88, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.47
Std Dev 8.328
Entry
ampl
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4
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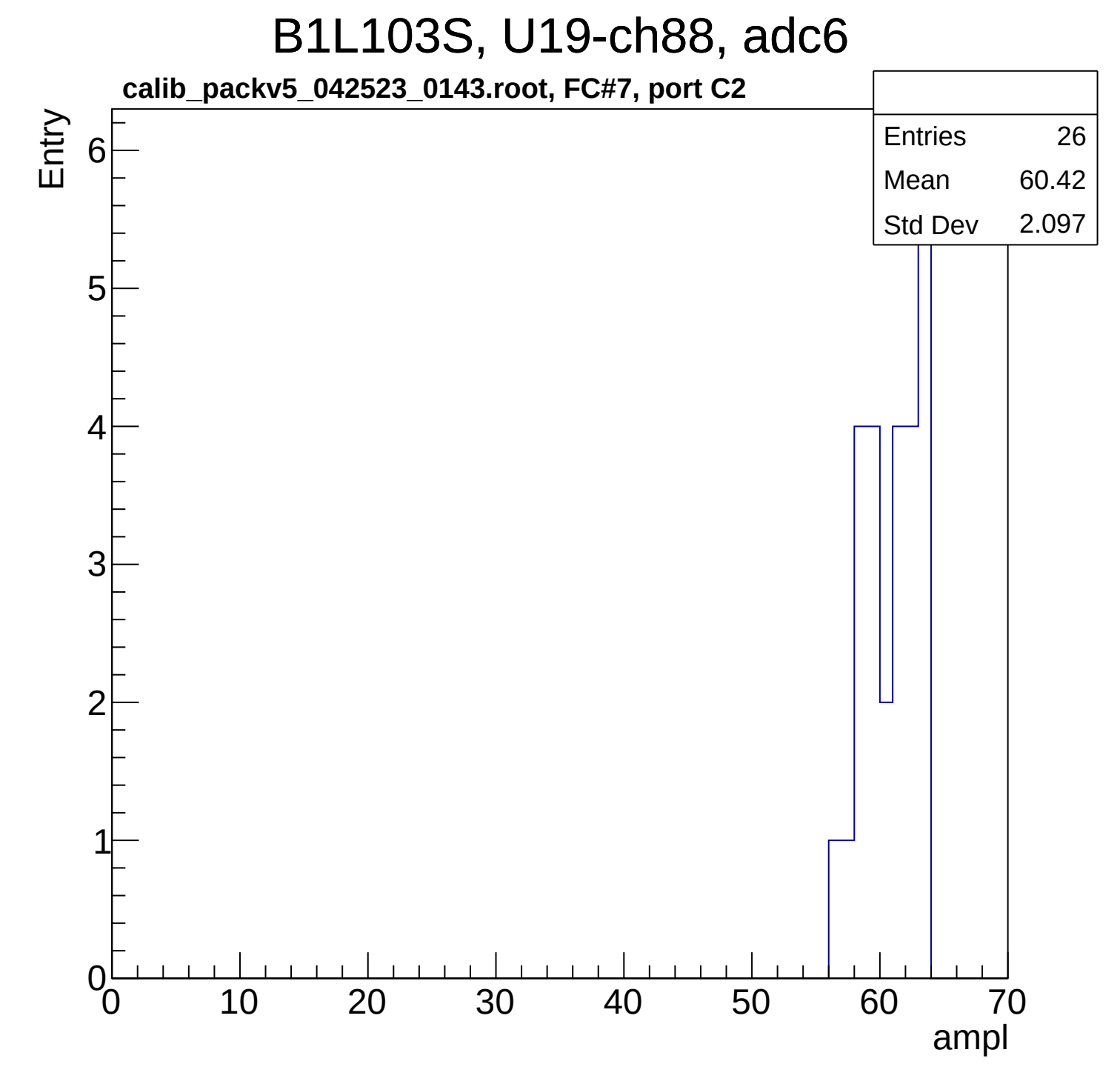
B1L103S, U19-ch88, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.42
Std Dev 2.097
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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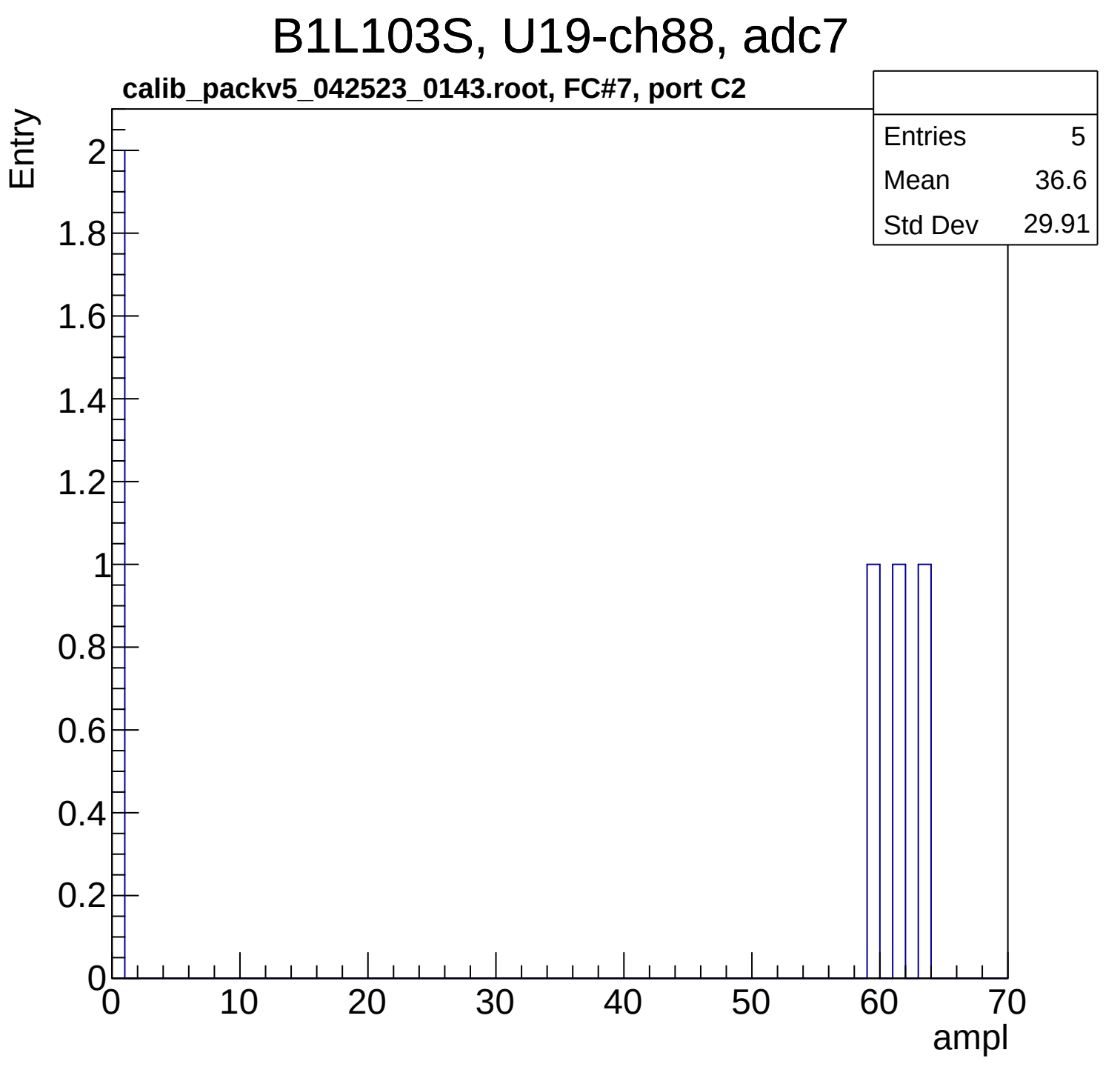
B1L103S, U19-ch88, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 36.6
Std Dev 29.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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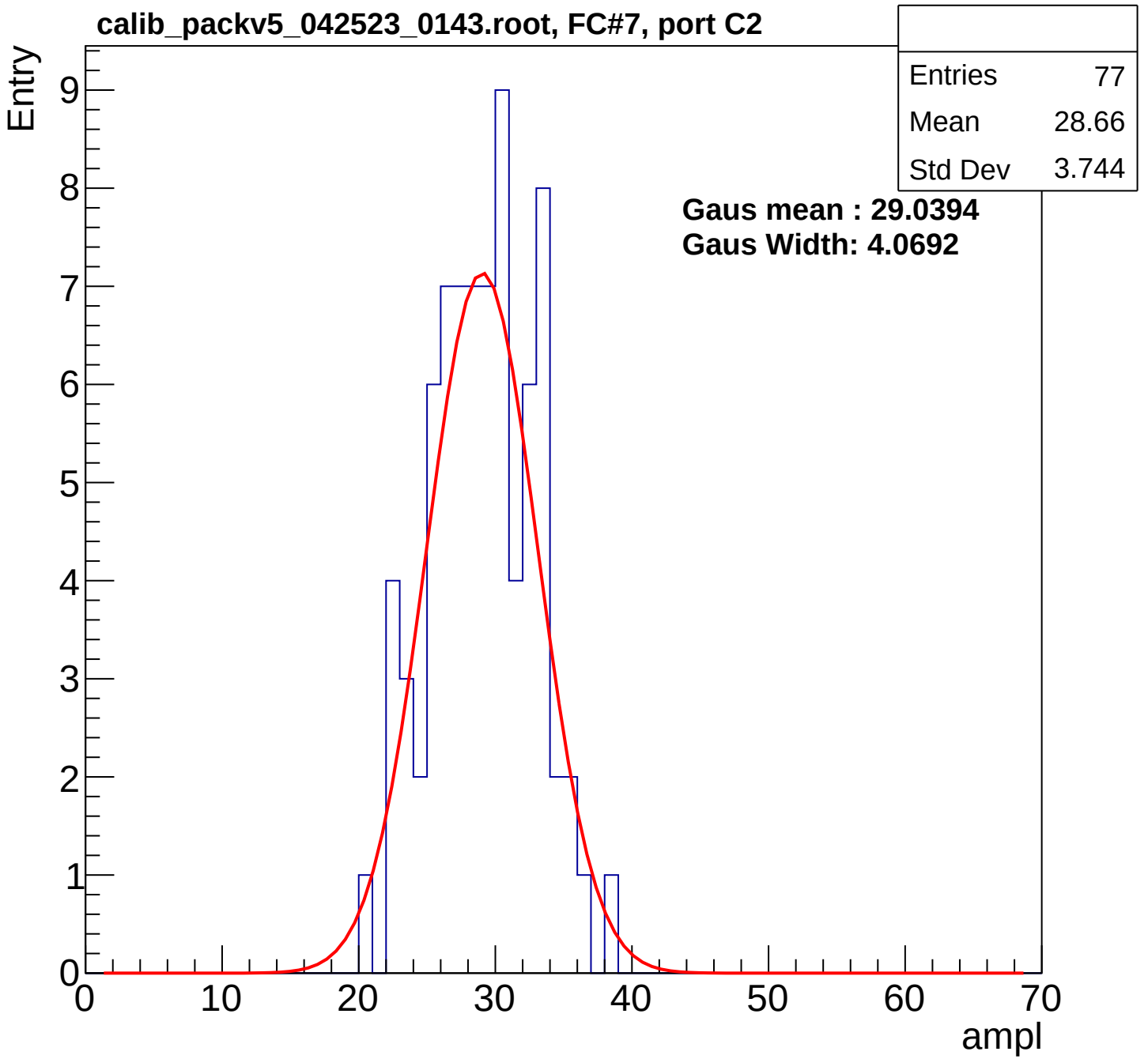
B1L103S, U19-ch89, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 28.66
Std Dev 3.744
Gaus mean : 29.0394
Gaus Width: 4.0692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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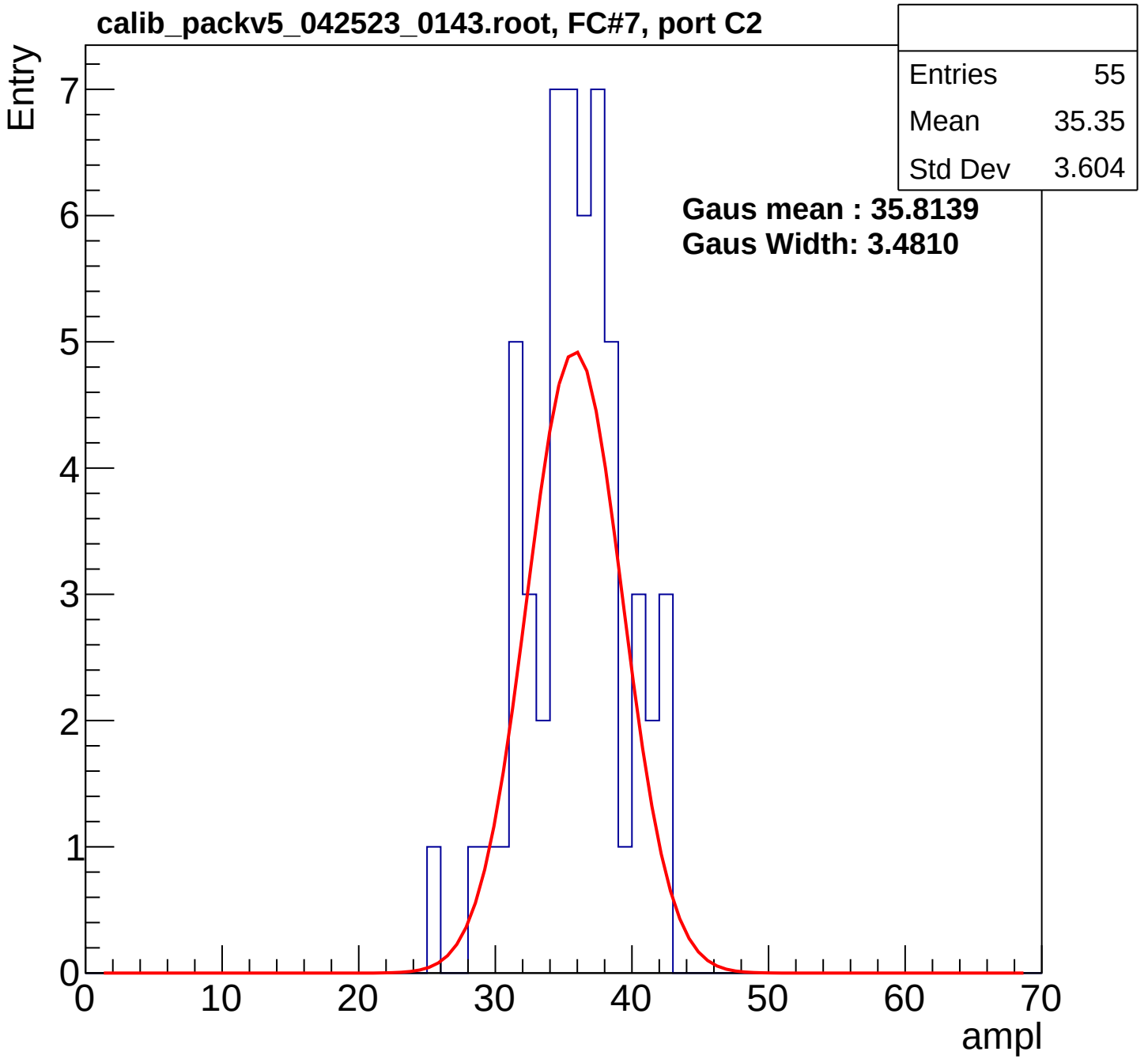
B1L103S, U19-ch89, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 35.35
Std Dev 3.604
Gaus mean : 35.8139
Gaus Width: 3.4810
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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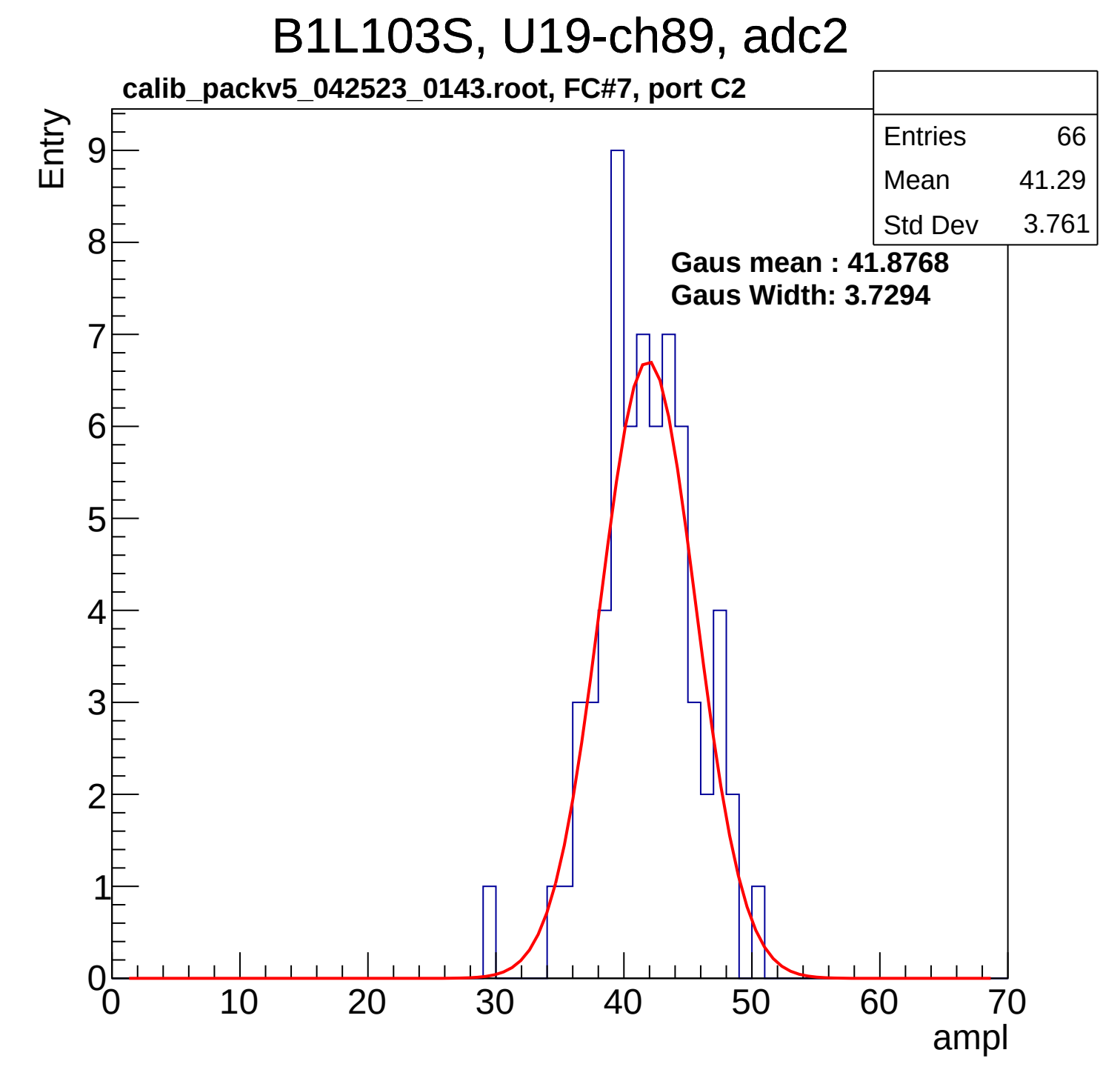
B1L103S, U19-ch89, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 41.29
Std Dev 3.761
Gaus mean : 41.8768
Gaus Width: 3.7294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
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60
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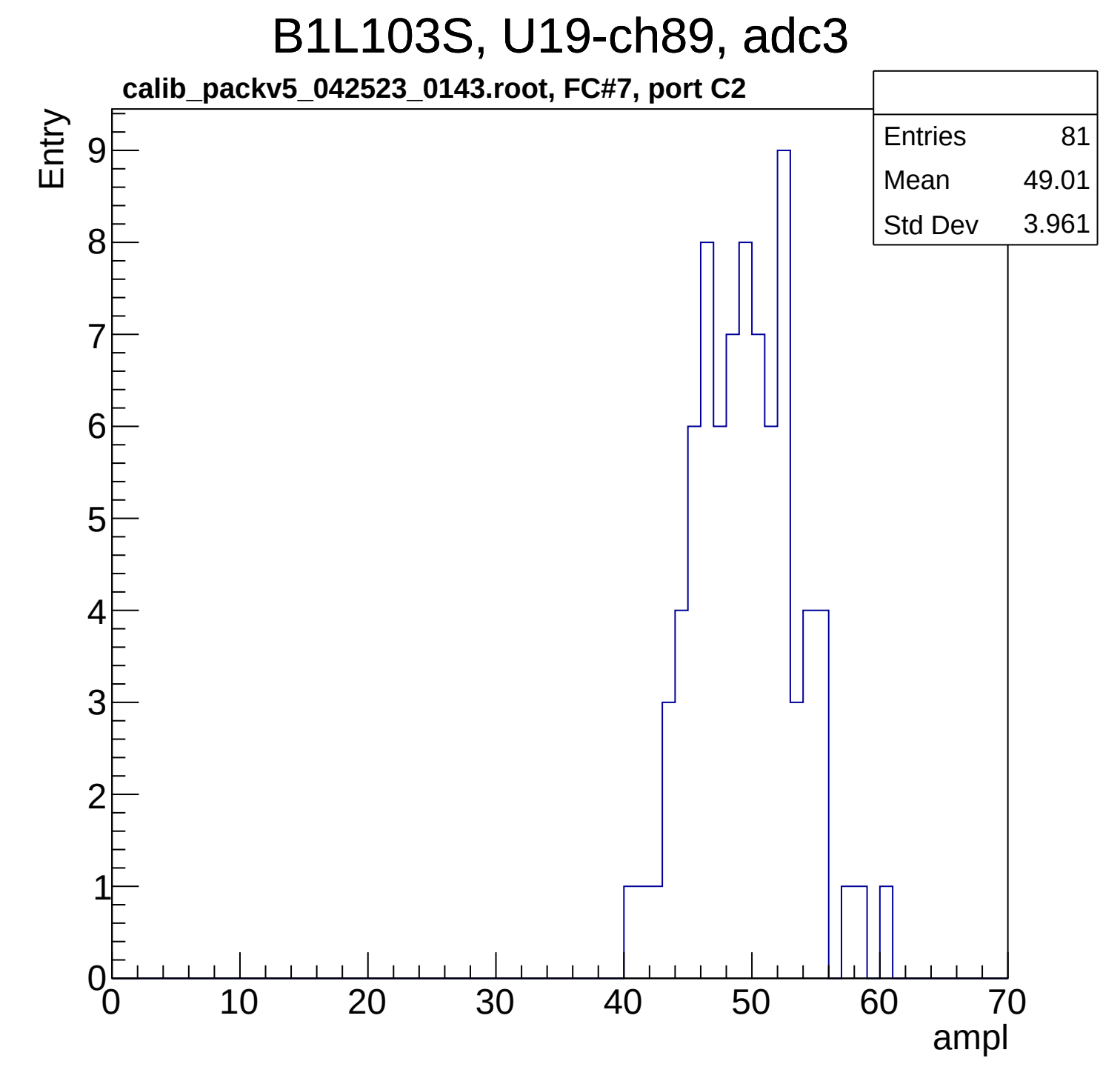
B1L103S, U19-ch89, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 49.01
Std Dev 3.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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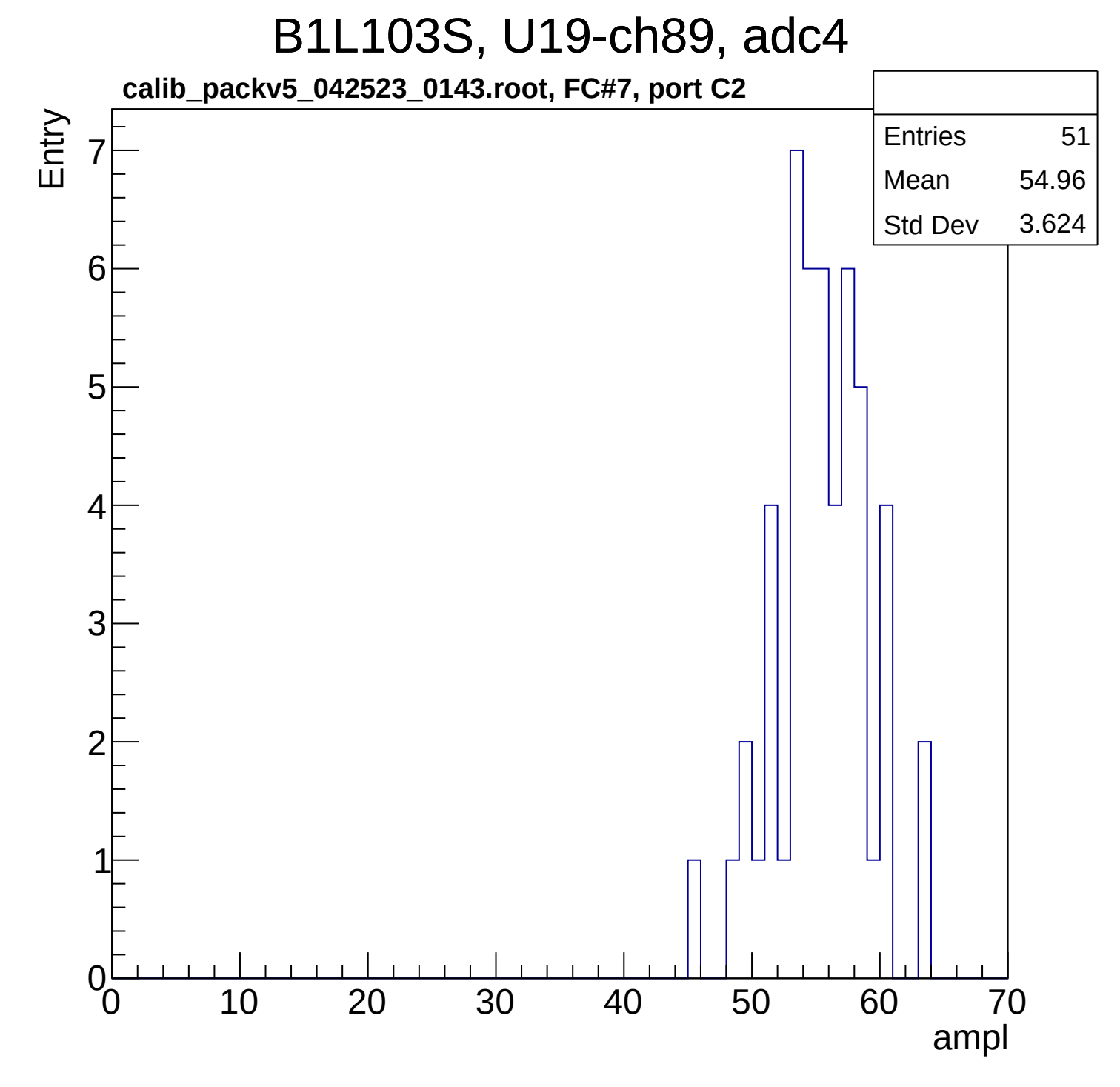
B1L103S, U19-ch89, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.96
Std Dev 3.624
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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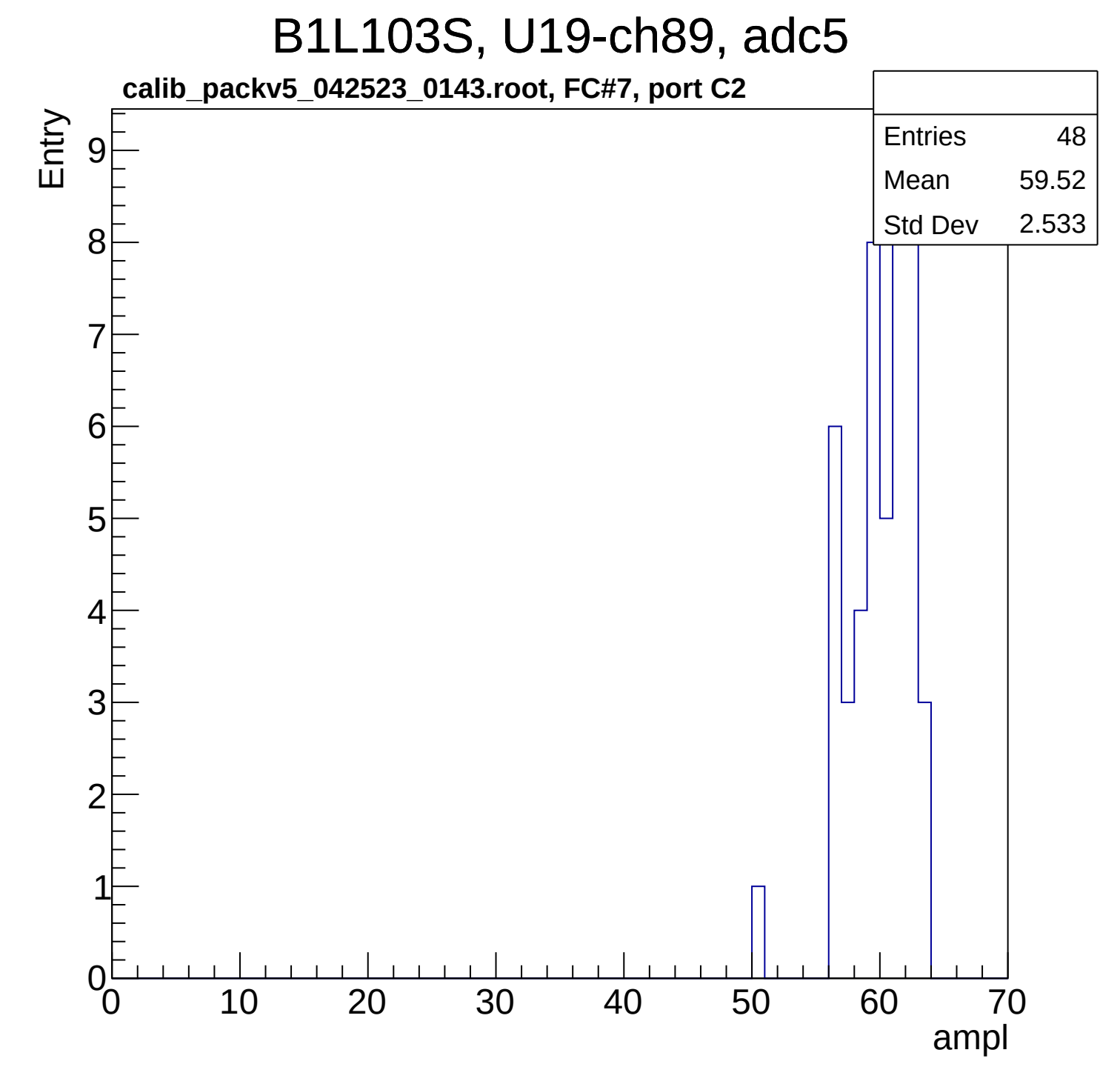
B1L103S, U19-ch89, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 59.52
Std Dev 2.533
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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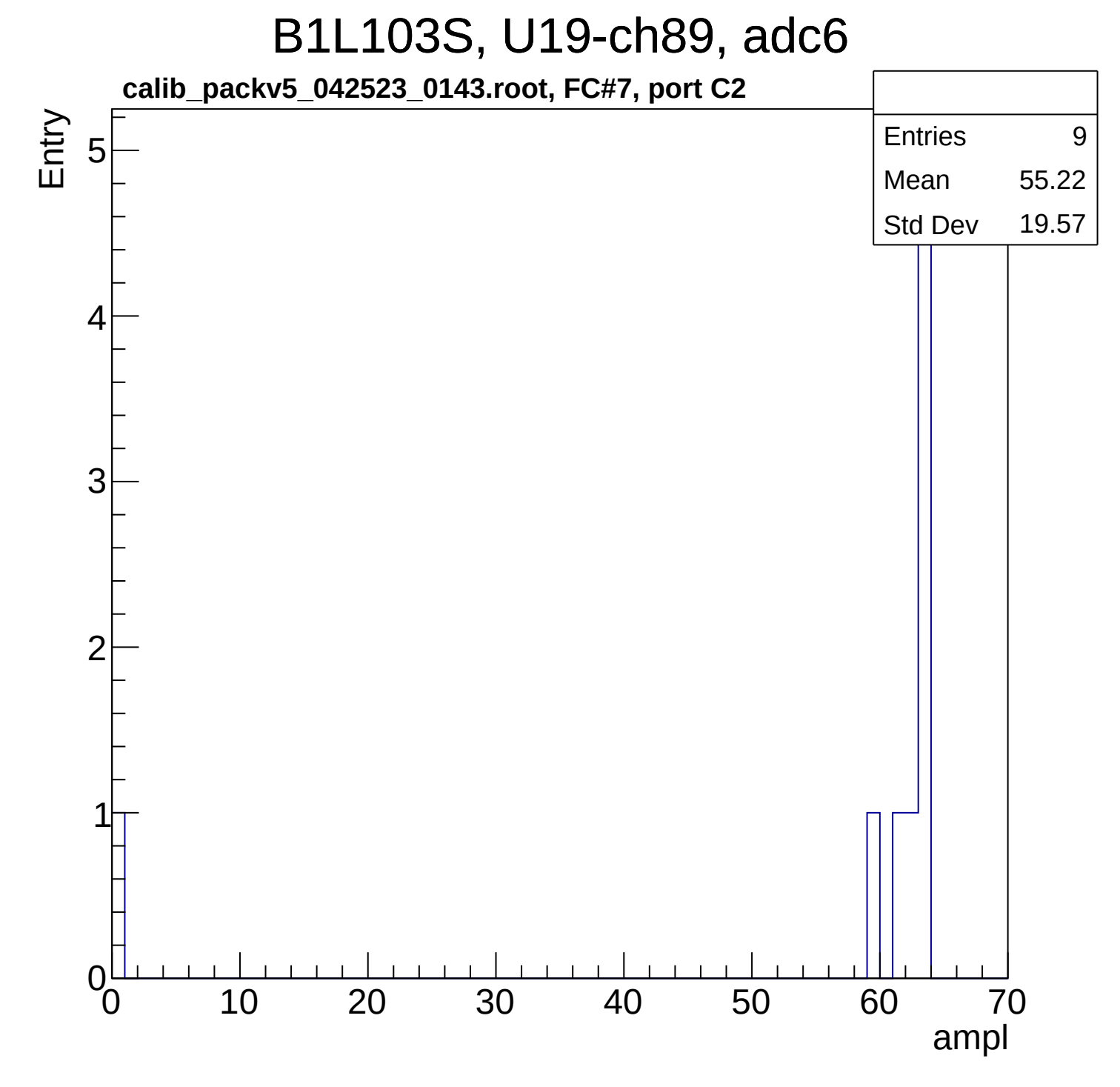
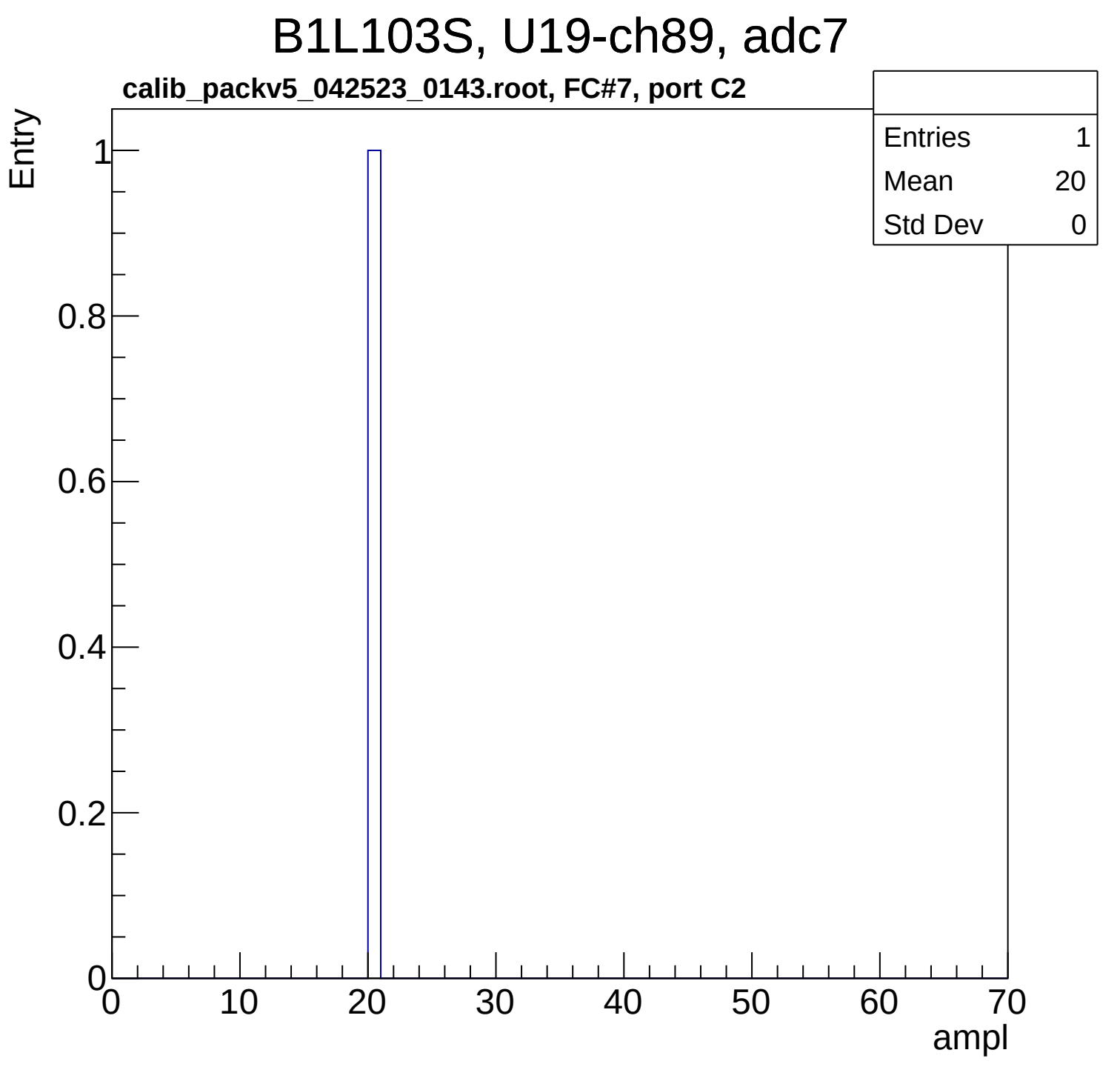
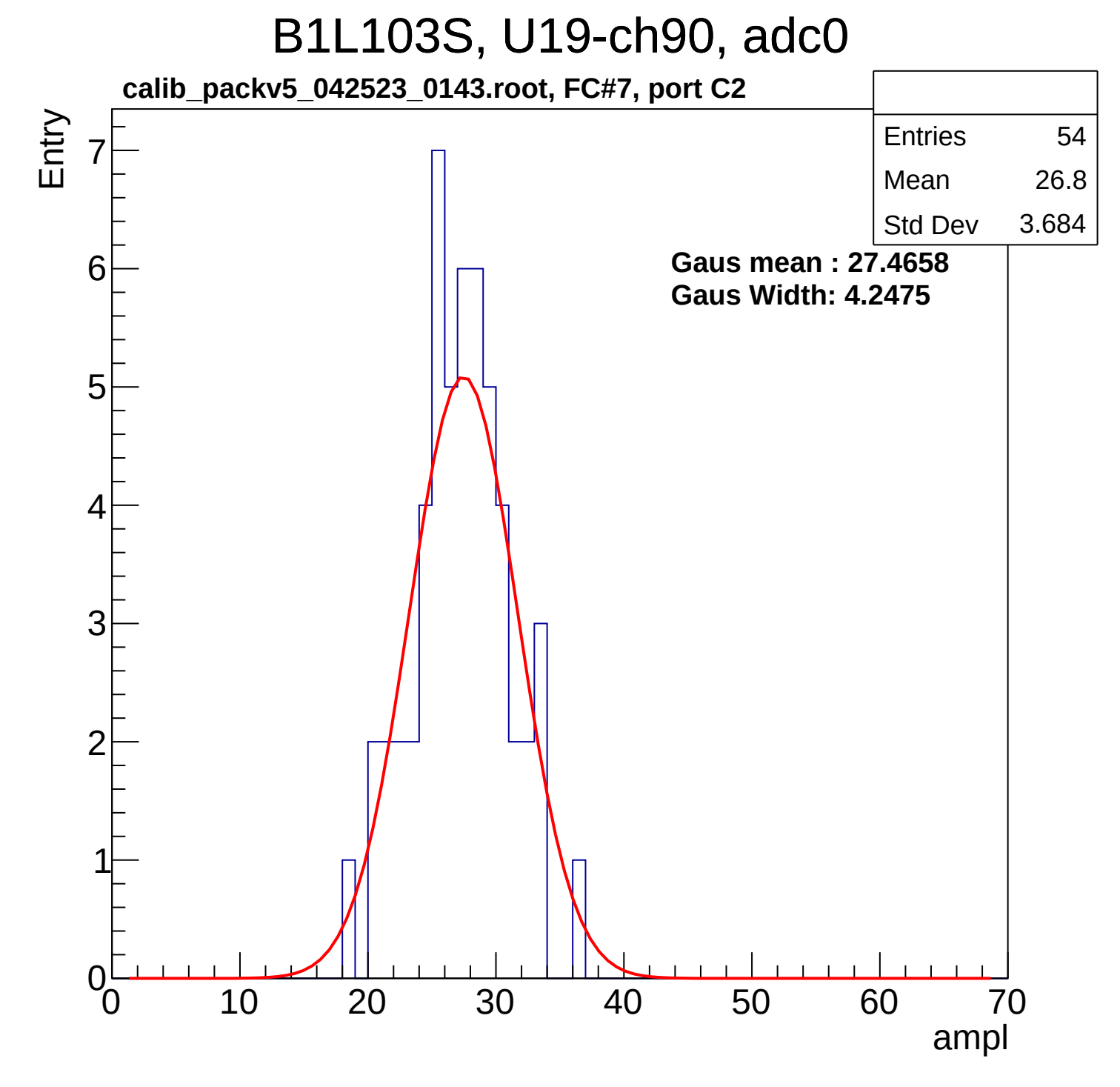

B1L103S, U19-ch89, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 55.22
Std Dev 19.57
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch89, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch90, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 26.8
Std Dev 3.684
Gaus mean : 27.4658
Gaus Width: 4.2475
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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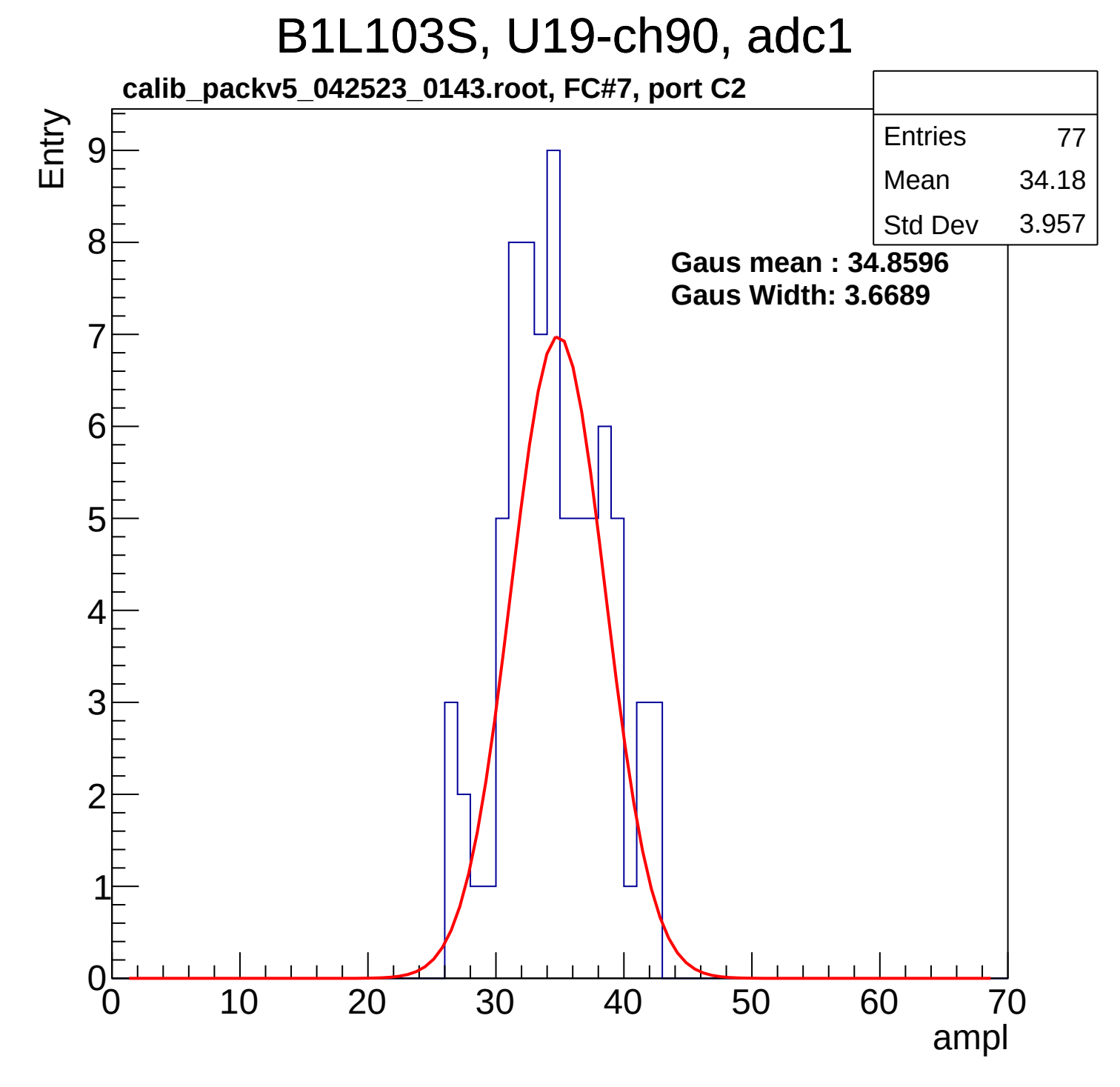
B1L103S, U19-ch90, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 34.18
Std Dev 3.957
Gaus mean : 34.8596
Gaus Width: 3.6689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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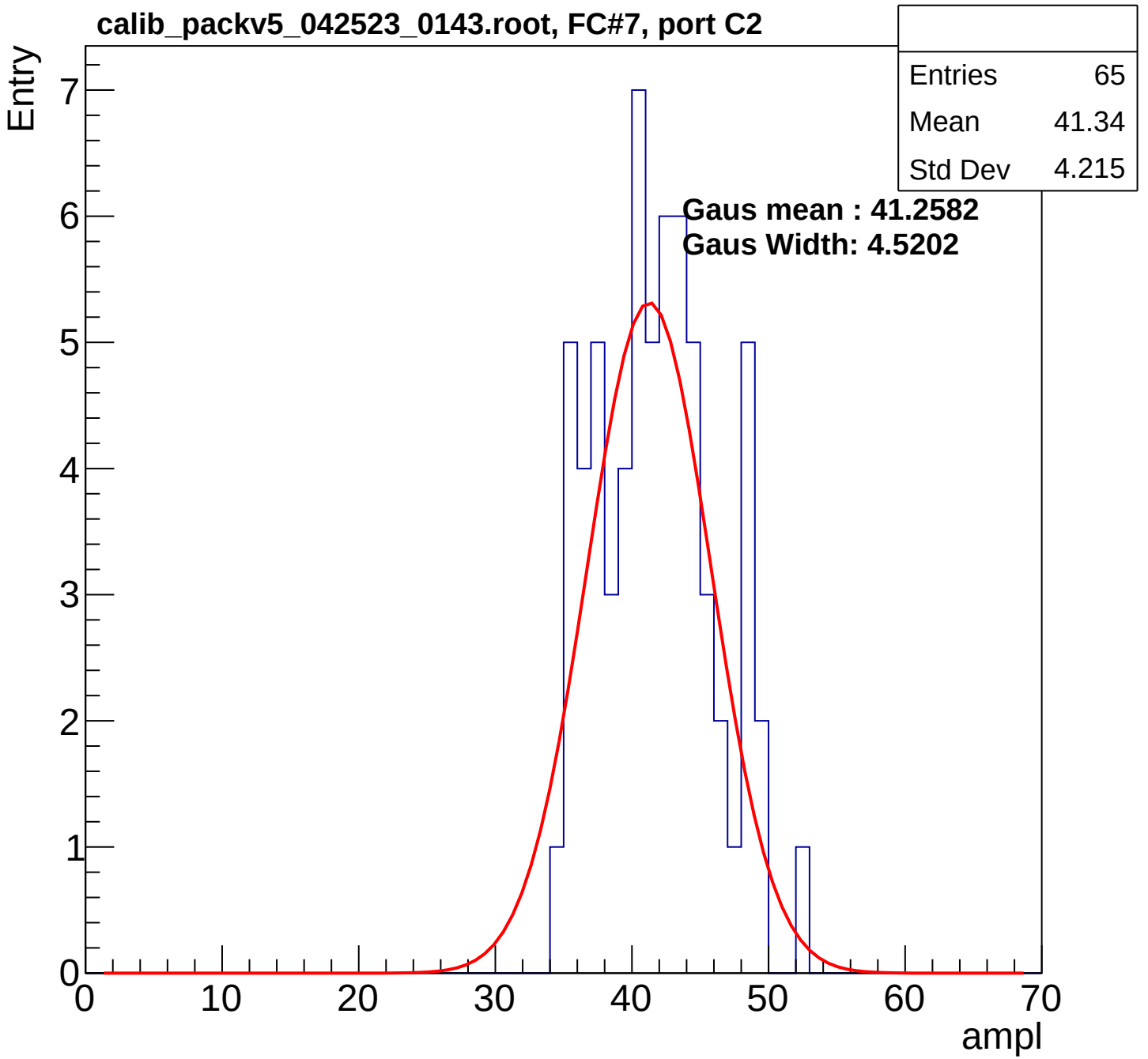
B1L103S, U19-ch90, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.34
Std Dev 4.215
Gaus mean : 41.2582
Gaus Width: 4.5202
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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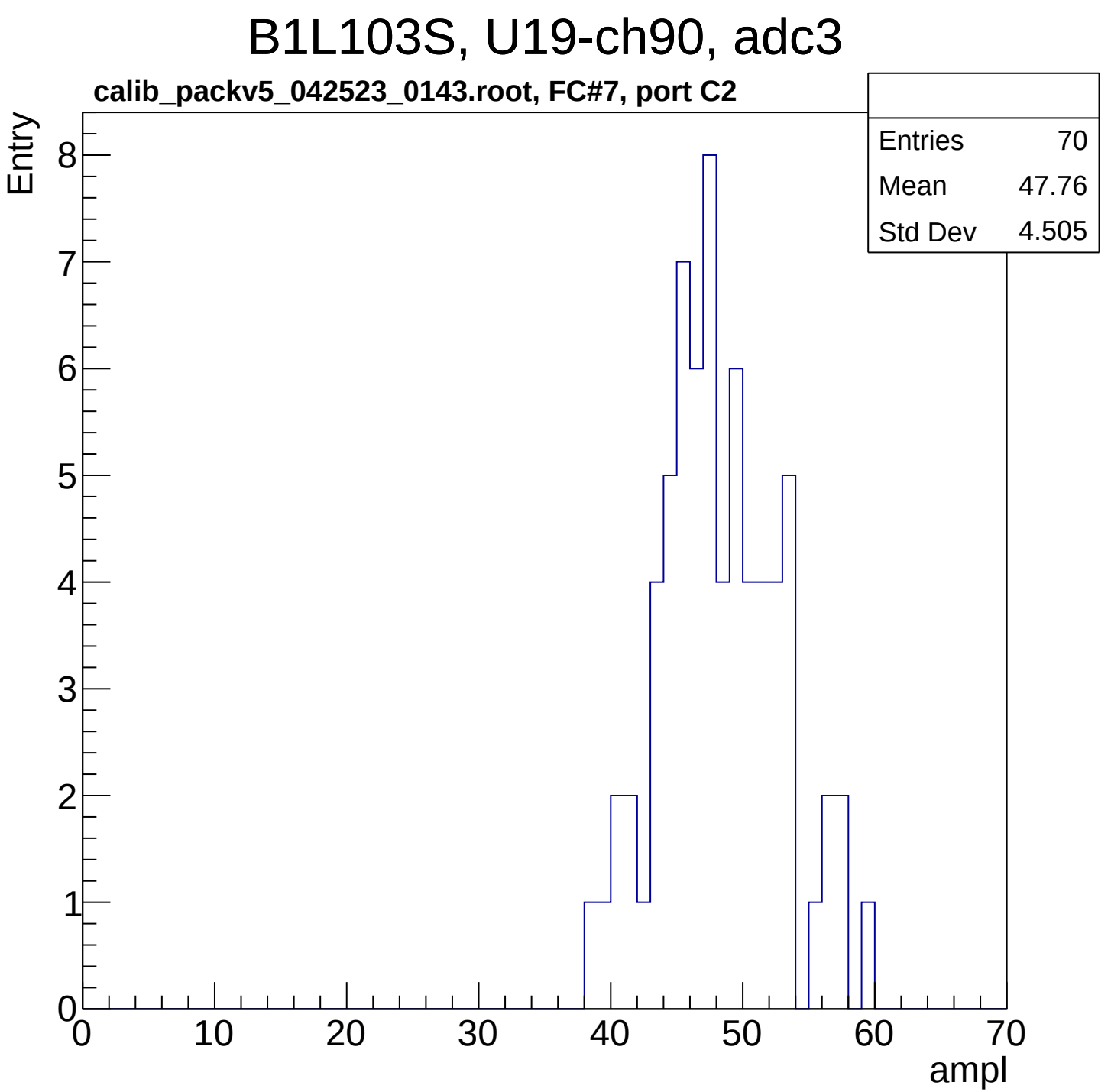
B1L103S, U19-ch90, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 47.76
Std Dev 4.505
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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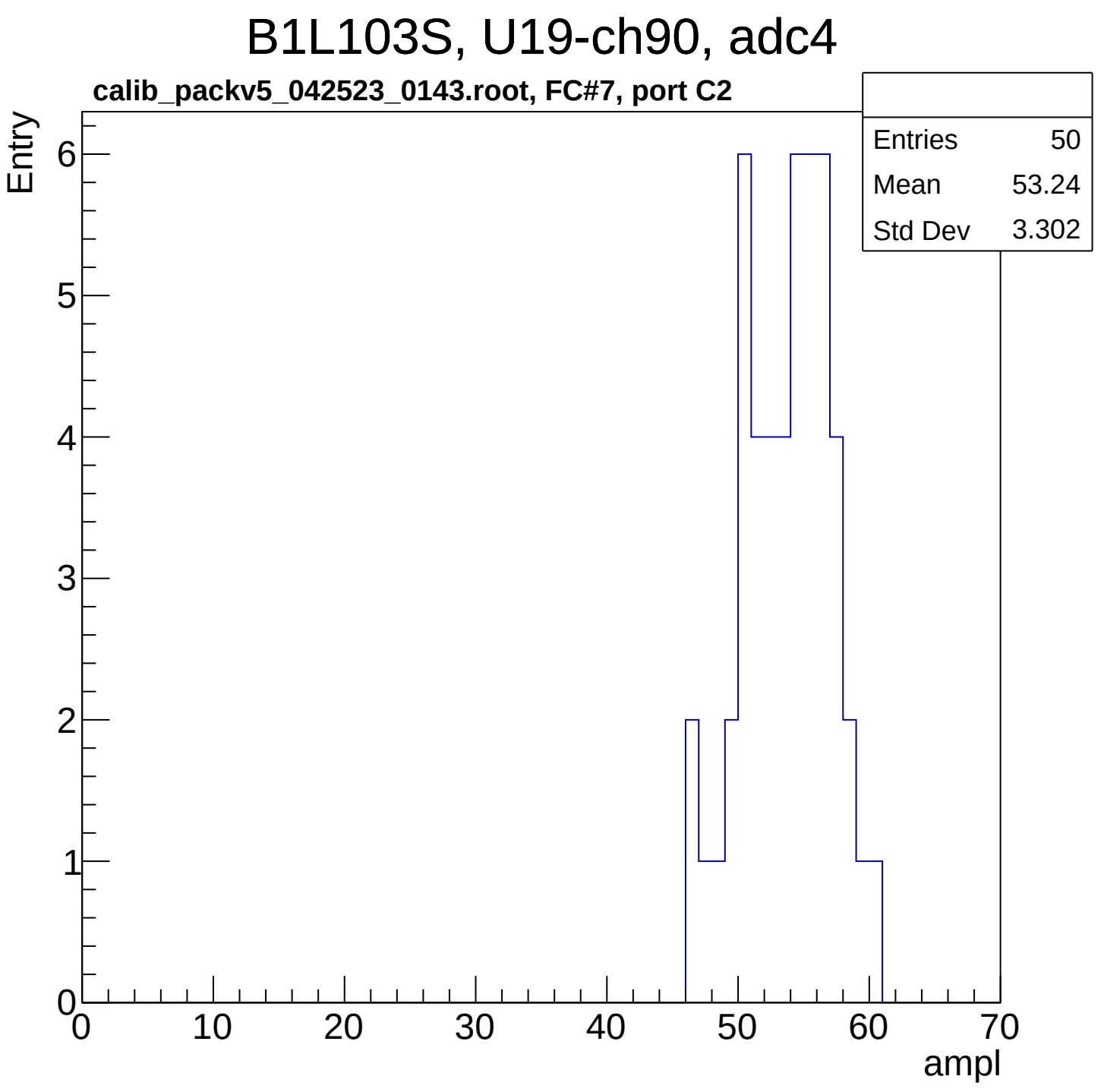
B1L103S, U19-ch90, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 53.24
Std Dev 3.302
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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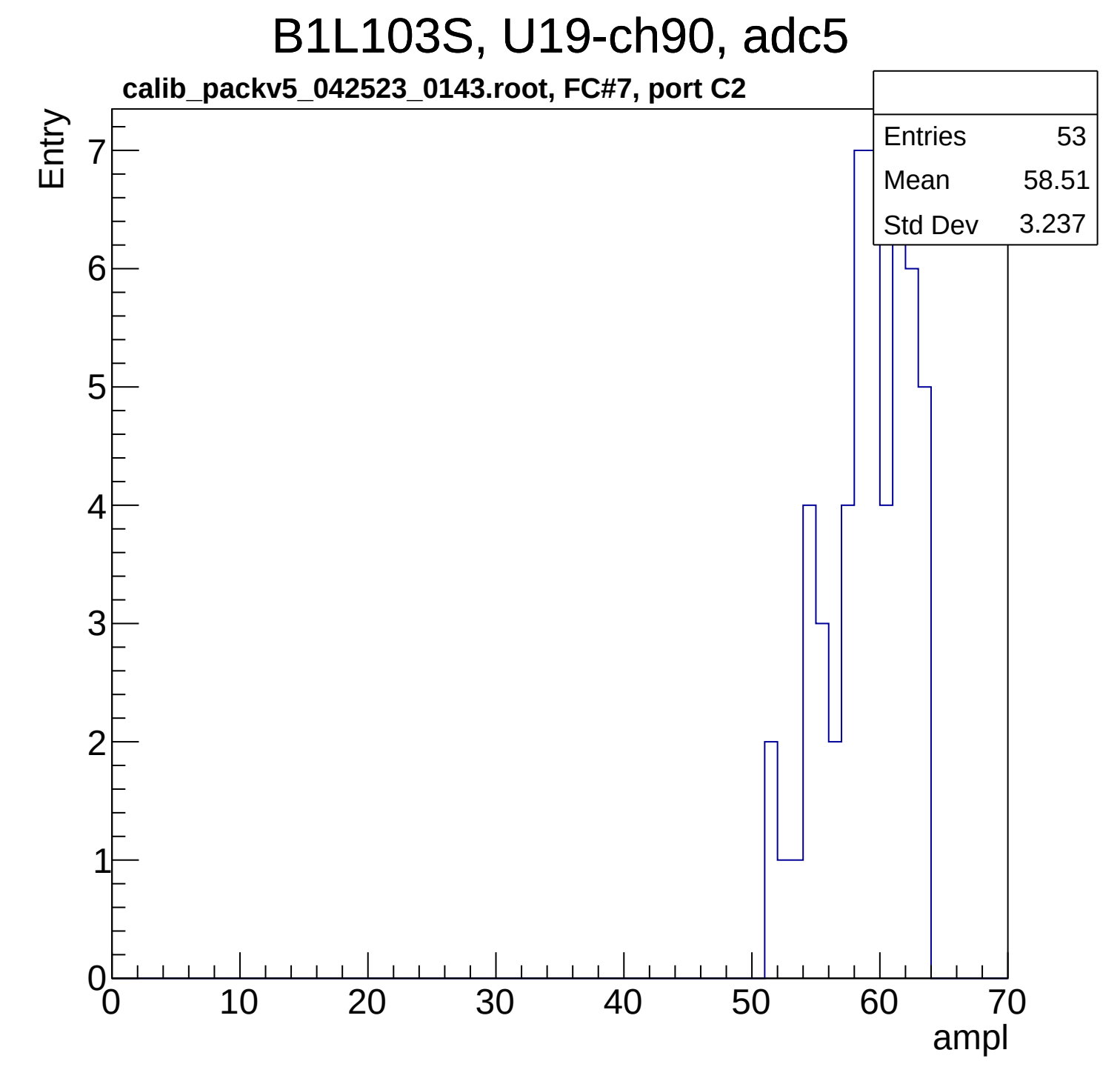
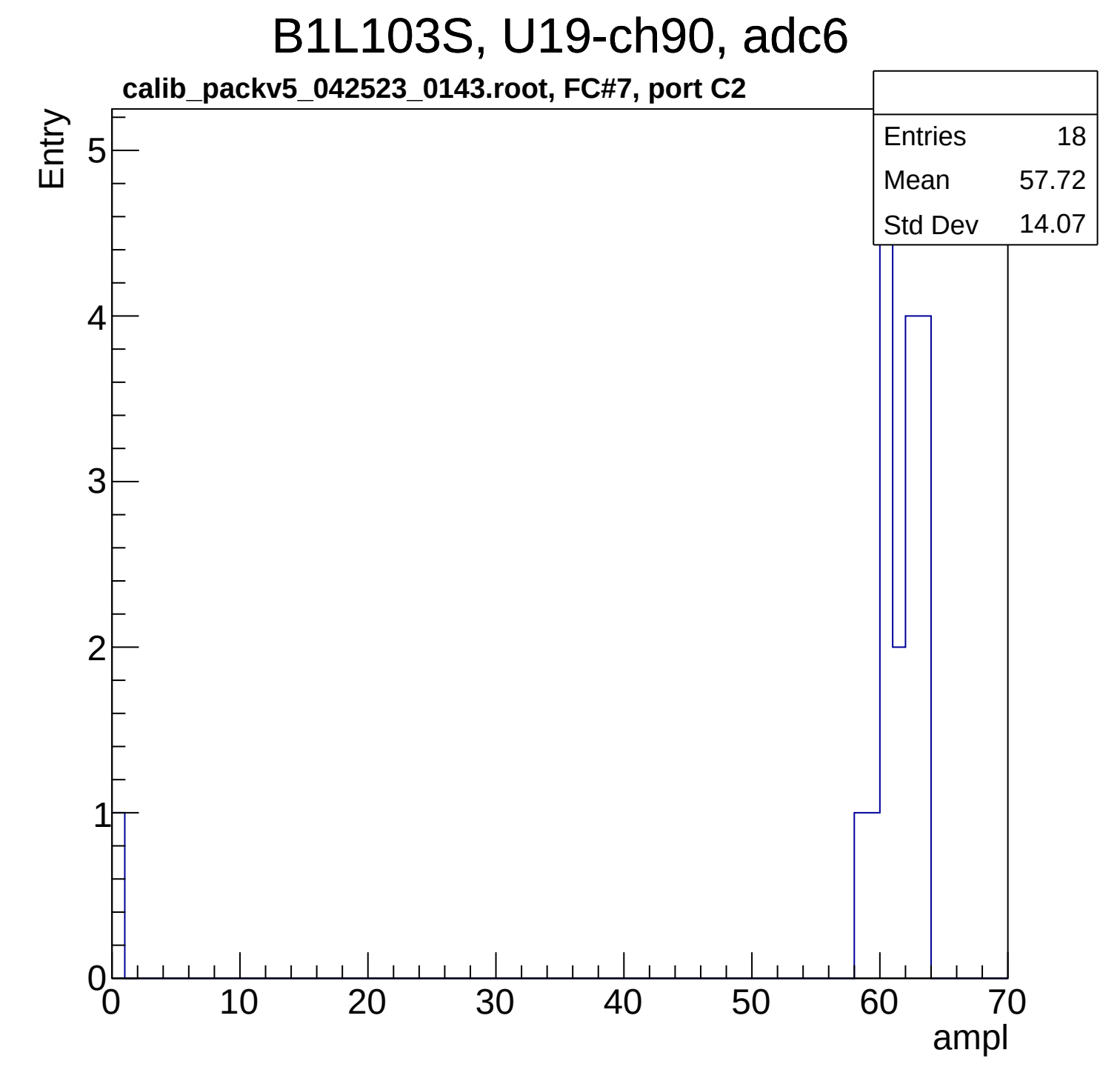
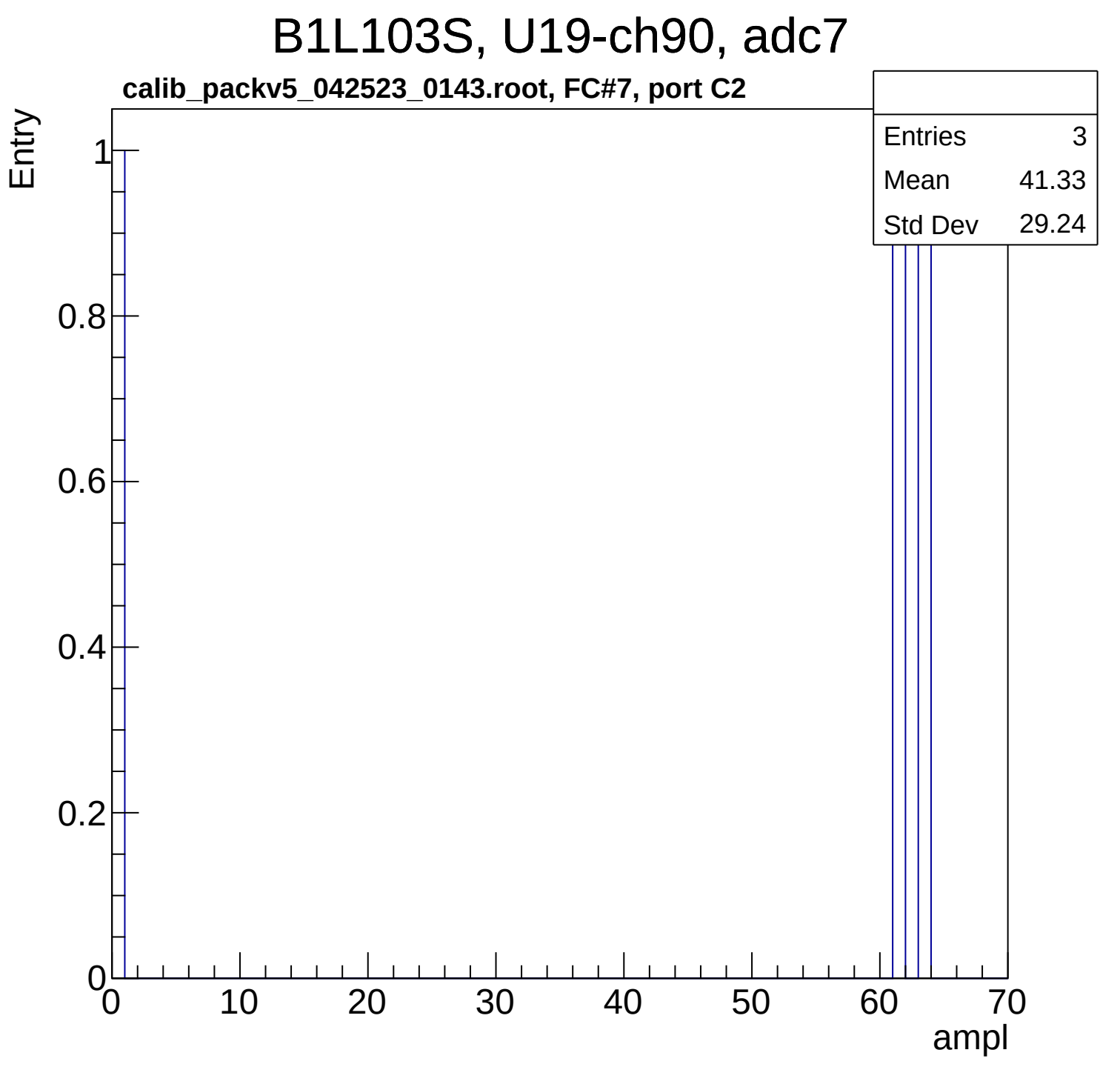

B1L103S, U19-ch90, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.51
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch90, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 57.72
Std Dev 14.07
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch90, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
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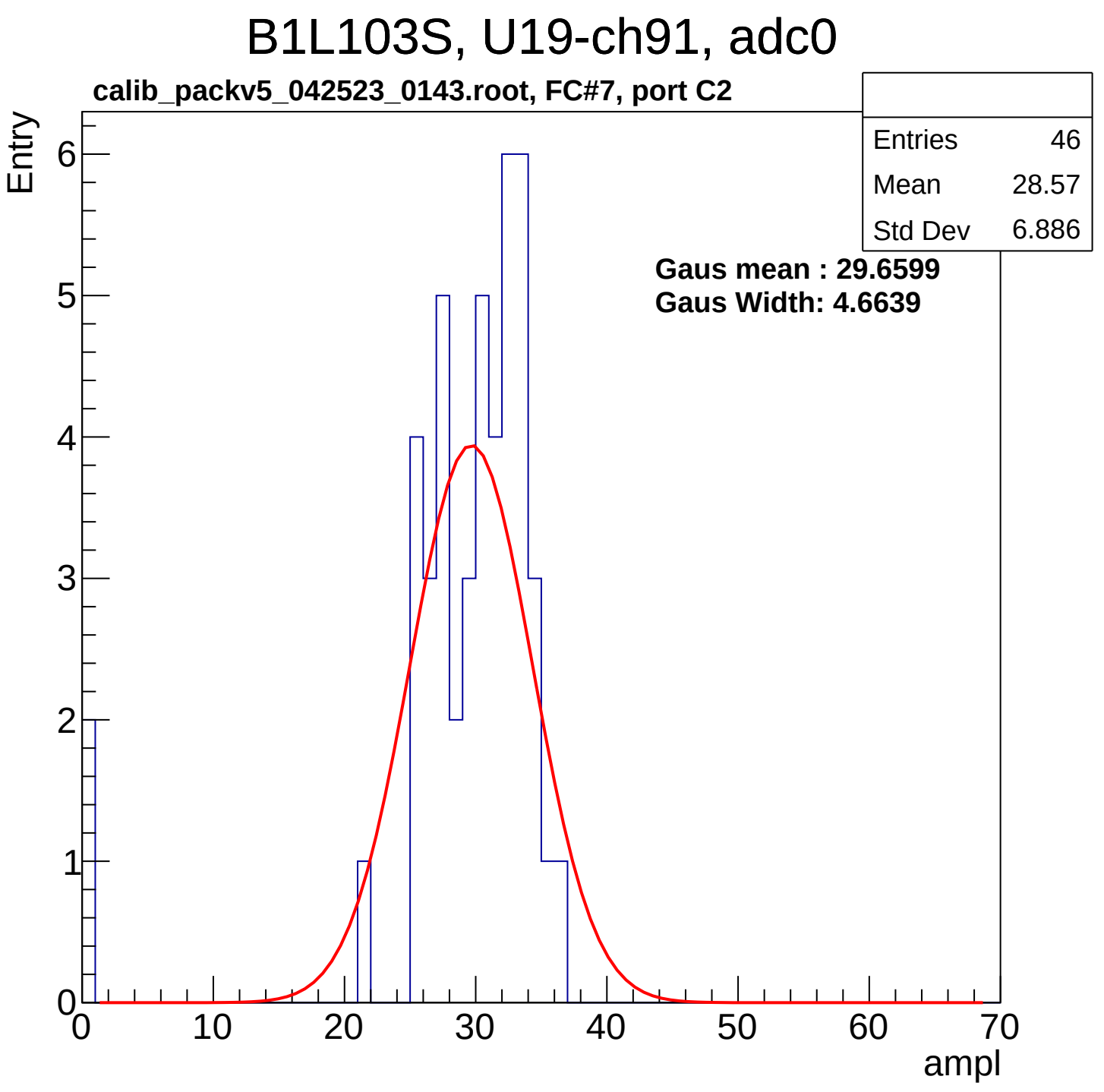
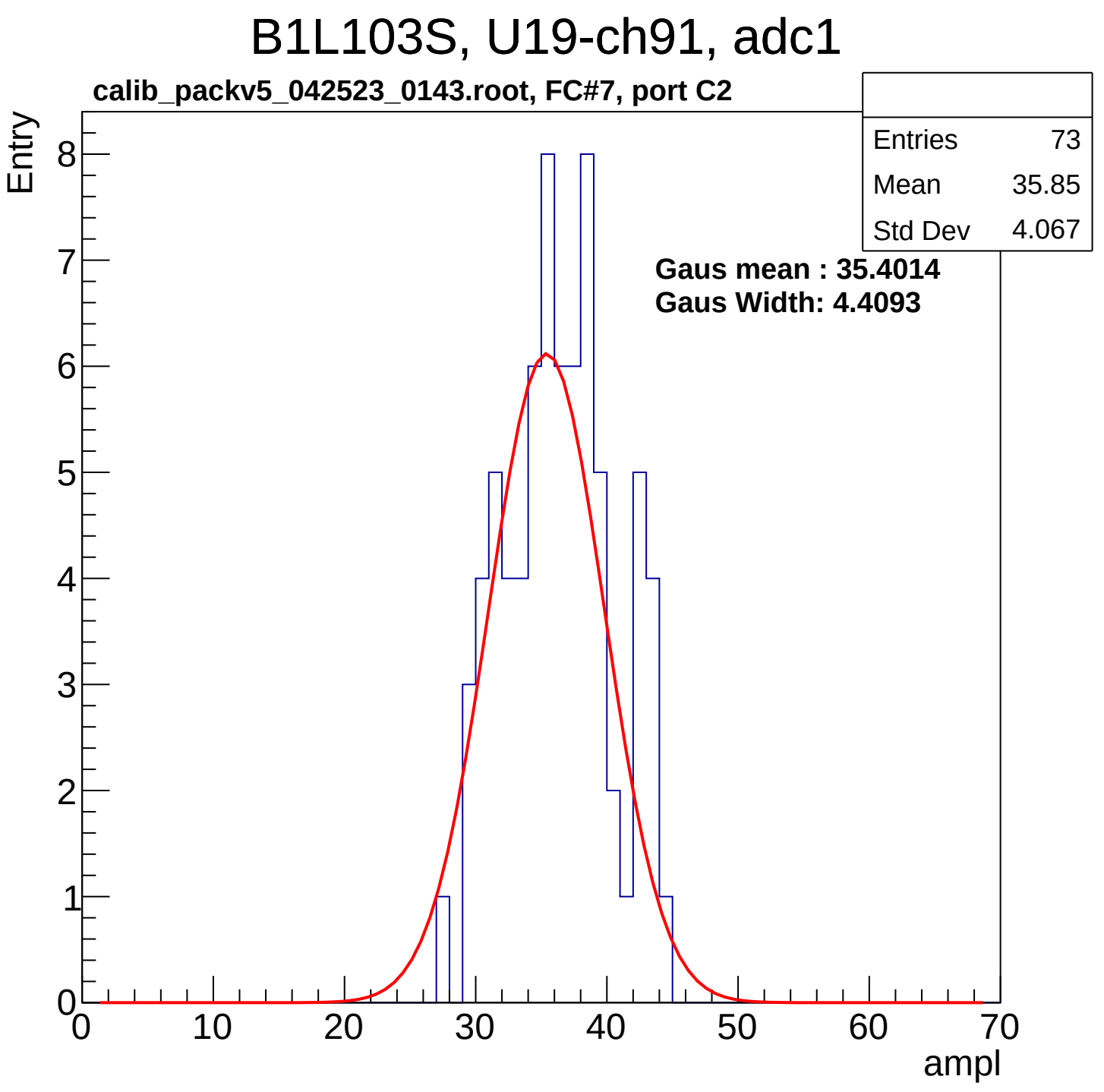
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U19-ch91, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 28.57
Std Dev 6.886
Gaus mean : 29.6599
Gaus Width: 4.6639
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch91, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 35.85
Std Dev 4.067
Gaus mean : 35.4014
Gaus Width: 4.4093
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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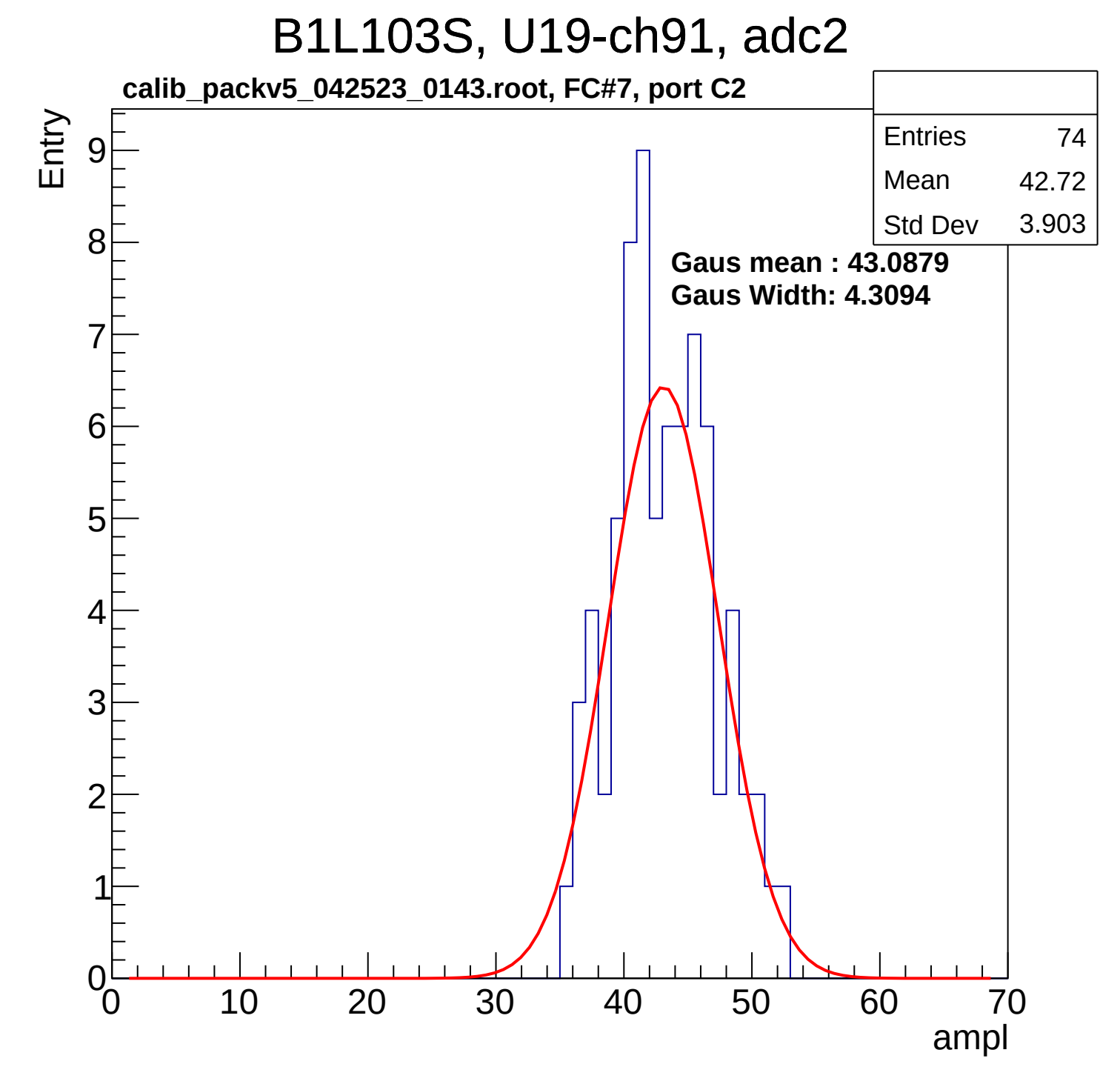
B1L103S, U19-ch91, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 42.72
Std Dev 3.903
Gaus mean : 43.0879
Gaus Width: 4.3094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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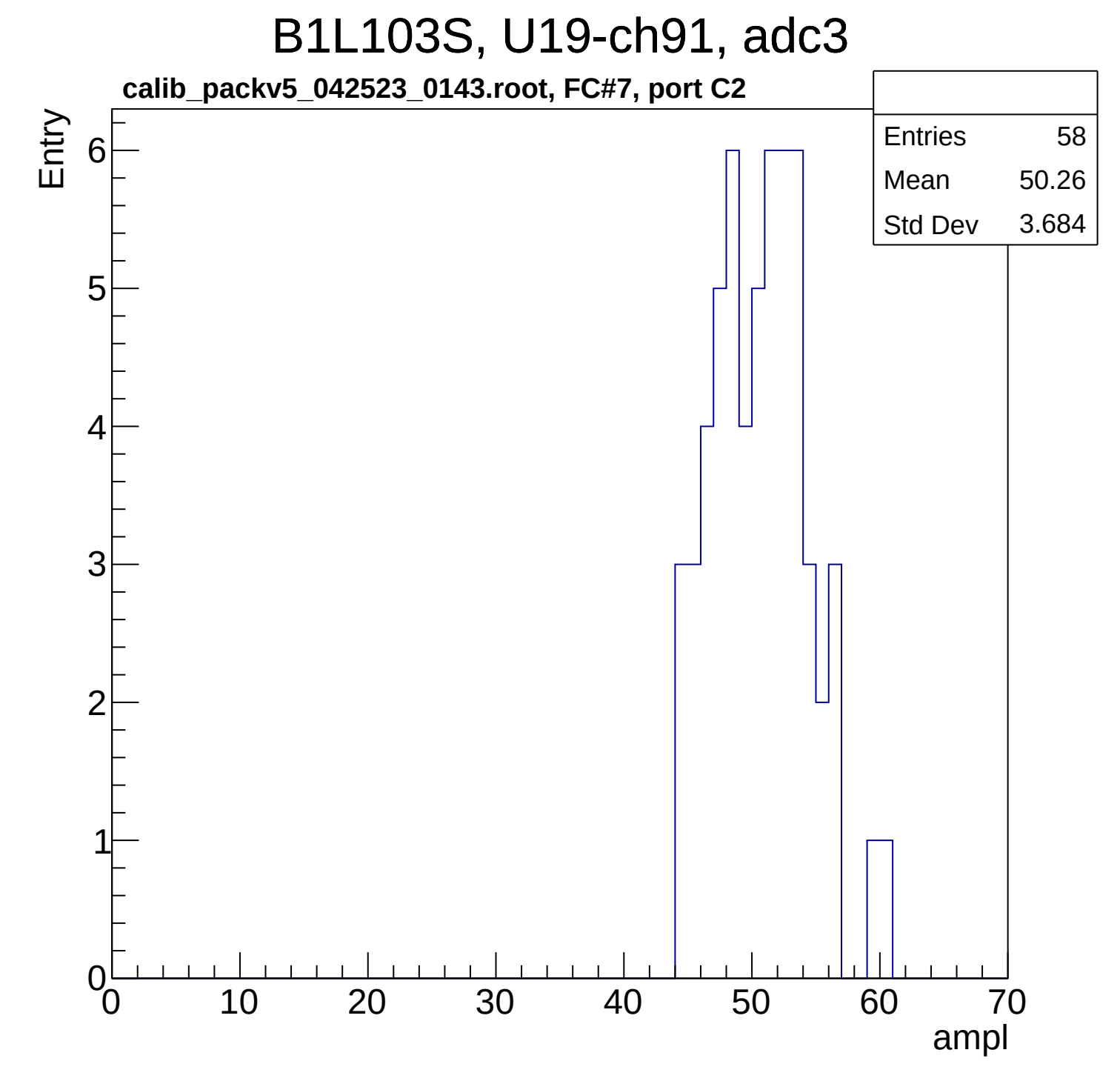
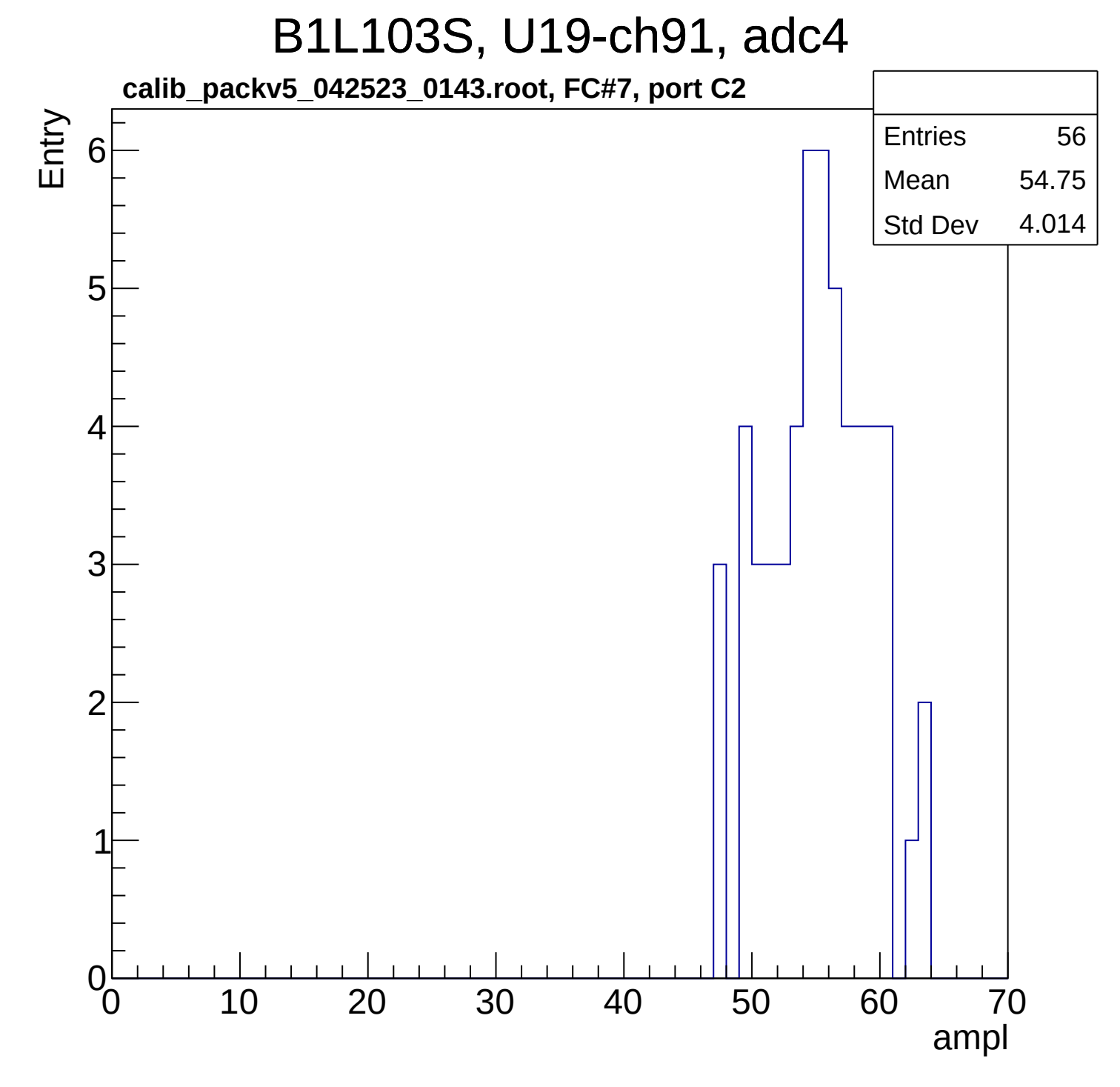

B1L103S, U19-ch91, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 50.26
Std Dev 3.684
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch91, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 54.75
Std Dev 4.014
Entry
0
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2
3
4
5
6
0
10
20
30
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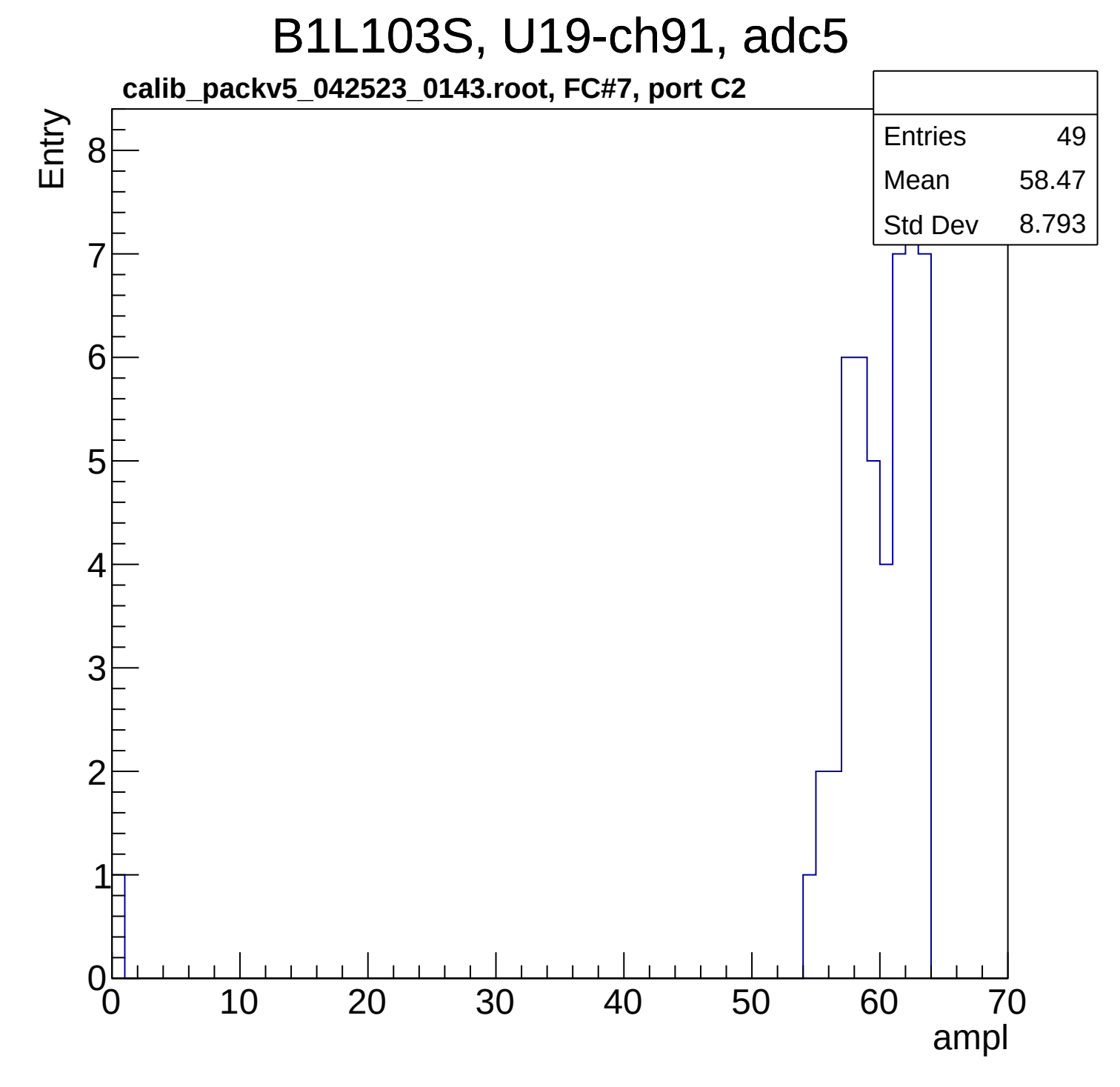
50
60
70
ampl


B1L103S, U19-ch91, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.47
Std Dev 8.793
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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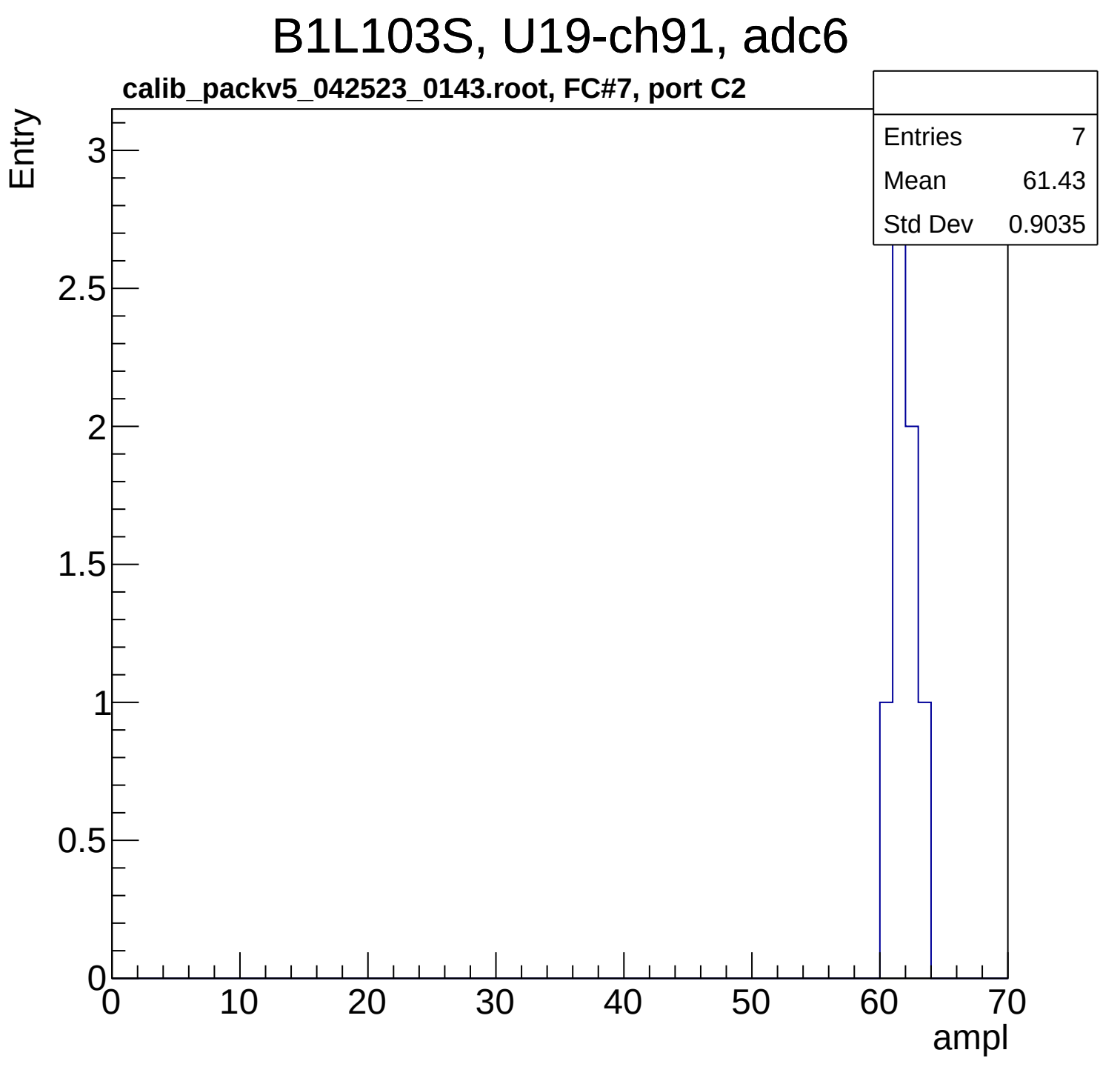
B1L103S, U19-ch91, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.43
Std Dev 0.9035
Entry
ampl
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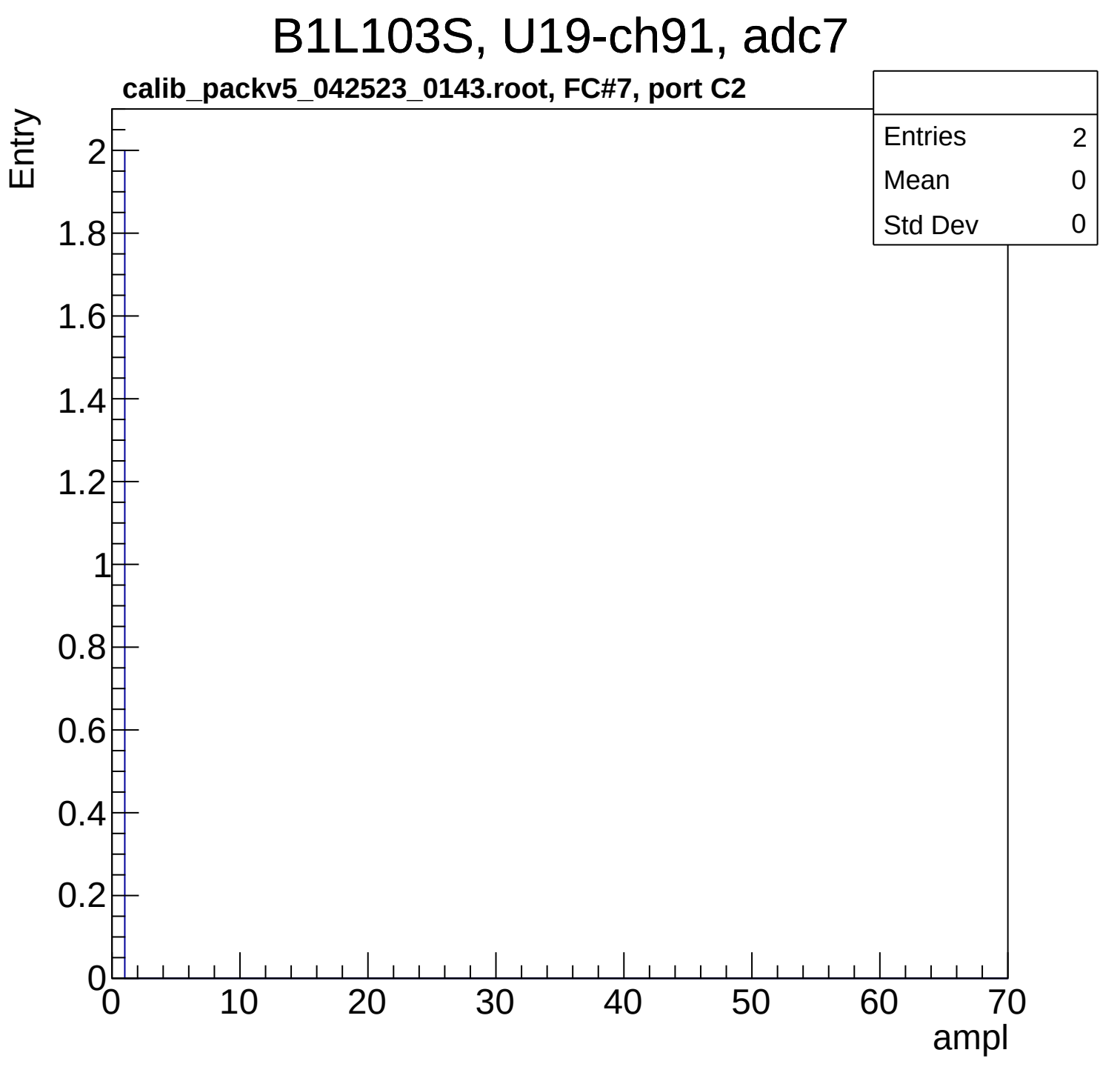
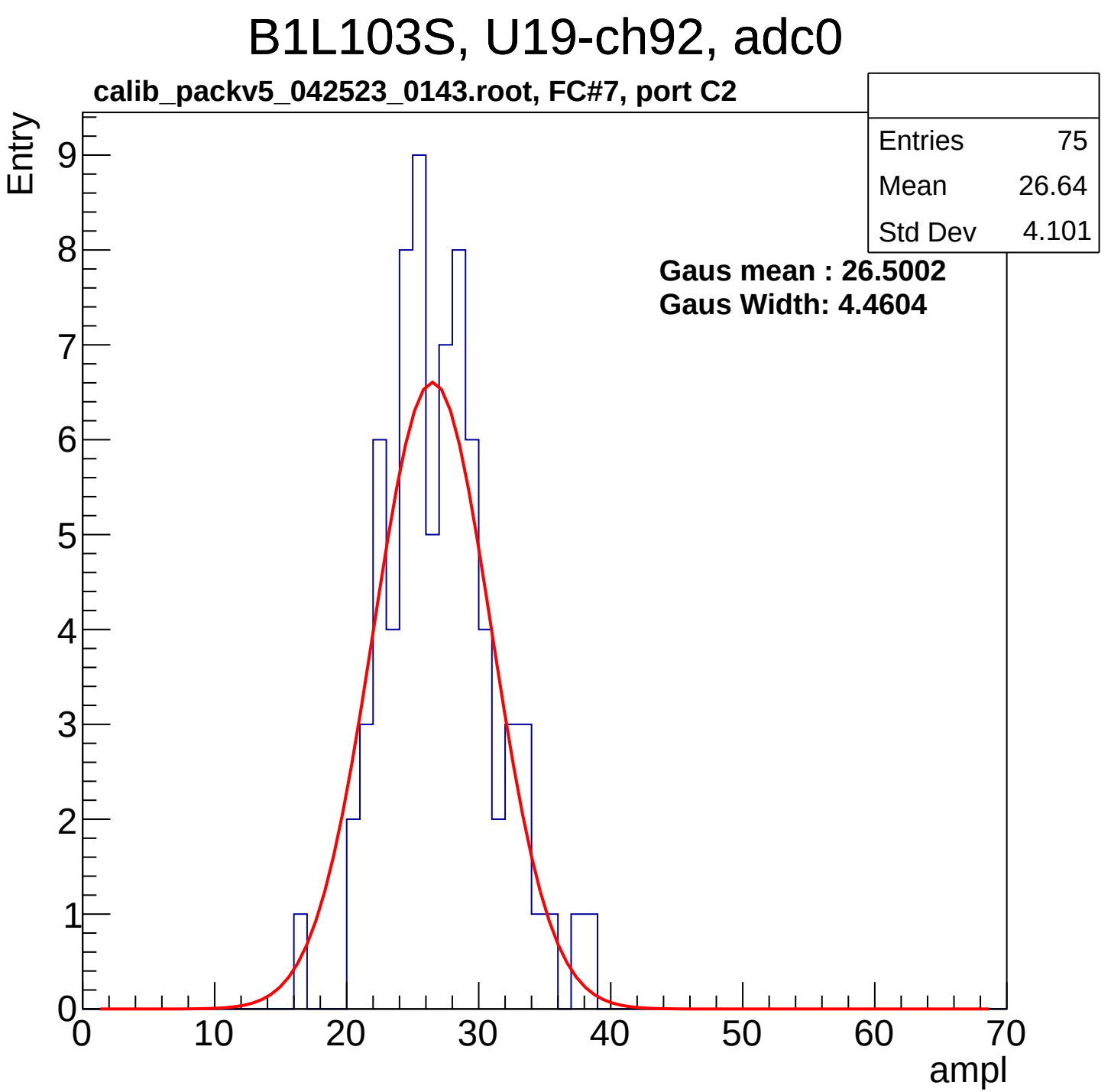
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch91, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch92, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 26.64
Std Dev 4.101
Gaus mean : 26.5002
Gaus Width: 4.4604
Entry
ampl
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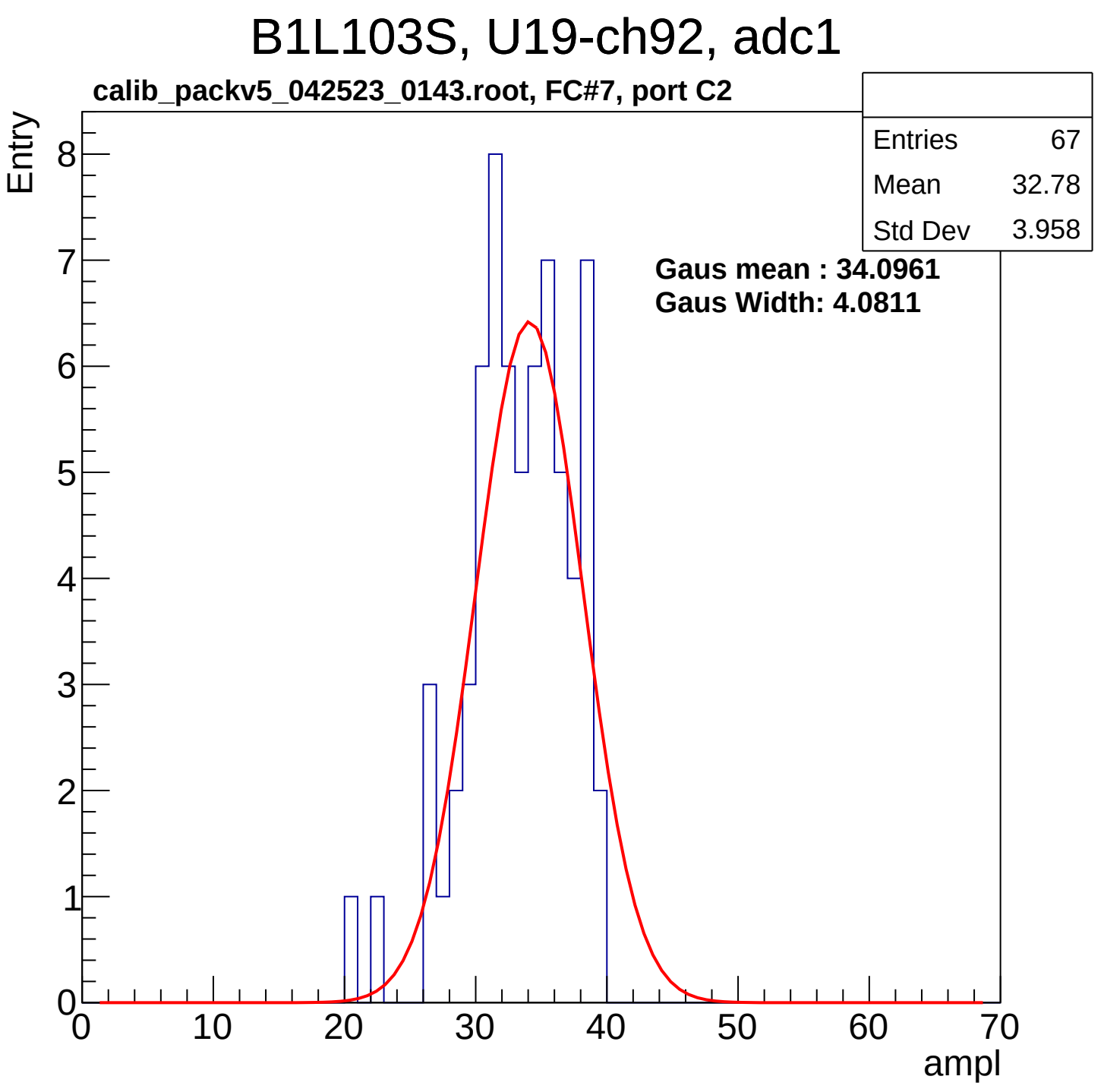
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch92, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 32.78
Std Dev 3.958
Gaus mean : 34.0961
Gaus Width: 4.0811
Entry
ampl
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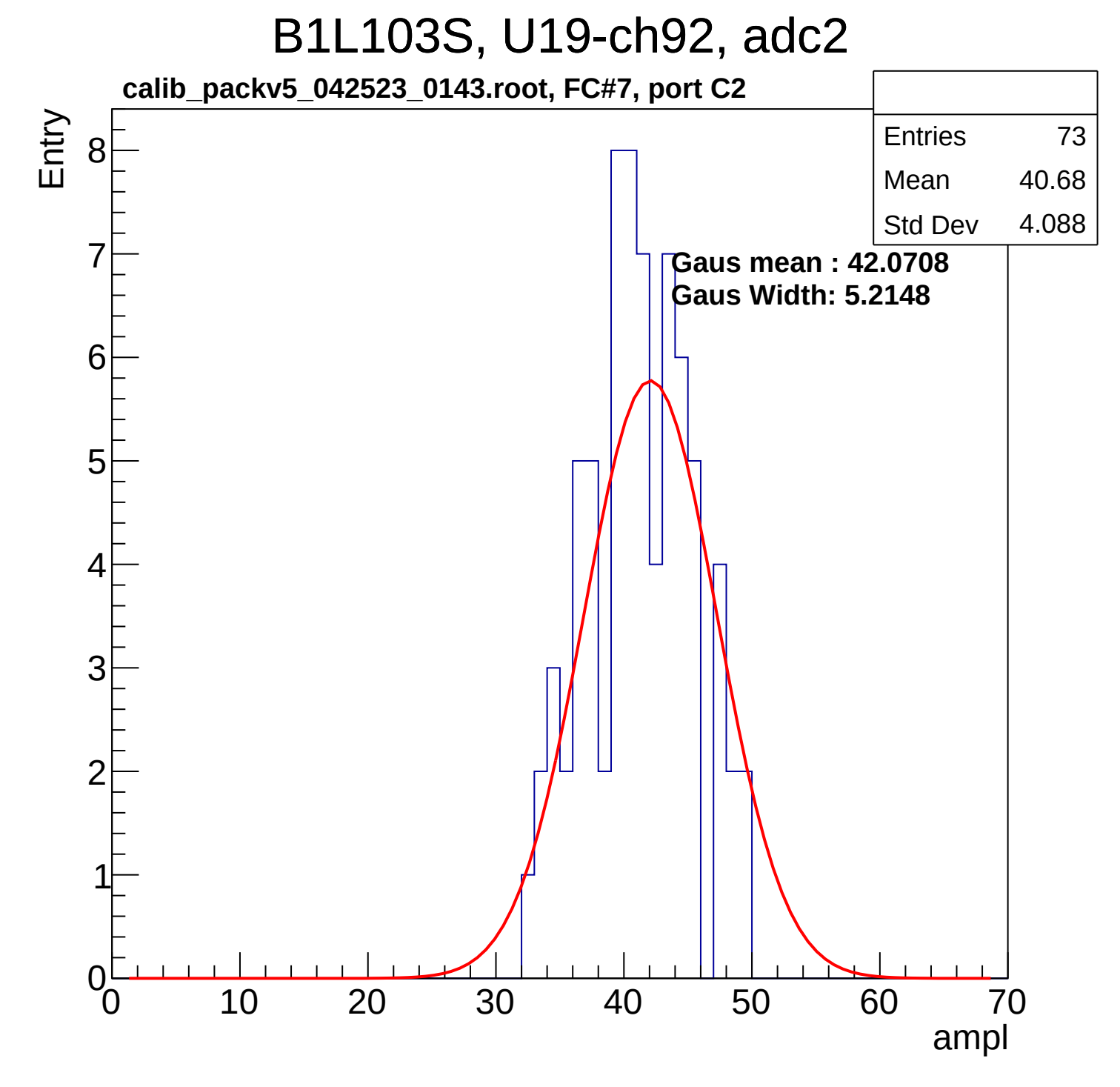
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch92, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 40.68
Std Dev 4.088
Gaus mean : 42.0708
Gaus Width: 5.2148
Entry
ampl
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1
2
3
4
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0
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60
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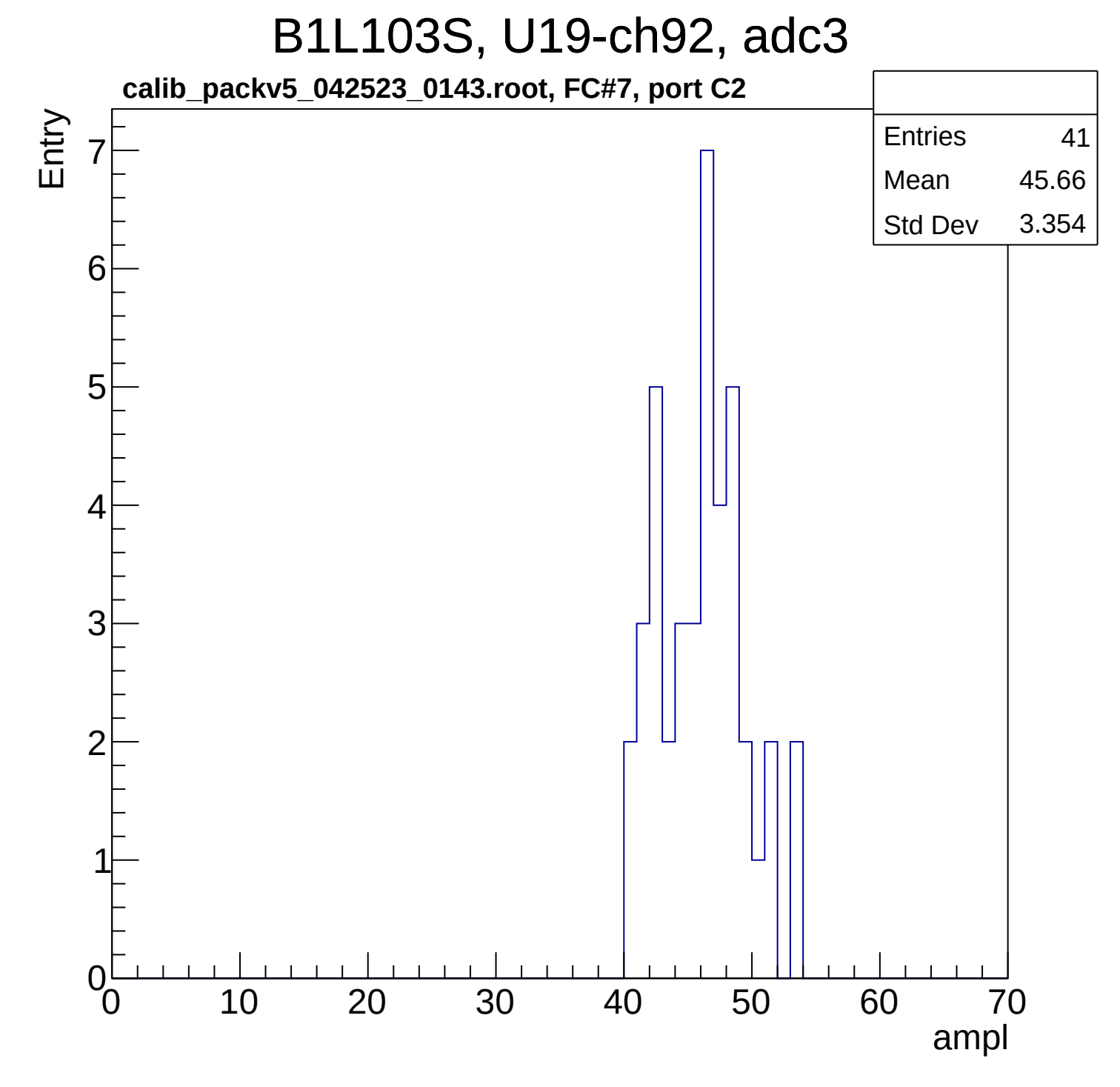
B1L103S, U19-ch92, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 45.66
Std Dev 3.354
Entry
ampl
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3
4
5
6
7
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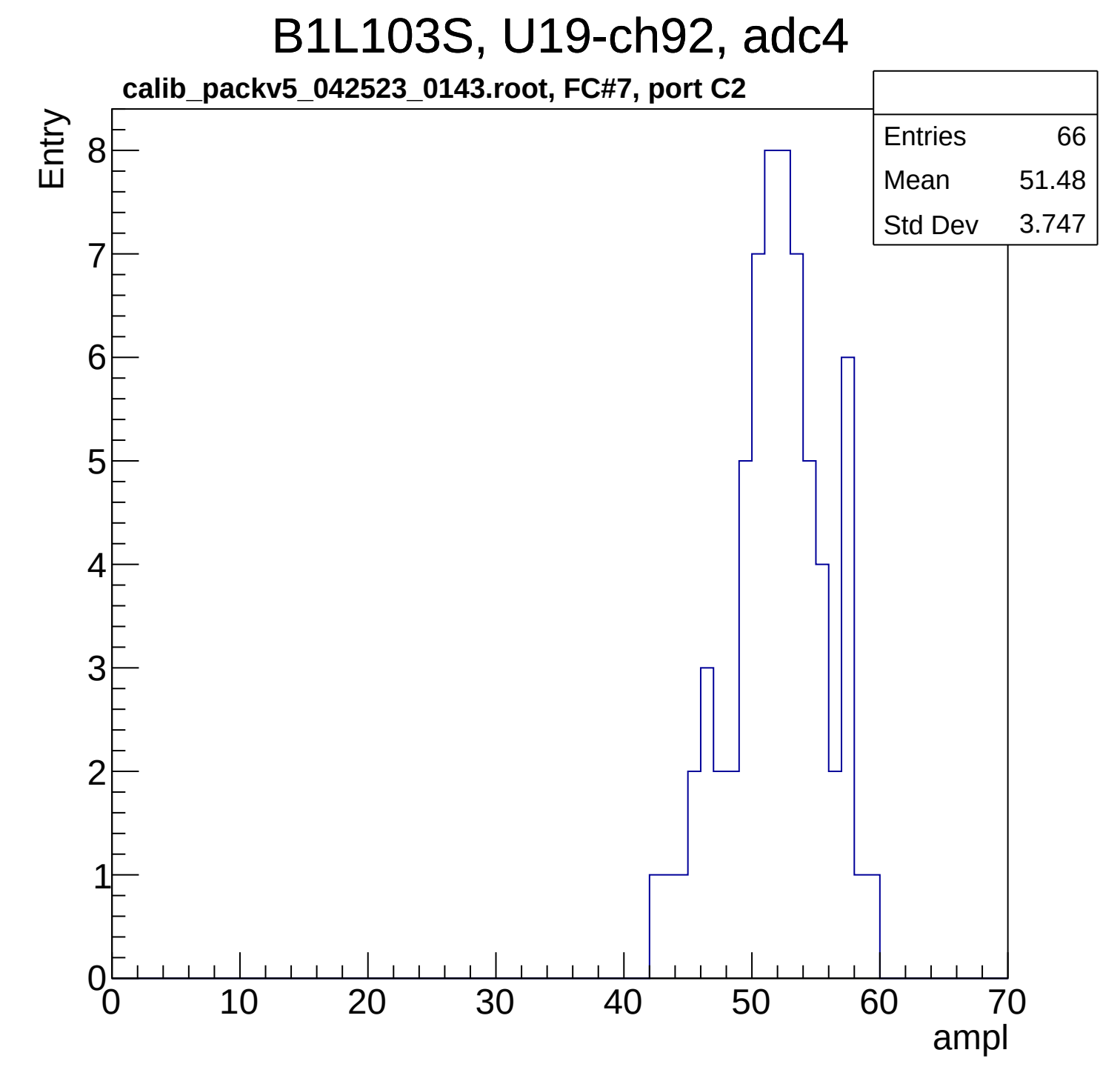
B1L103S, U19-ch92, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 51.48
Std Dev 3.747
Entry
ampl
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1
2
3
4
5
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0
10
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40
50
60
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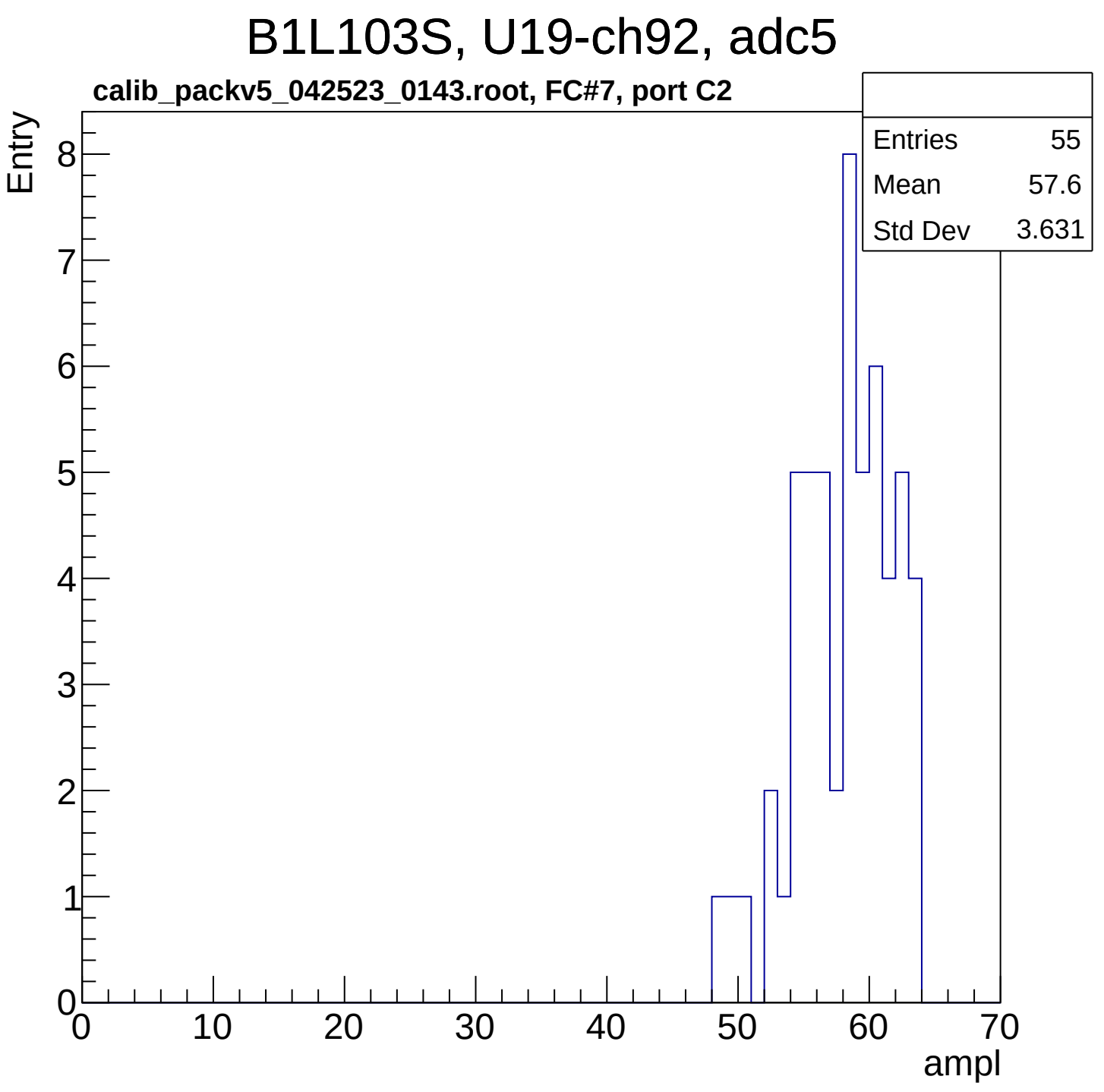
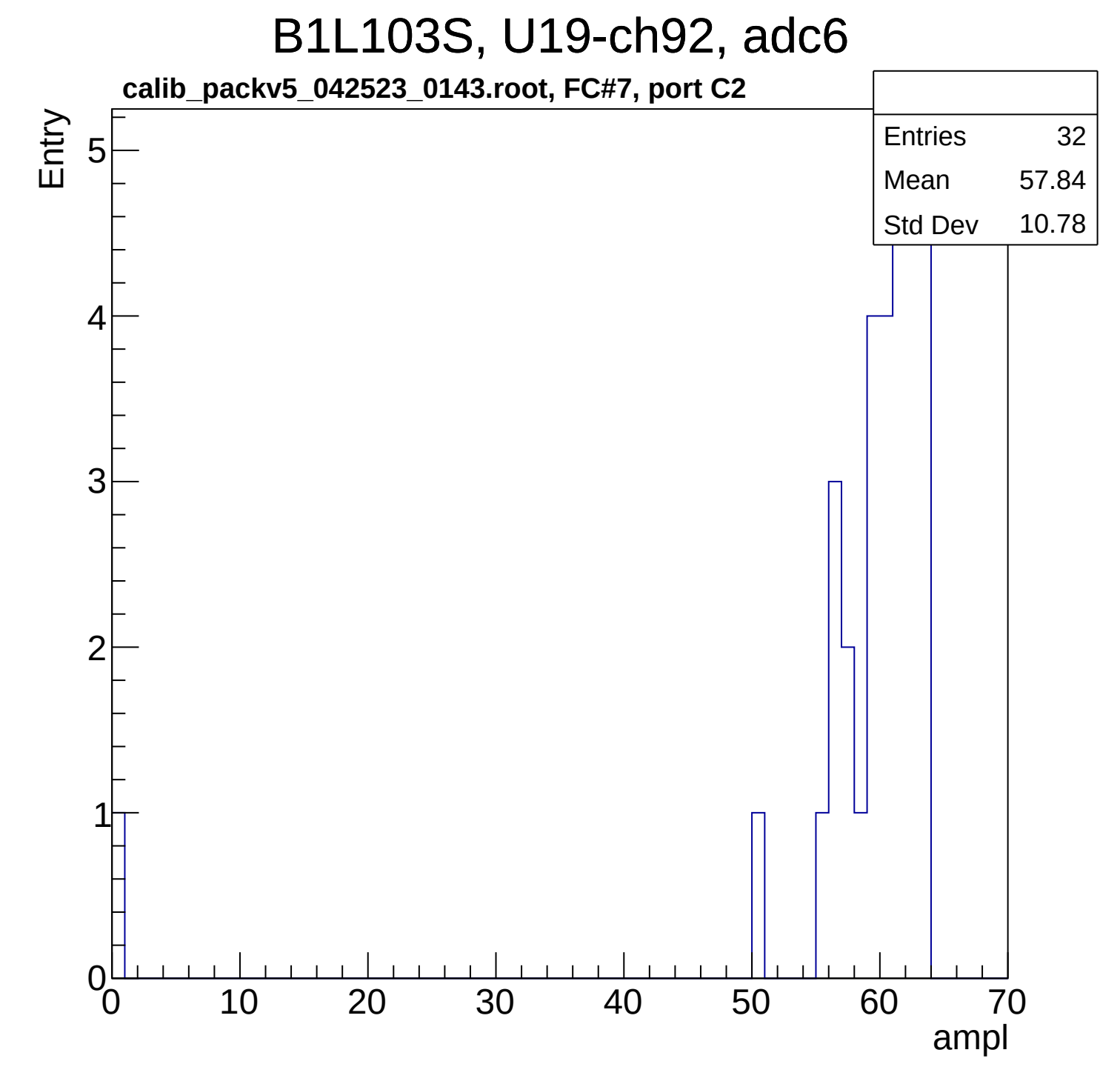
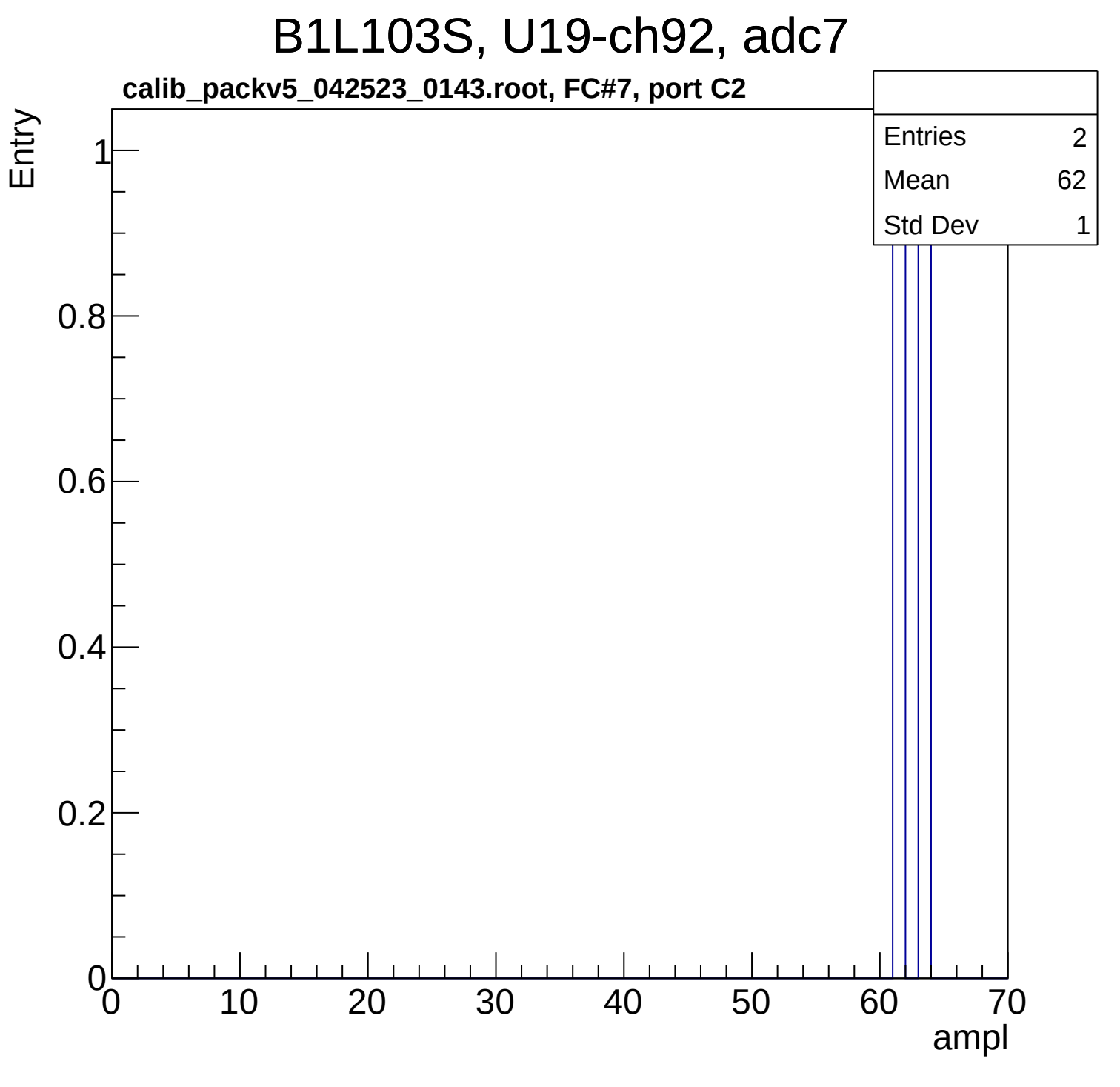

B1L103S, U19-ch92, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 57.6
Std Dev 3.631
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch92, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 57.84
Std Dev 10.78
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch92, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
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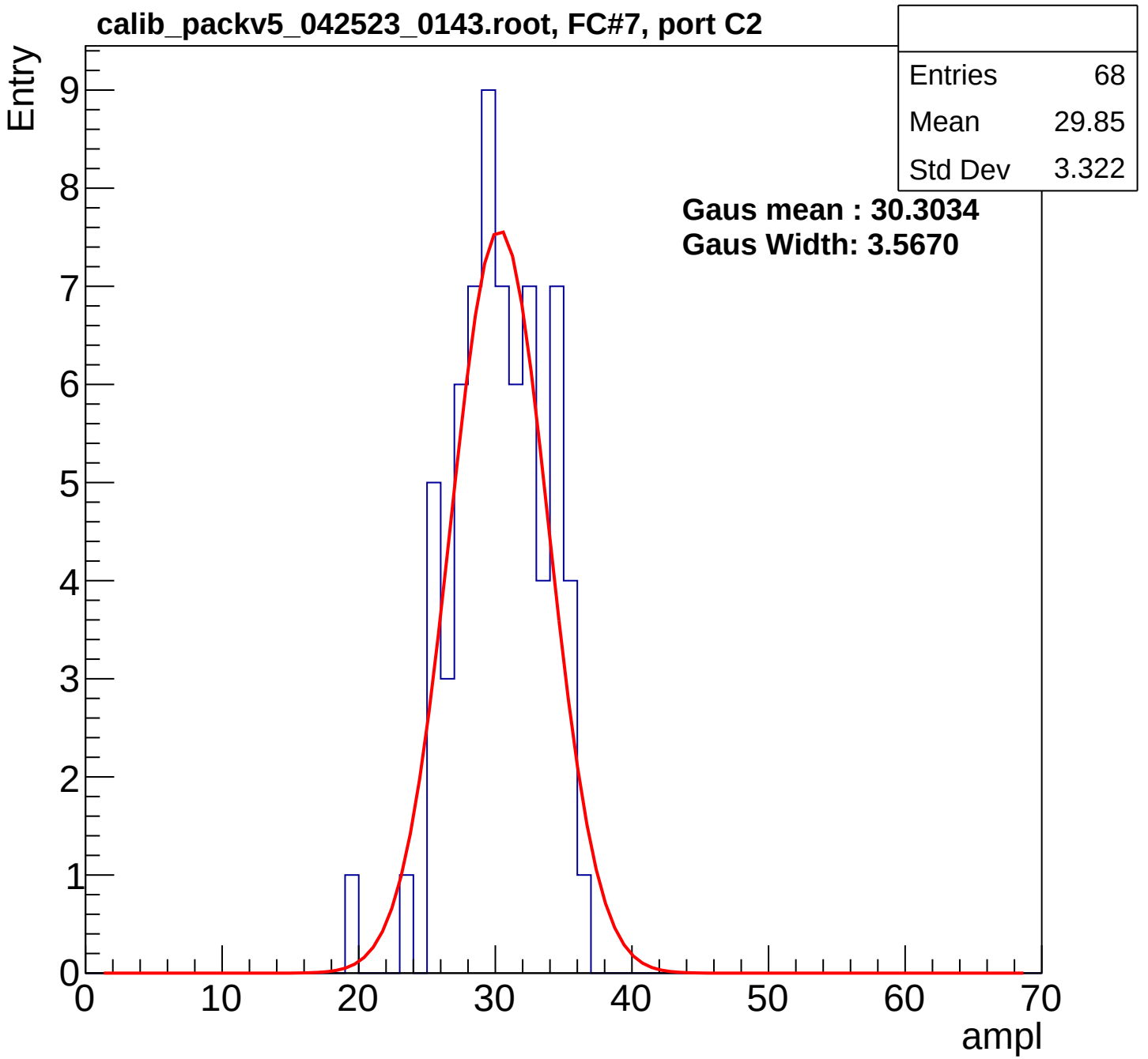
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch93, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 29.85
Std Dev 3.322
Gaus mean : 30.3034
Gaus Width: 3.5670
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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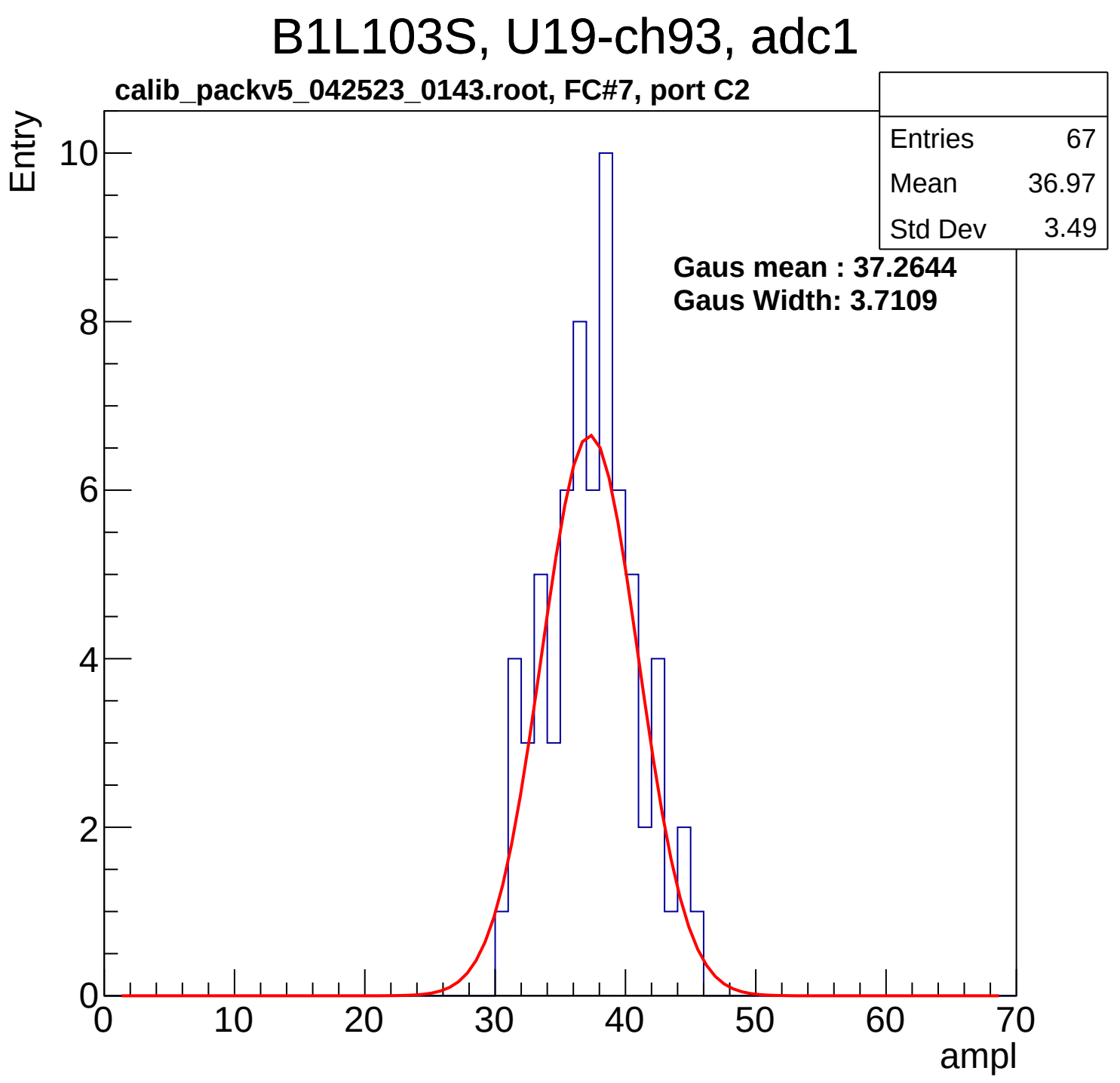
B1L103S, U19-ch93, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.97
Std Dev 3.49
Gaus mean : 37.2644
Gaus Width: 3.7109
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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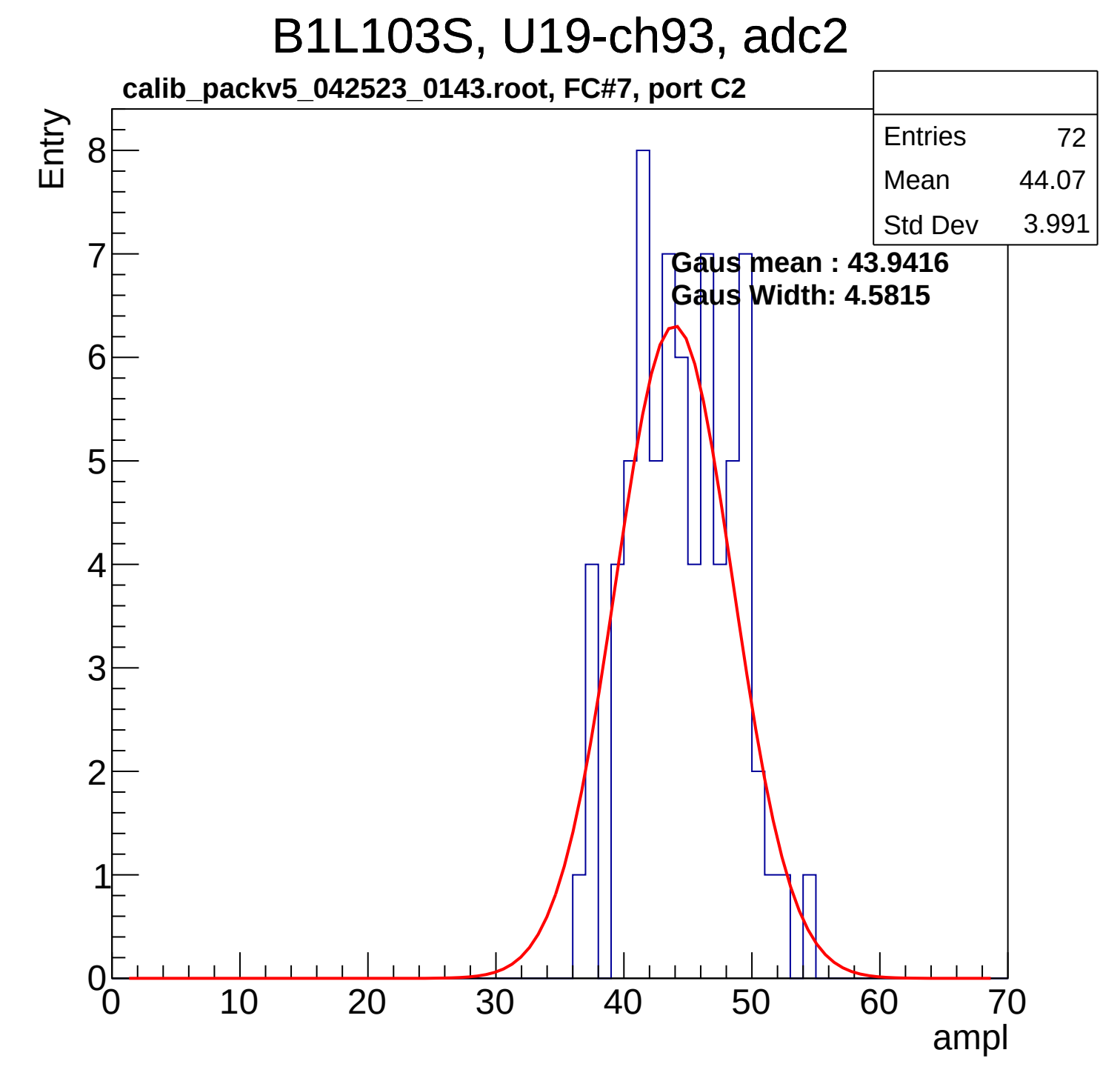
B1L103S, U19-ch93, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 44.07
Std Dev 3.991
Gaus mean : 43.9416
Gaus Width: 4.5815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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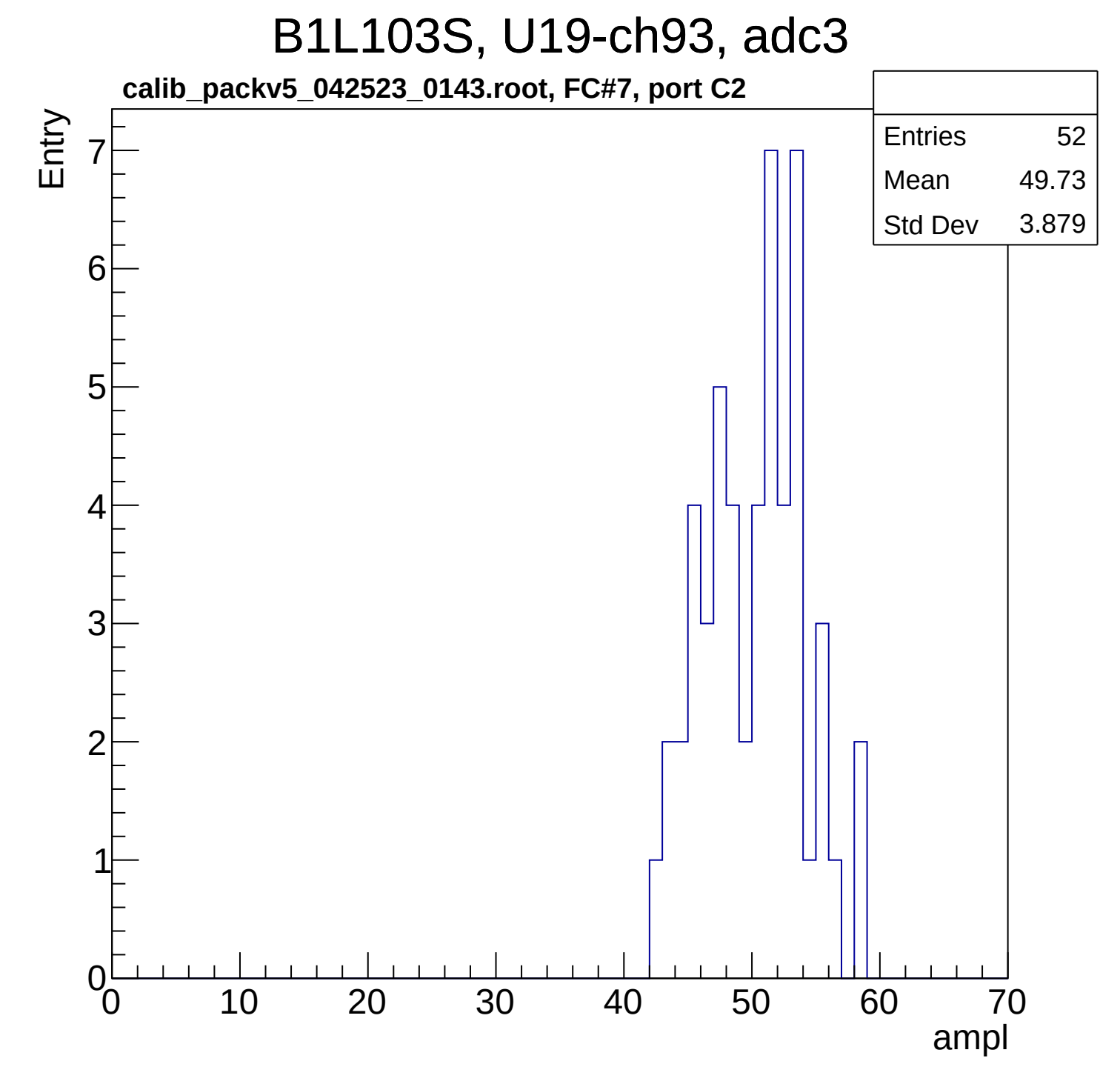
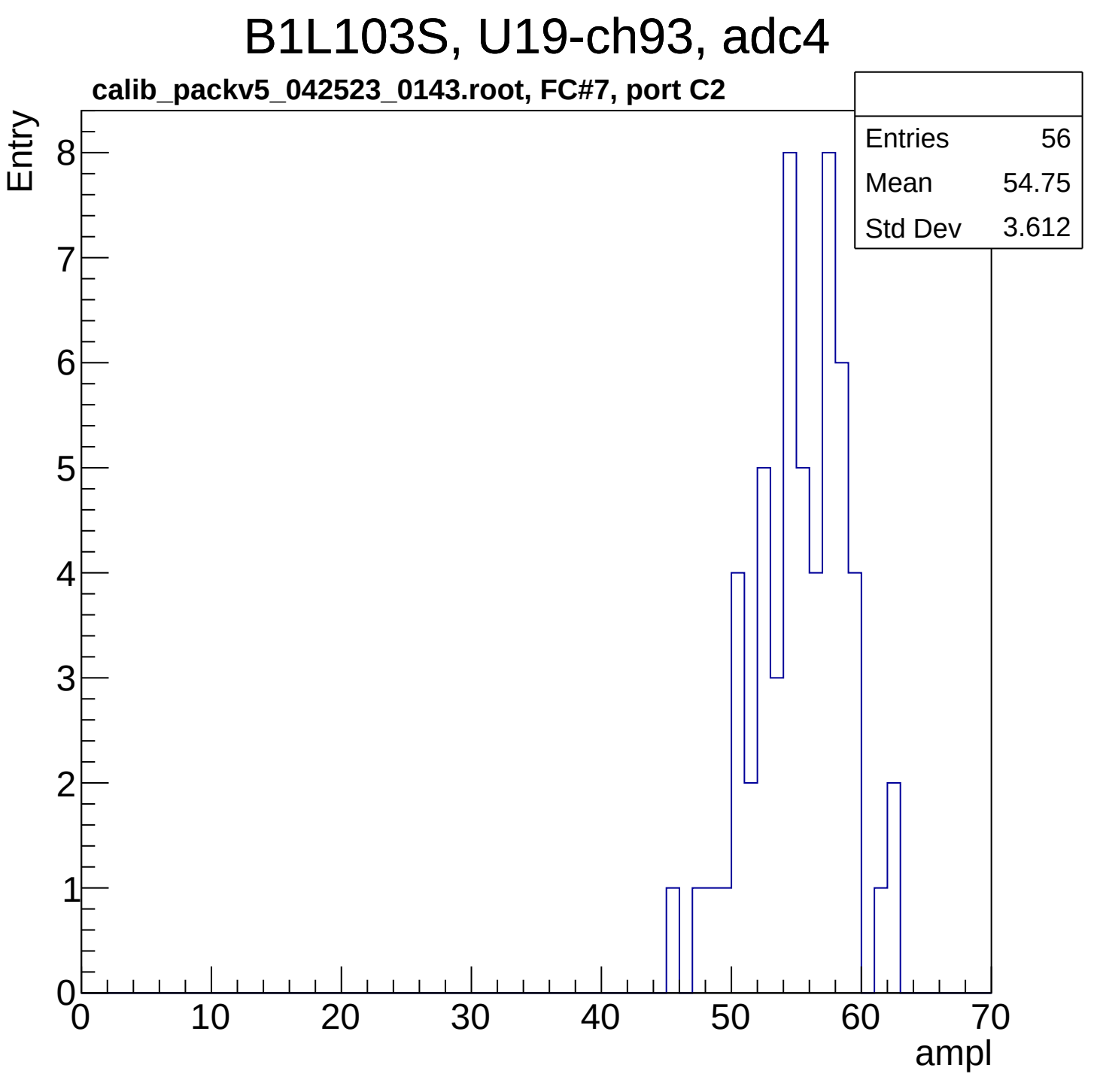
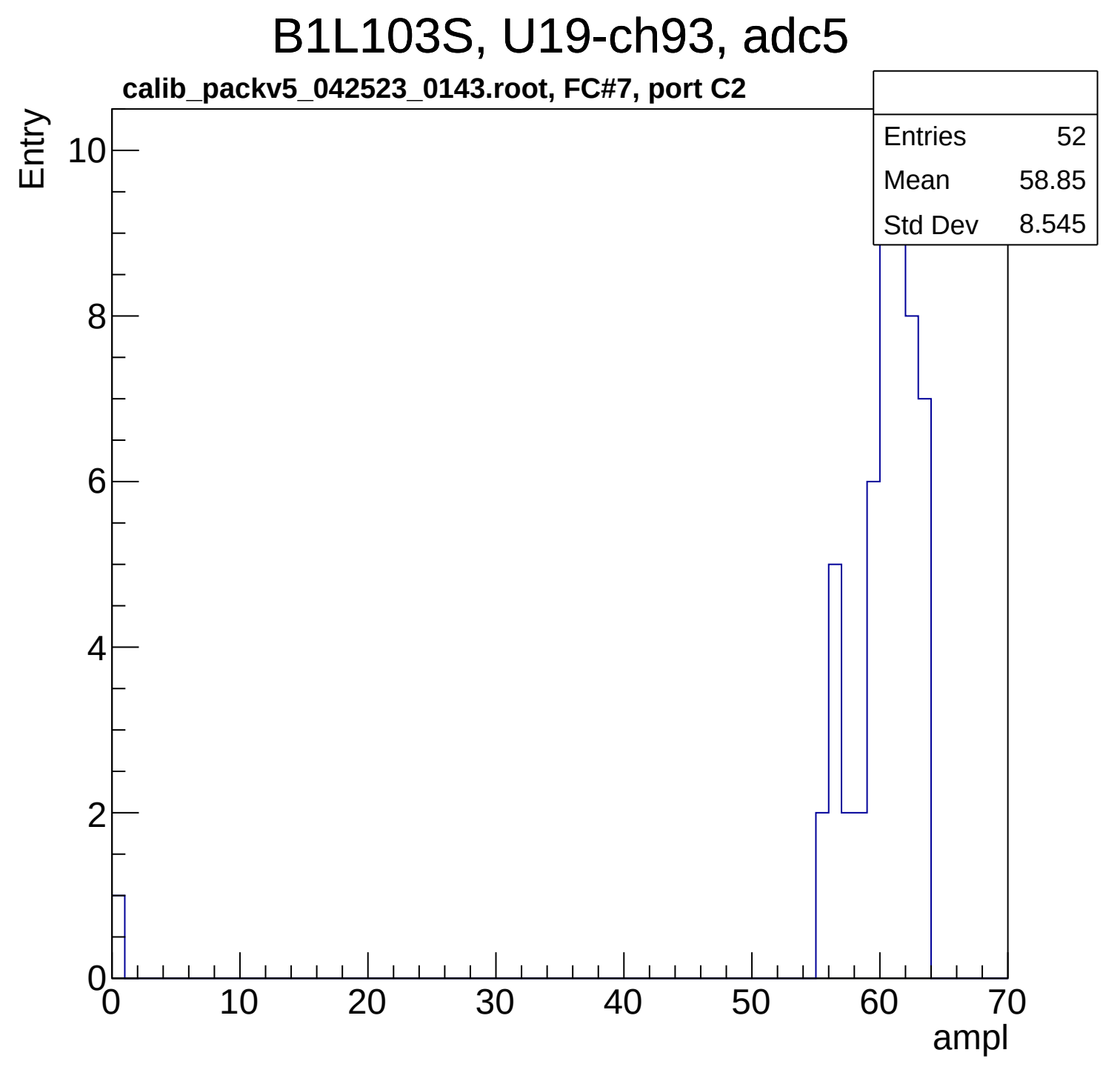
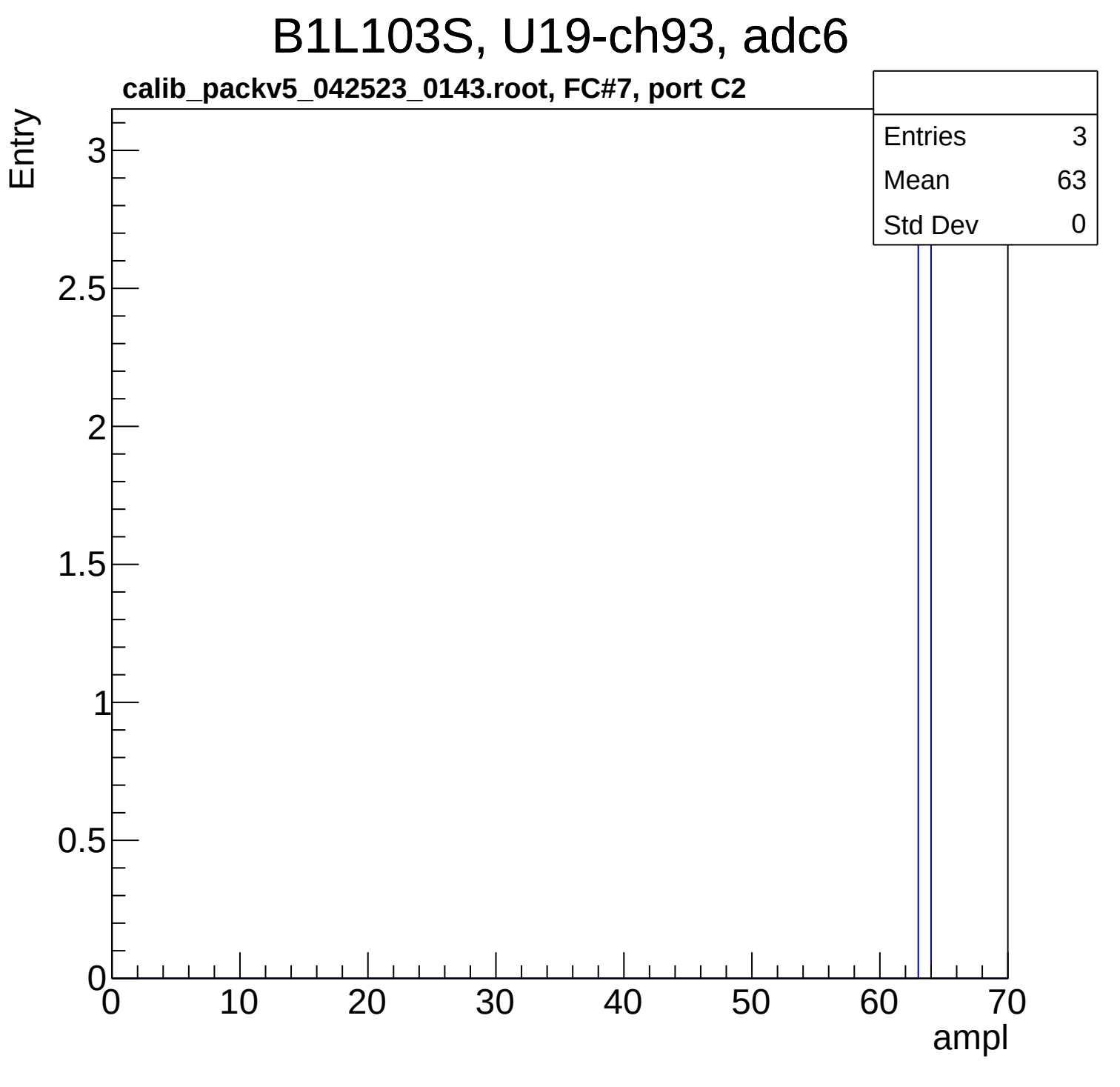
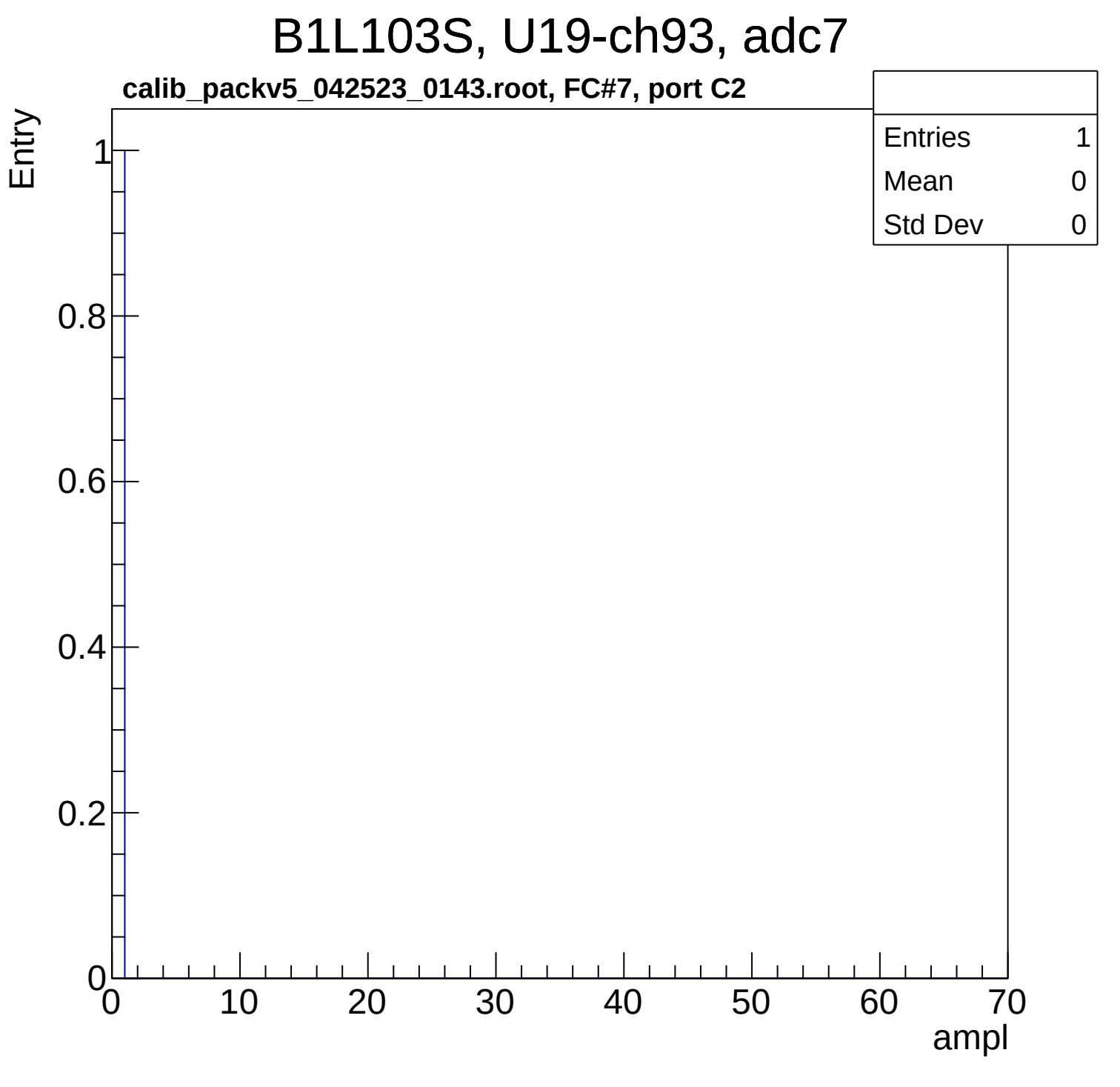

B1L103S, U19-ch93, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 49.73
Std Dev 3.879
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch93, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 54.75
Std Dev 3.612
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch93, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.85
Std Dev 8.545
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch93, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch93, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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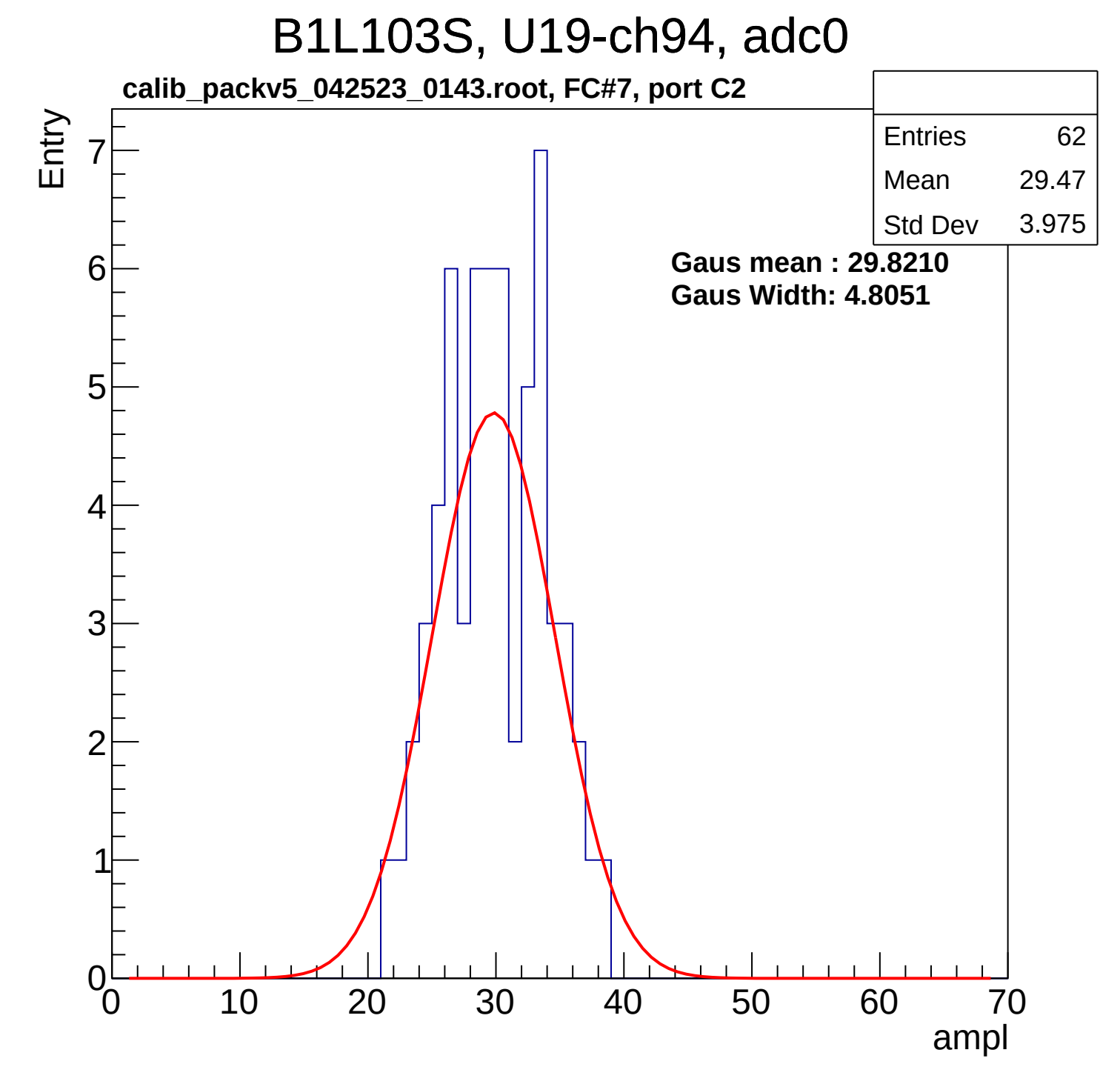
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch94, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 29.47
Std Dev 3.975
Gaus mean : 29.8210
Gaus Width: 4.8051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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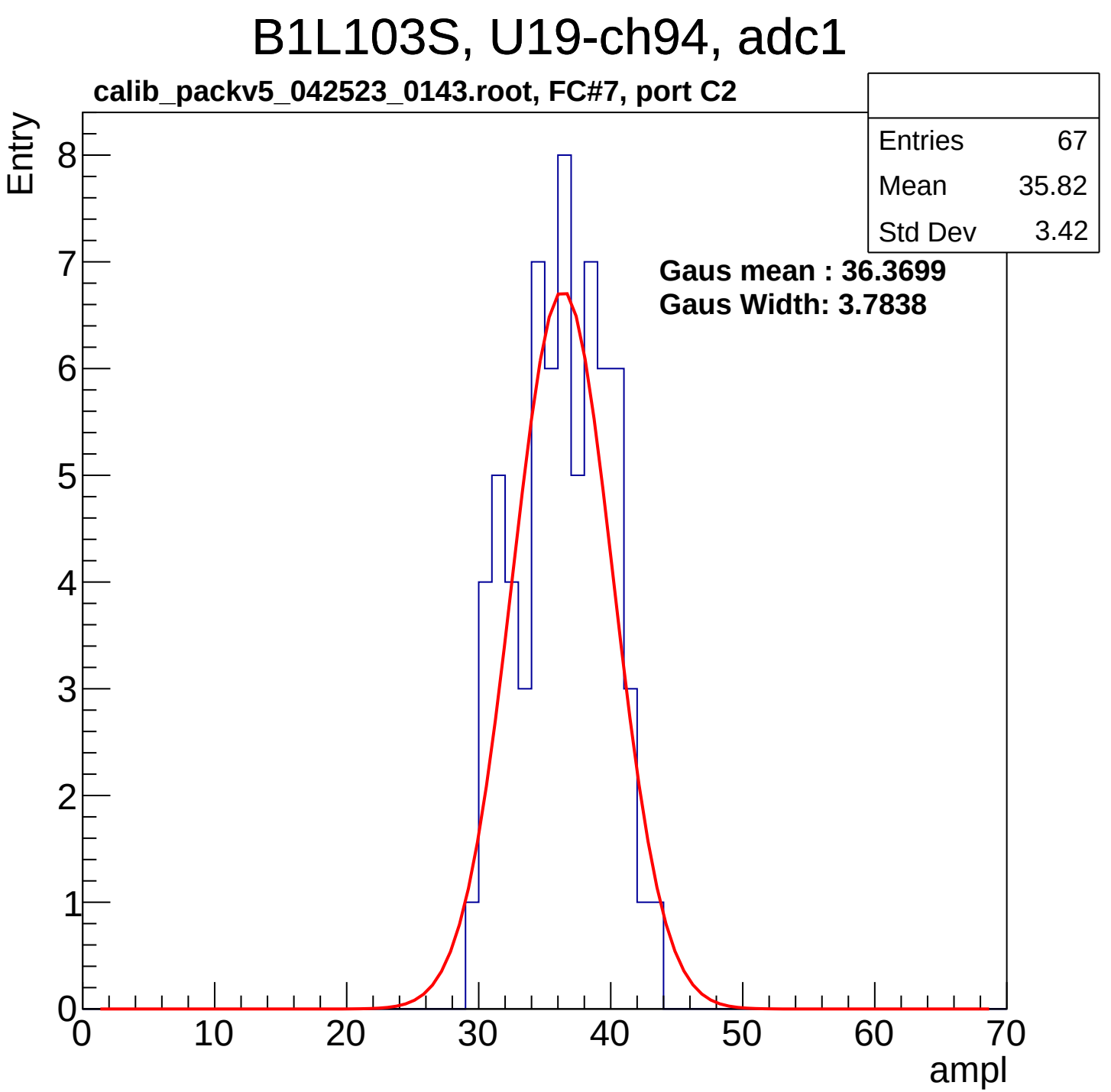
B1L103S, U19-ch94, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.82
Std Dev 3.42
Gaus mean : 36.3699
Gaus Width: 3.7838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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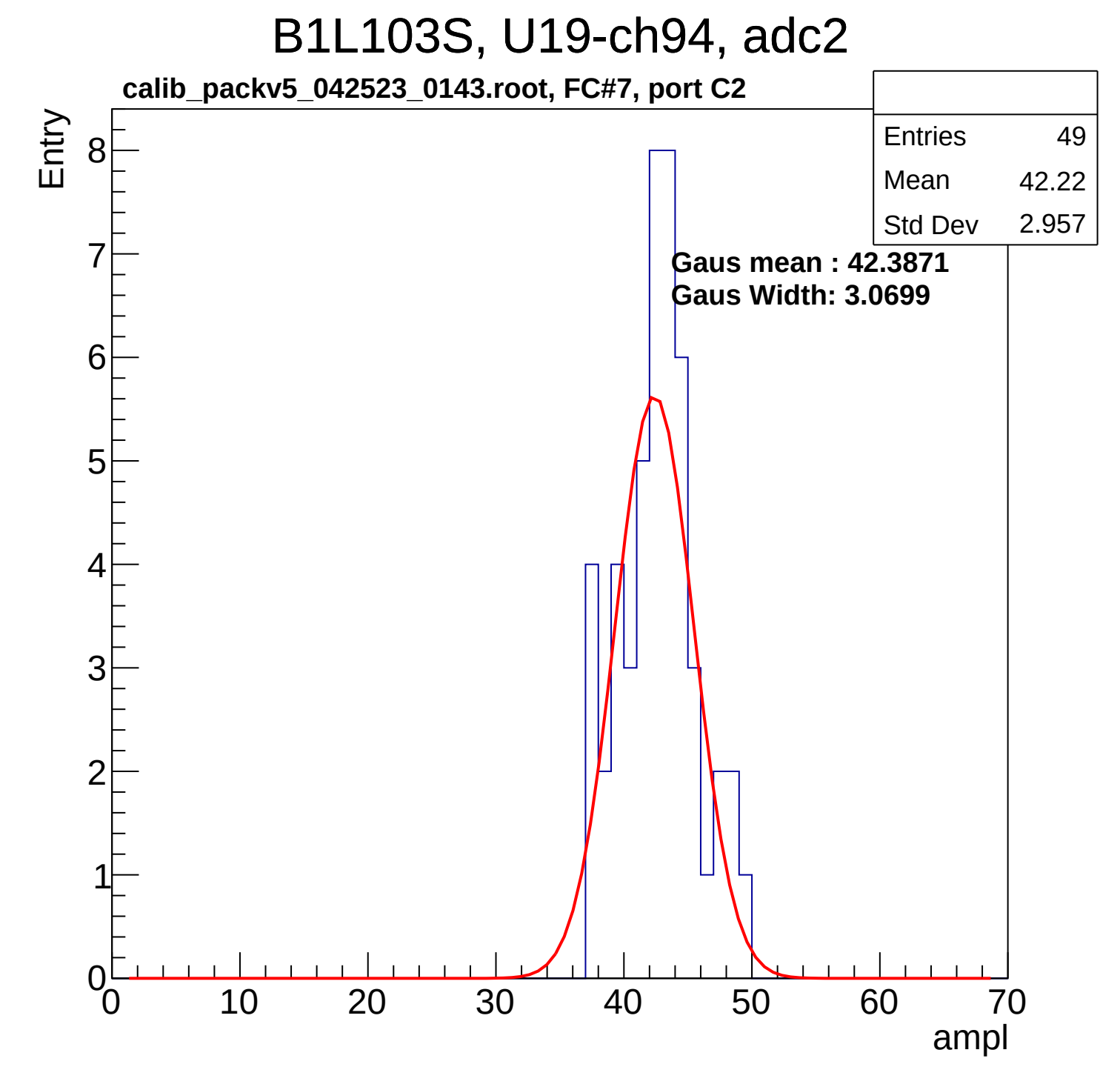
B1L103S, U19-ch94, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 42.22
Std Dev 2.957
Gaus mean : 42.3871
Gaus Width: 3.0699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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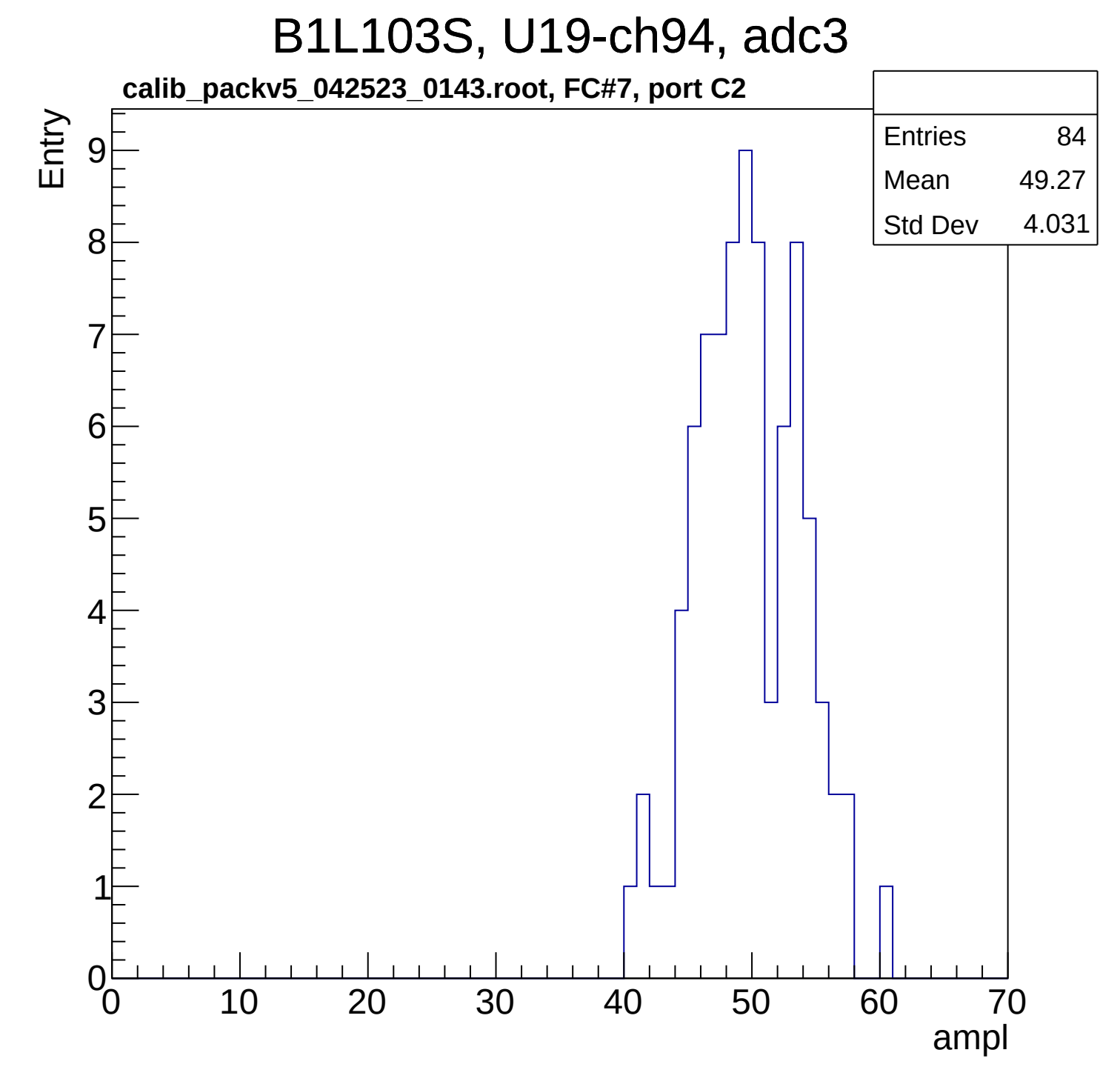
B1L103S, U19-ch94, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 49.27
Std Dev 4.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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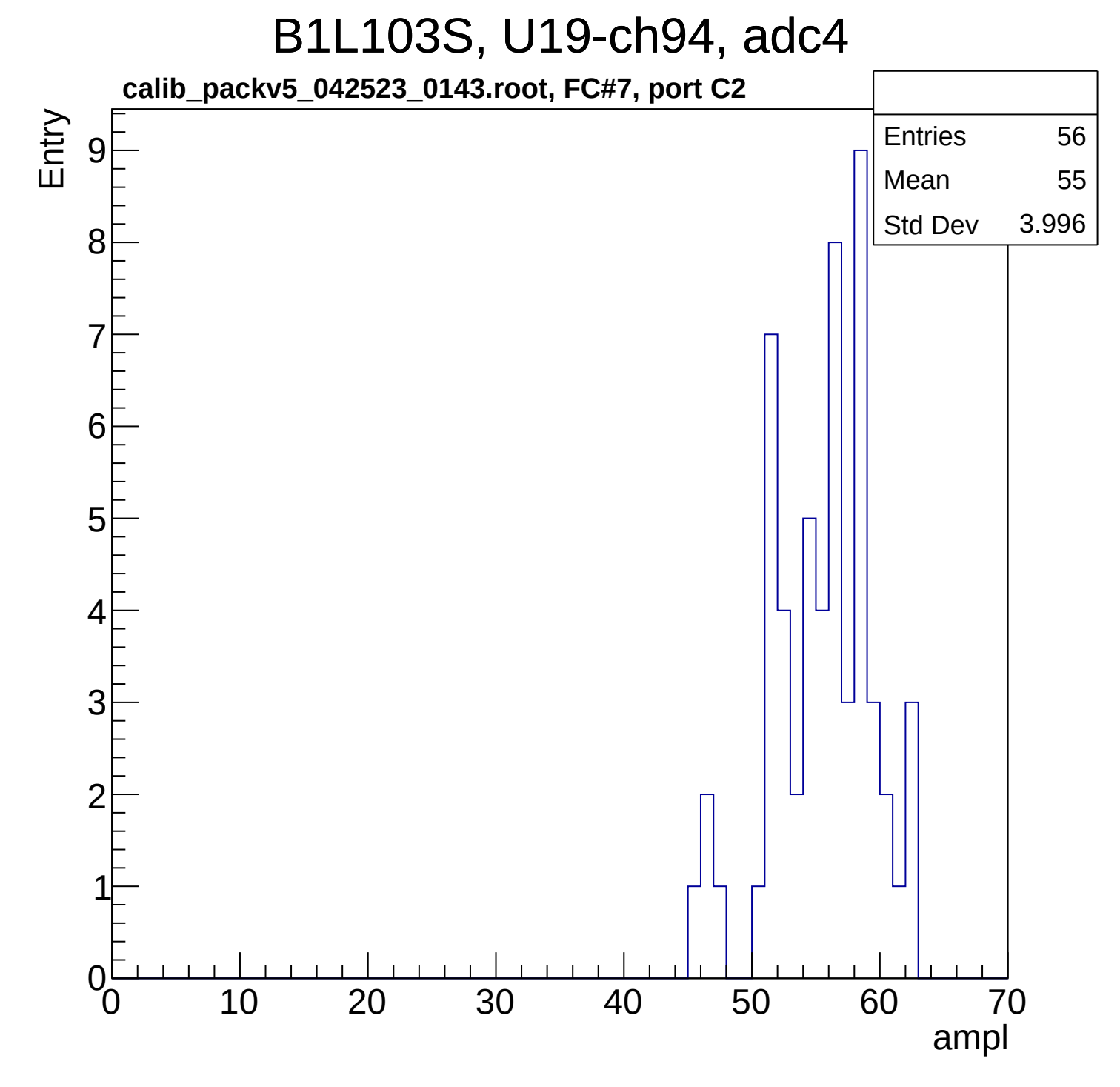
B1L103S, U19-ch94, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 55
Std Dev 3.996
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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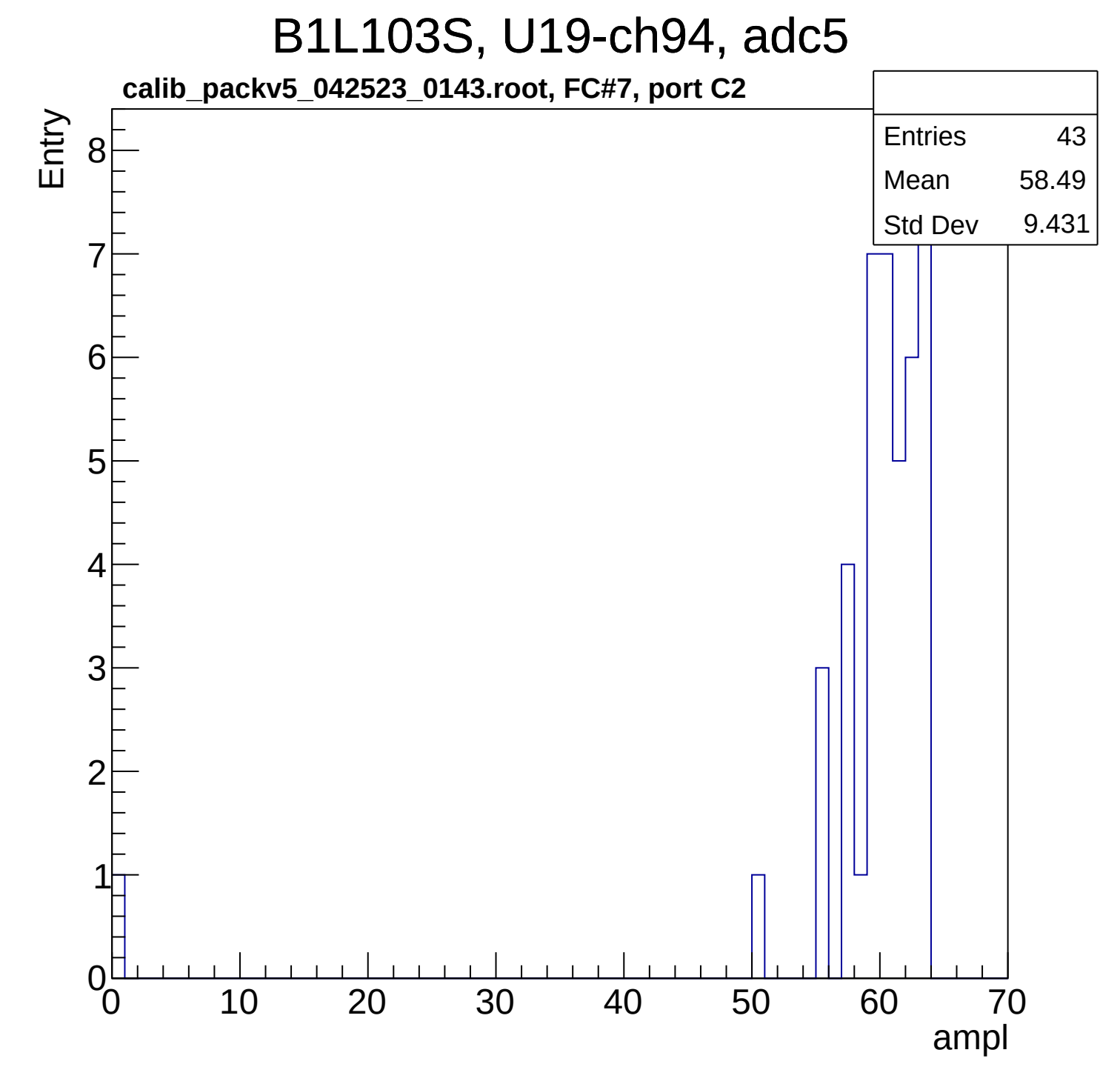
B1L103S, U19-ch94, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 58.49
Std Dev 9.431
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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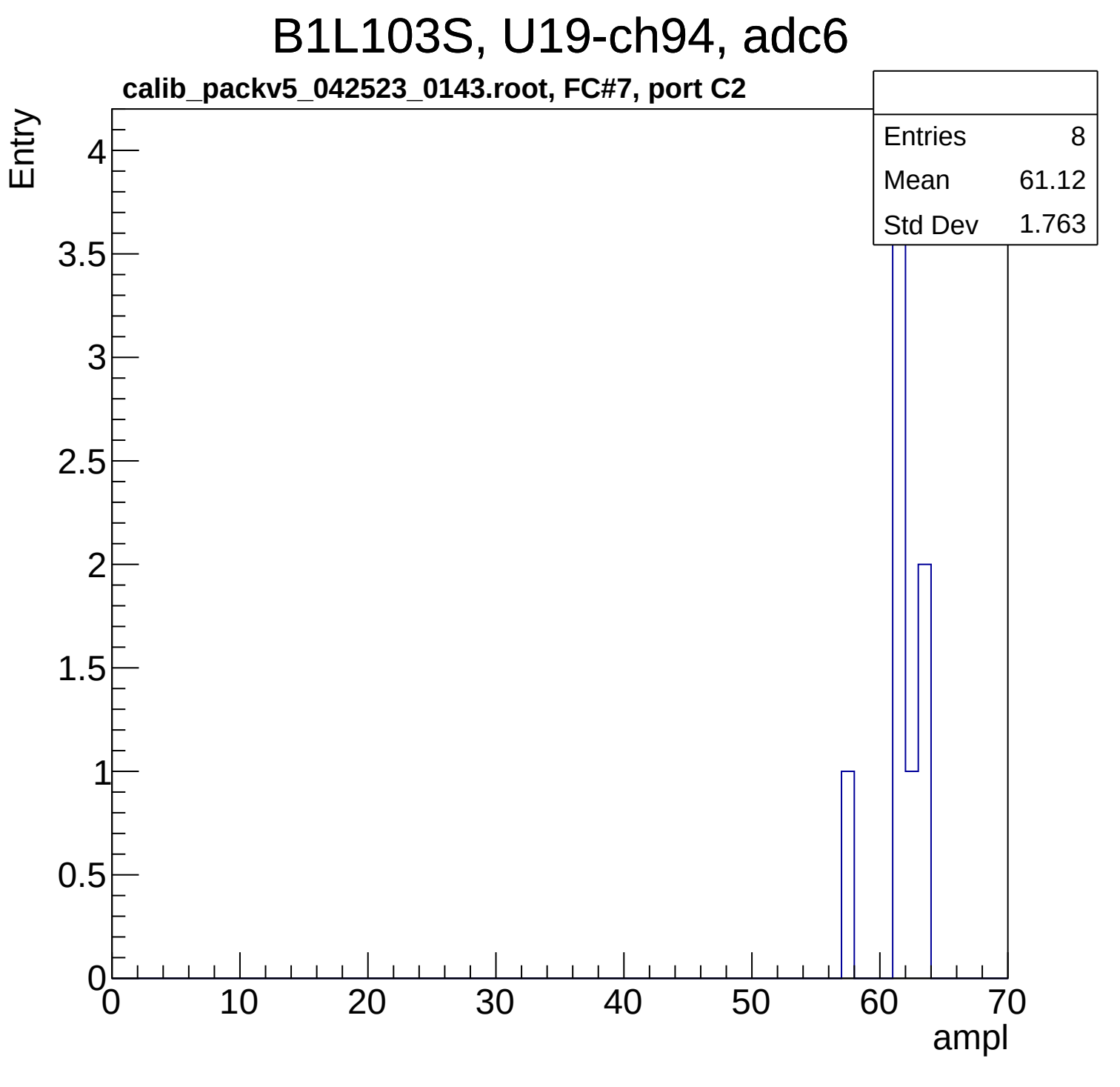
B1L103S, U19-ch94, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.12
Std Dev 1.763
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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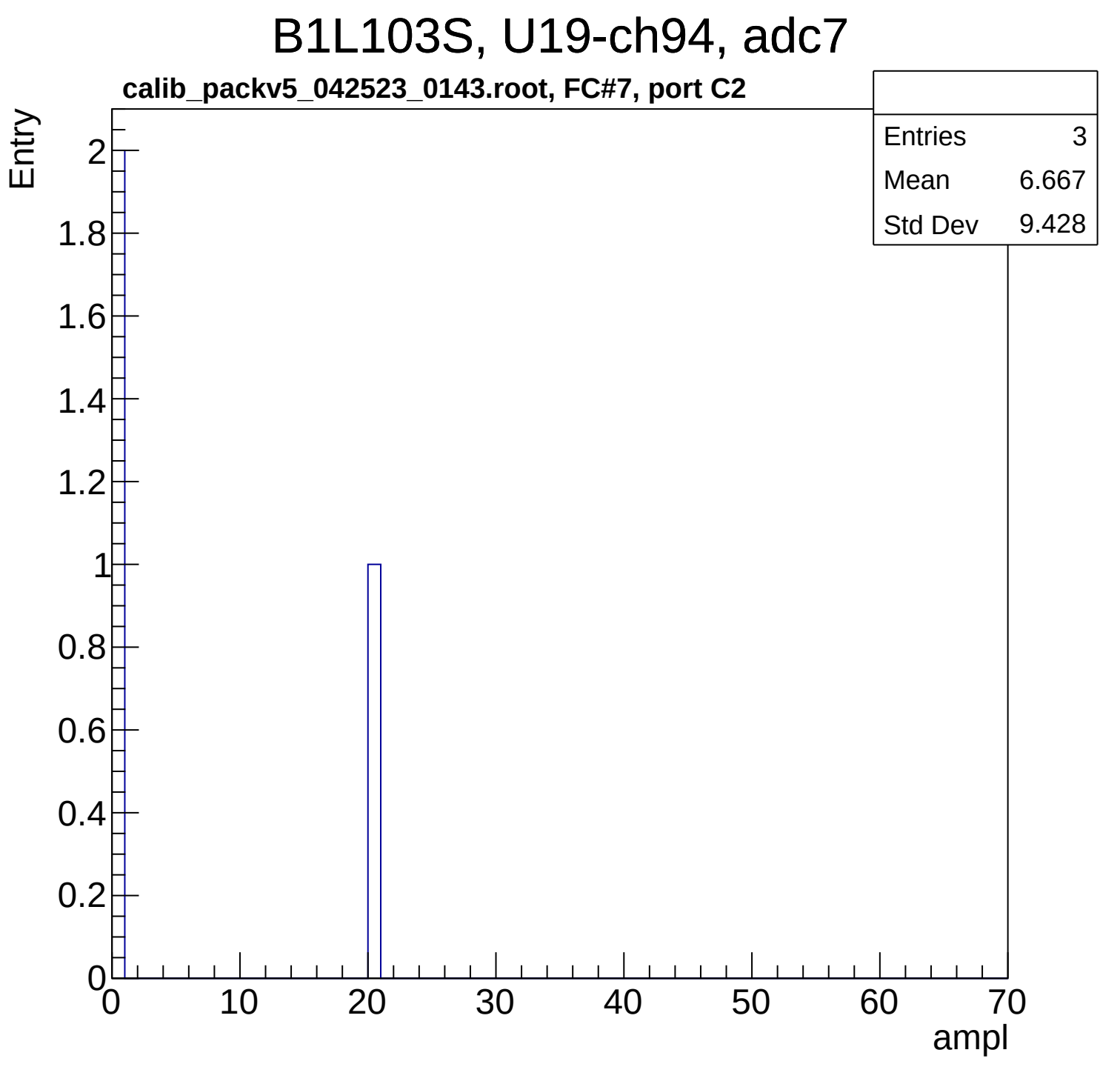
B1L103S, U19-ch94, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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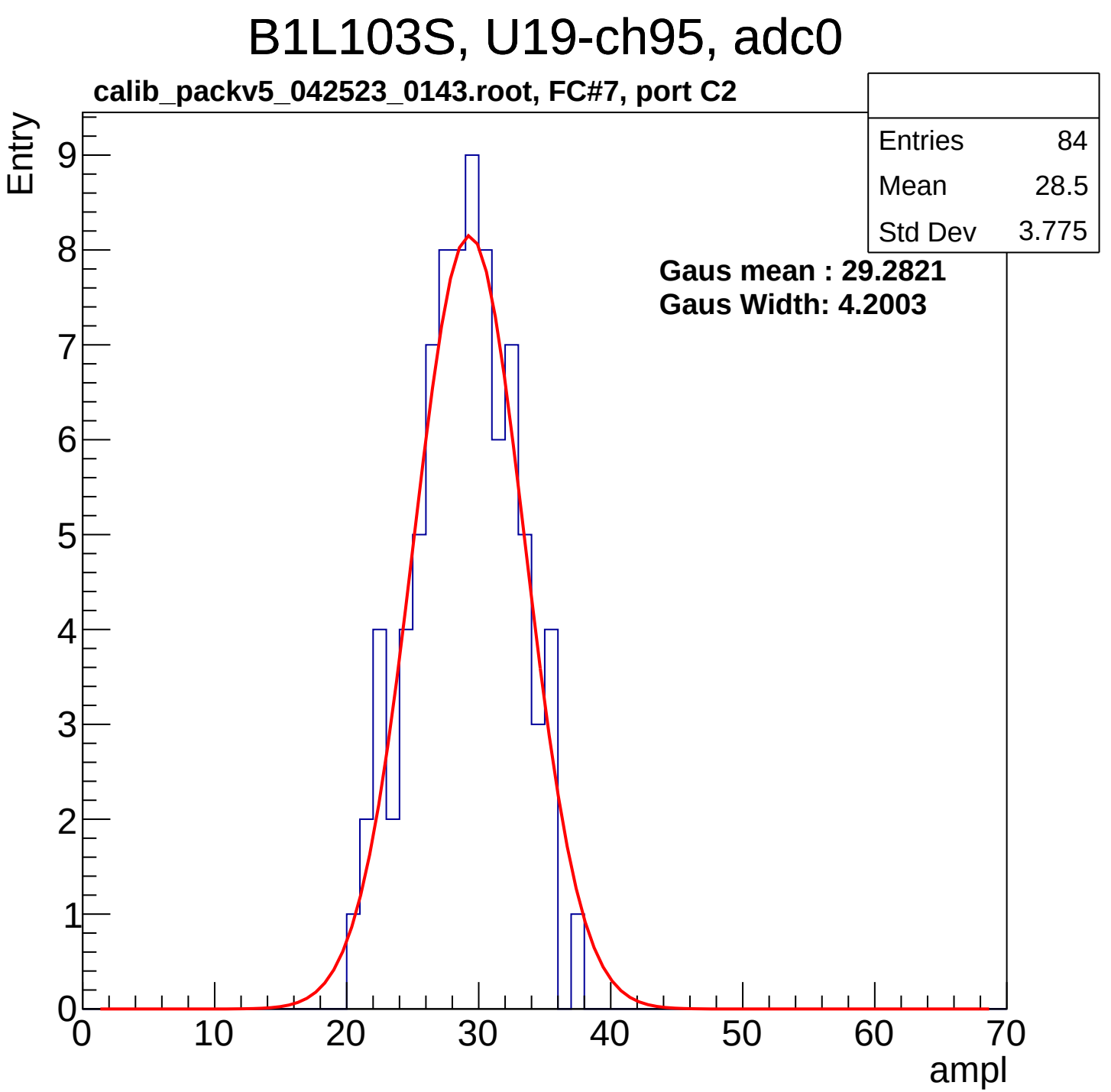
B1L103S, U19-ch95, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 28.5
Std Dev 3.775
Gaus mean : 29.2821
Gaus Width: 4.2003
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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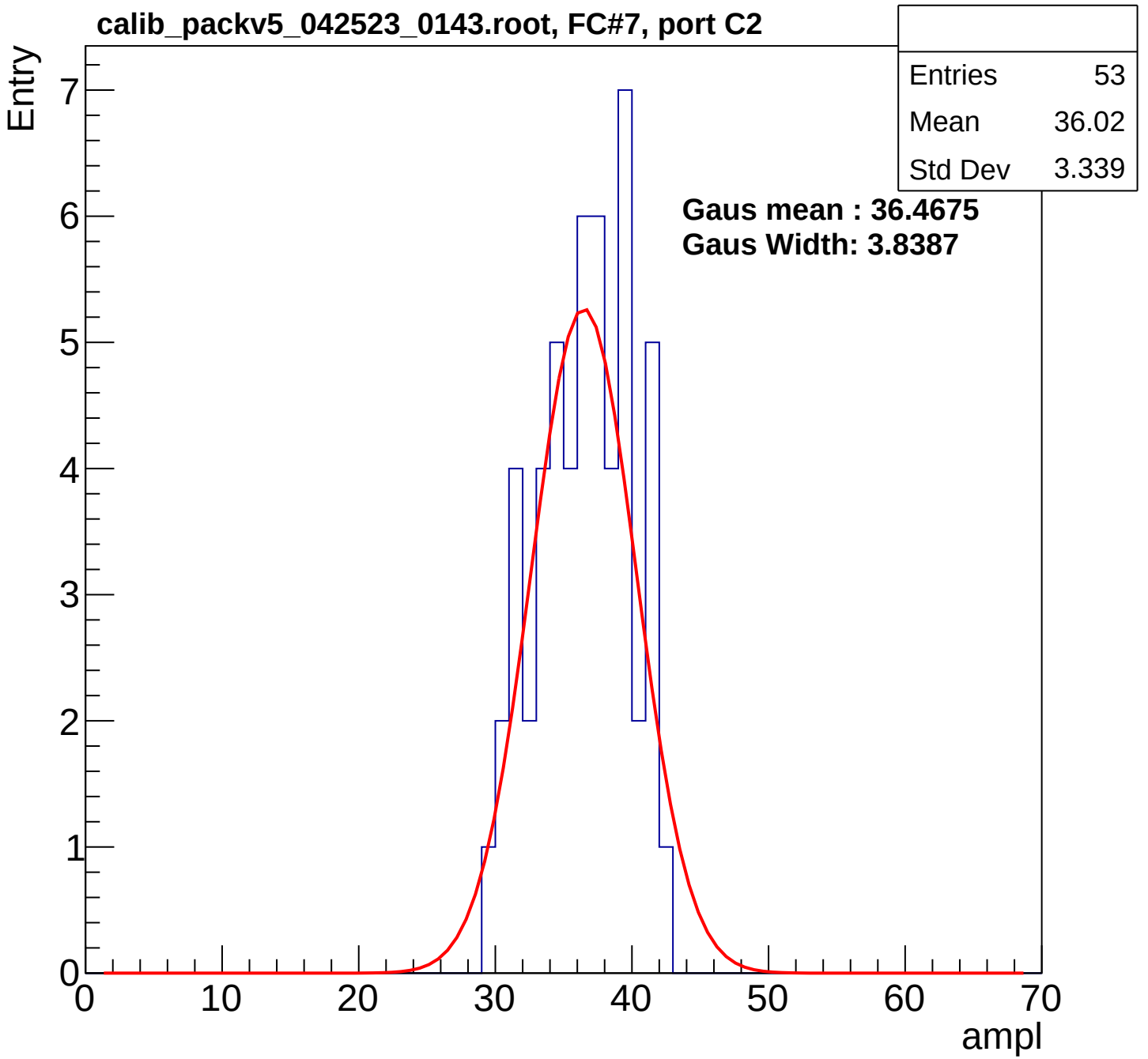
40
50
60
70


B1L103S, U19-ch95, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 36.02
Std Dev 3.339
Gaus mean : 36.4675
Gaus Width: 3.8387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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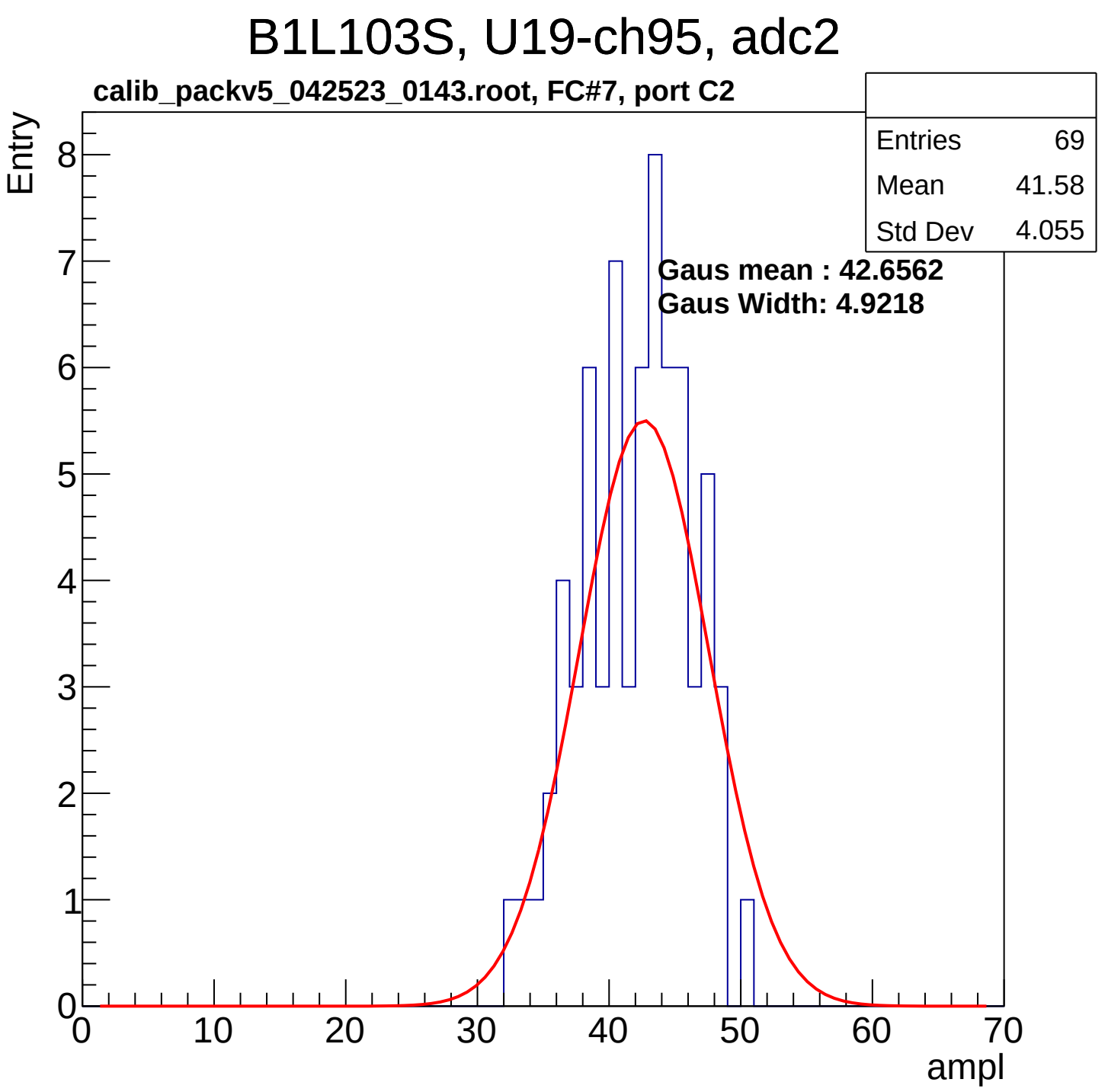
B1L103S, U19-ch95, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 41.58
Std Dev 4.055
Gaus mean : 42.6562
Gaus Width: 4.9218
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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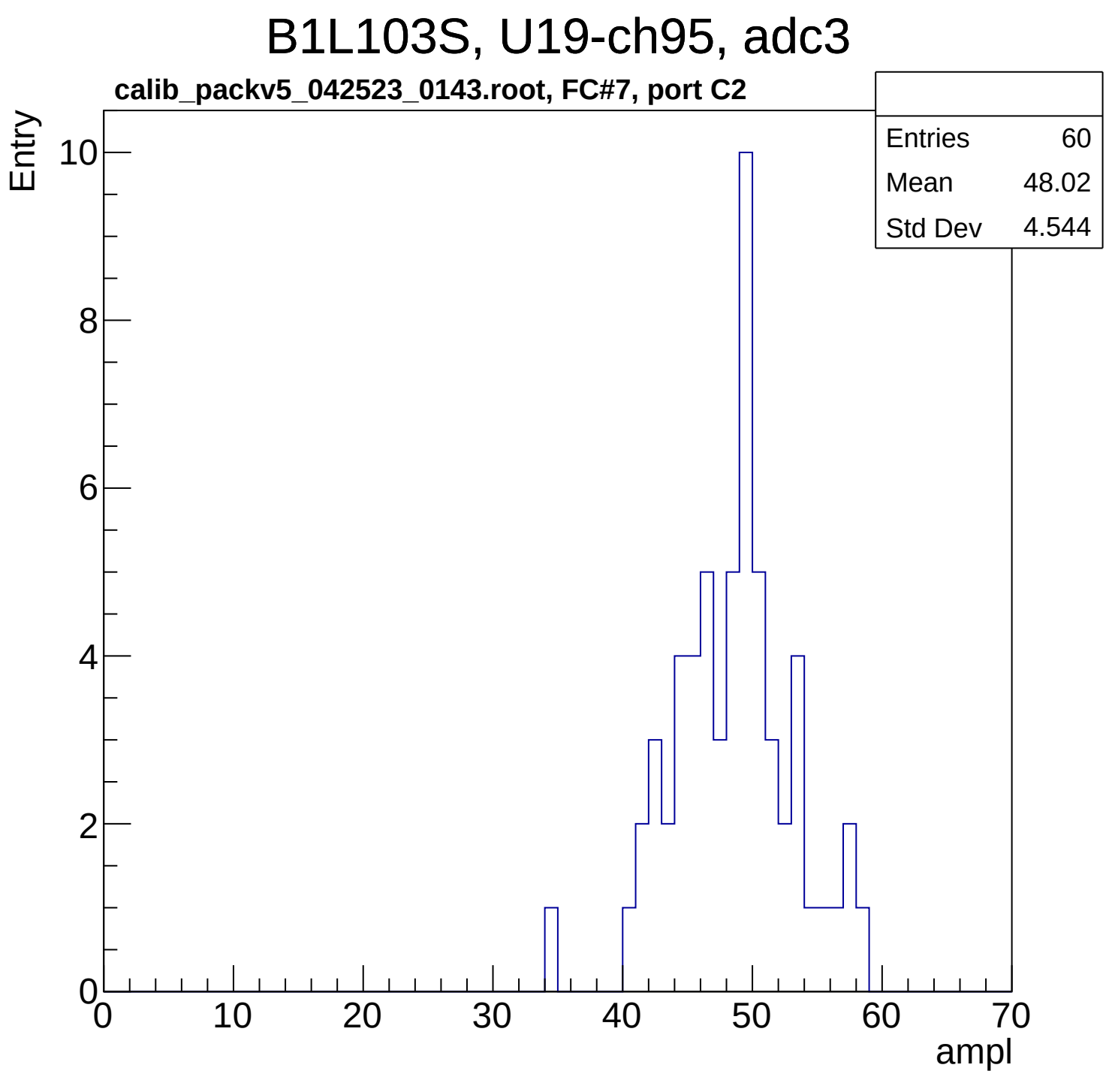
40
50
60
70


B1L103S, U19-ch95, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.02
Std Dev 4.544
Entry
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2
4
6
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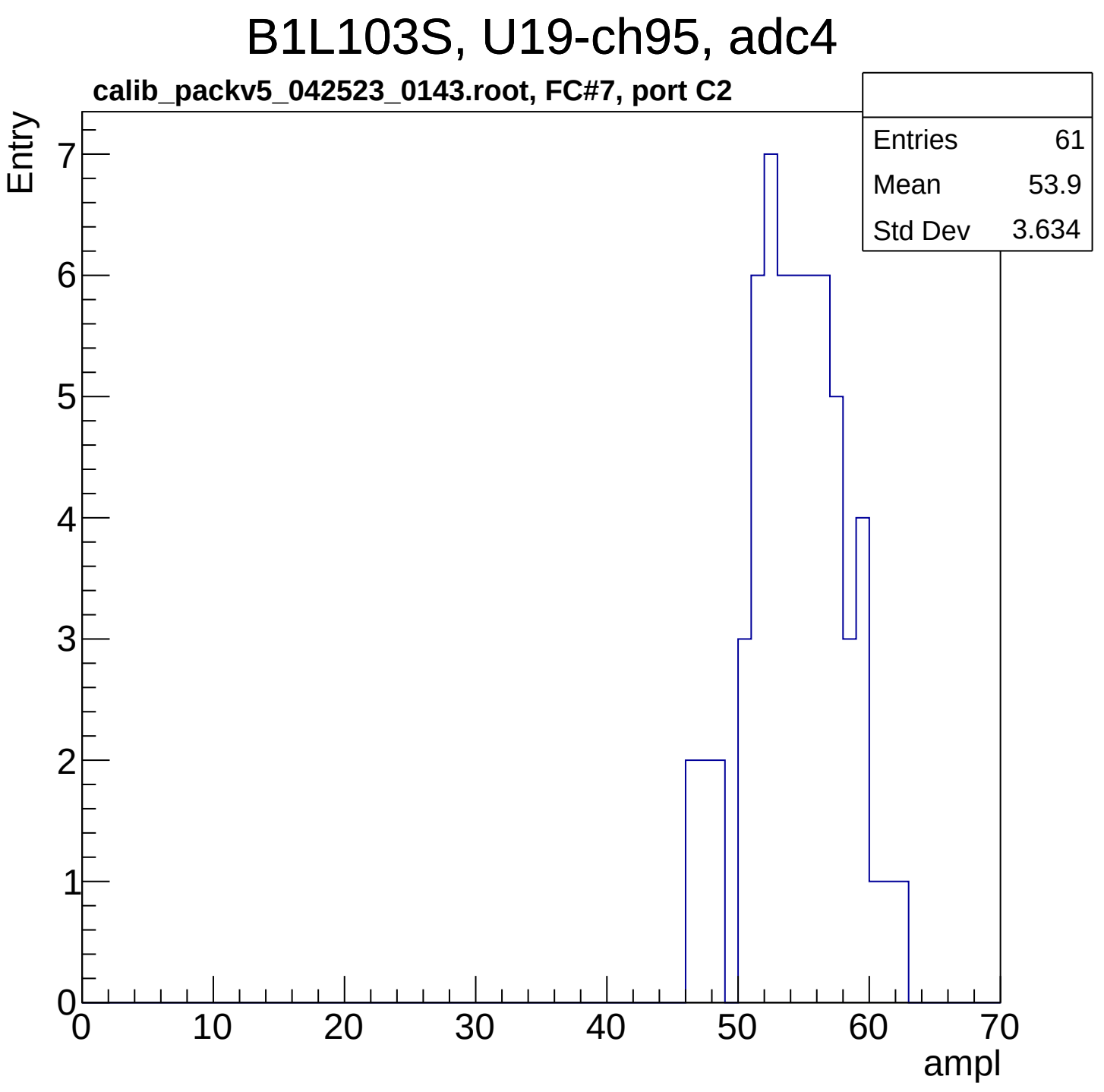
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch95, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 53.9
Std Dev 3.634
Entry
ampl
0
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2
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4
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6
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0
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20
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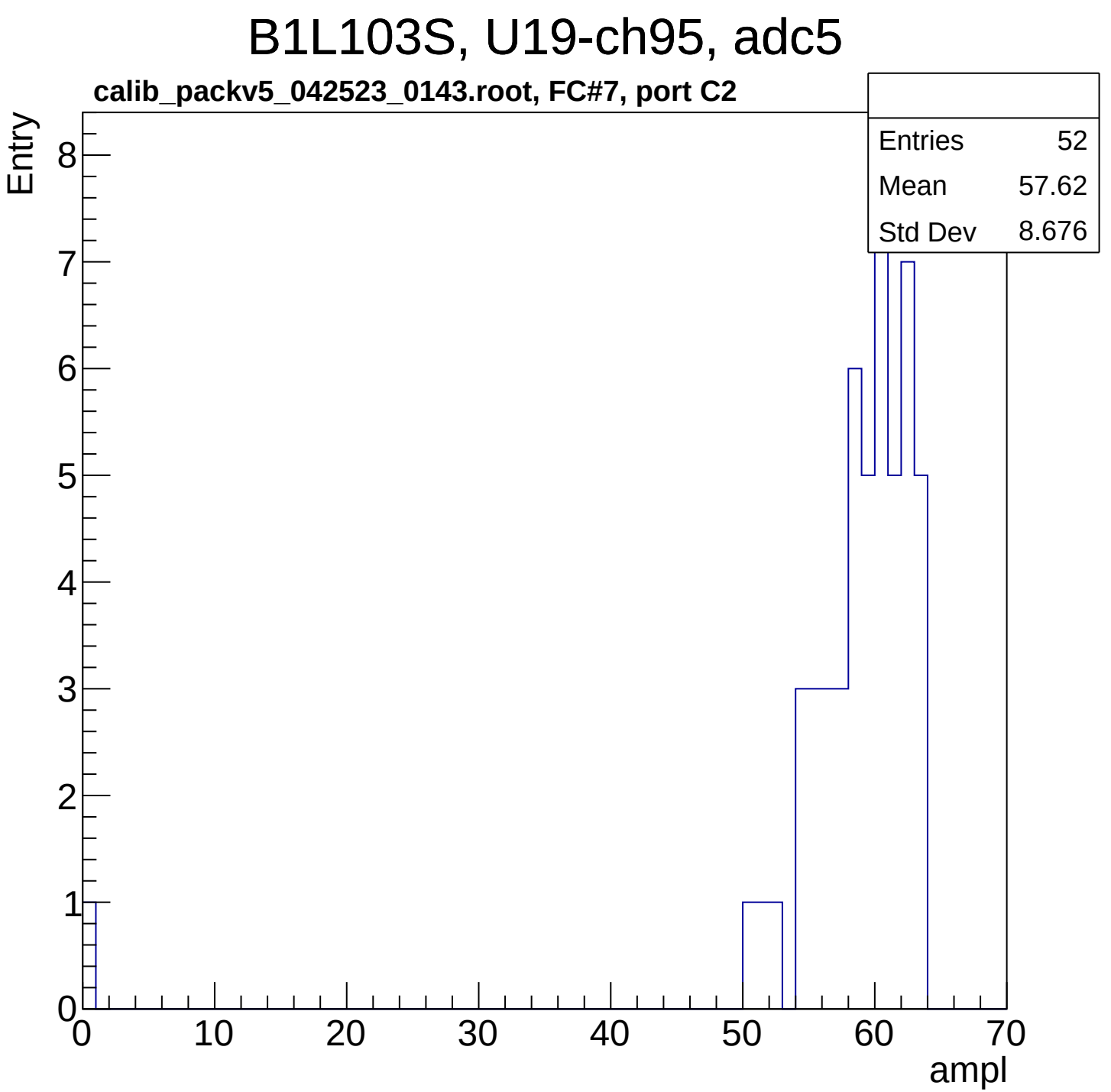
40
50
60
70


B1L103S, U19-ch95, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.62
Std Dev 8.676
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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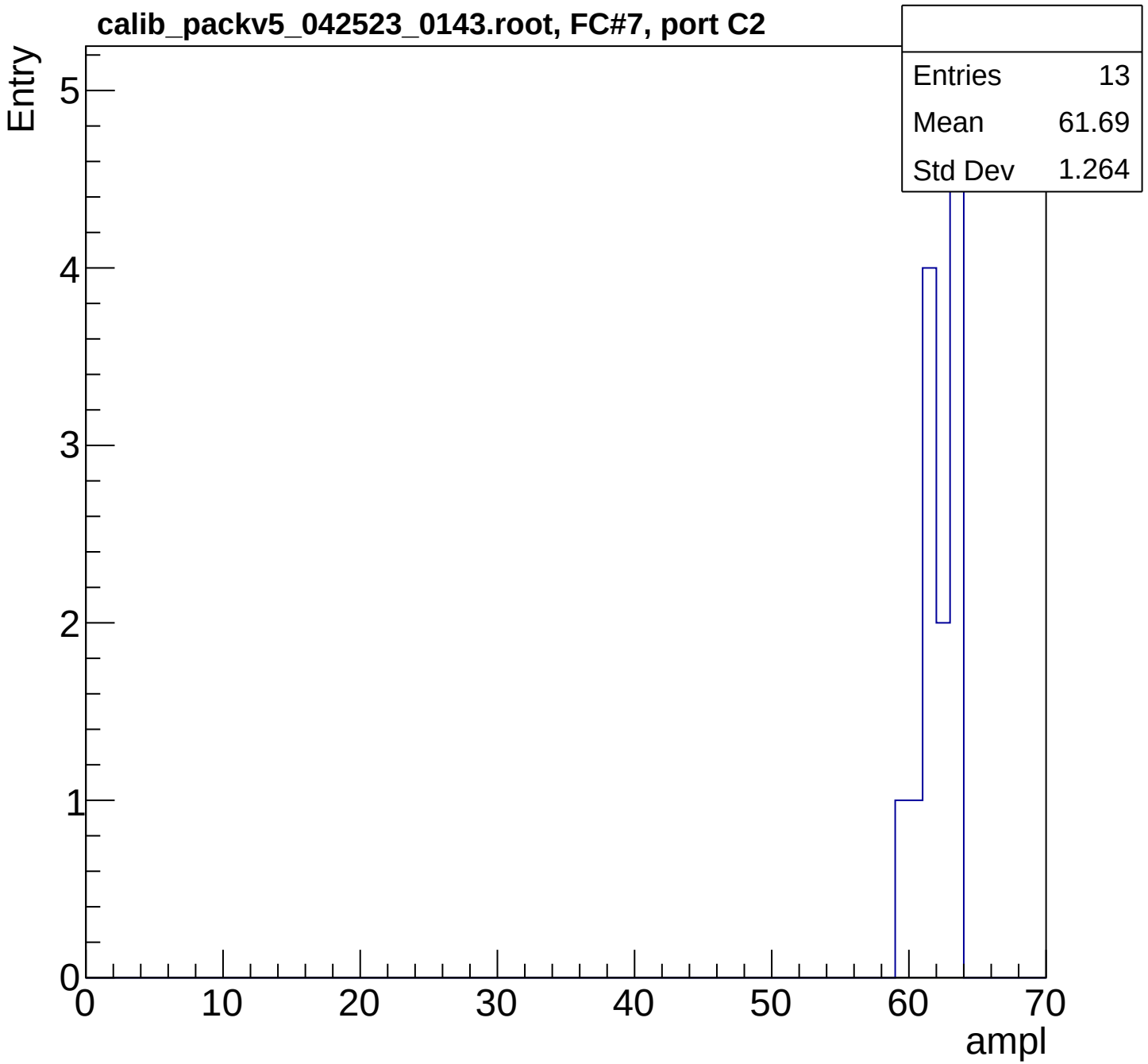
B1L103S, U19-ch95, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.69
Std Dev 1.264
Entry
0
1
2
3
4
5
0
10
20
30
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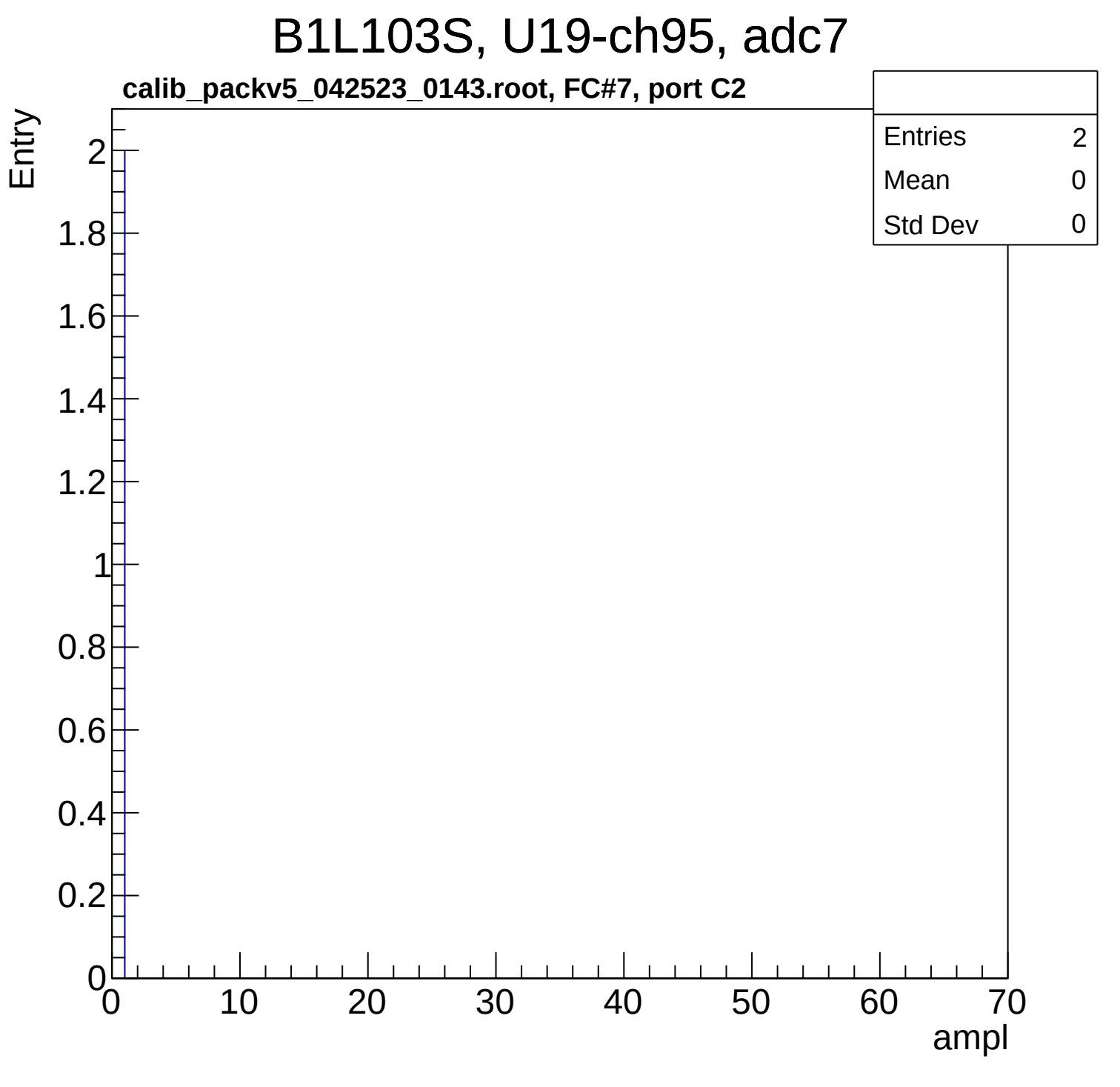
50
60
70
ampl


B1L103S, U19-ch95, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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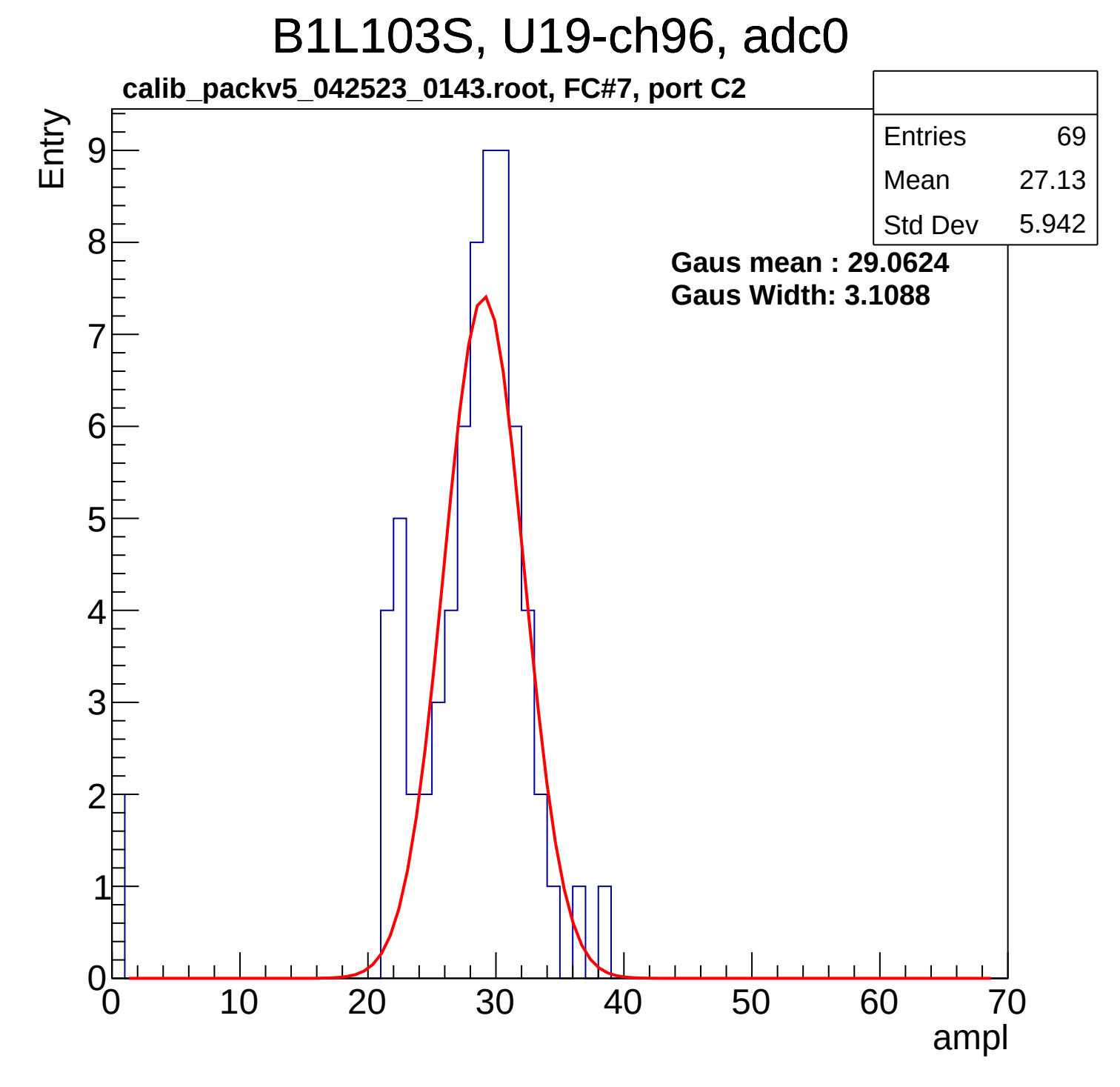
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch96, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 27.13
Std Dev 5.942
Gaus mean : 29.0624
Gaus Width: 3.1088
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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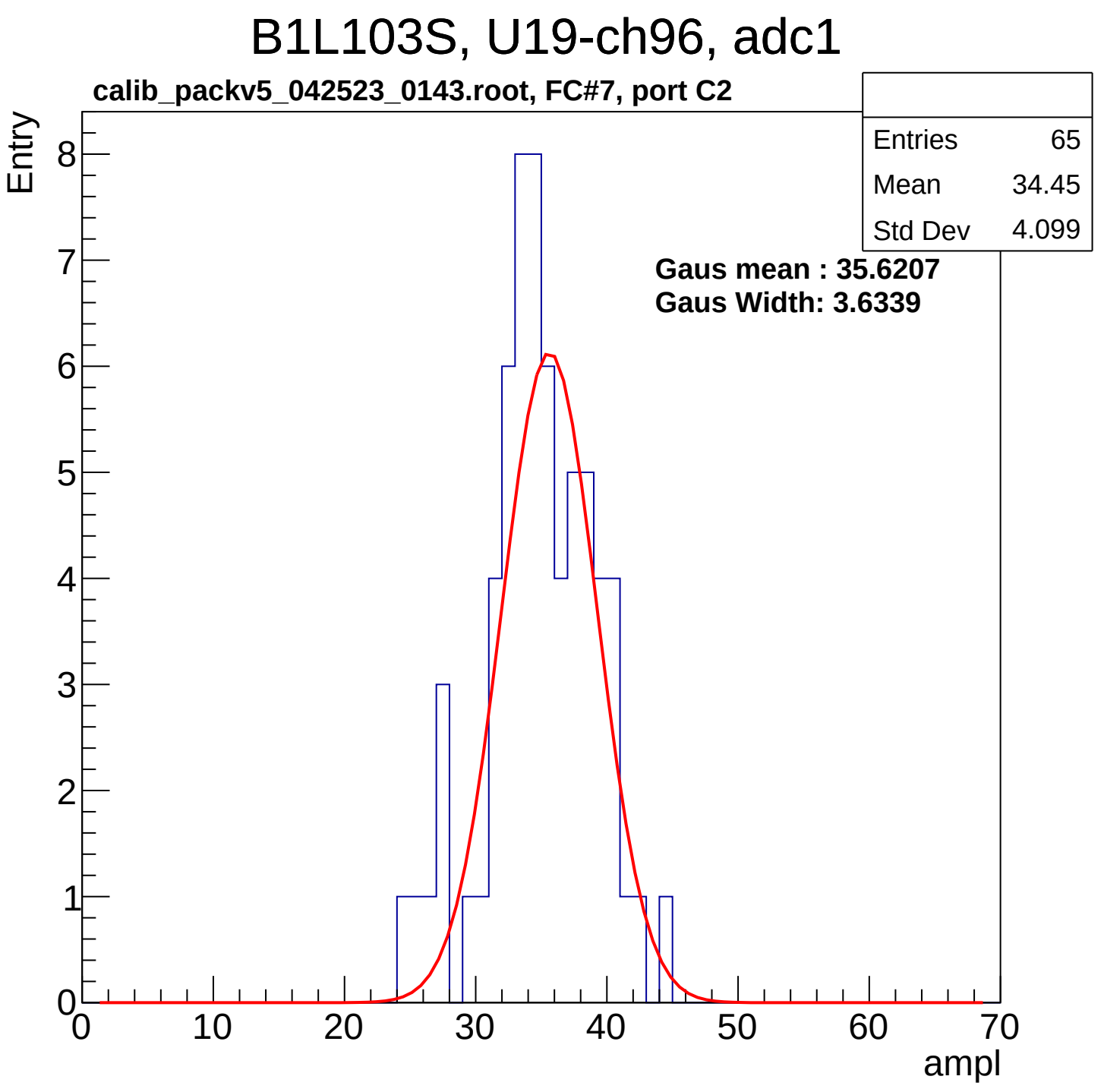
B1L103S, U19-ch96, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 34.45
Std Dev 4.099
Gaus mean : 35.6207
Gaus Width: 3.6339
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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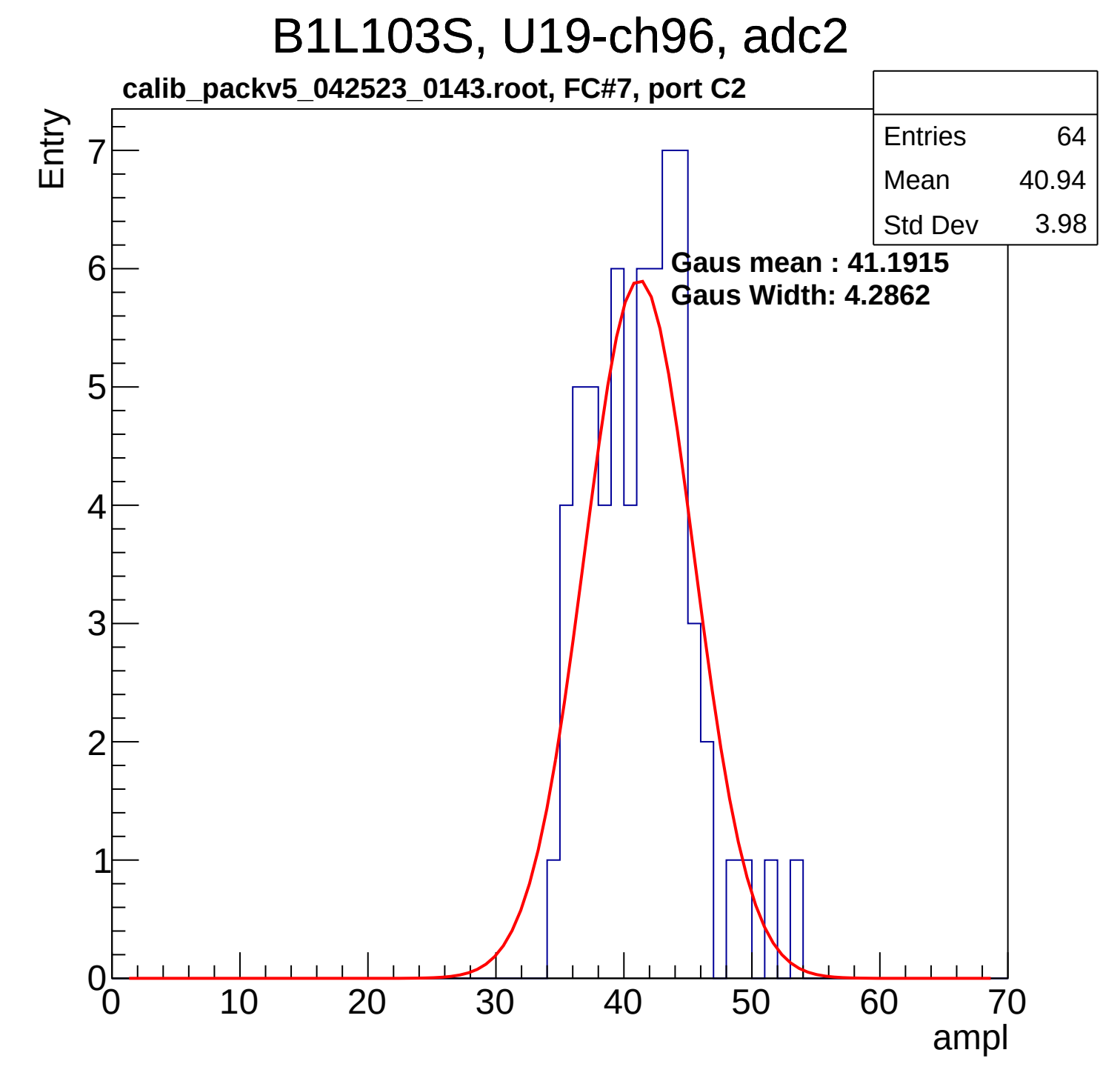
B1L103S, U19-ch96, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.94
Std Dev 3.98
Gaus mean : 41.1915
Gaus Width: 4.2862
Entry
ampl
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4
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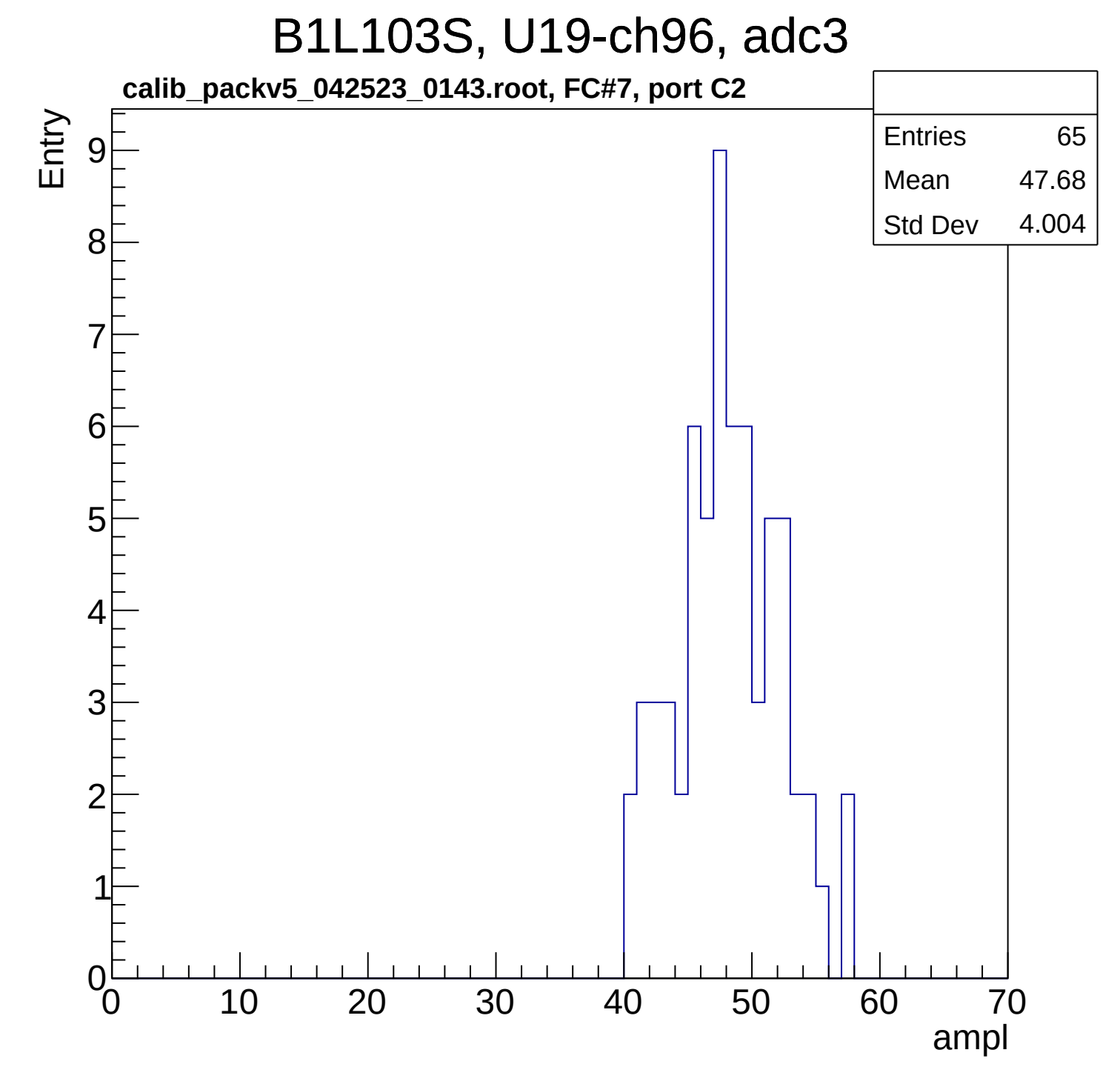
B1L103S, U19-ch96, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 47.68
Std Dev 4.004
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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50
60
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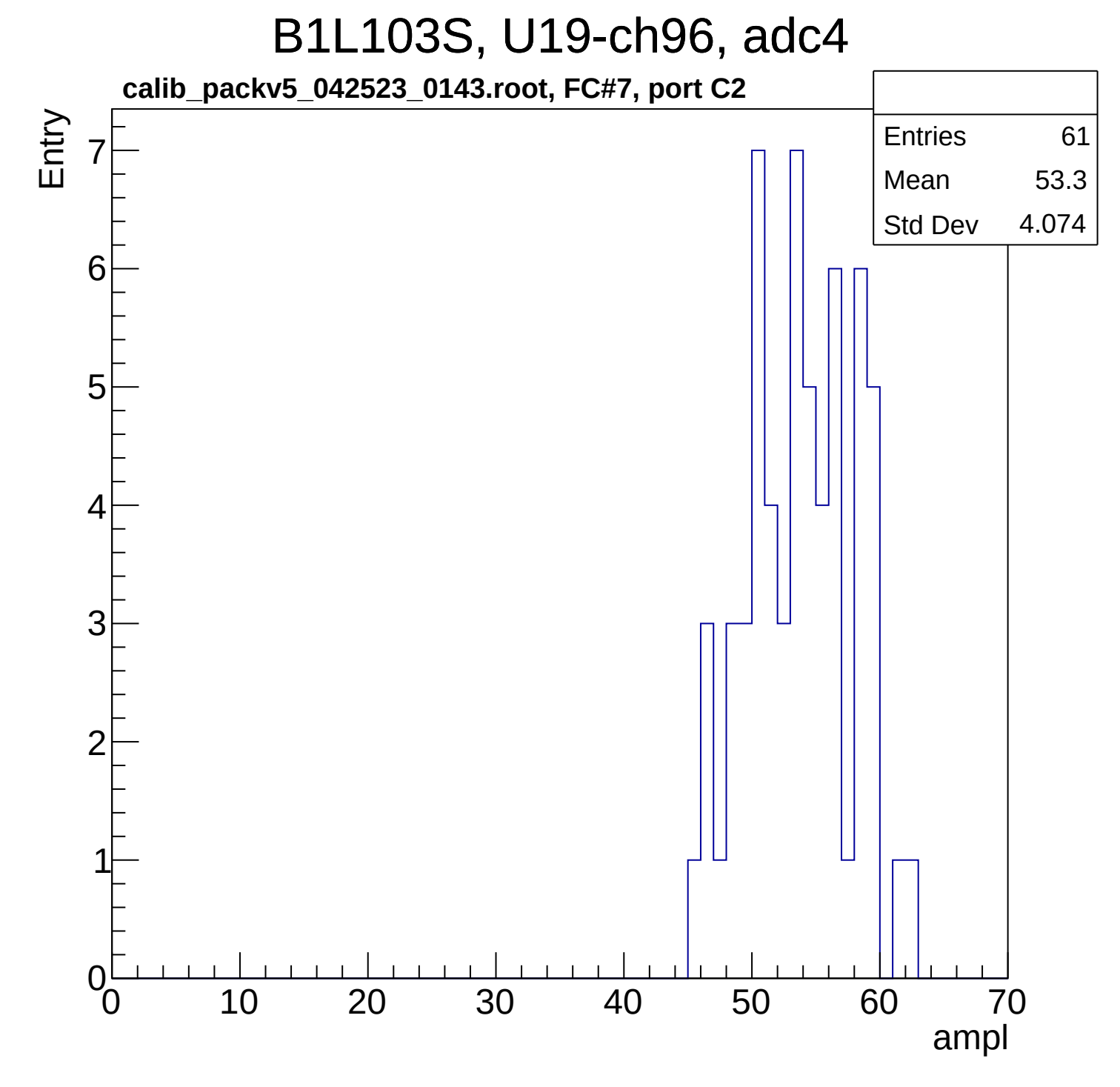
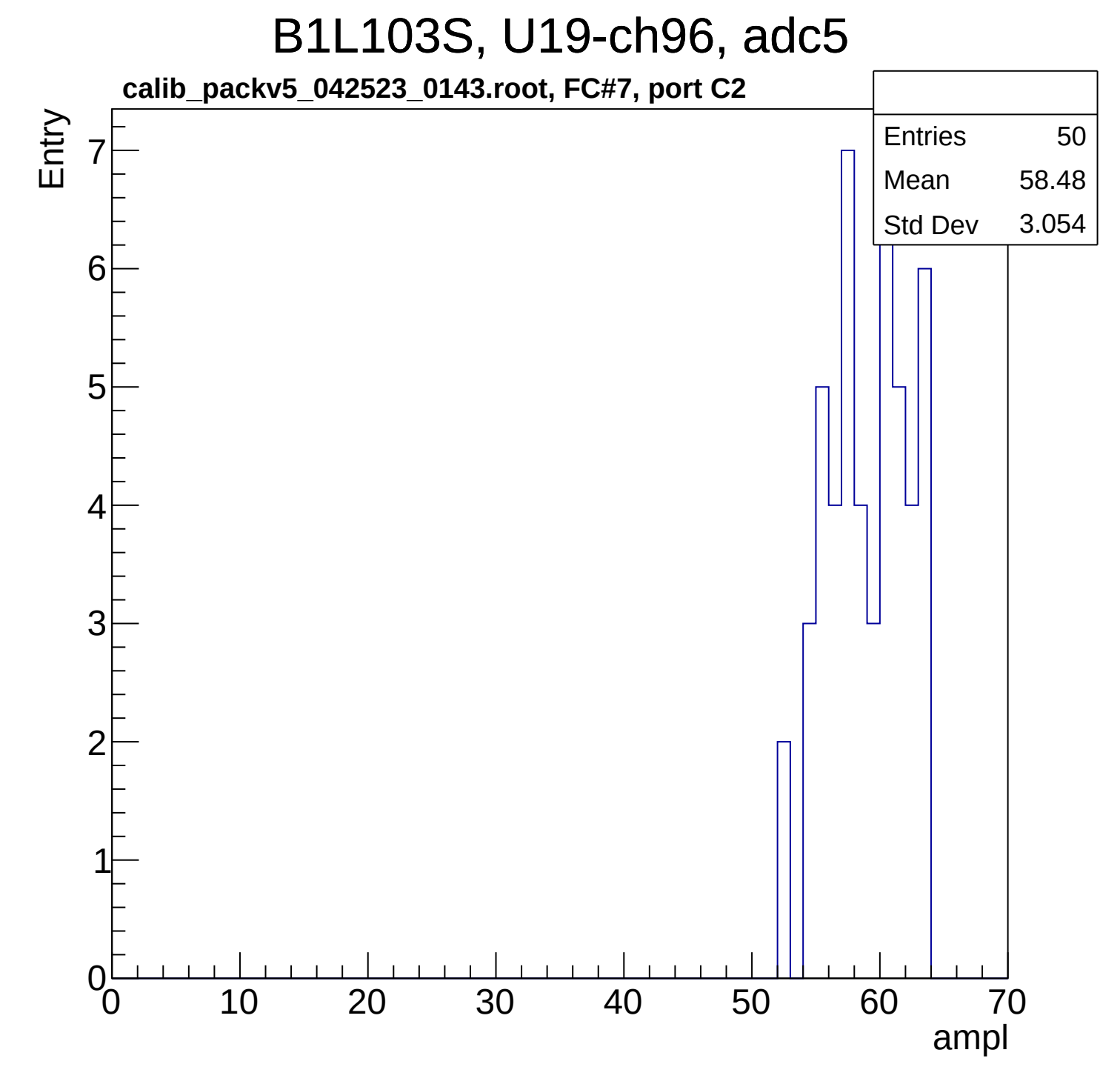

B1L103S, U19-ch96, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 53.3
Std Dev 4.074
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch96, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.48
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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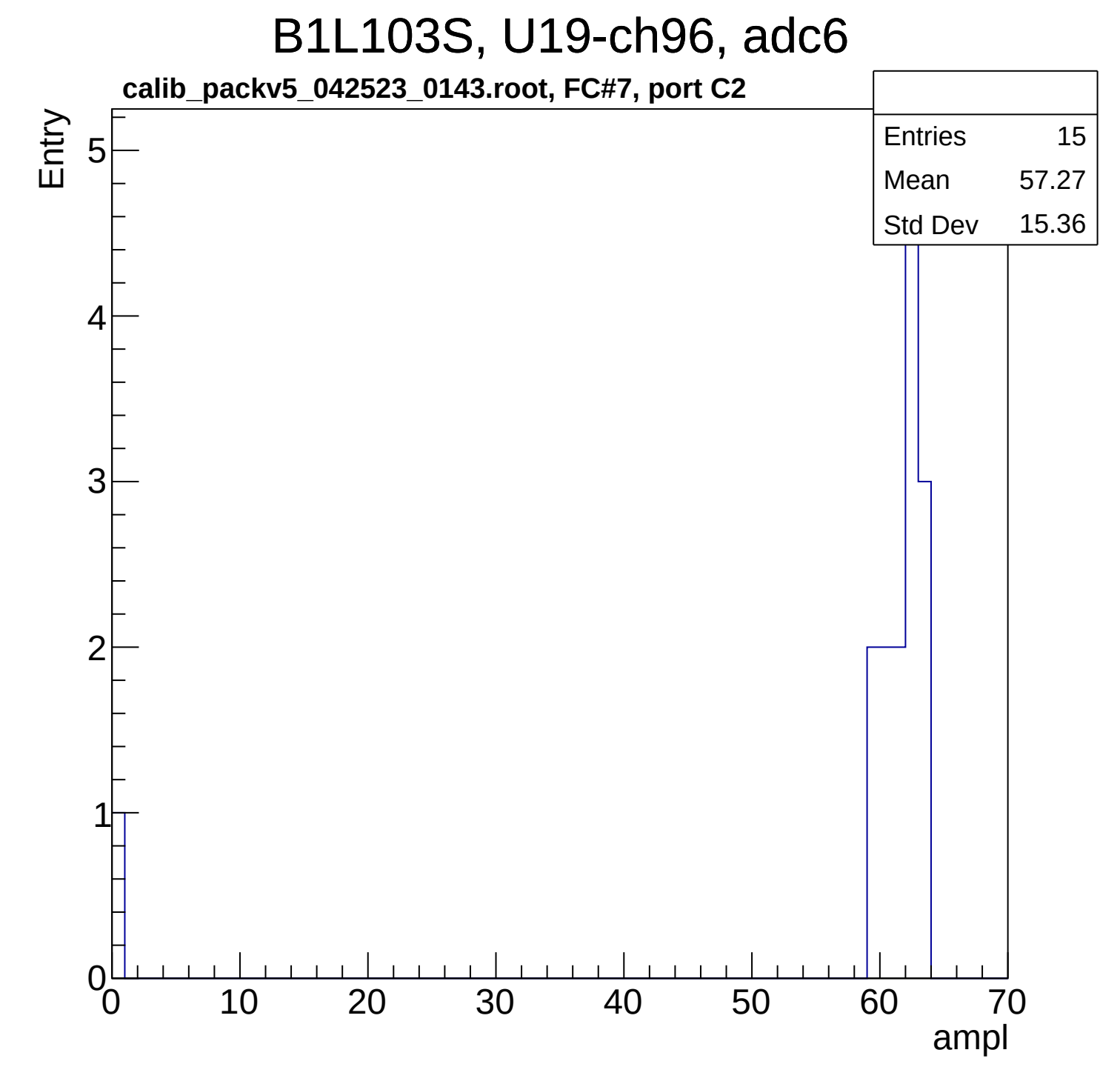
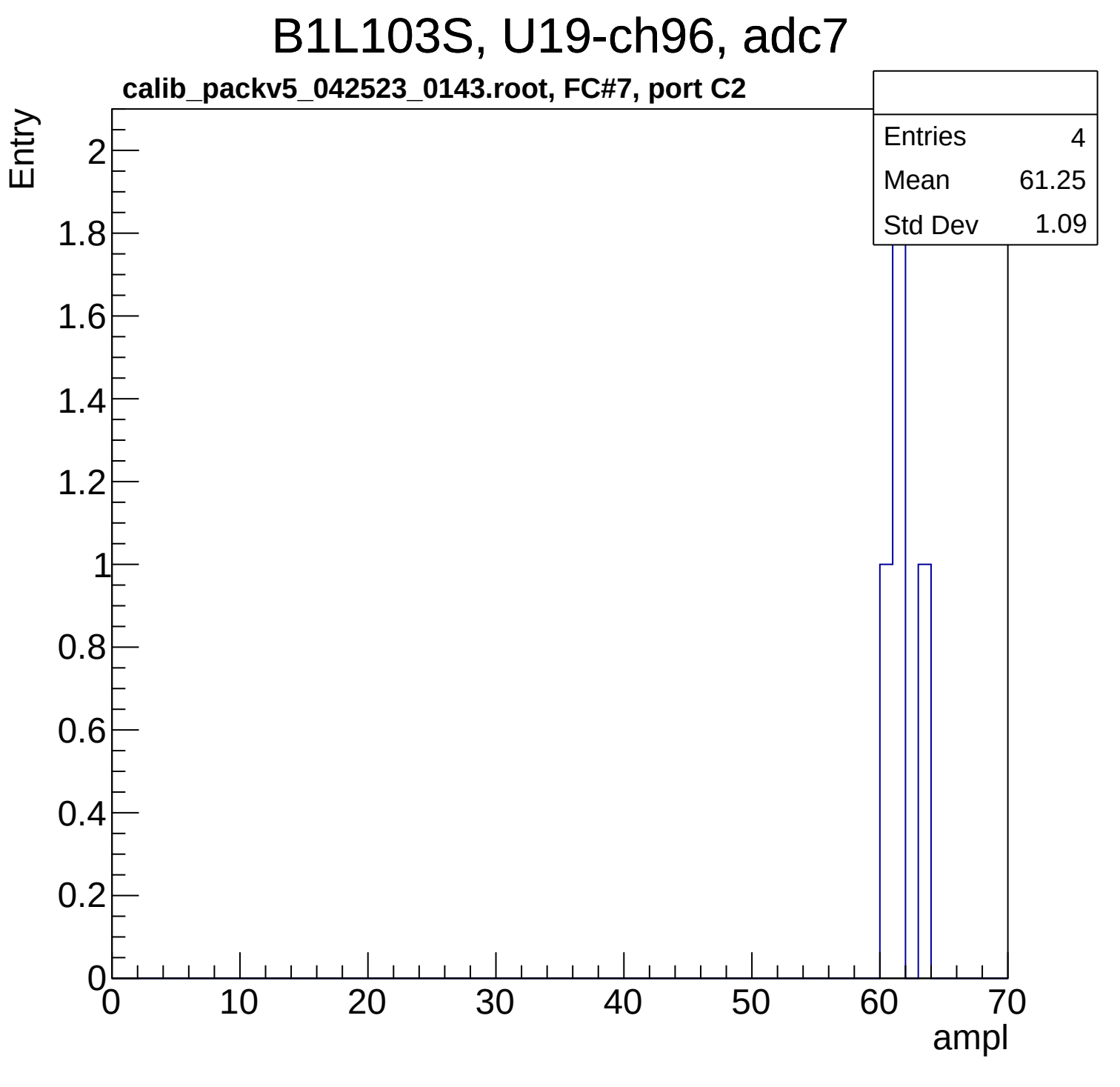
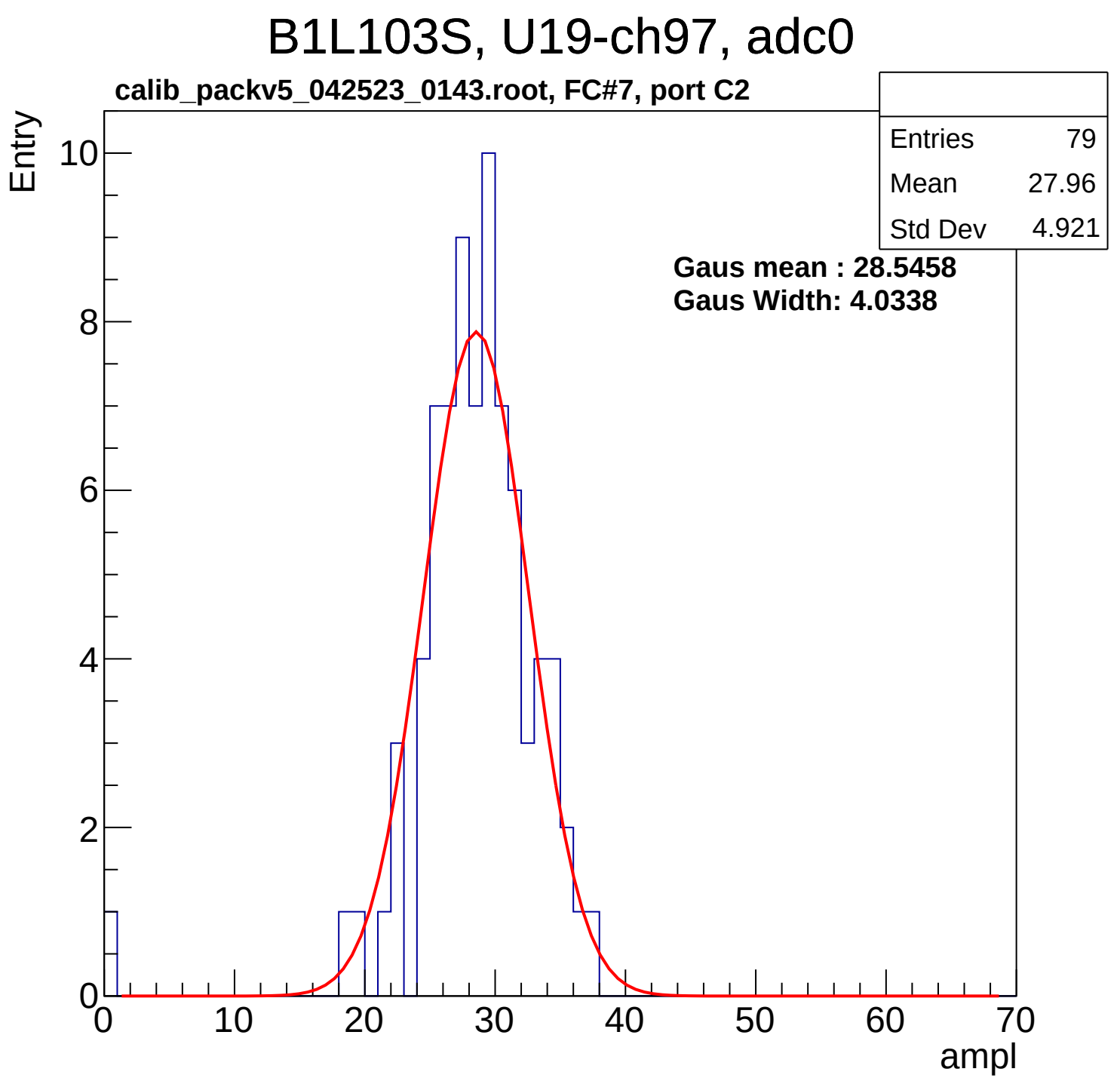
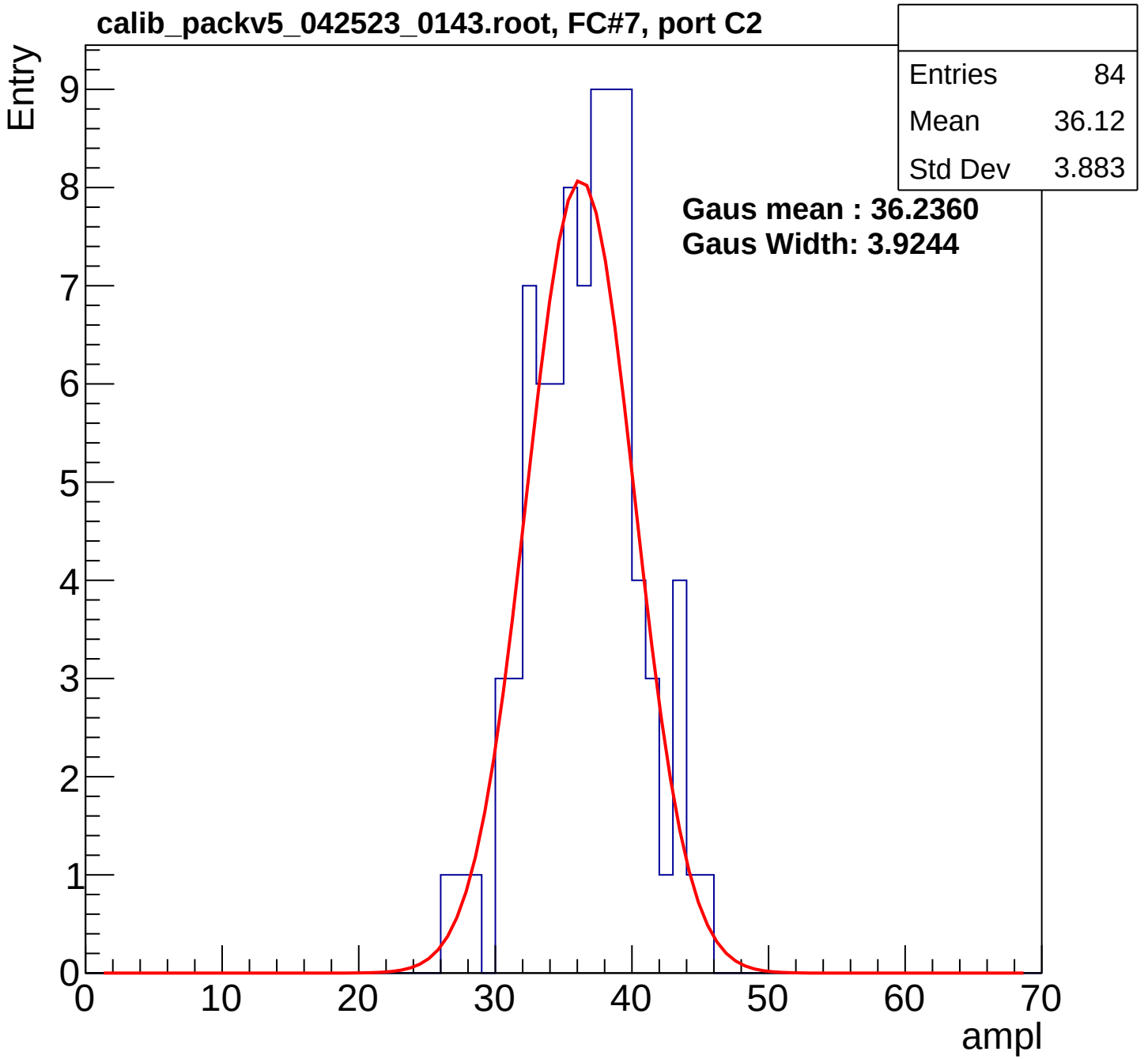

B1L103S, U19-ch96, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 57.27
Std Dev 15.36
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch96, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.25
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch97, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 27.96
Std Dev 4.921
Gaus mean : 28.5458
Gaus Width: 4.0338
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch97, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 36.12
Std Dev 3.883
Gaus mean : 36.2360
Gaus Width: 3.9244
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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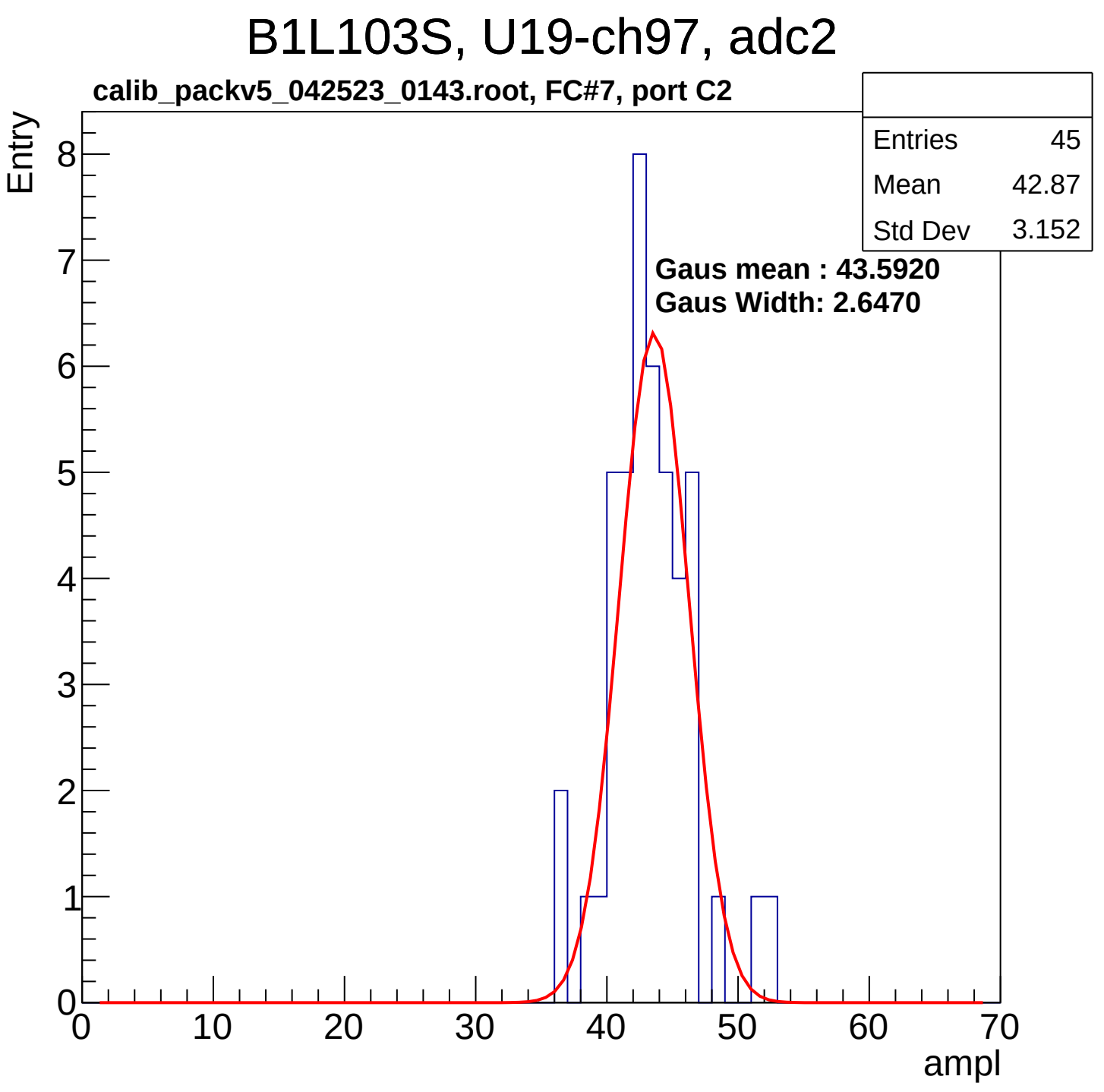
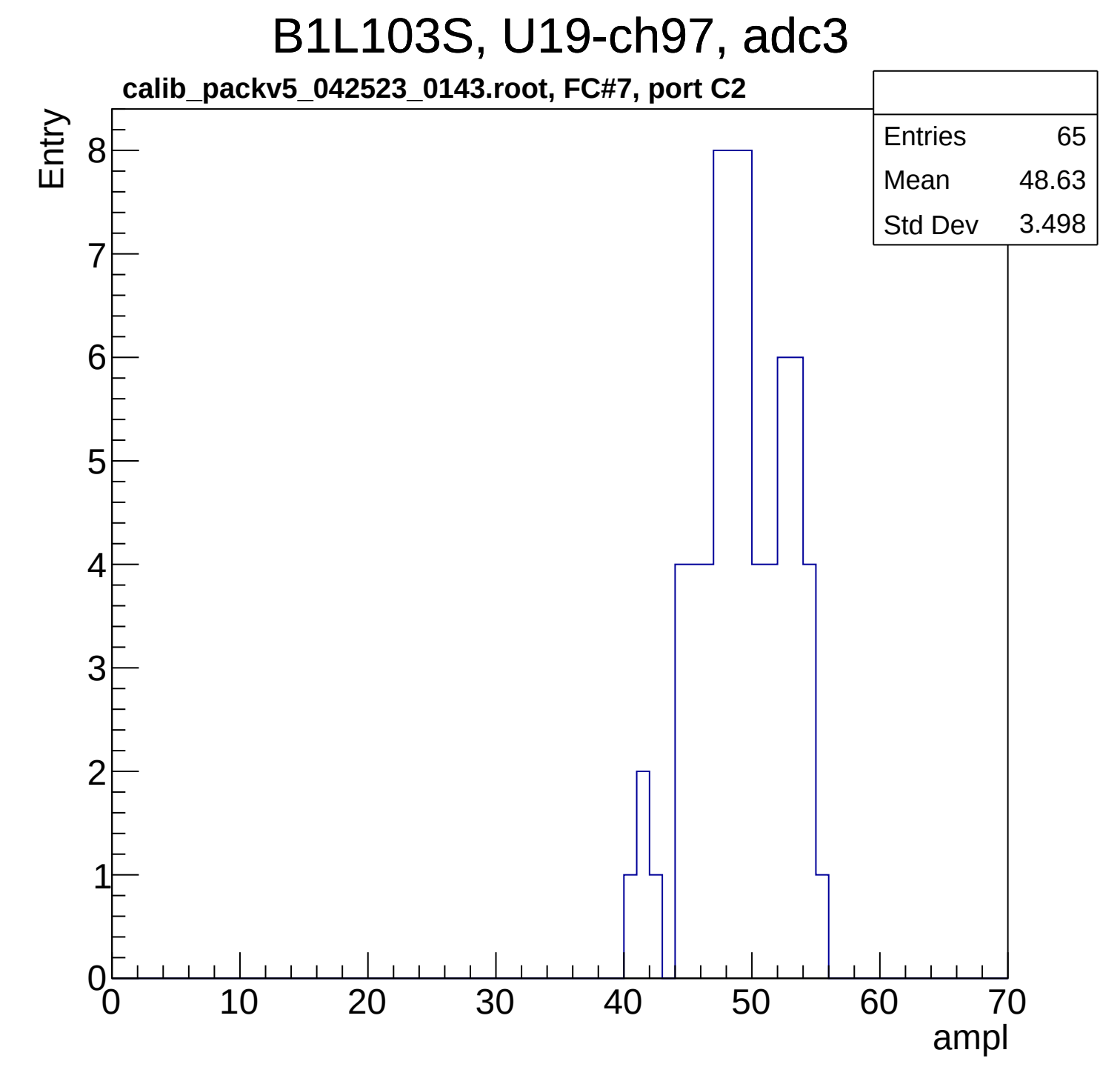
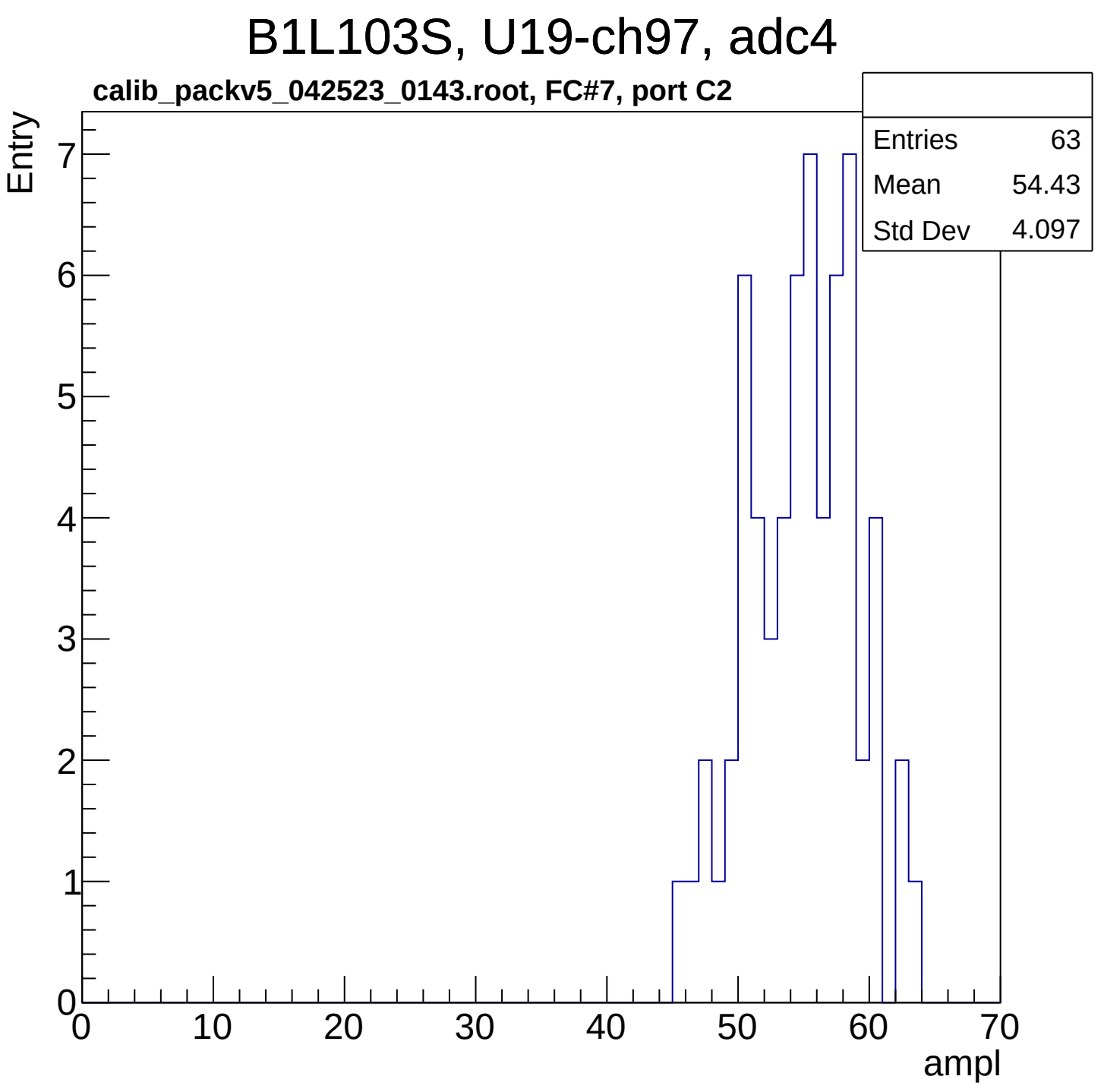
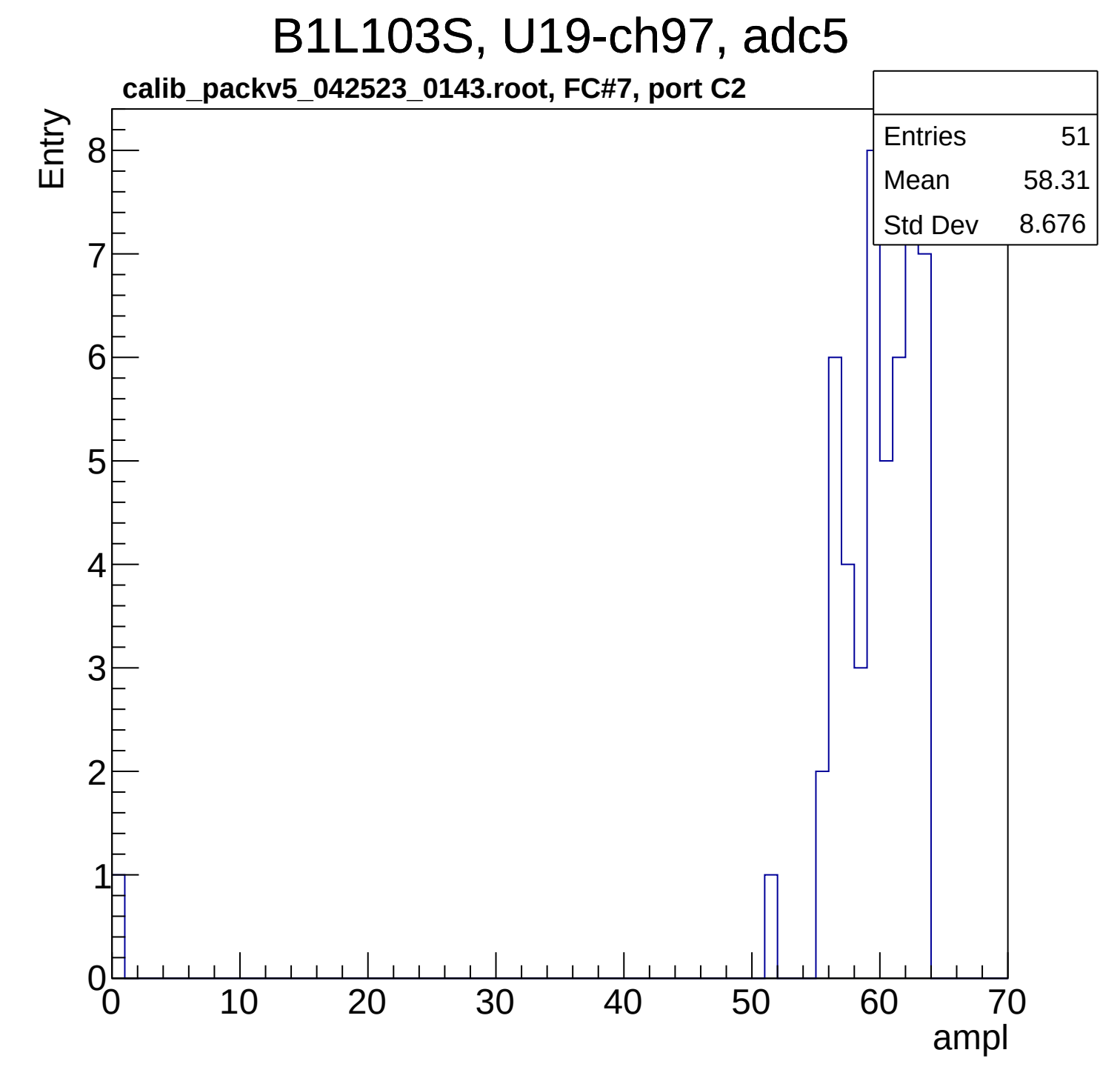
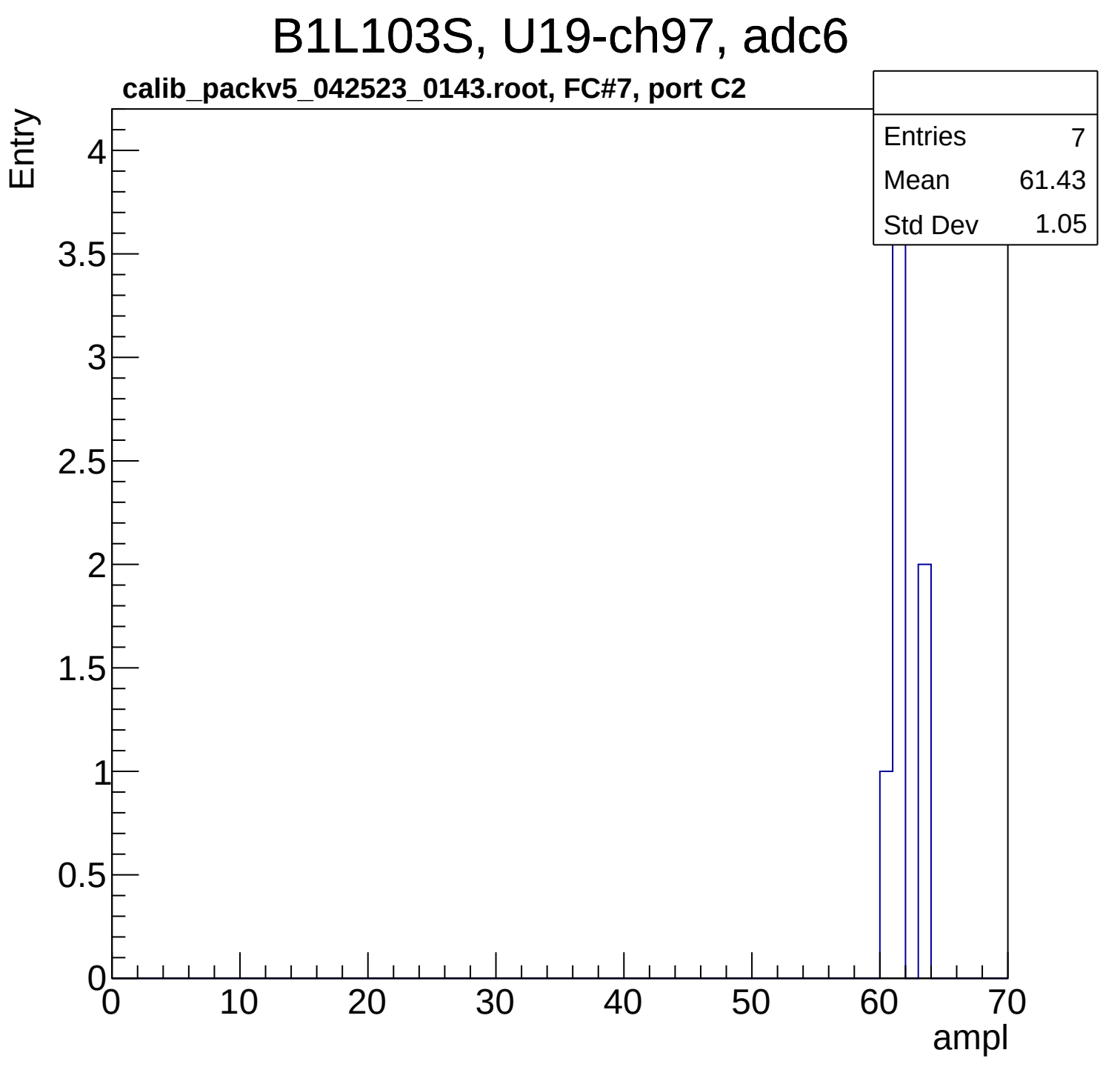
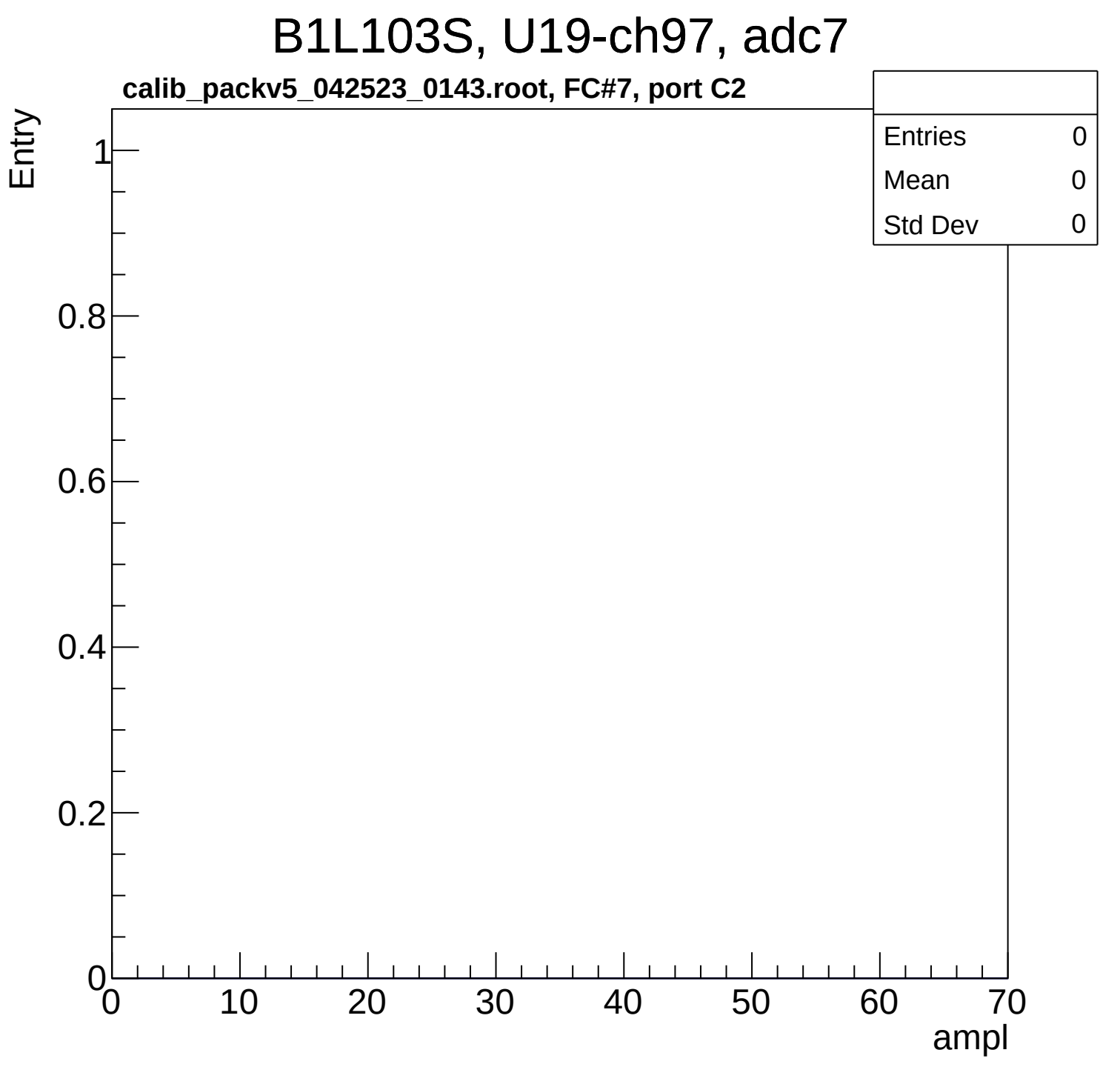

B1L103S, U19-ch97, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 42.87
Std Dev 3.152
Gaus mean : 43.5920
Gaus Width: 2.6470
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

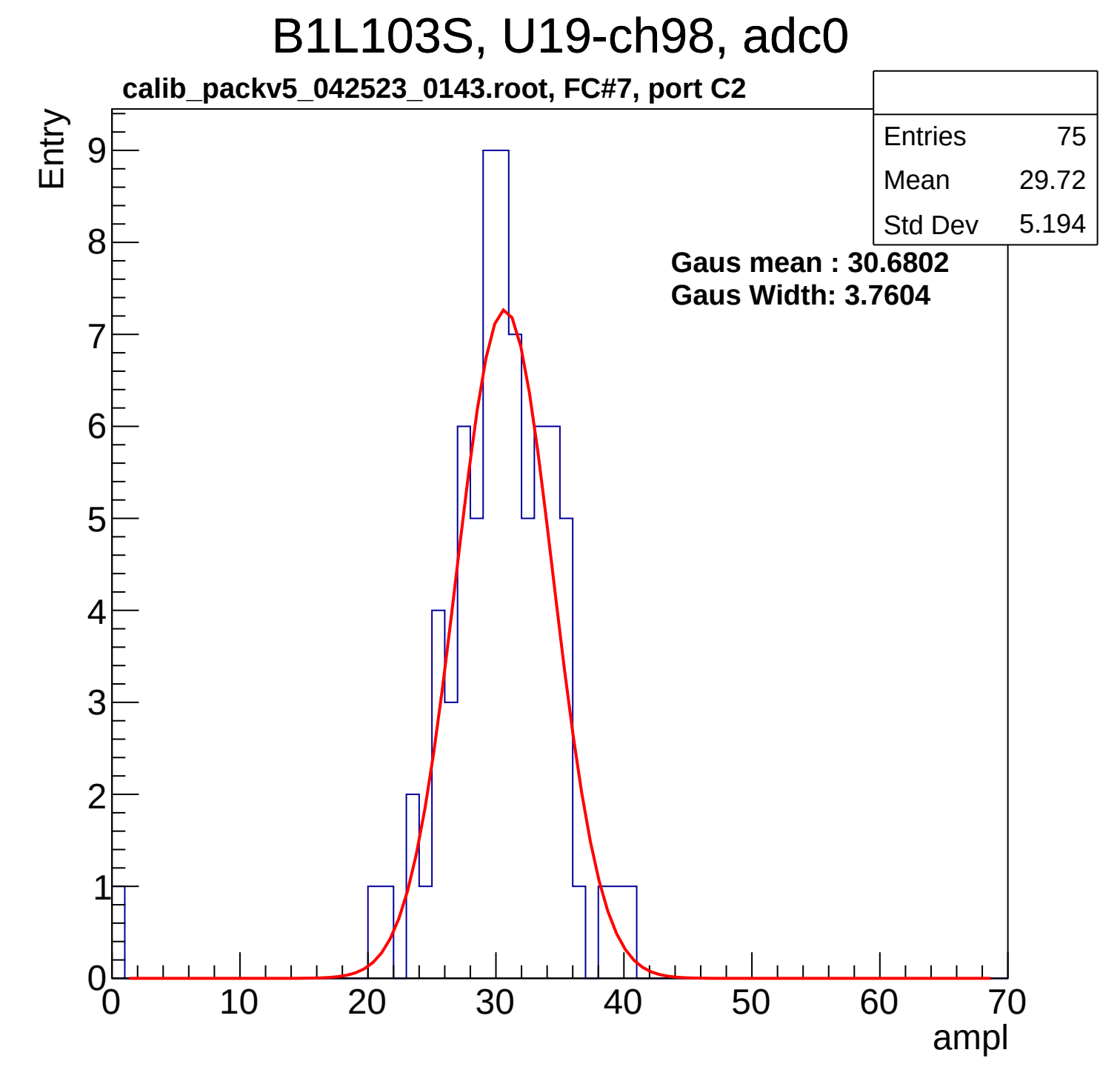

B1L103S, U19-ch97, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.63
Std Dev 3.498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

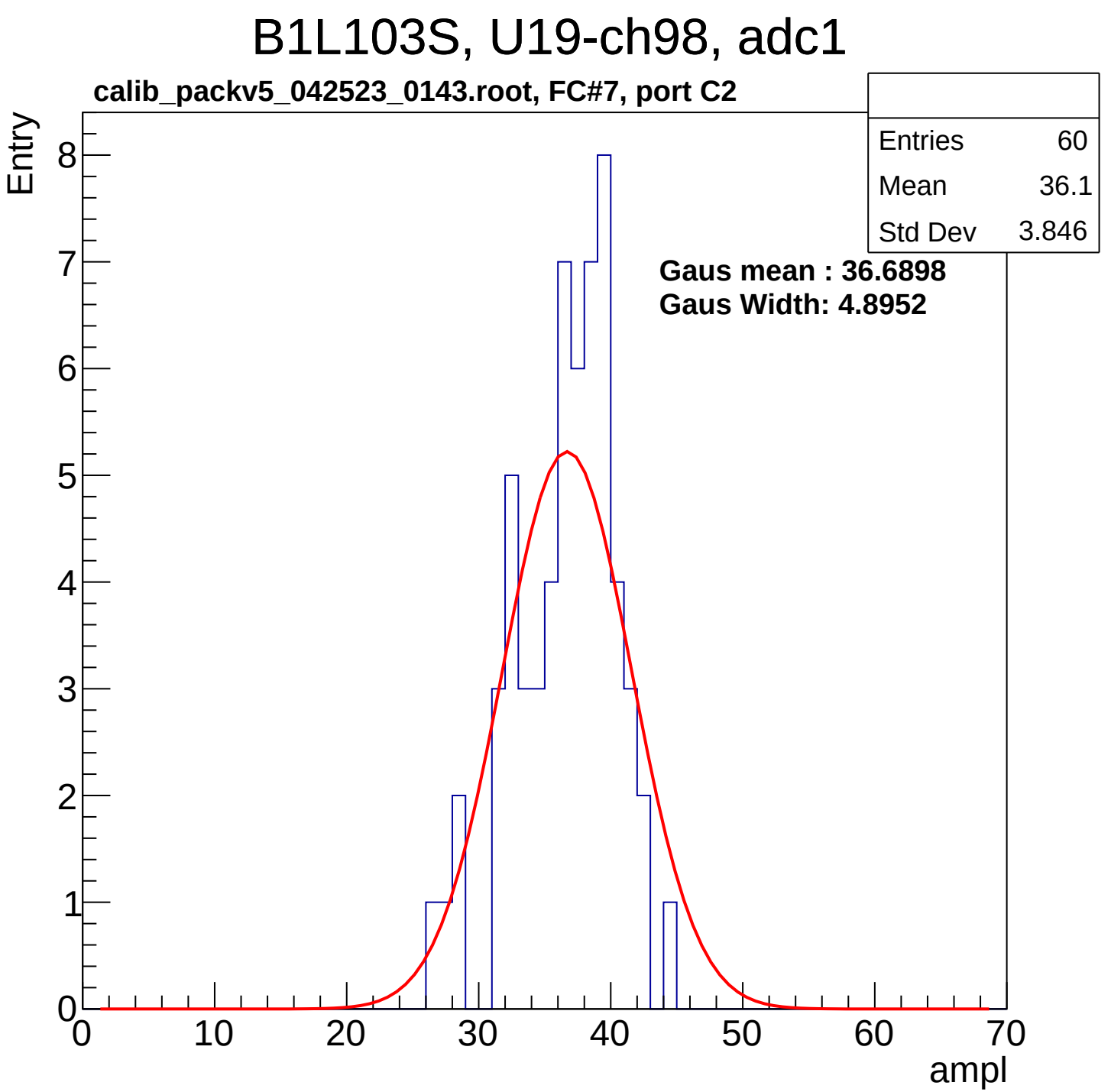

B1L103S, U19-ch97, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.43
Std Dev 4.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

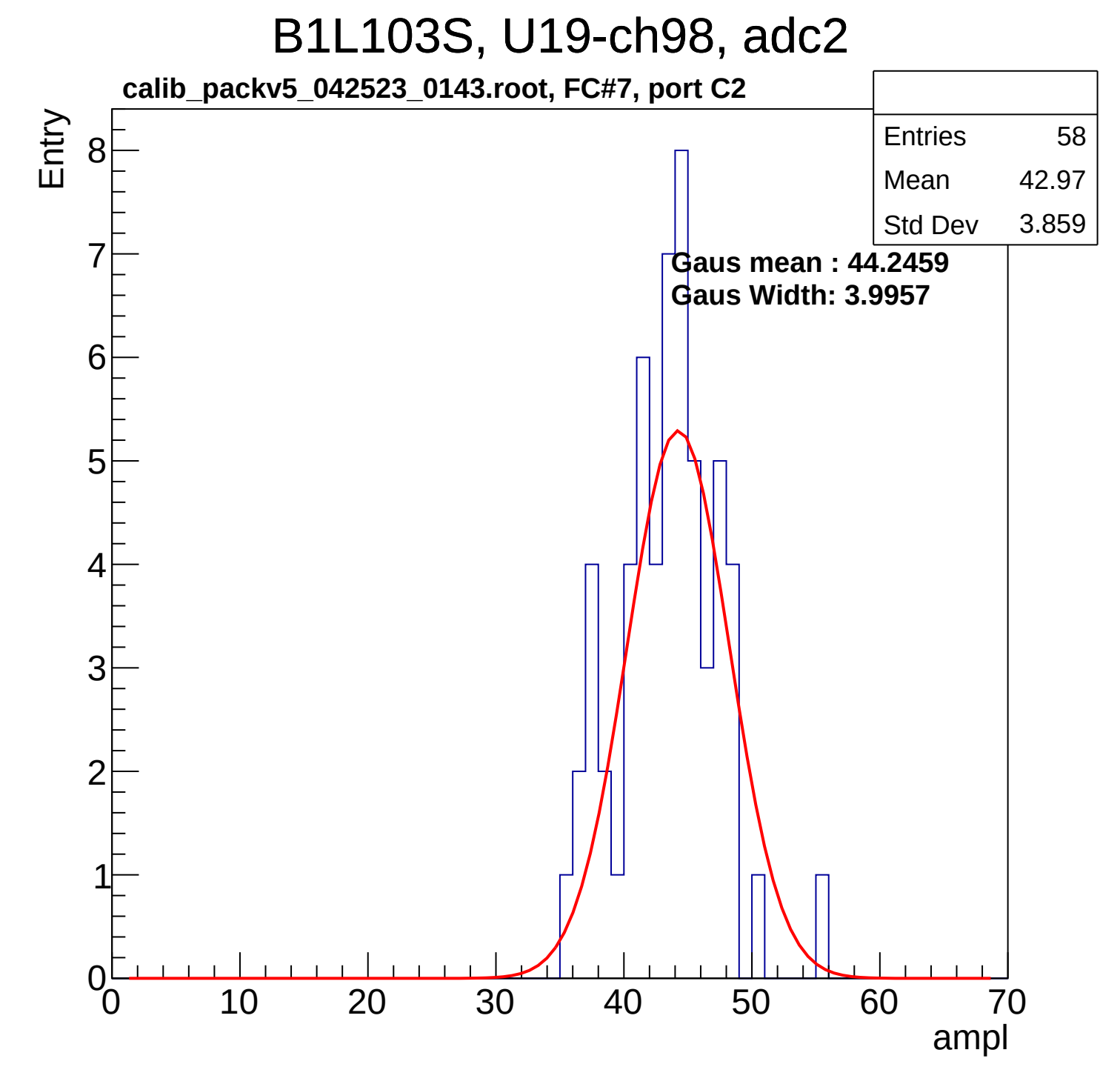

B1L103S, U19-ch97, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.31
Std Dev 8.676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

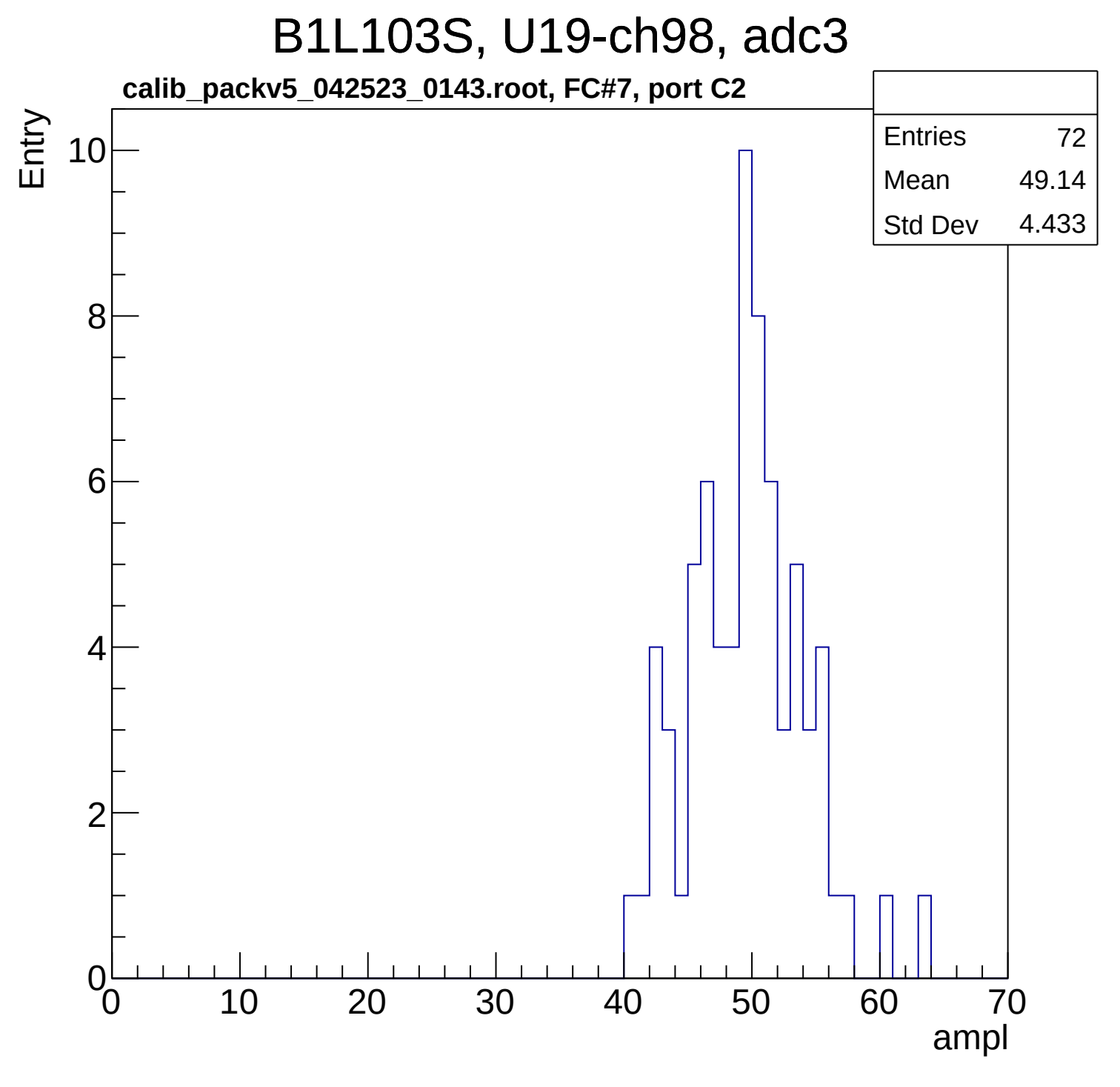

B1L103S, U19-ch97, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.43
Std Dev 1.05
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

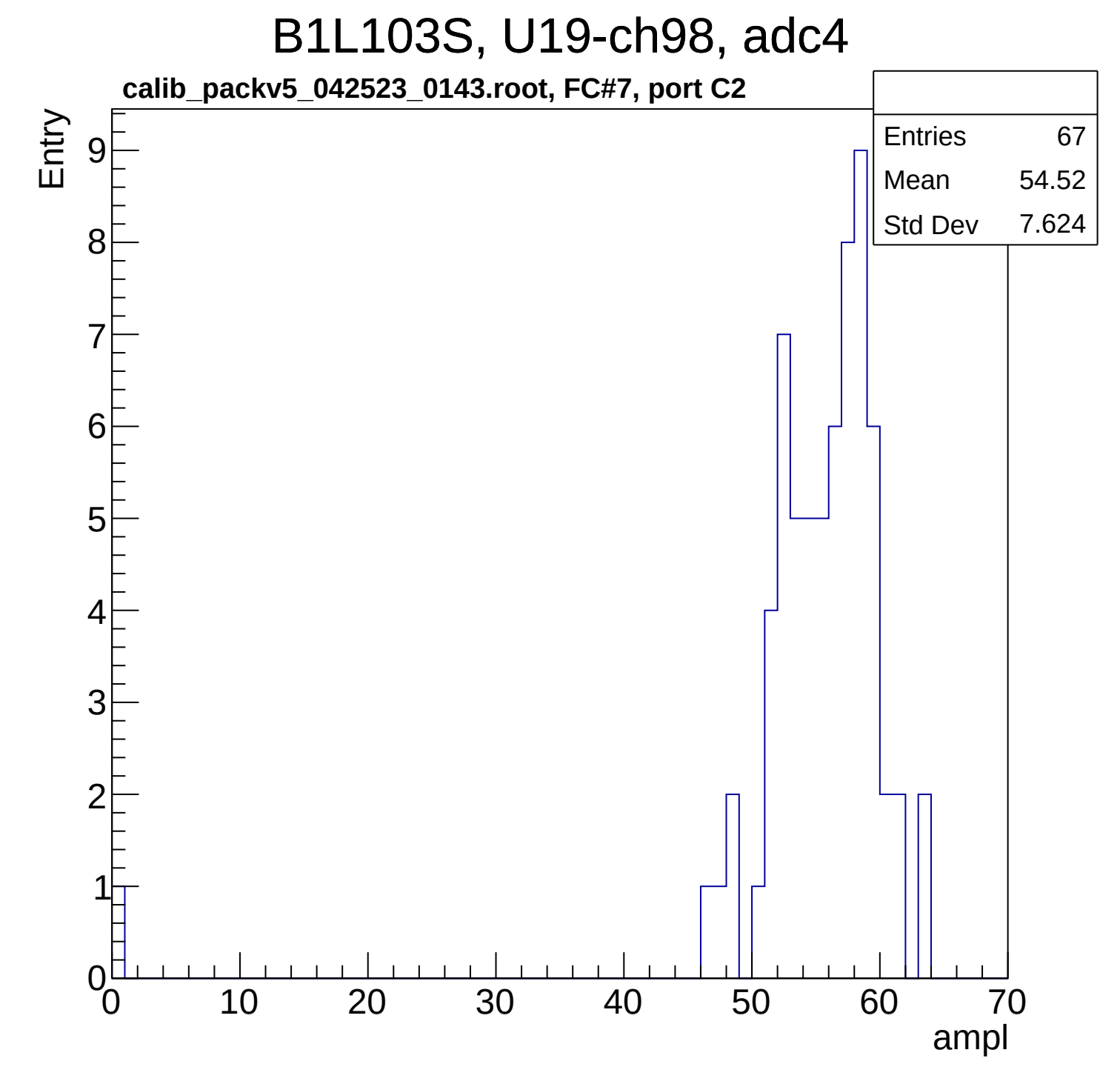

B1L103S, U19-ch97, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

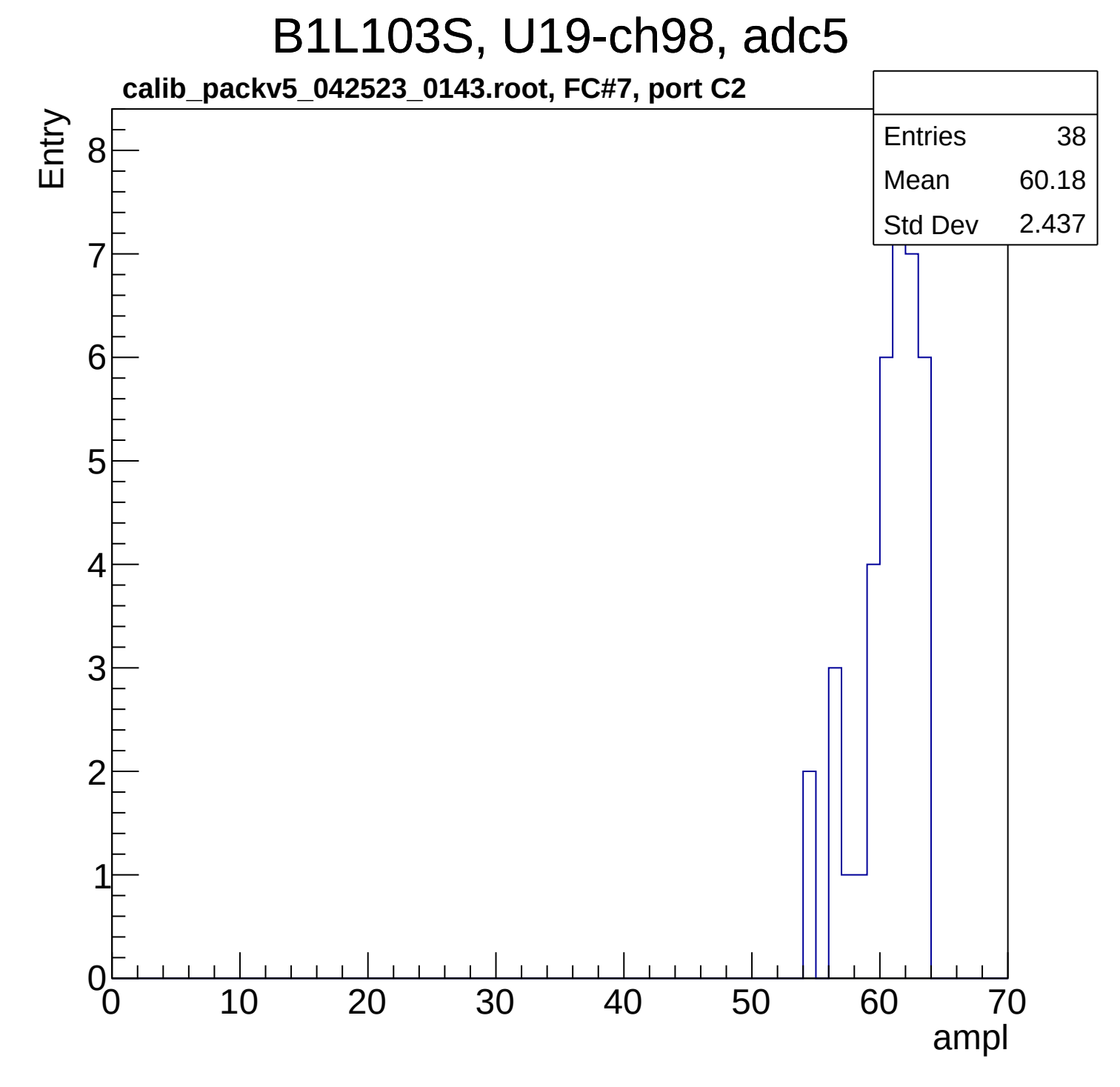

B1L103S, U19-ch98, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.72
Std Dev 5.194
Gaus mean : 30.6802
Gaus Width: 3.7604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

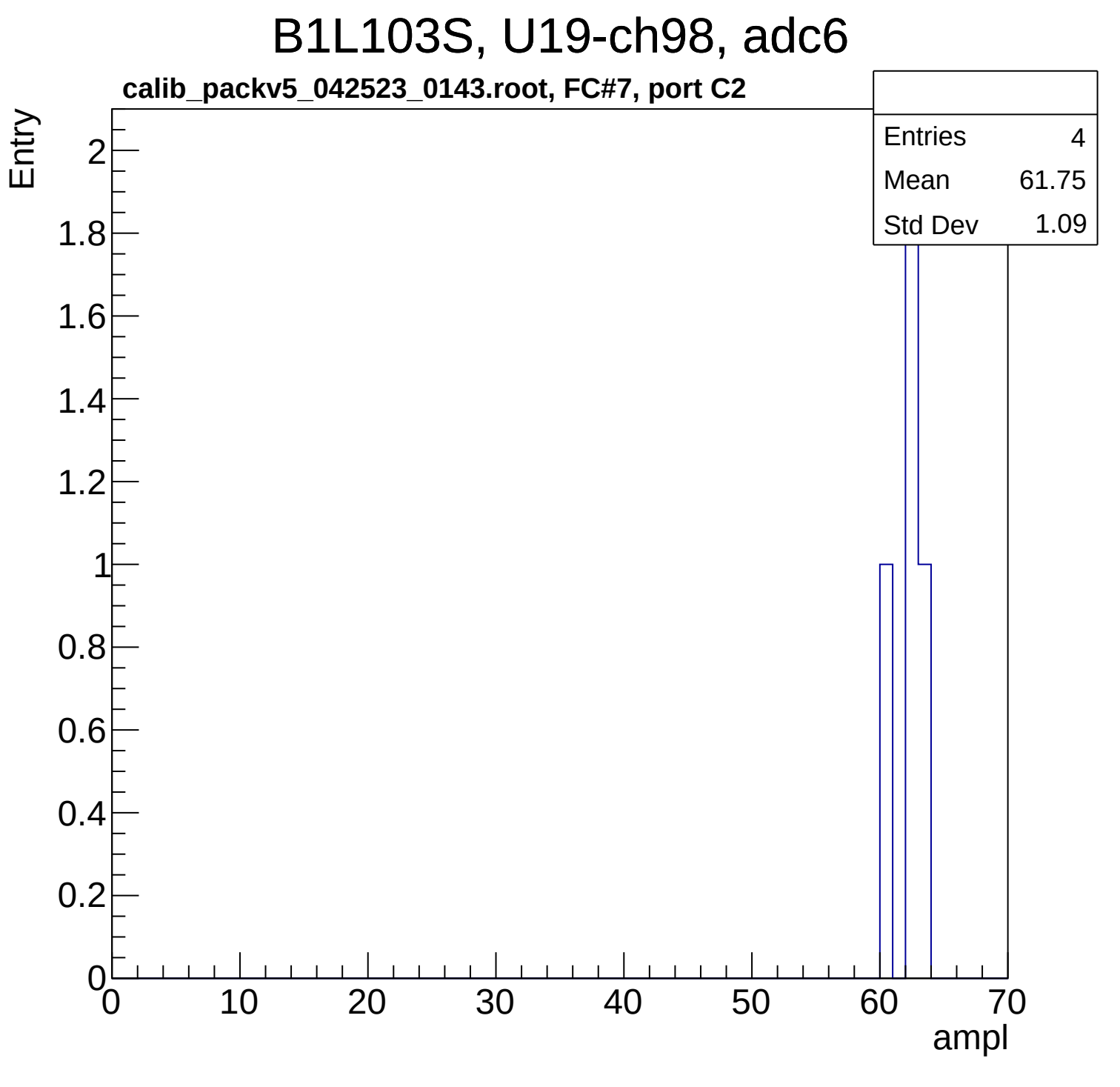

B1L103S, U19-ch98, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 36.1
Std Dev 3.846
Gaus mean : 36.6898
Gaus Width: 4.8952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

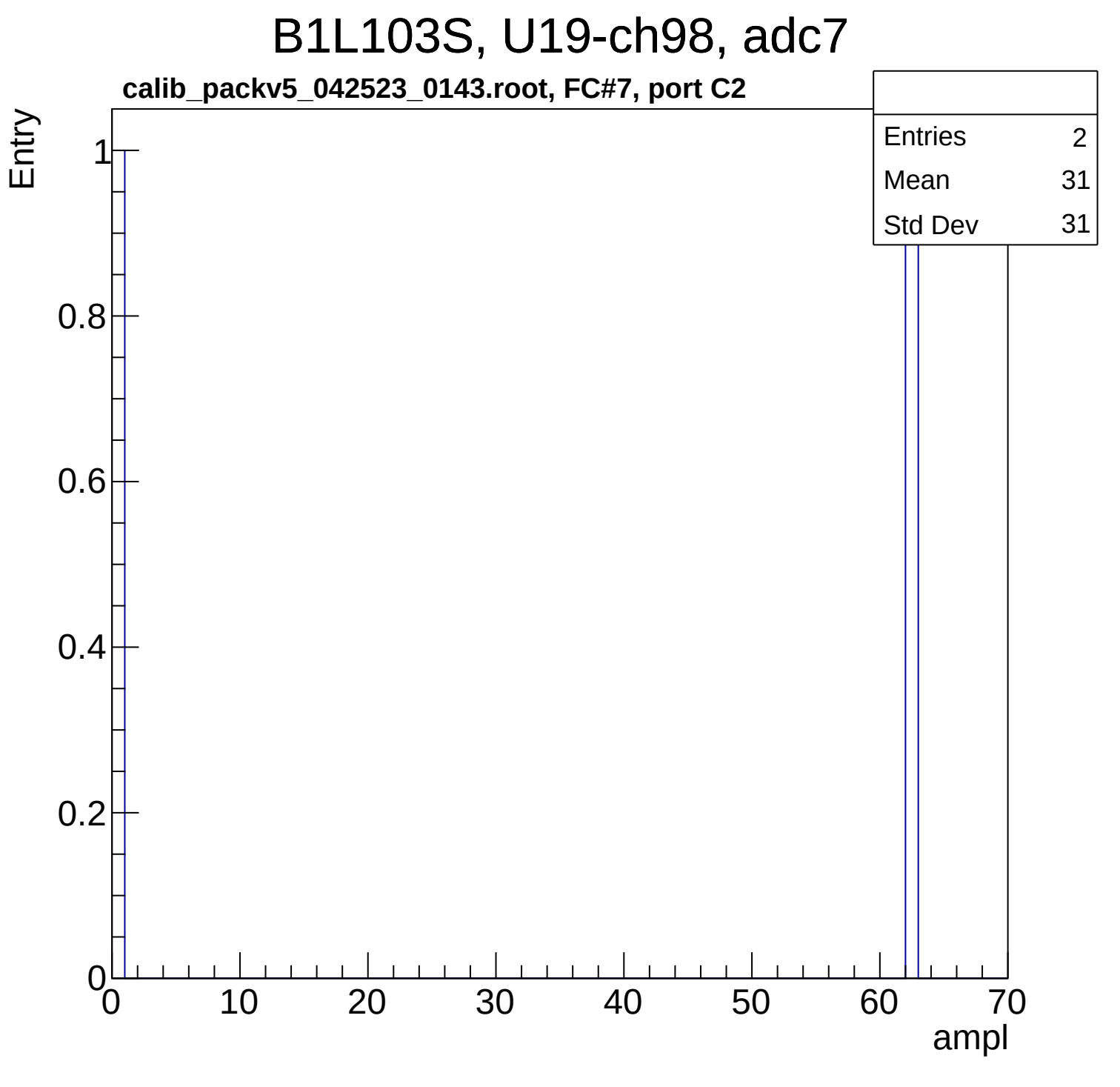

B1L103S, U19-ch98, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 42.97
Std Dev 3.859
Gaus mean : 44.2459
Gaus Width: 3.9957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

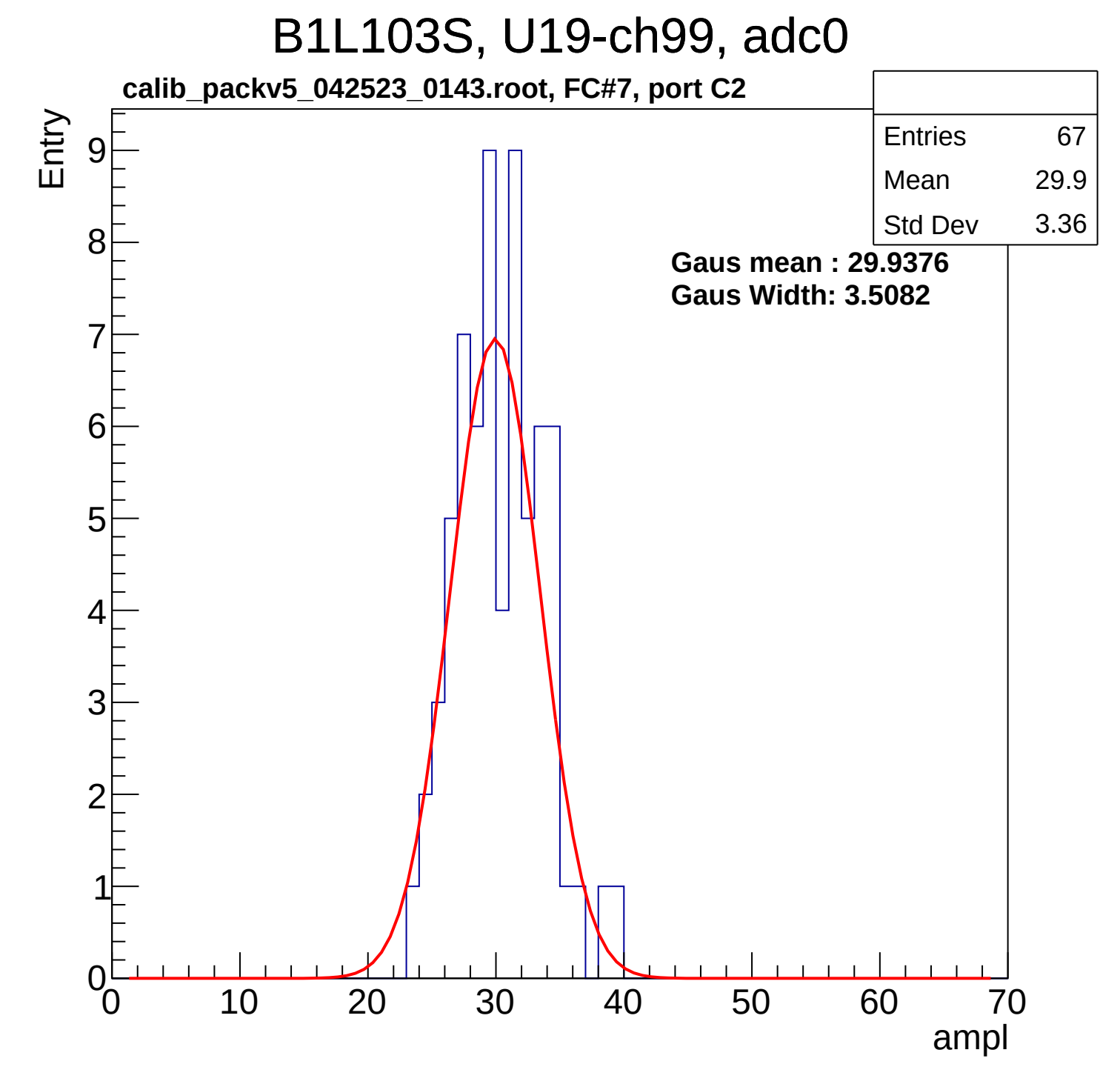

B1L103S, U19-ch98, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.14
Std Dev 4.433
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

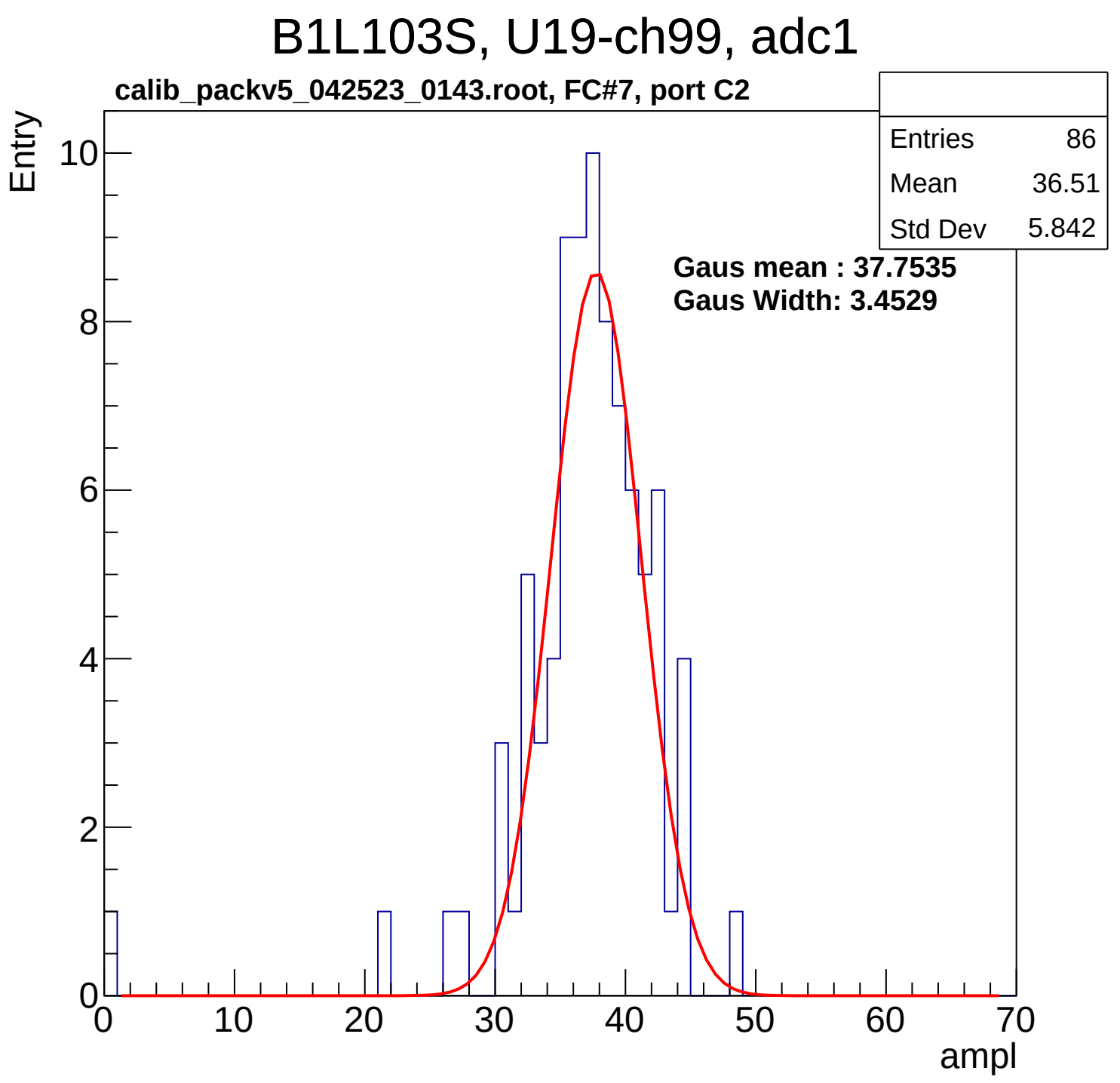

B1L103S, U19-ch98, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.52
Std Dev 7.624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

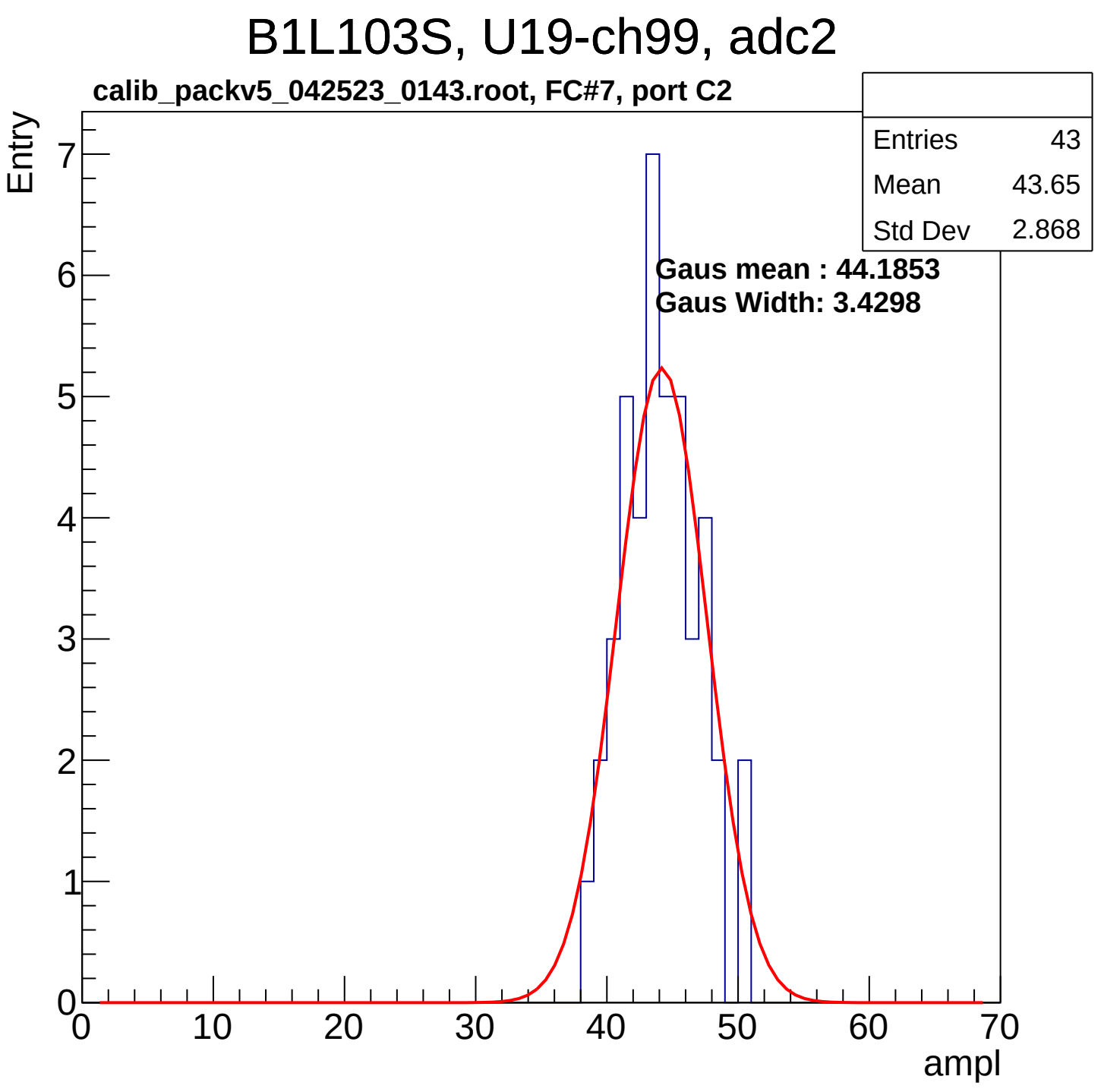

B1L103S, U19-ch98, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 60.18
Std Dev 2.437
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

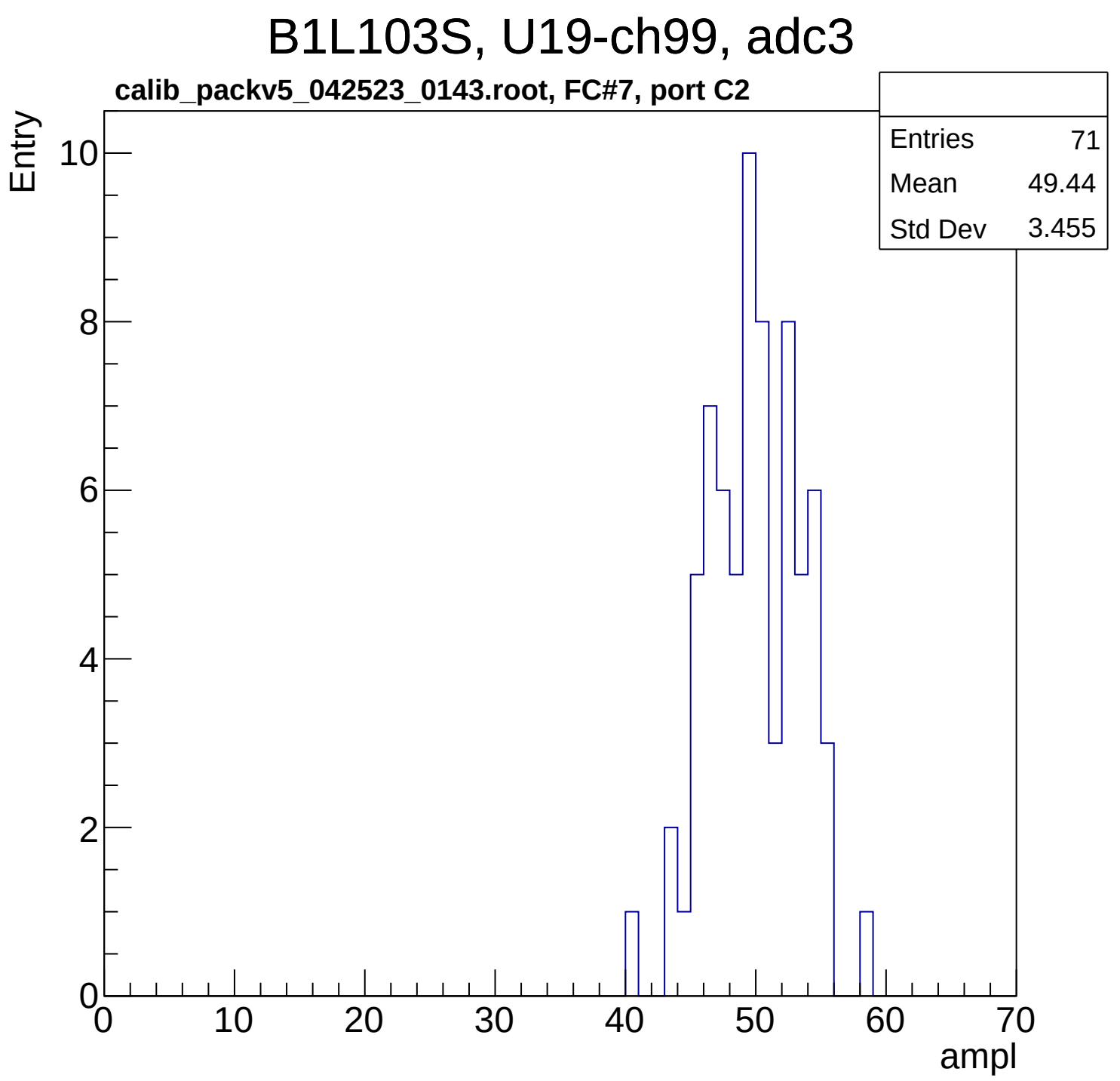

B1L103S, U19-ch98, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch98, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch99, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.9
Std Dev 3.36
Gaus mean : 29.9376
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch99, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 36.51
Std Dev 5.842
Gaus mean : 37.7535
Gaus Width: 3.4529
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch99, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 43.65
Std Dev 2.868
Gaus mean : 44.1853
Gaus Width: 3.4298
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch99, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.44
Std Dev 3.455
Entry
ampl
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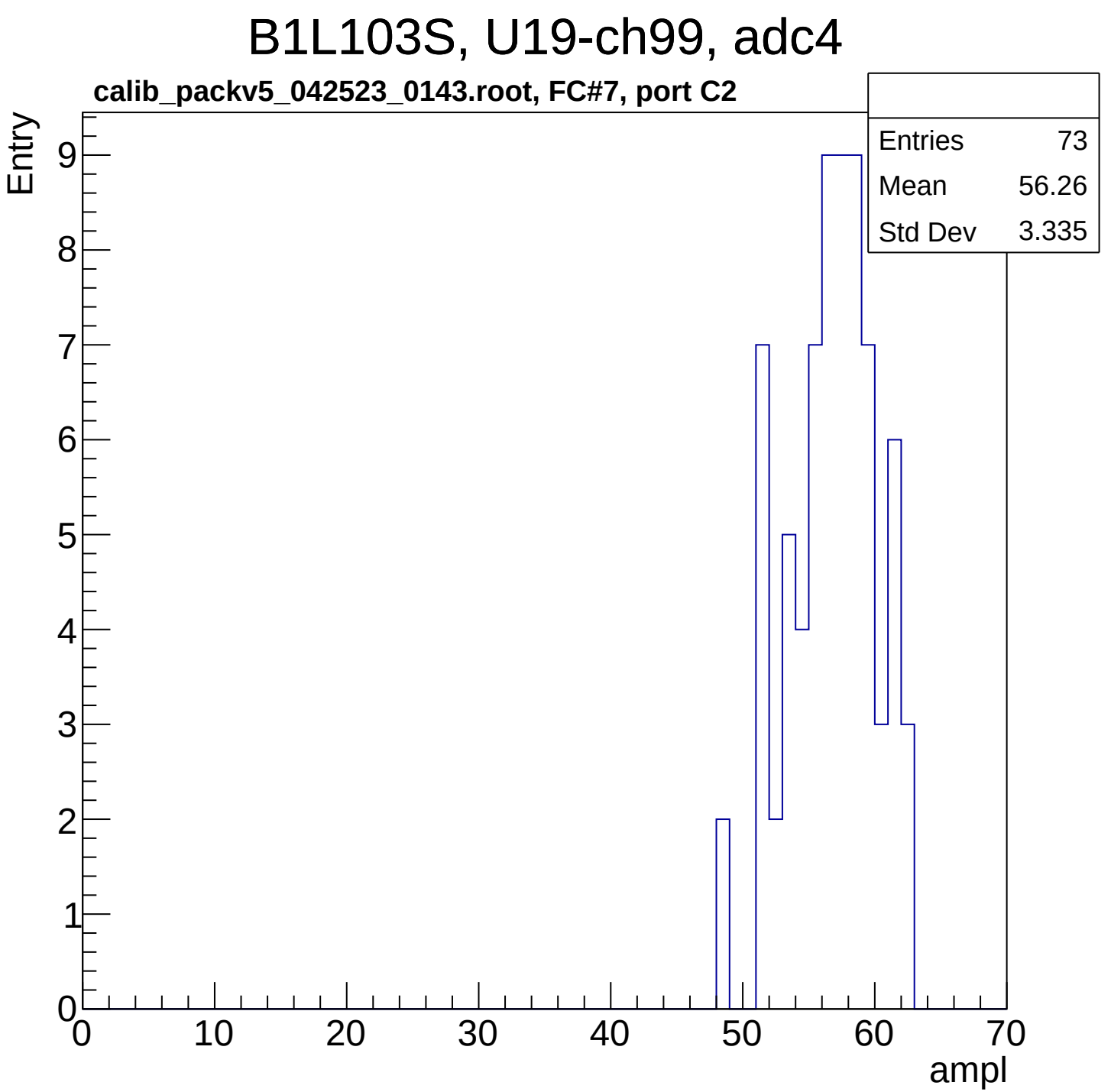
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch99, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 56.26
Std Dev 3.335
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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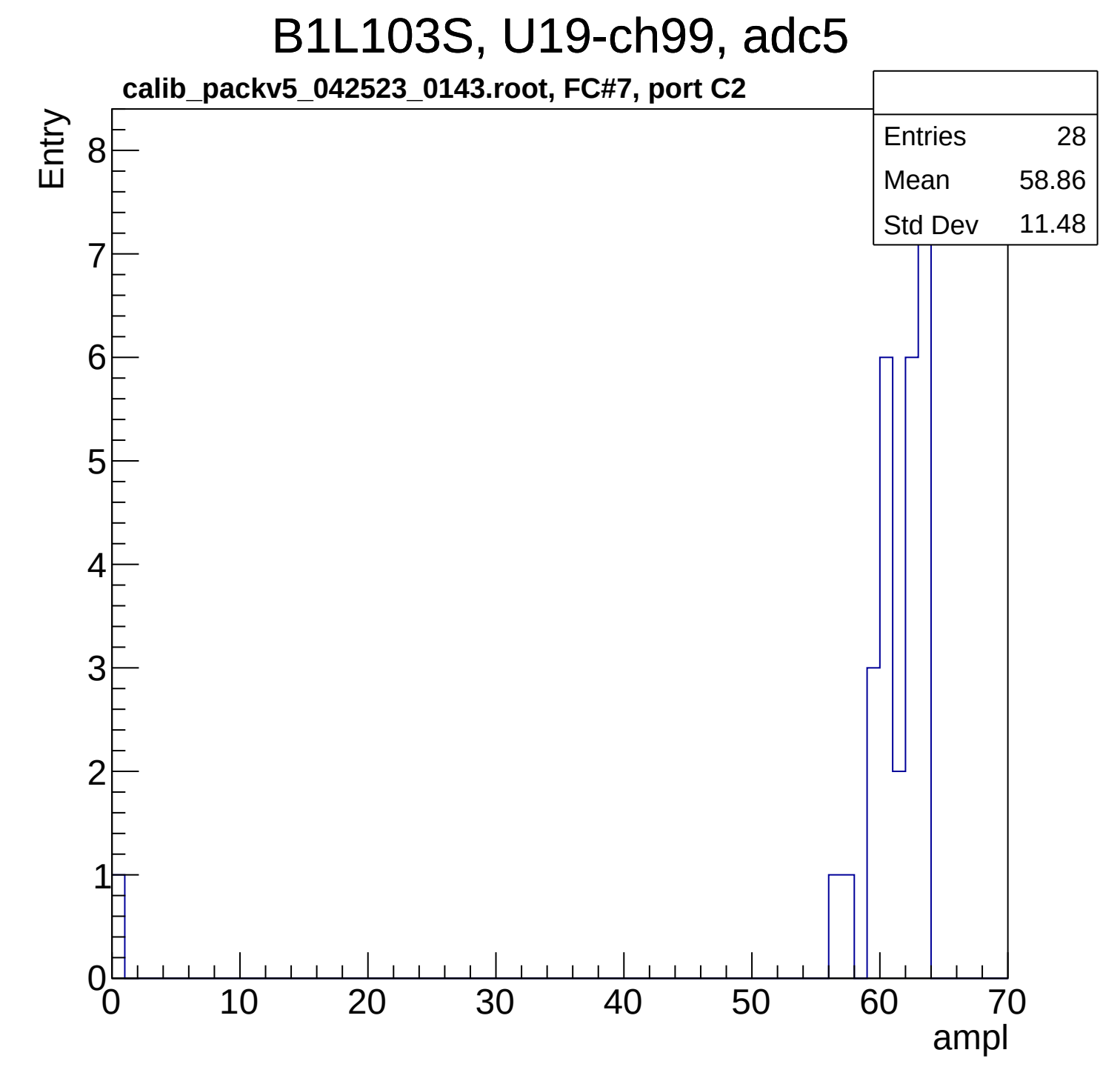
B1L103S, U19-ch99, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 28
Mean 58.86
Std Dev 11.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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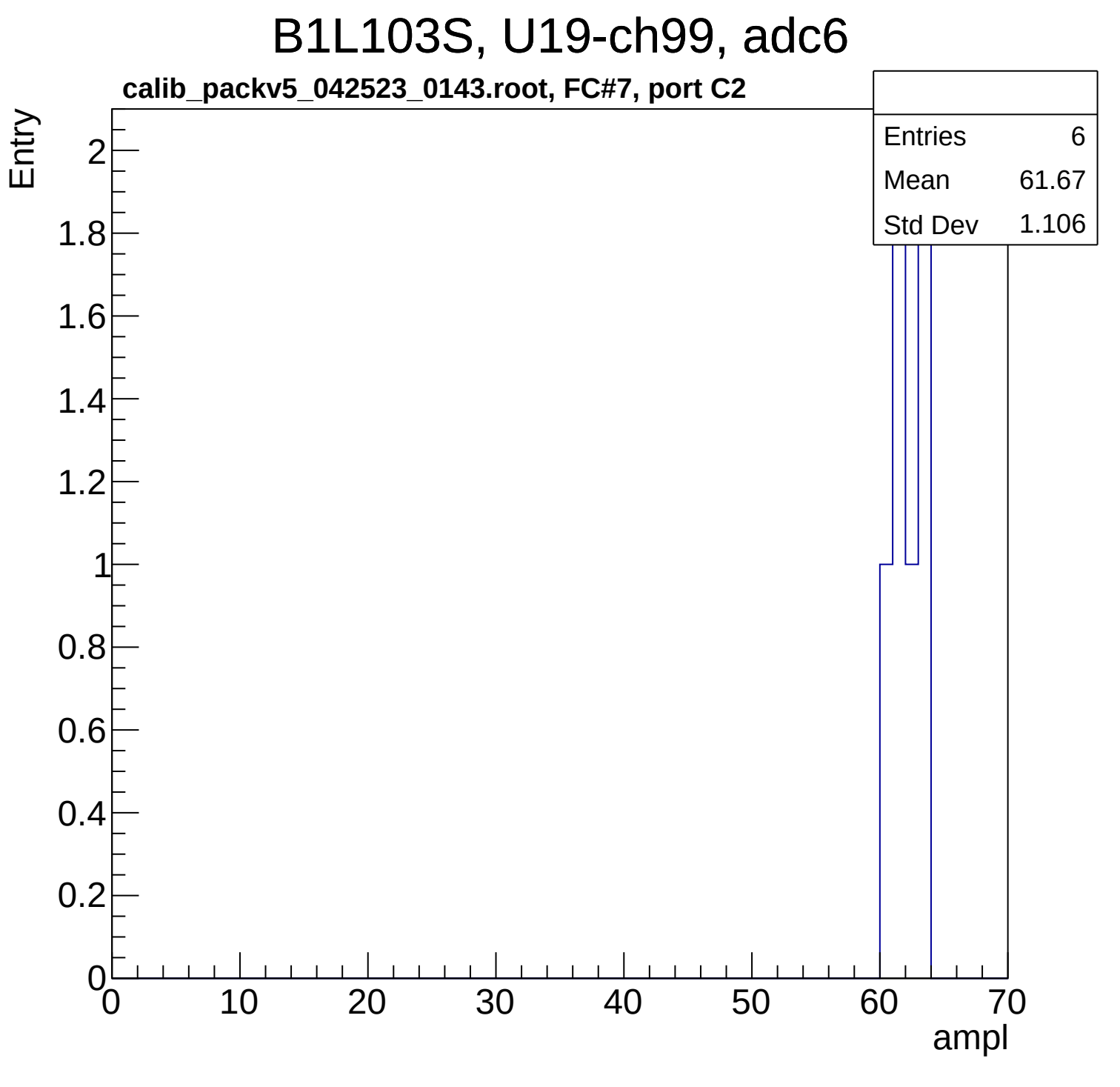
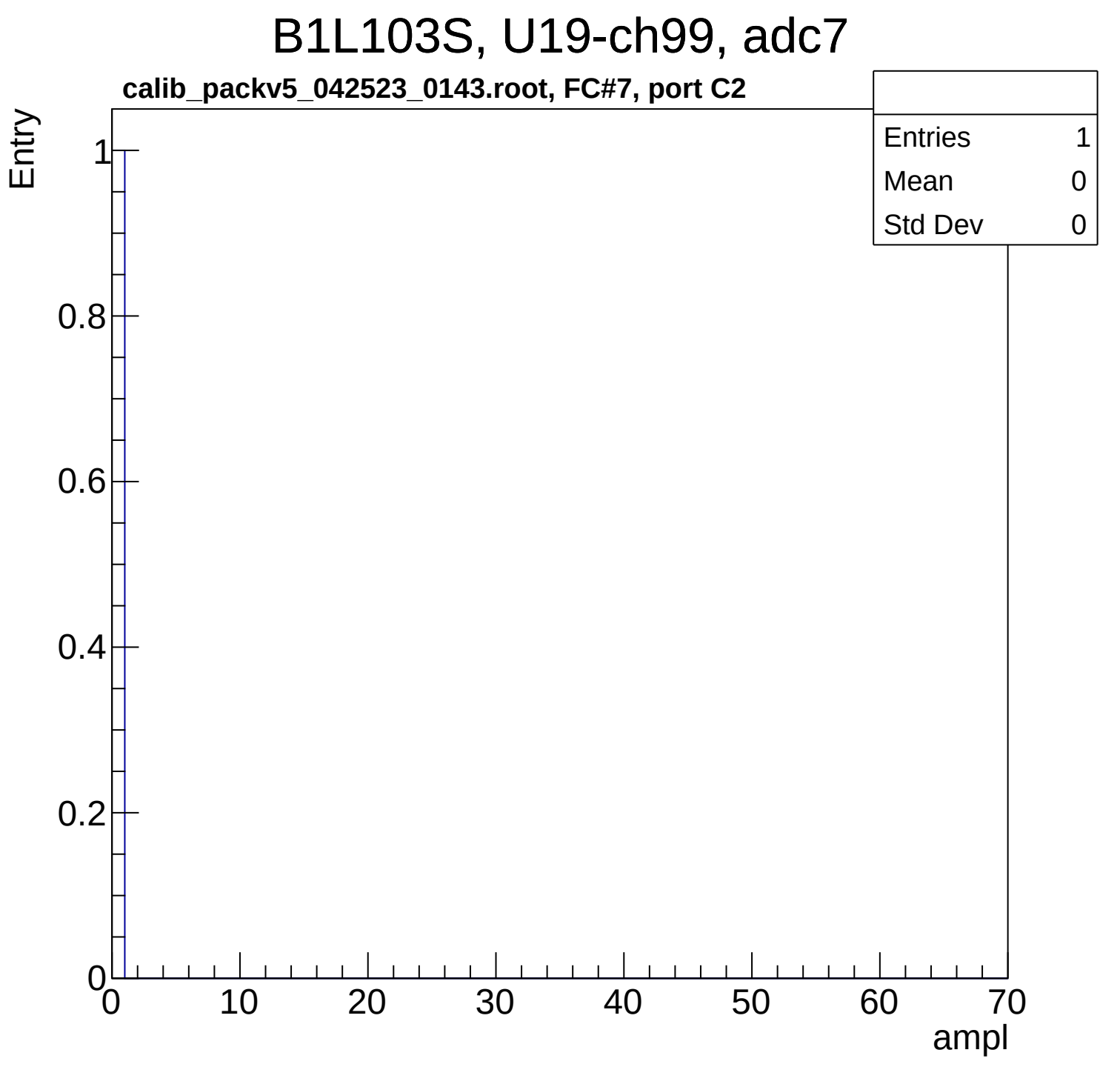
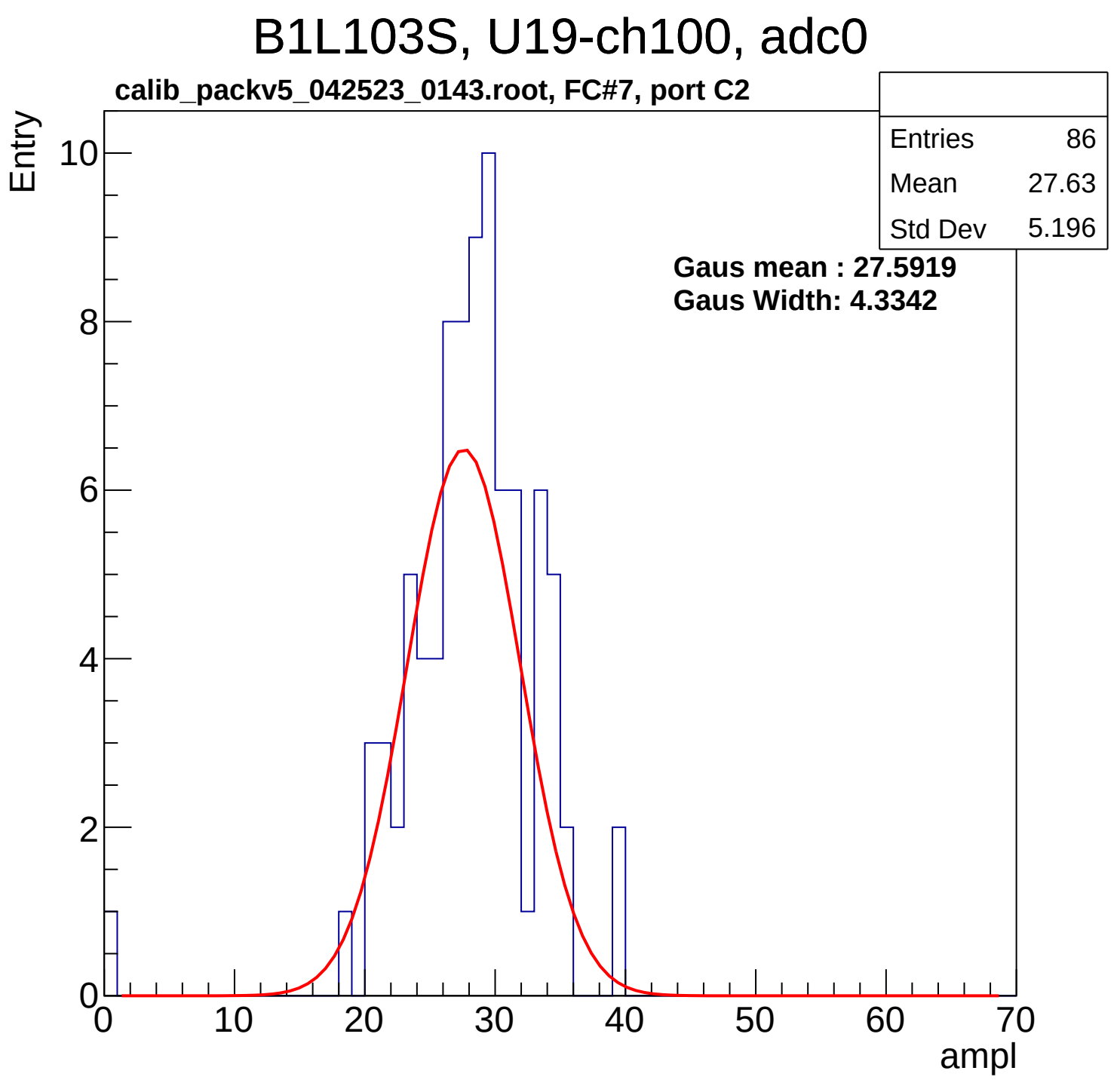
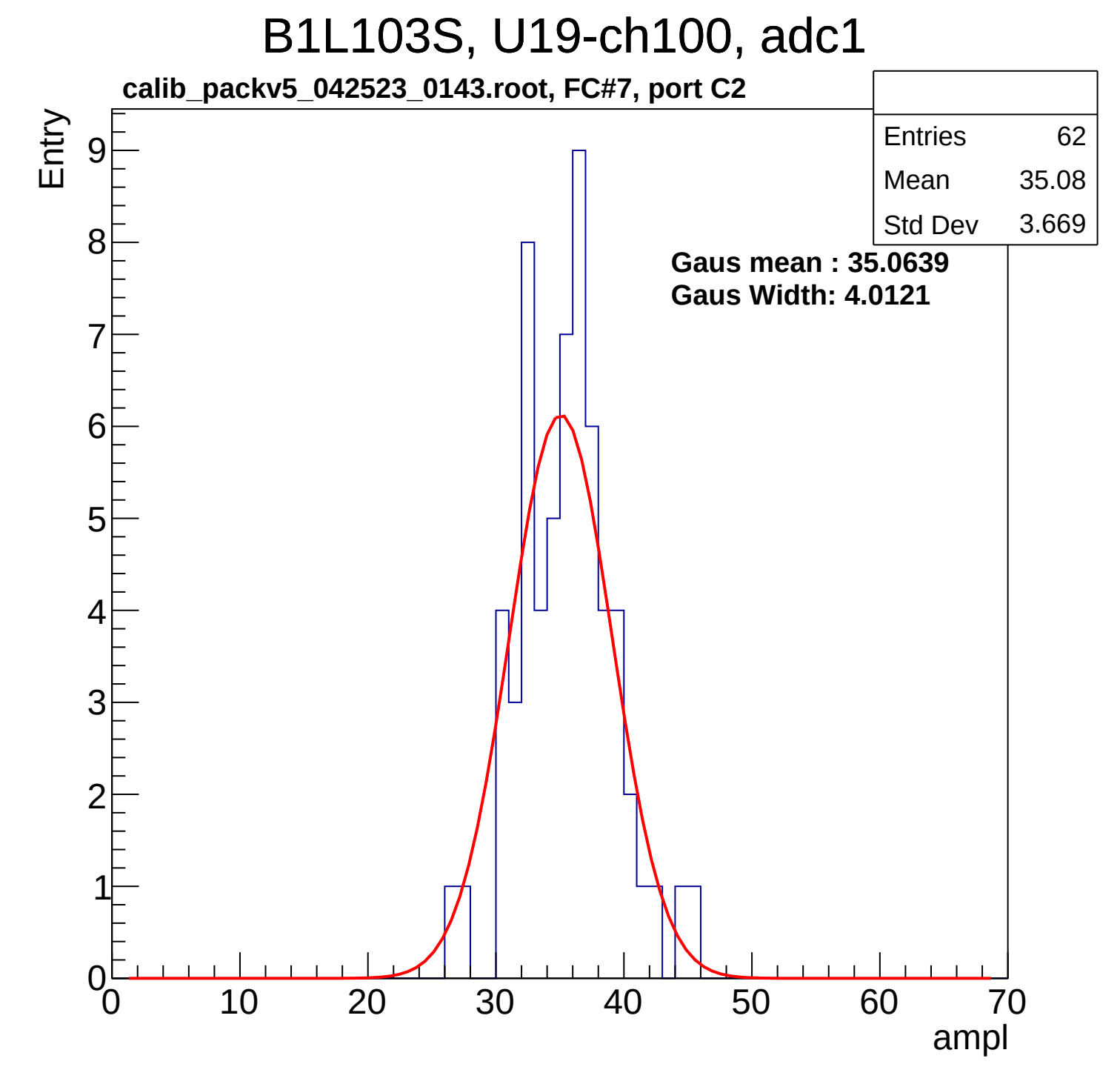
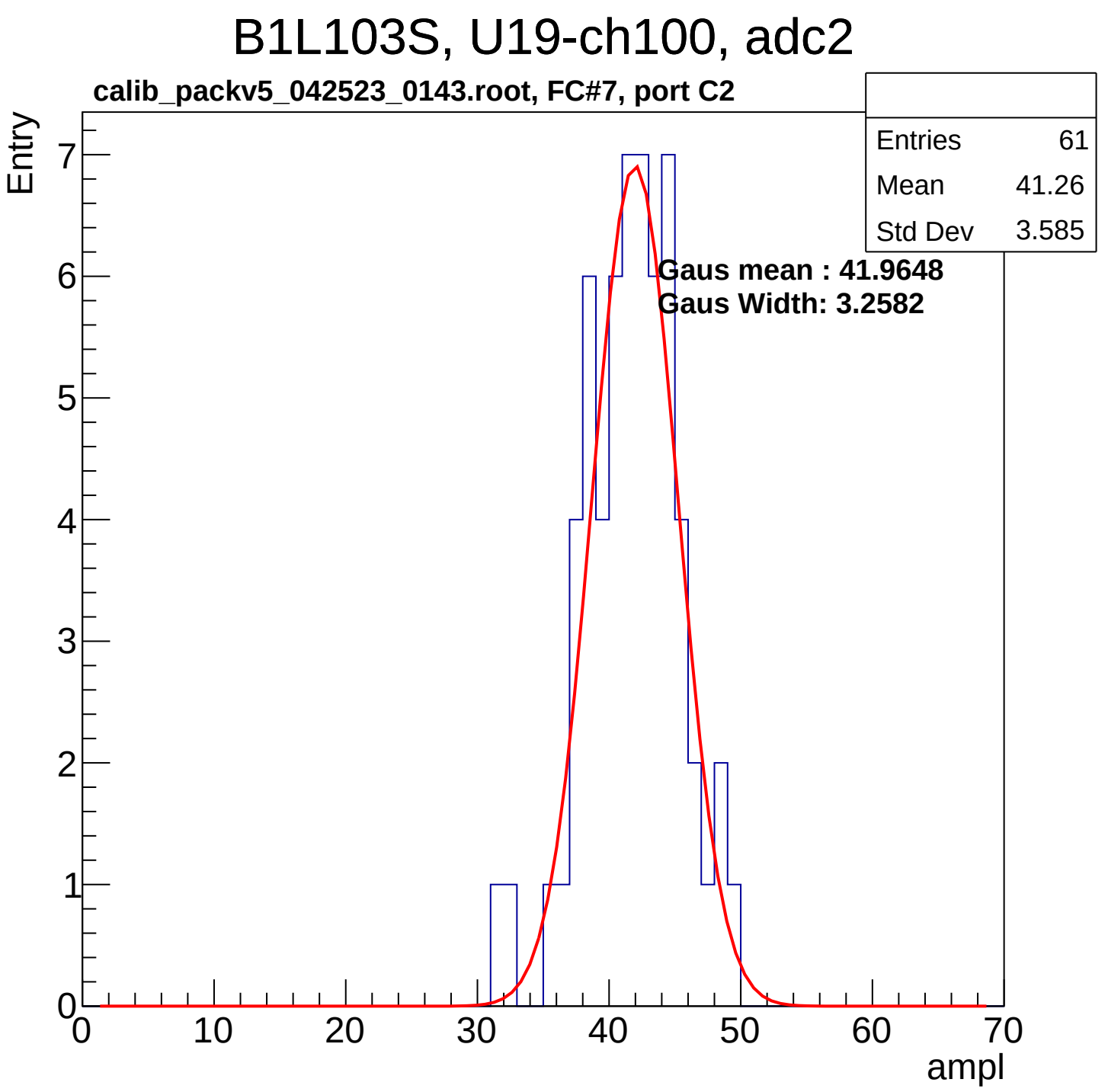

B1L103S, U19-ch99, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch99, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch100, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 27.63
Std Dev 5.196
Gaus mean : 27.5919
Gaus Width: 4.3342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch100, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 35.08
Std Dev 3.669
Gaus mean : 35.0639
Gaus Width: 4.0121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch100, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.26
Std Dev 3.585
Gaus mean : 41.9648
Gaus Width: 3.2582
Entry
ampl
0
1
2
3
4
5
6
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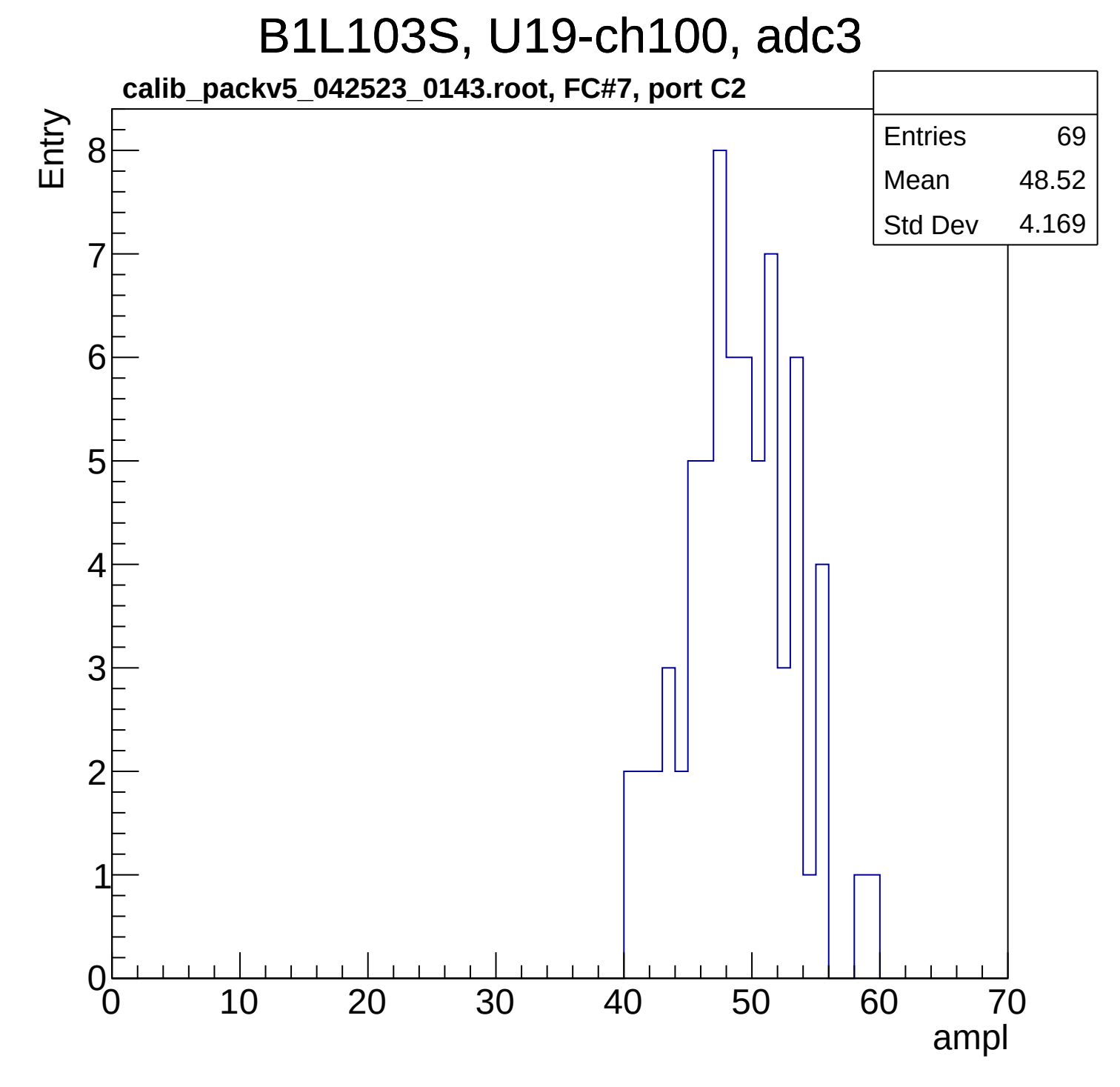
B1L103S, U19-ch100, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 48.52
Std Dev 4.169
Entry
ampl
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1
2
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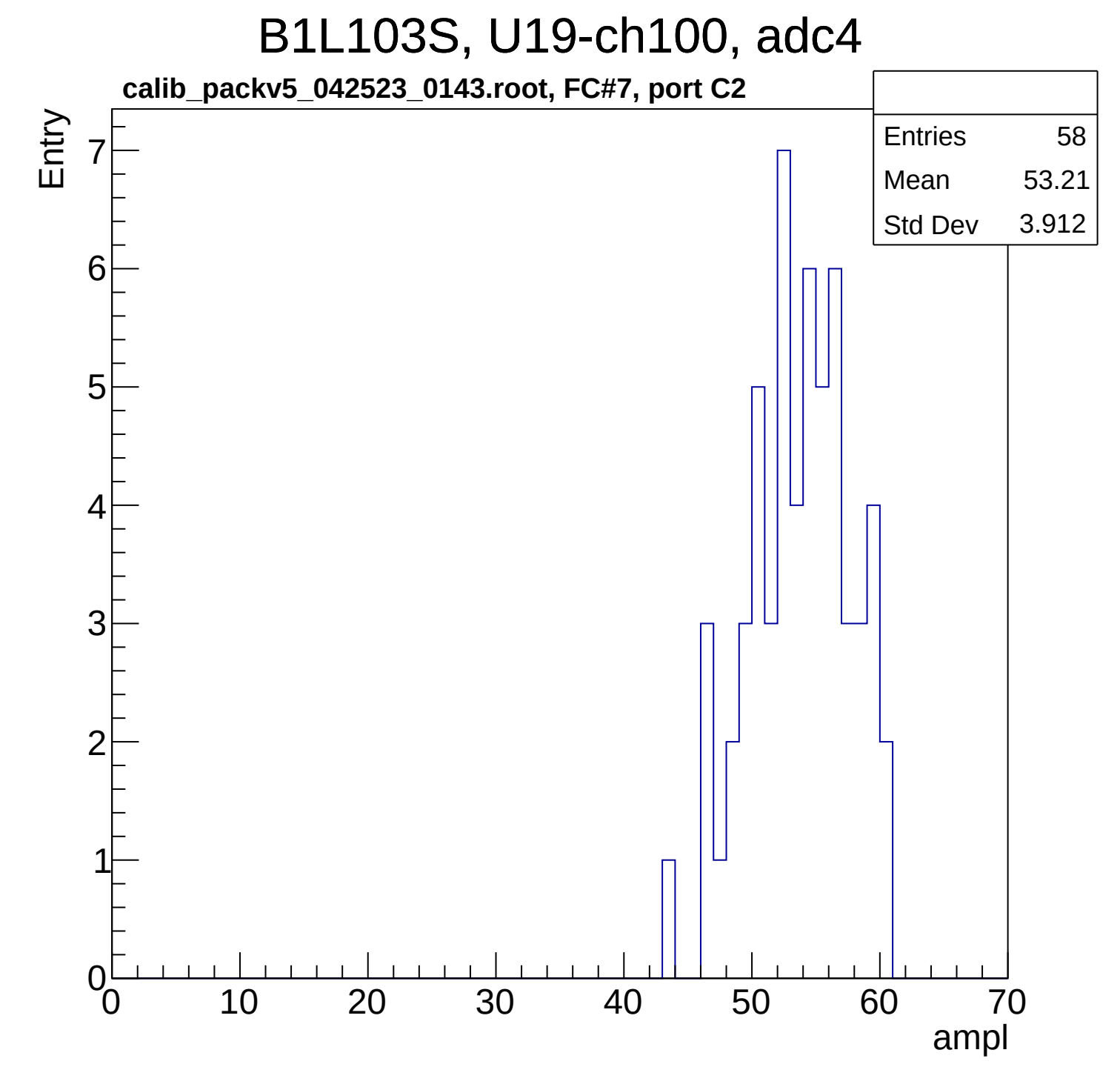
B1L103S, U19-ch100, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53.21
Std Dev 3.912
Entry
ampl
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1
2
3
4
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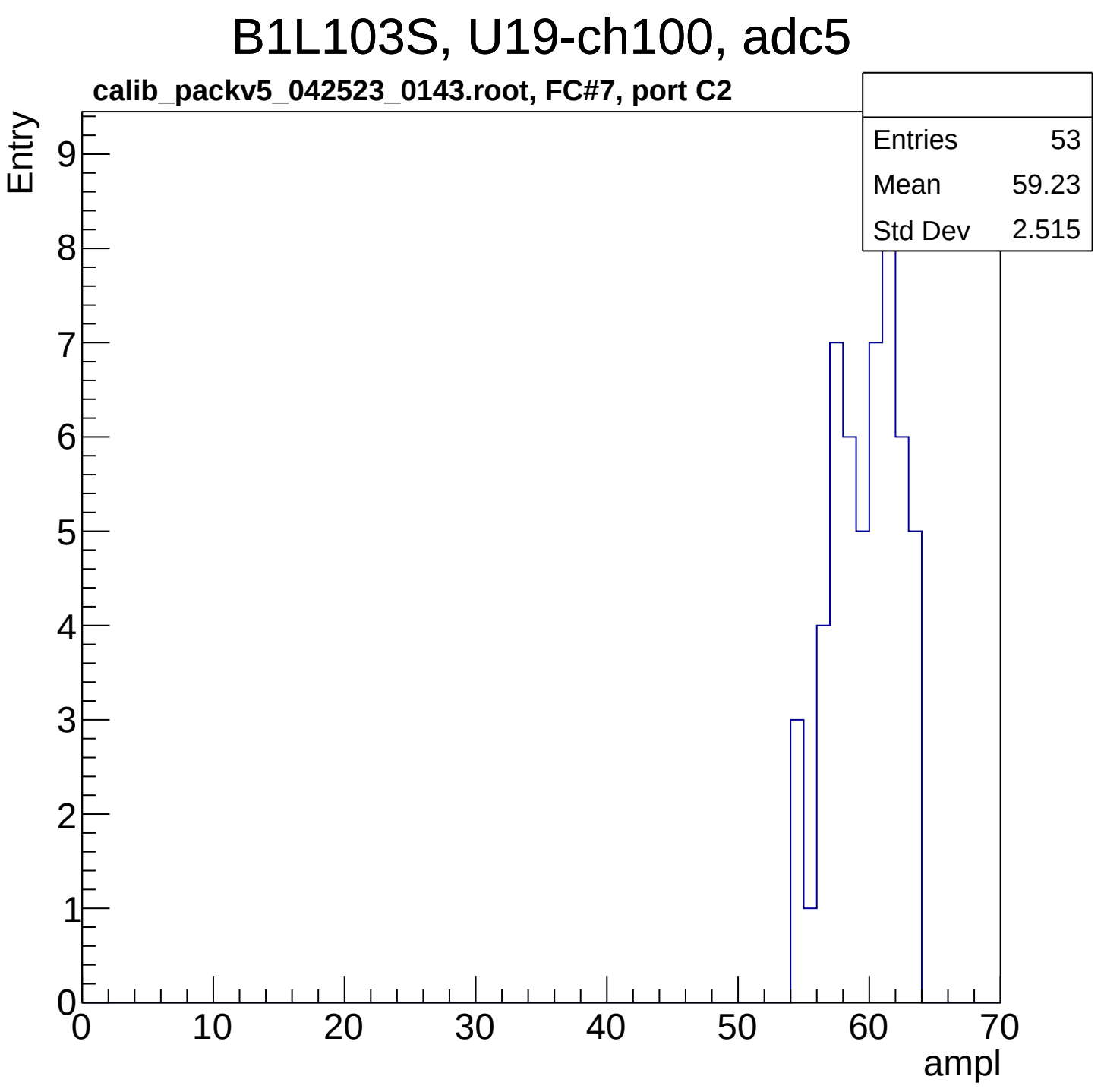
B1L103S, U19-ch100, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 59.23
Std Dev 2.515
Entry
ampl
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2
3
4
5
6
7
8
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0
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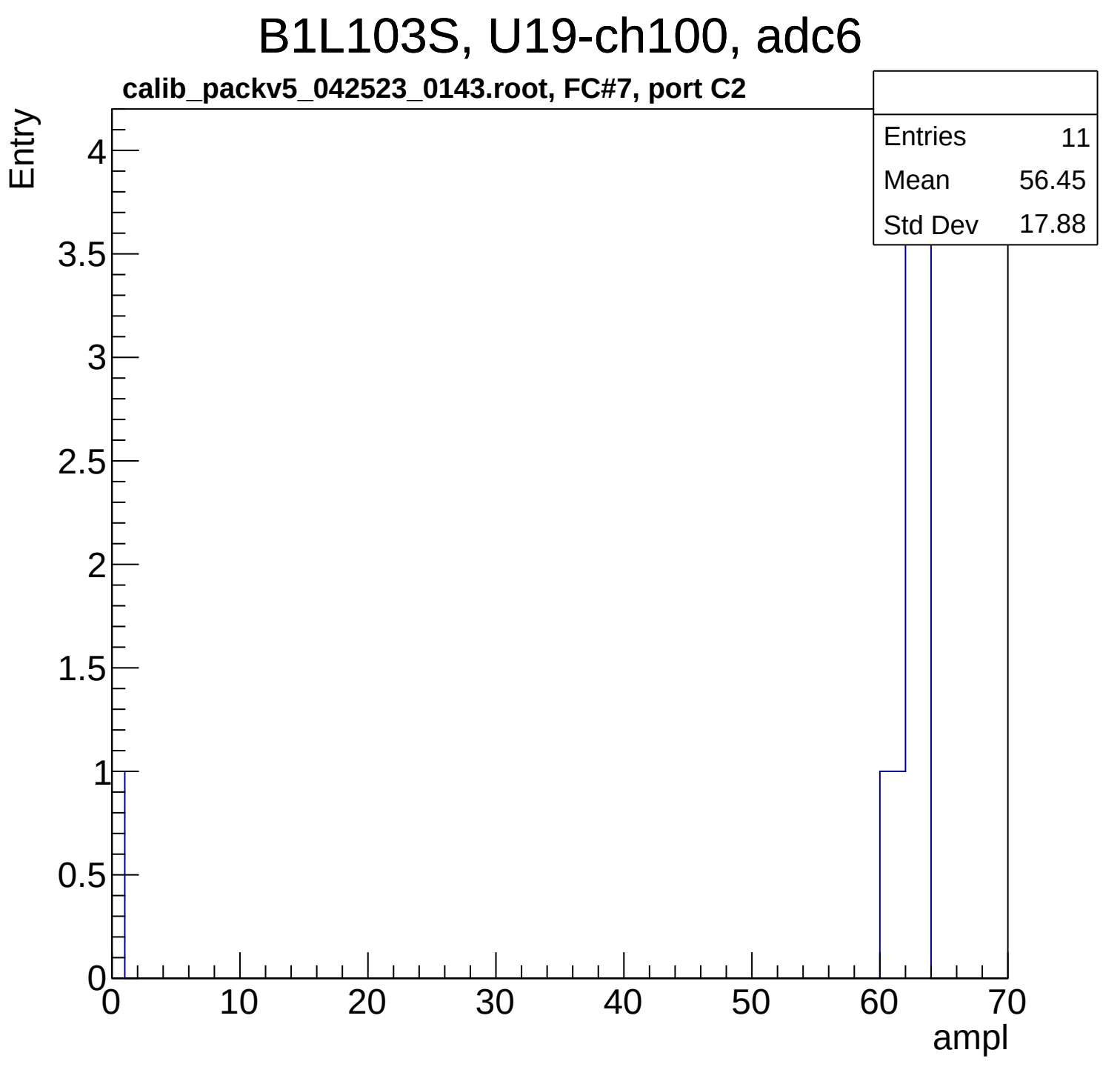
B1L103S, U19-ch100, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 56.45
Std Dev 17.88
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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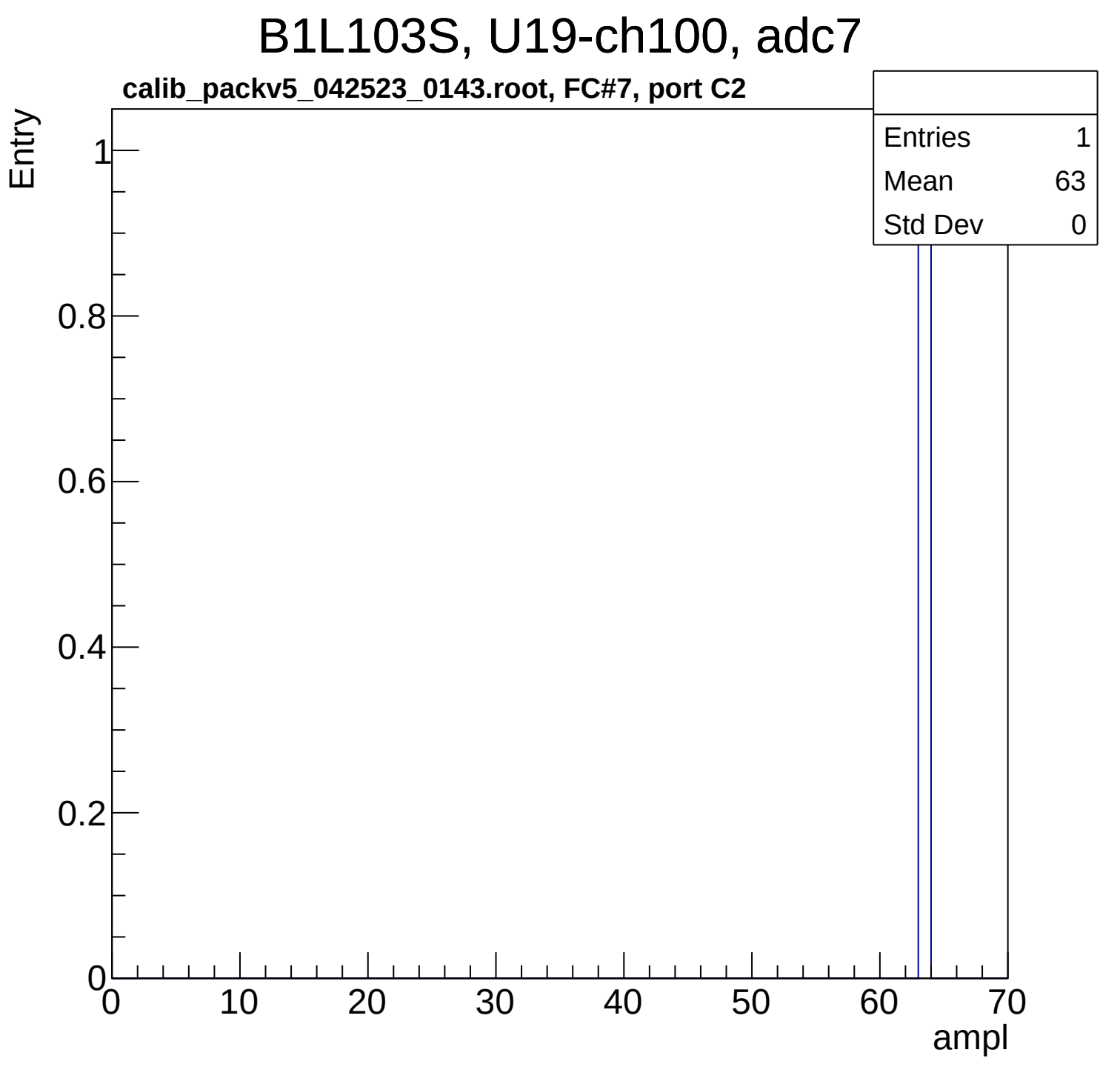
B1L103S, U19-ch100, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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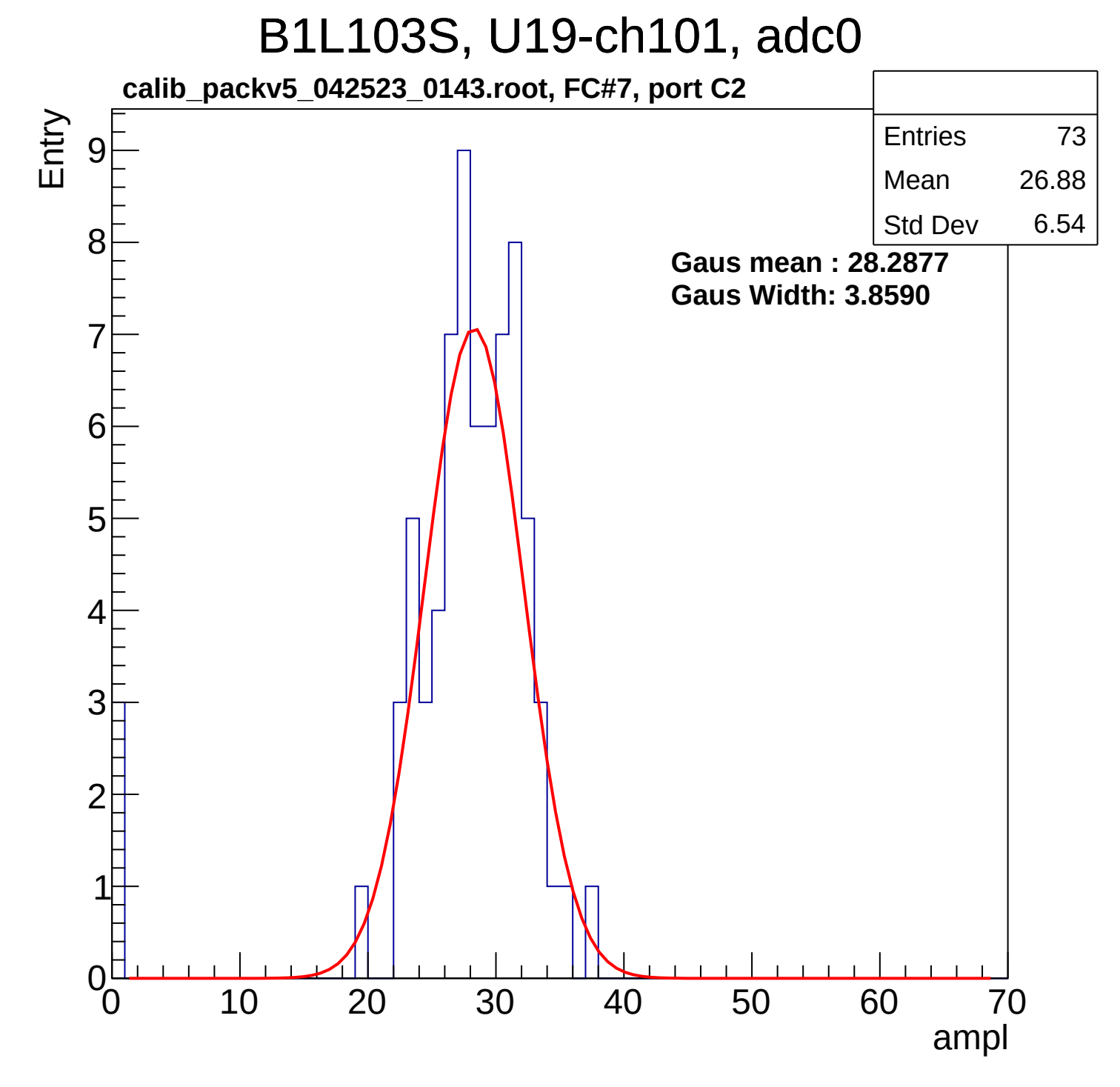
40
50
60
70
ampl


B1L103S, U19-ch101, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 26.88
Std Dev 6.54
Gaus mean : 28.2877
Gaus Width: 3.8590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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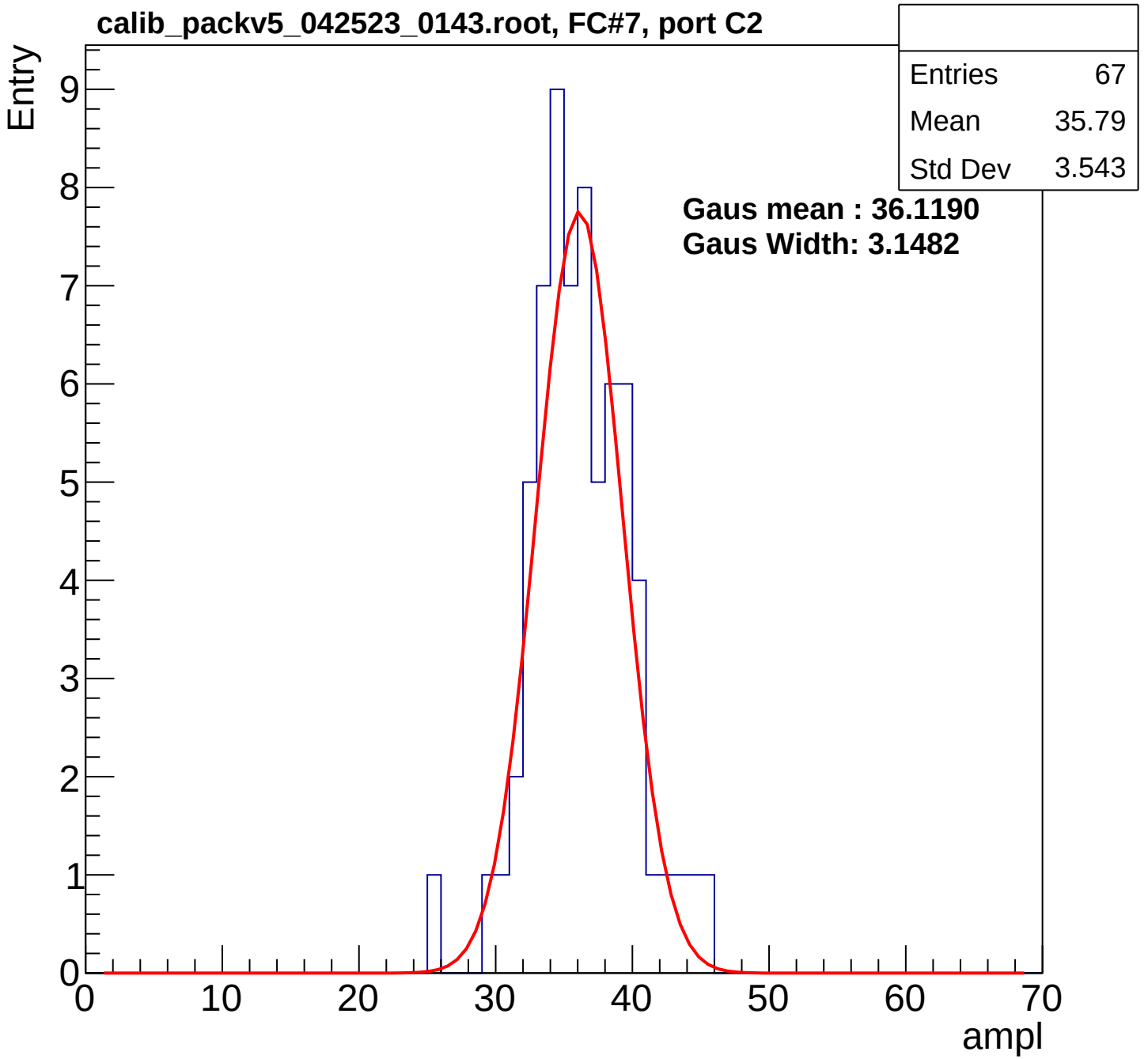
B1L103S, U19-ch101, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.79
Std Dev 3.543
Gaus mean : 36.1190
Gaus Width: 3.1482
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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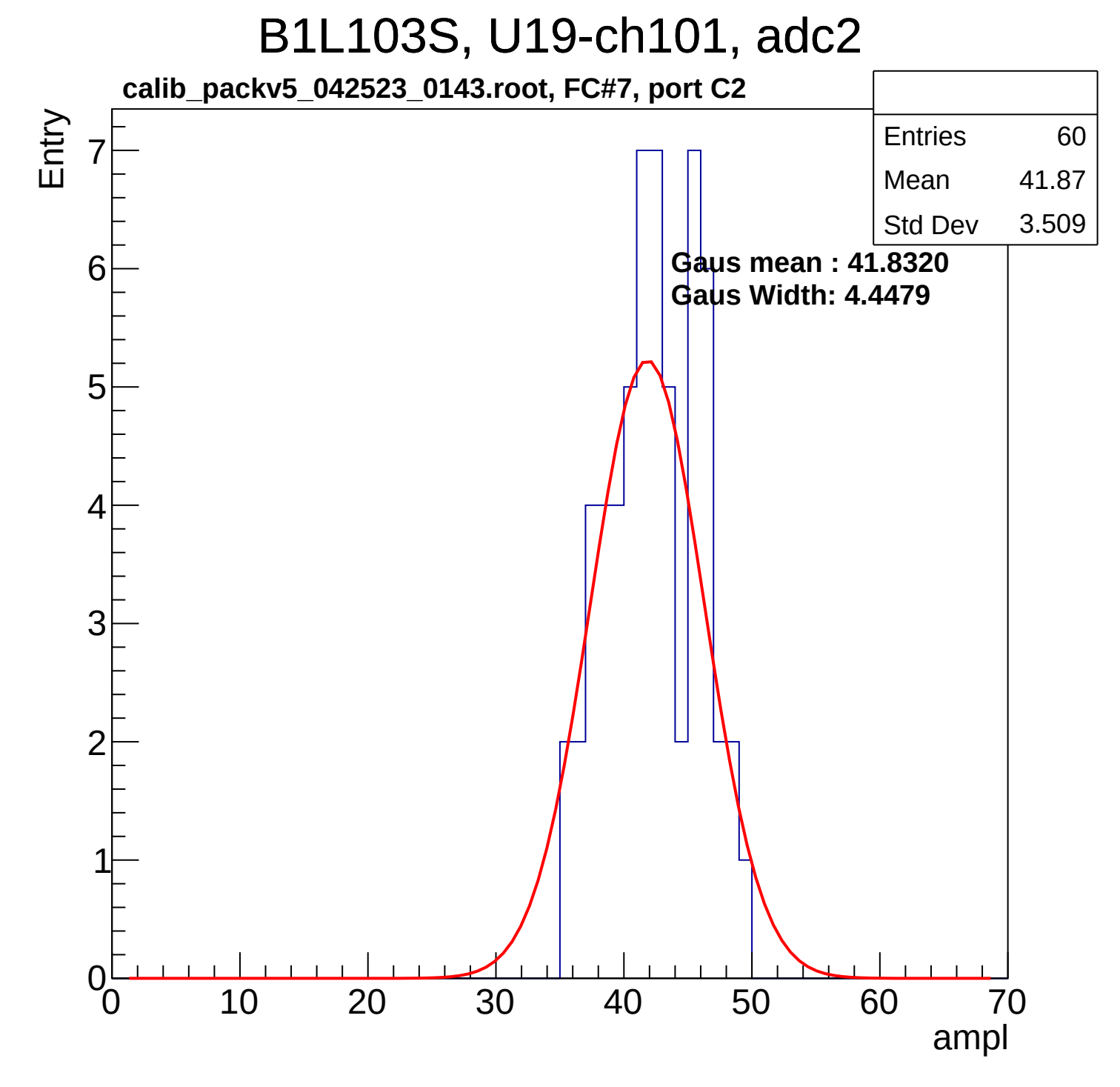
B1L103S, U19-ch101, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 41.87
Std Dev 3.509
Gaus mean : 41.8320
Gaus Width: 4.4479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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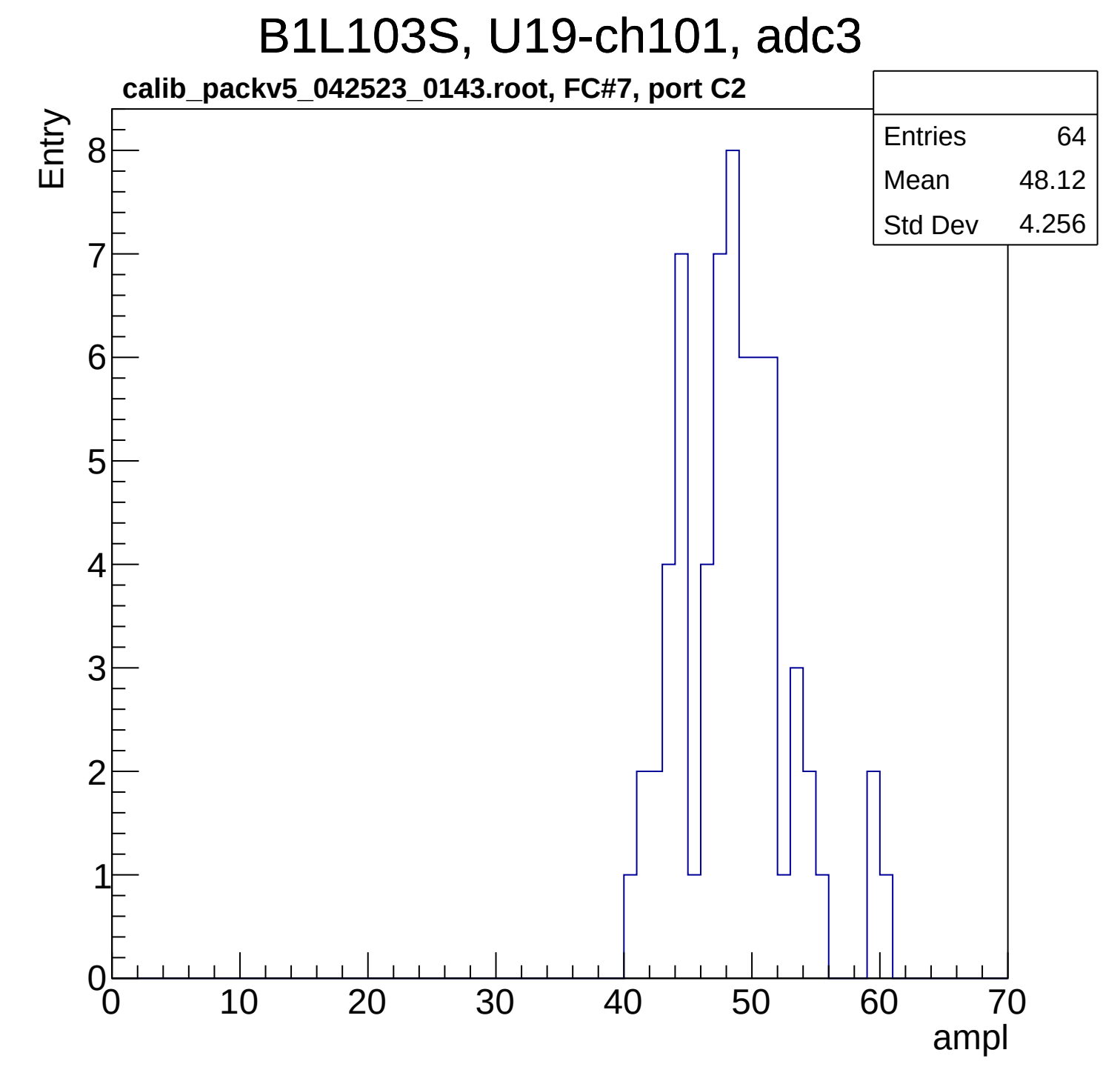
B1L103S, U19-ch101, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.12
Std Dev 4.256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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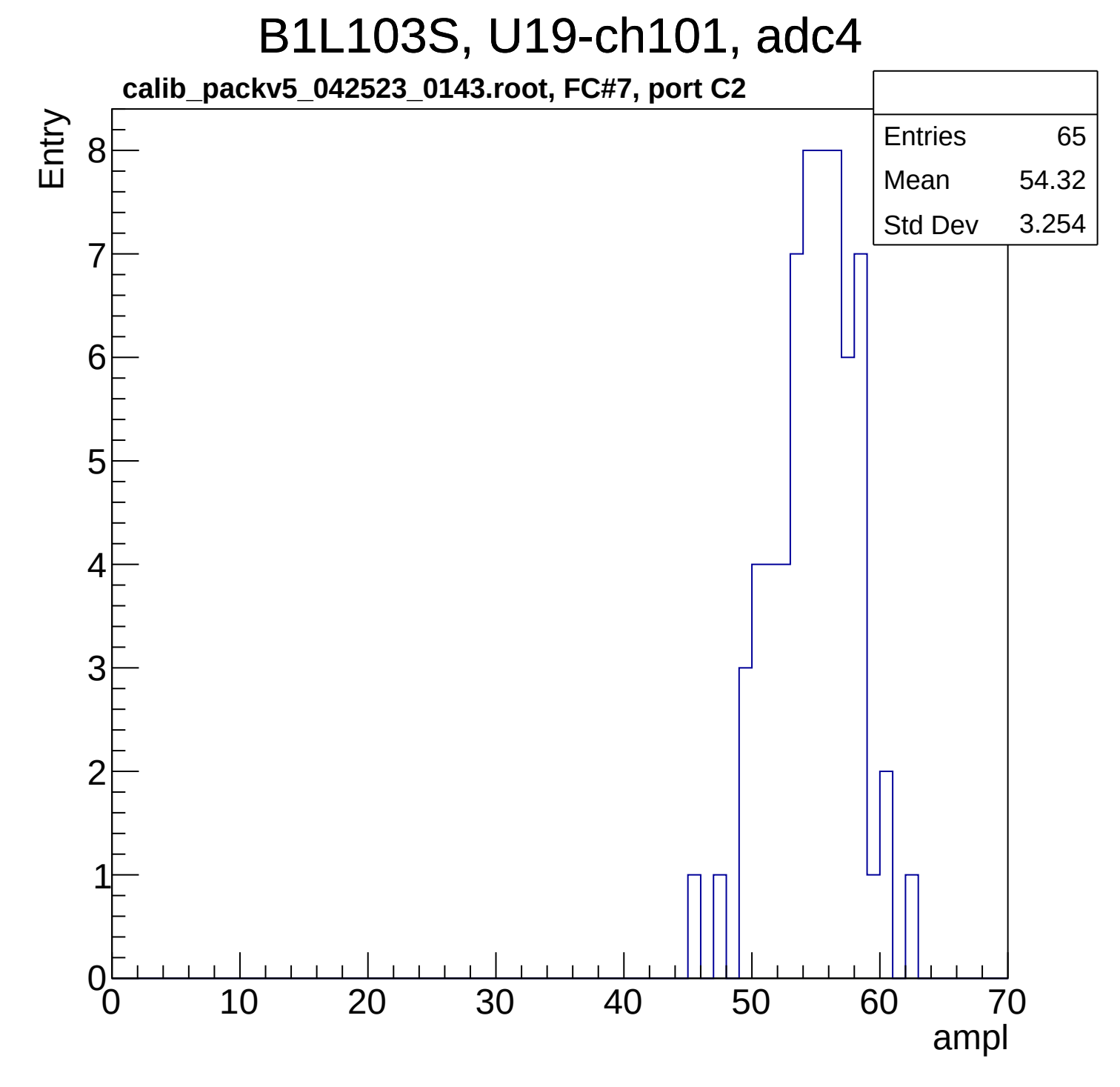
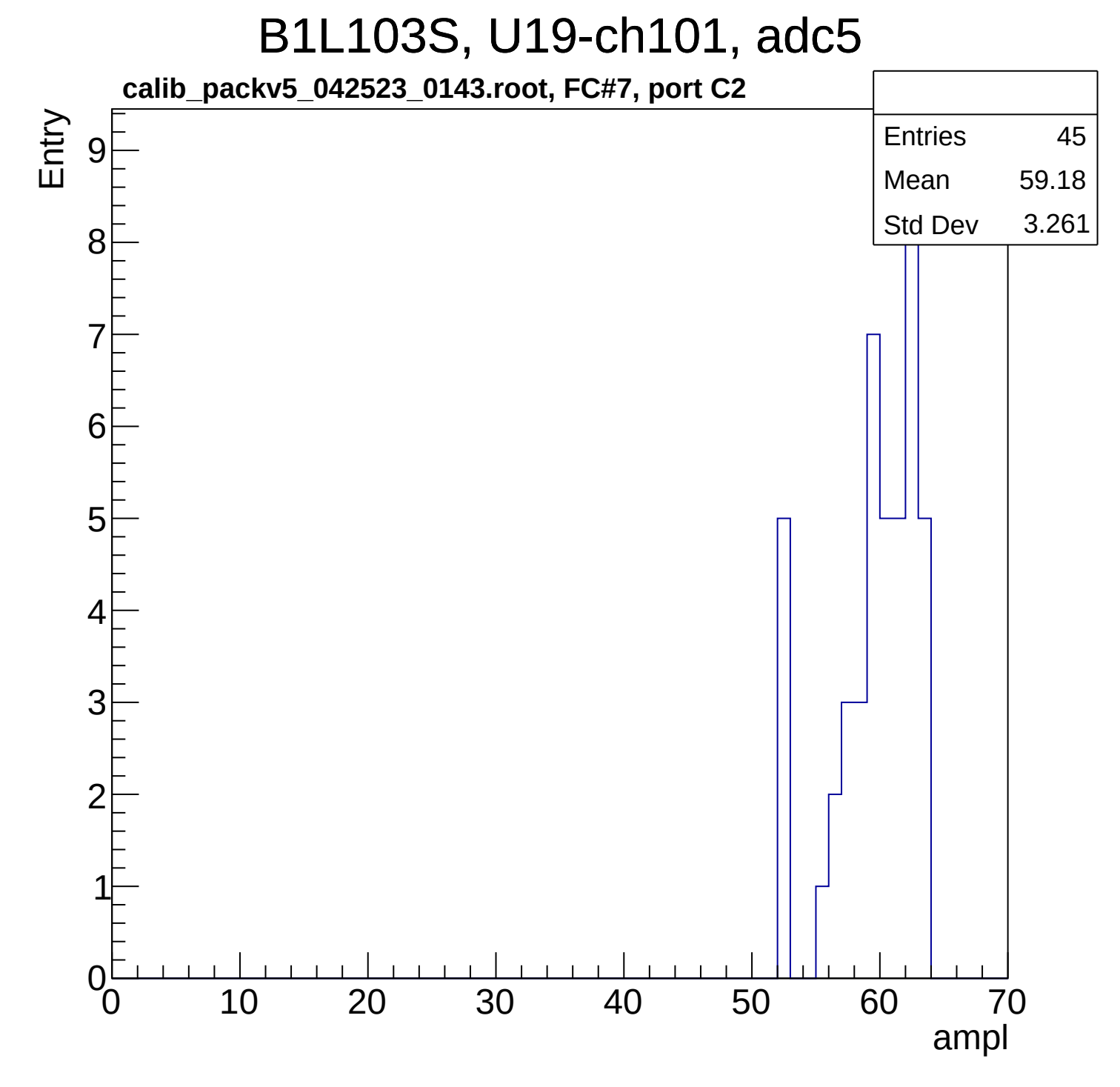

B1L103S, U19-ch101, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 54.32
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch101, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.18
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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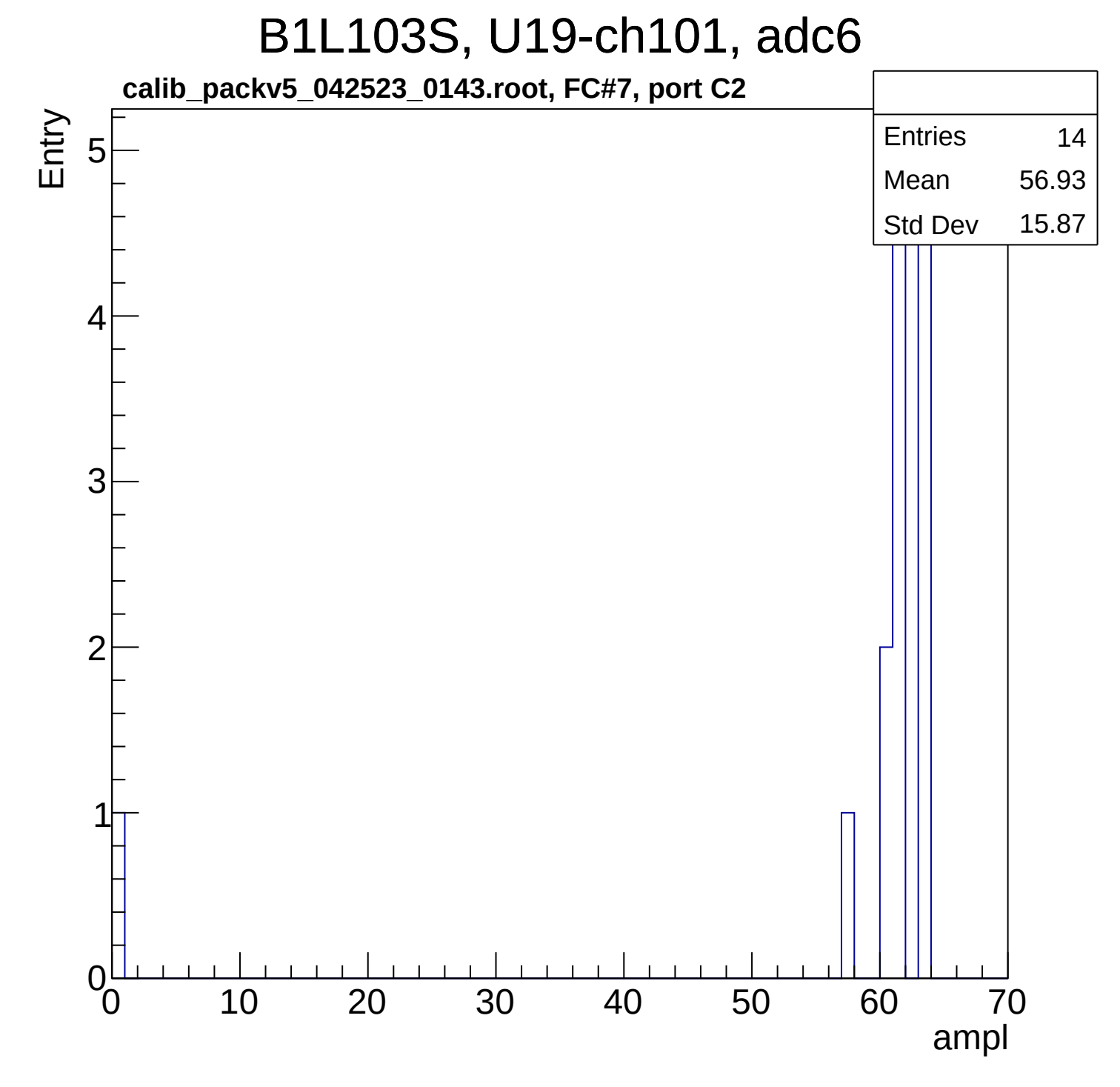
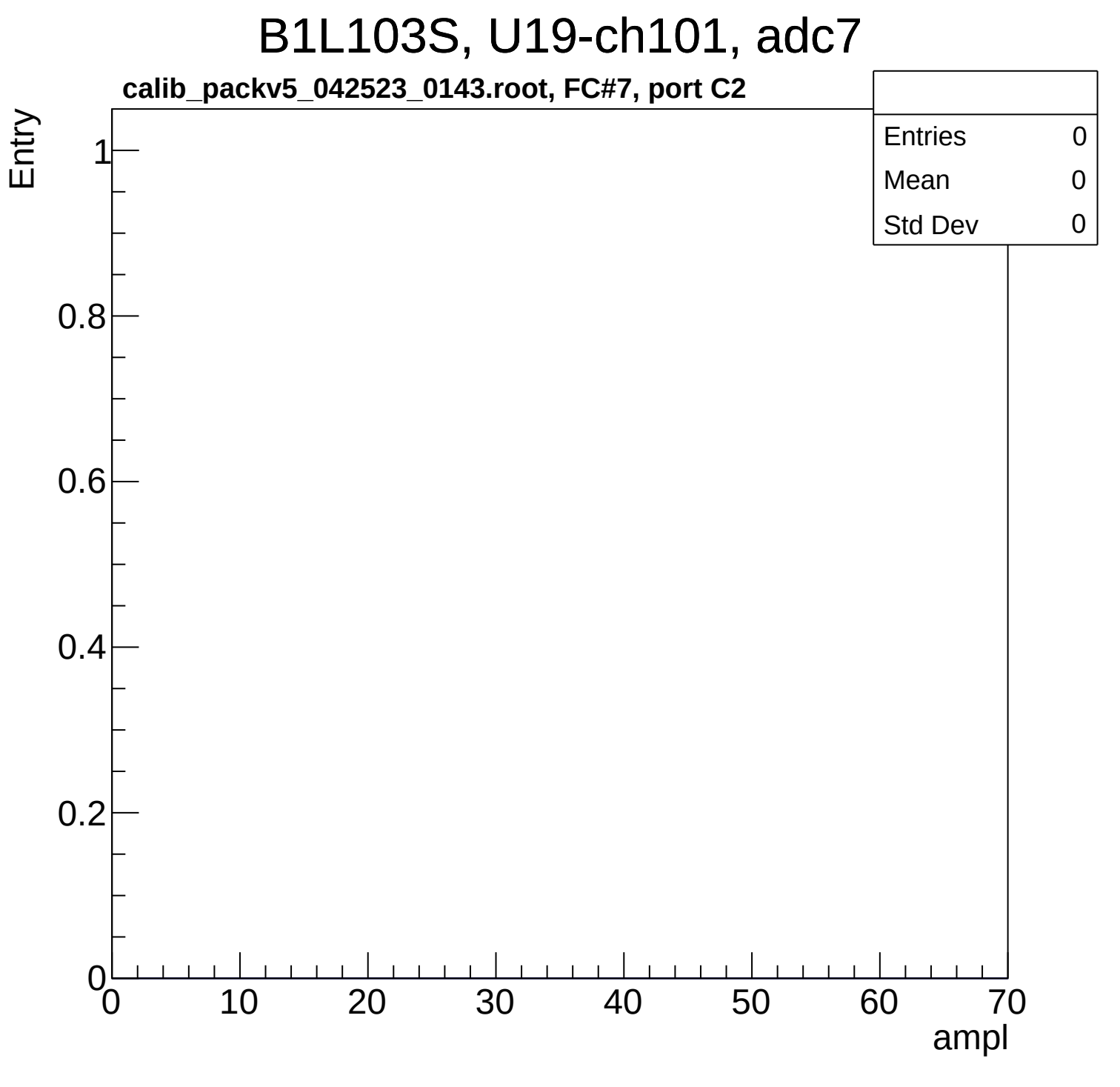
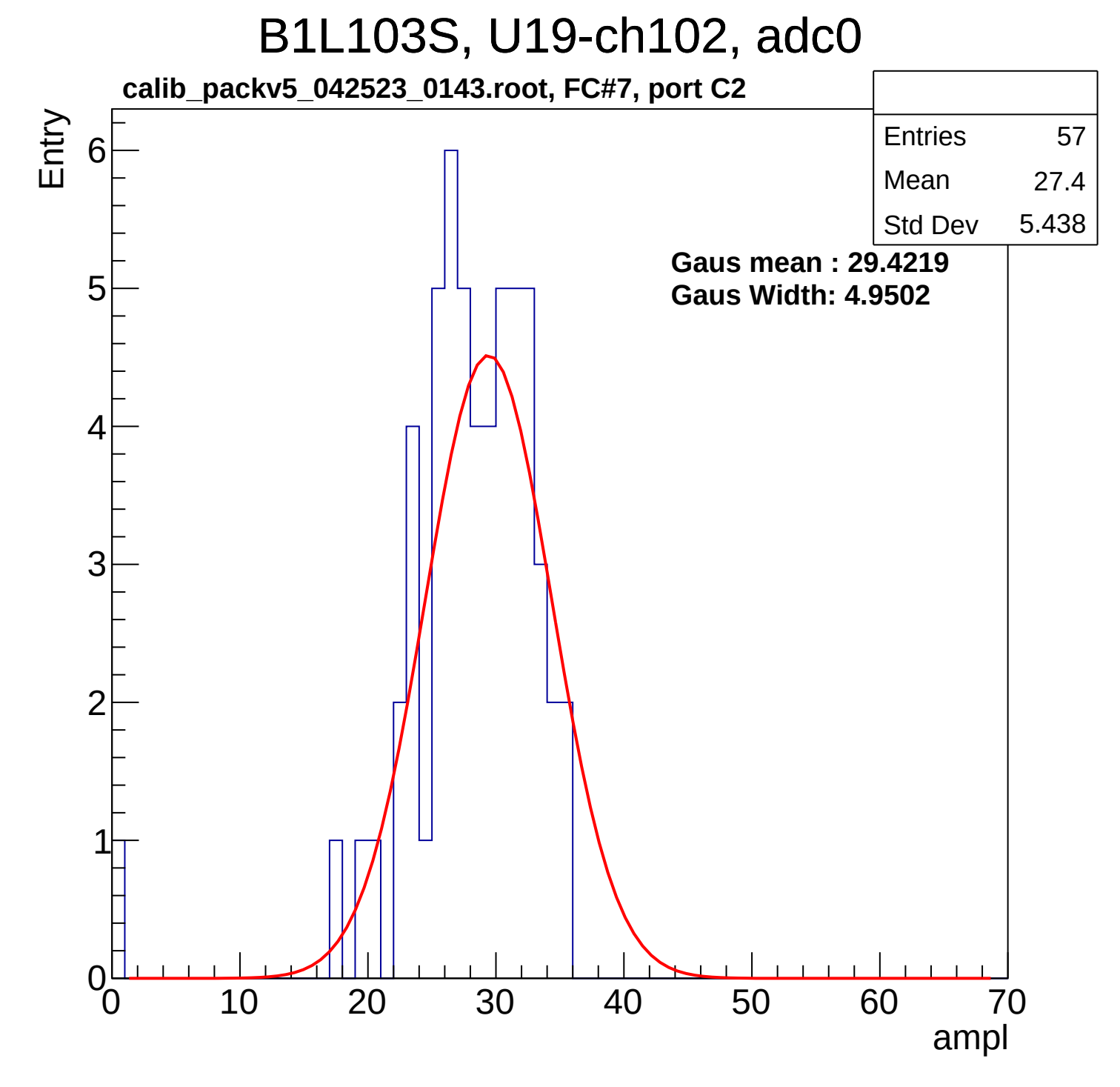
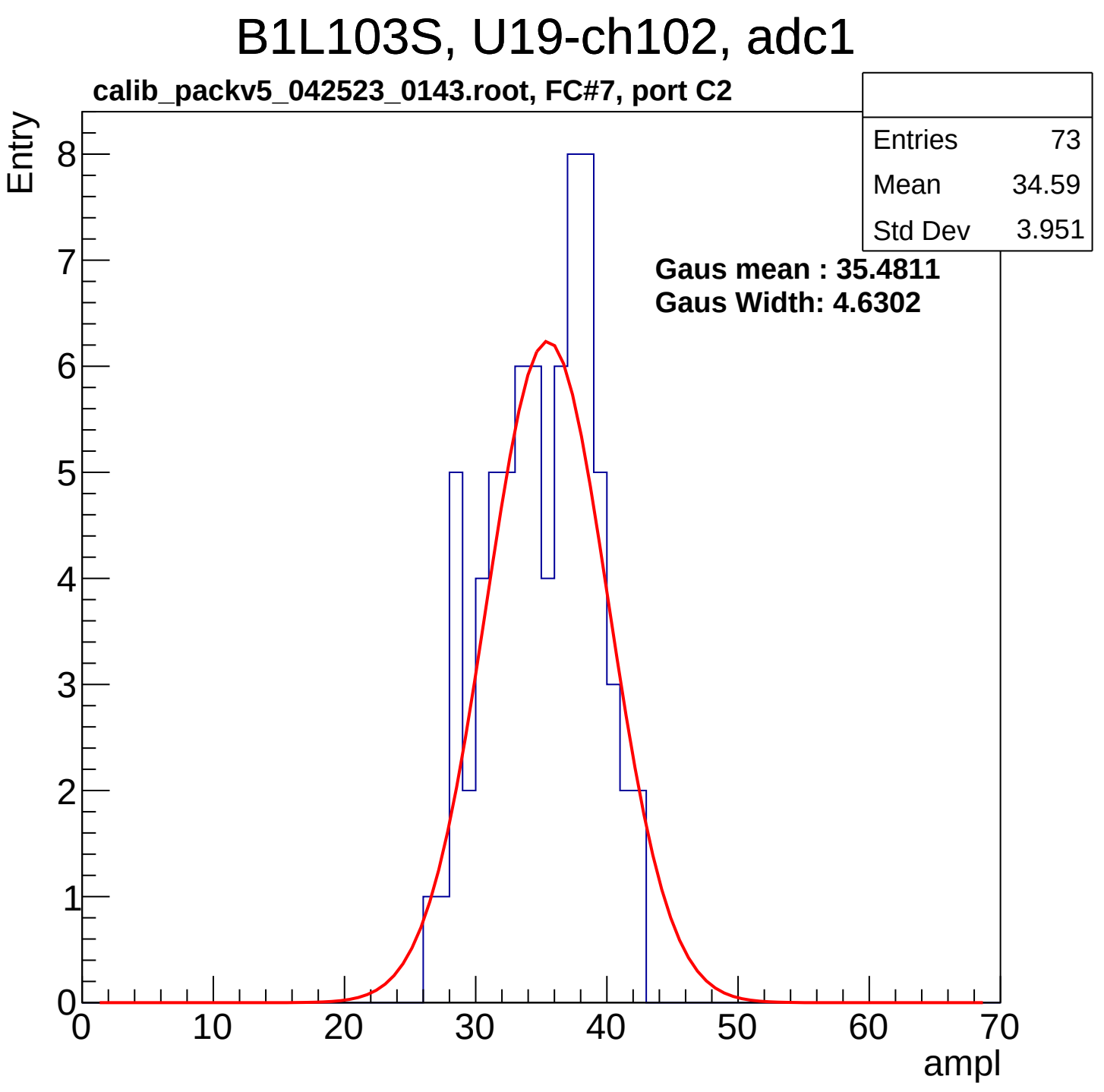

B1L103S, U19-ch101, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 56.93
Std Dev 15.87
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch101, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch102, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 27.4
Std Dev 5.438
Gaus mean : 29.4219
Gaus Width: 4.9502
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch102, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 34.59
Std Dev 3.951
Gaus mean : 35.4811
Gaus Width: 4.6302
Entry
ampl
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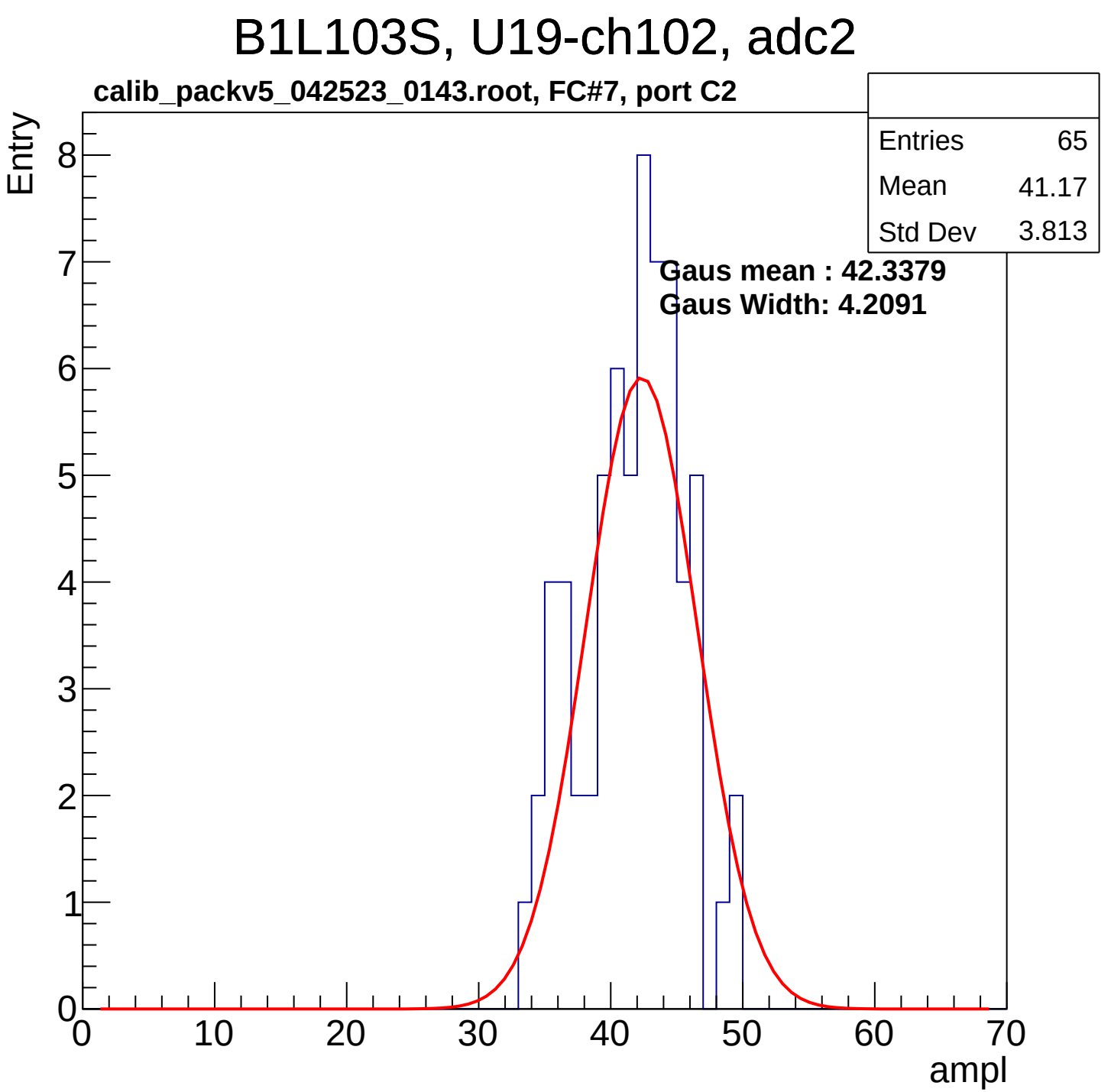
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch102, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.17
Std Dev 3.813
Gaus mean : 42.3379
Gaus Width: 4.2091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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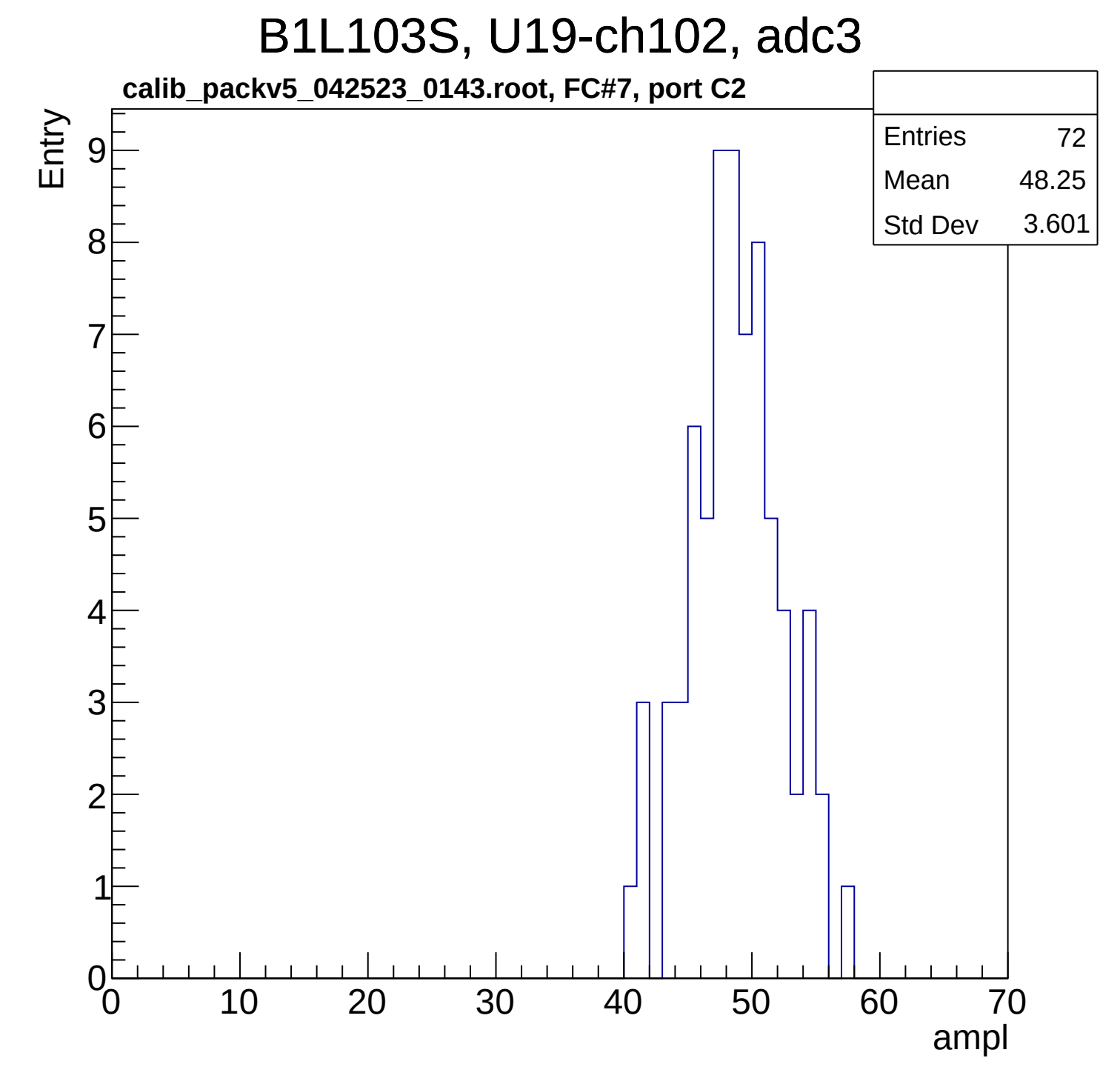
B1L103S, U19-ch102, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 48.25
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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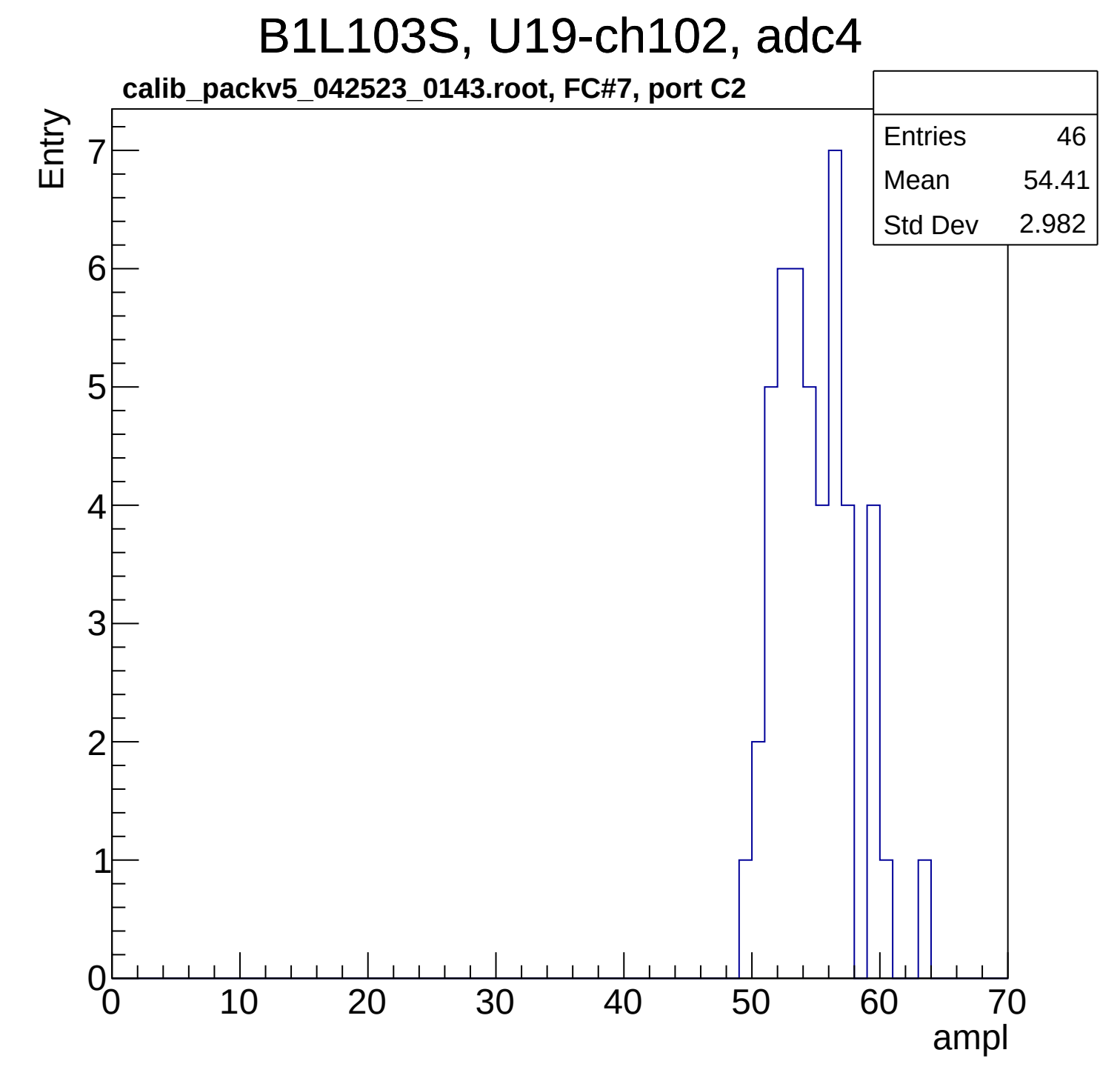
B1L103S, U19-ch102, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 54.41
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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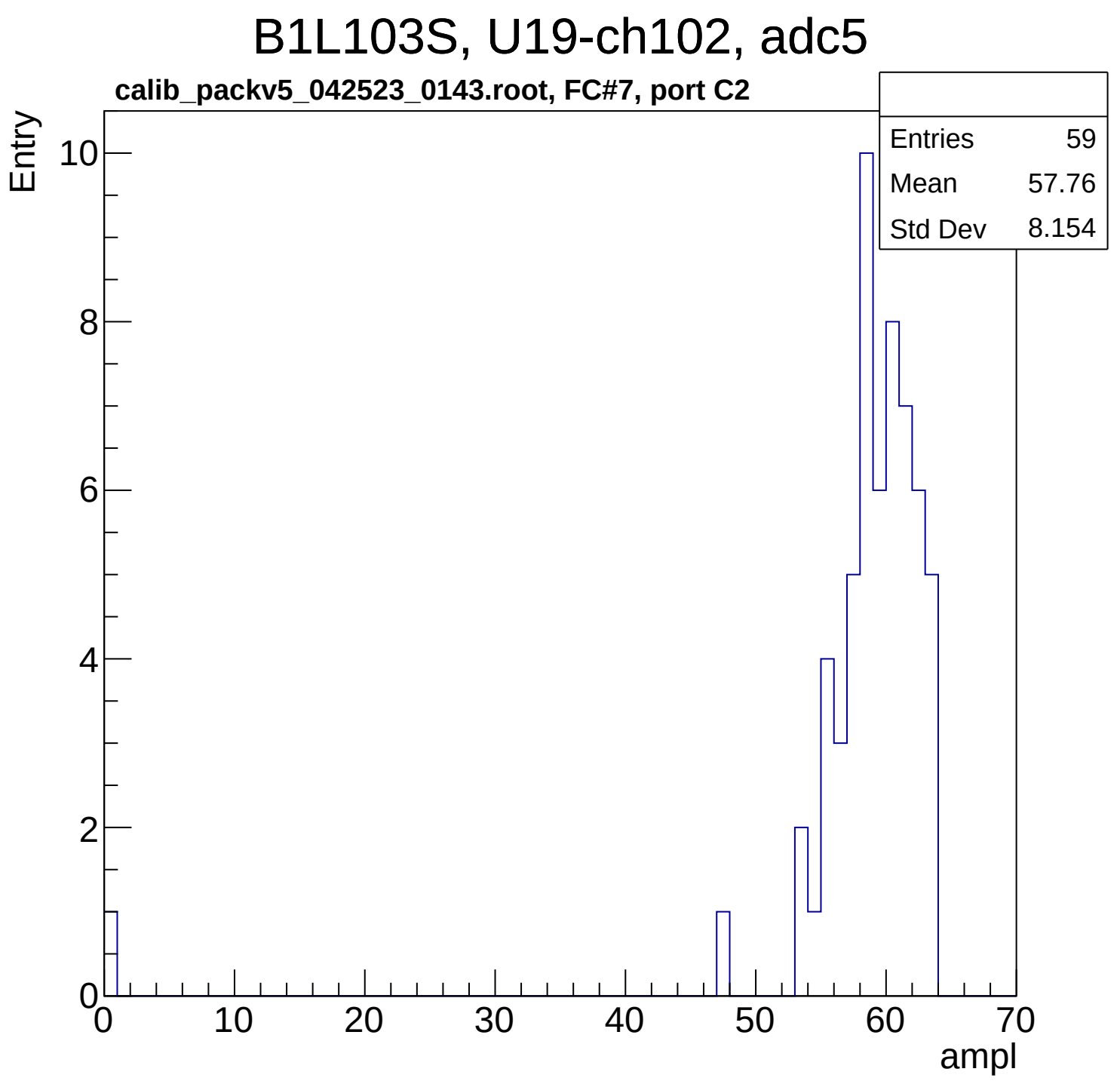
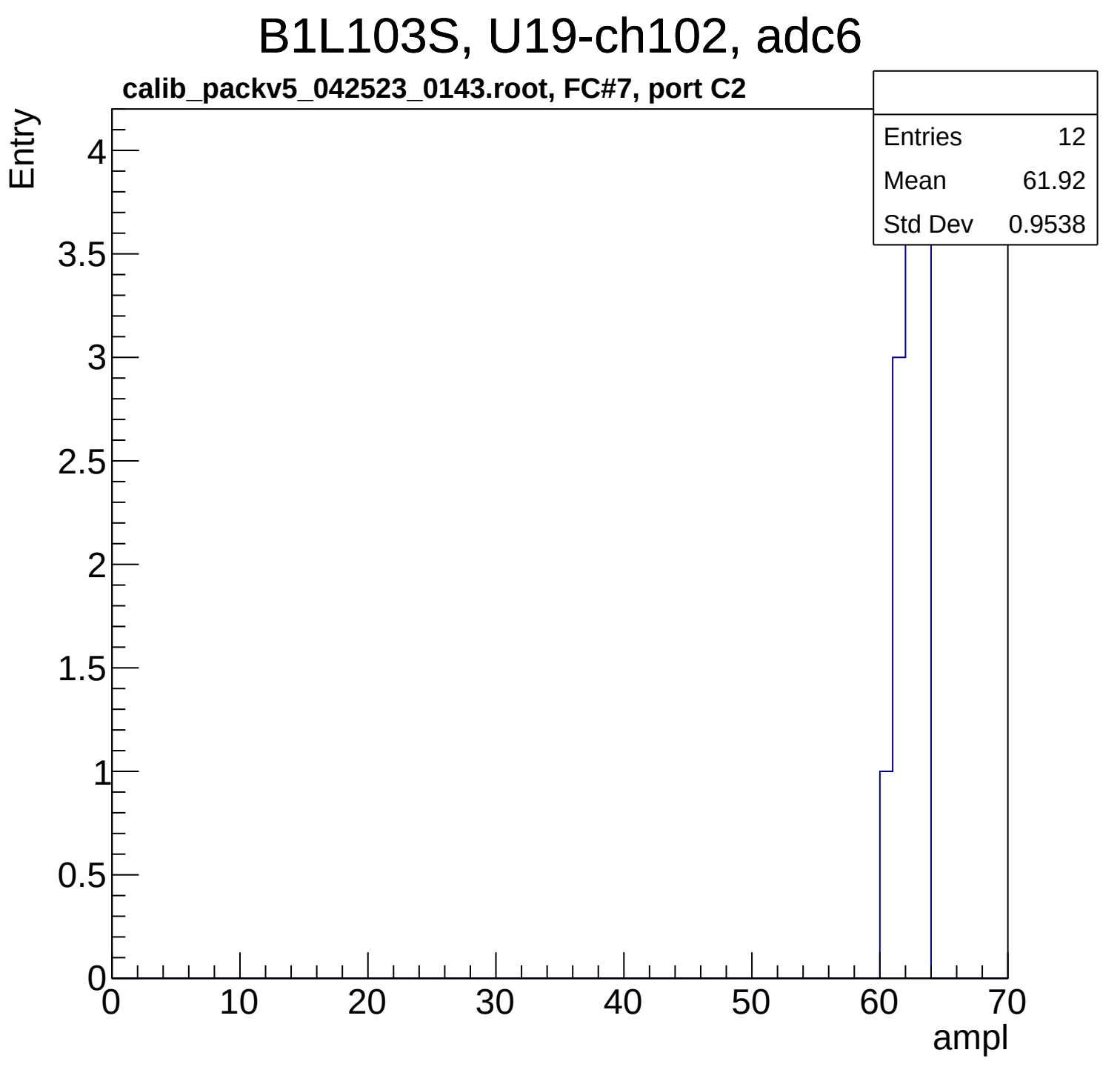

B1L103S, U19-ch102, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 57.76
Std Dev 8.154
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch102, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.92
Std Dev 0.9538
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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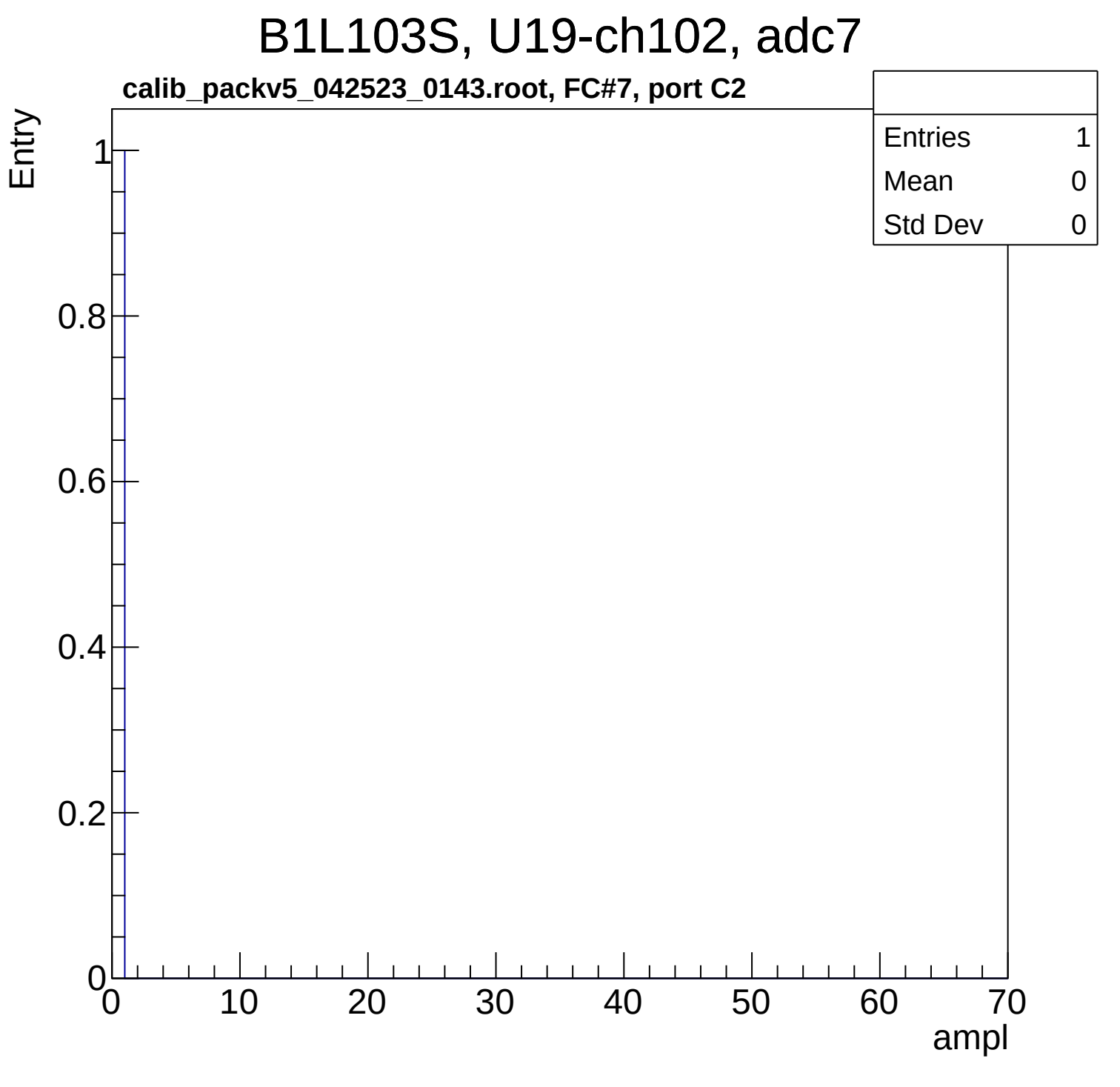
B1L103S, U19-ch102, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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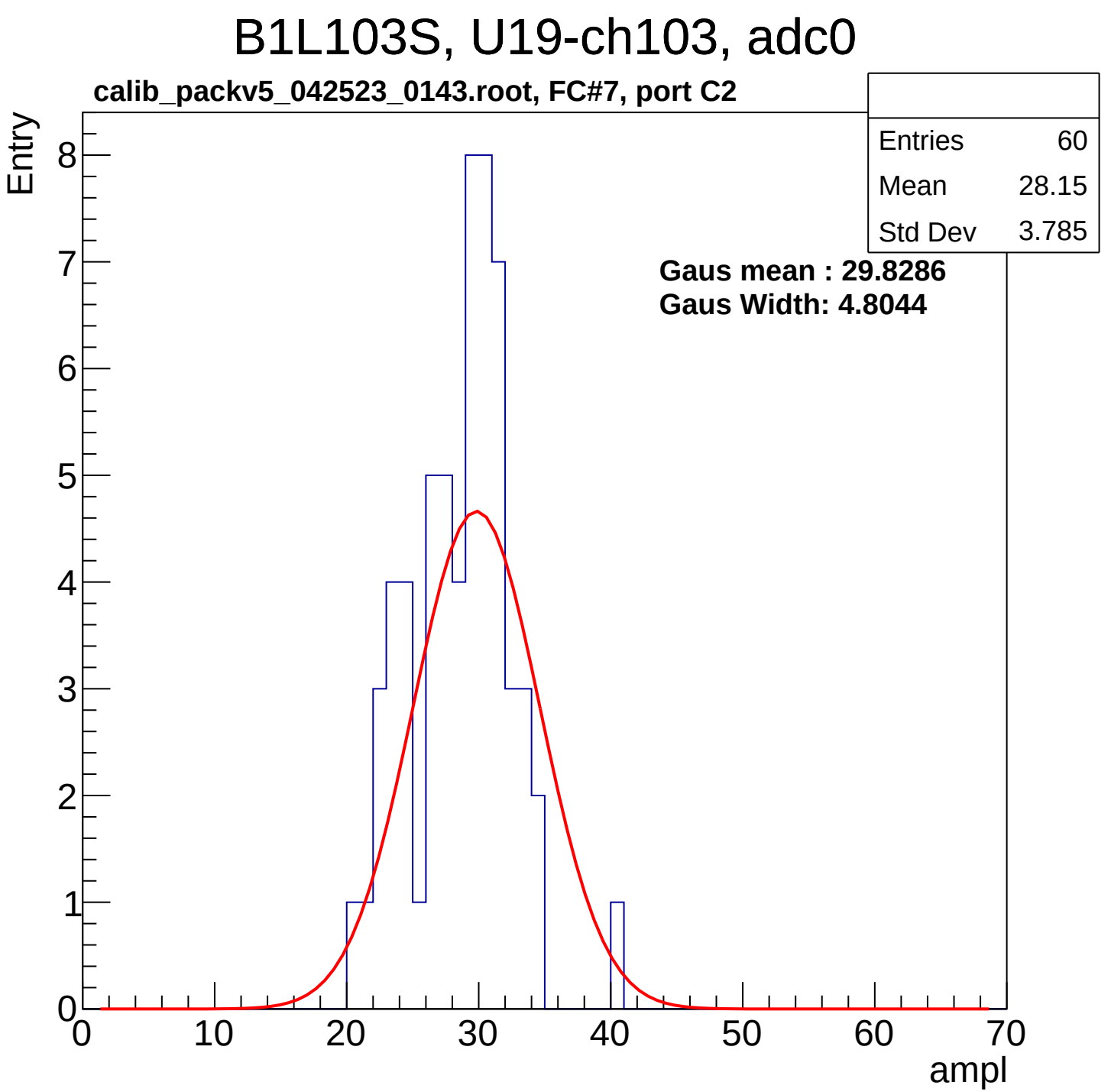
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch103, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 28.15
Std Dev 3.785
Gaus mean : 29.8286
Gaus Width: 4.8044
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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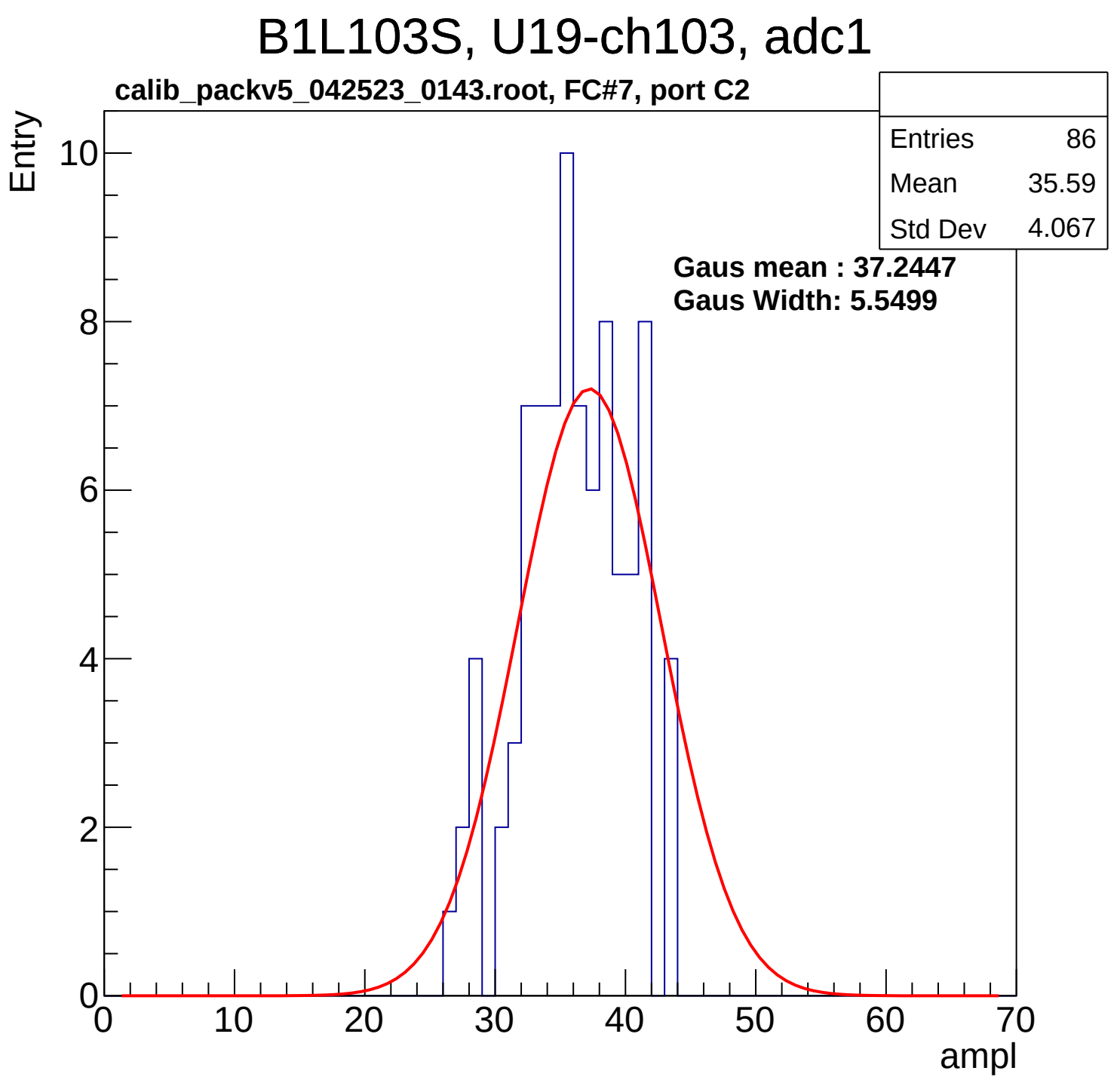
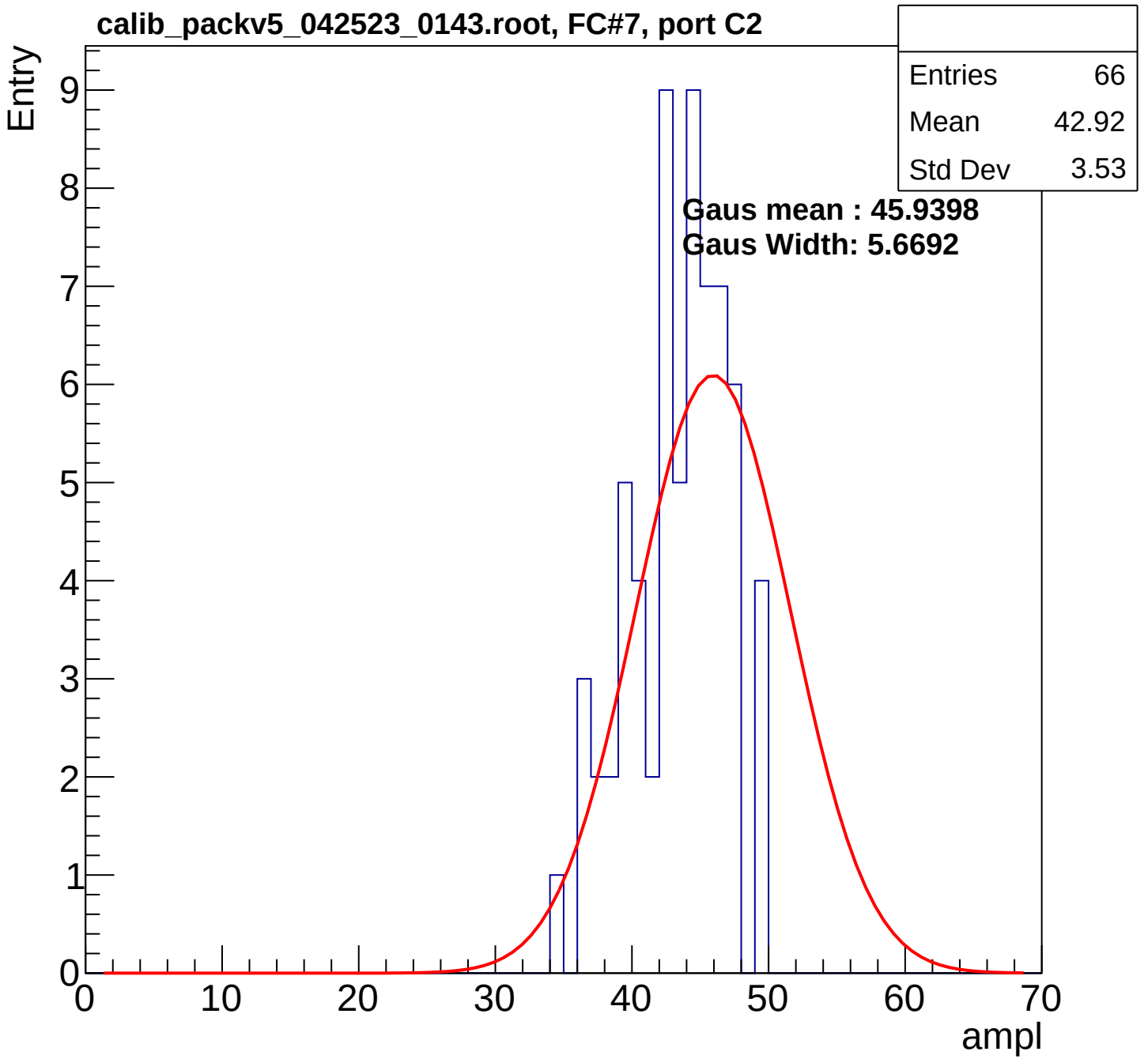
60
70


B1L103S, U19-ch103, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 35.59
Std Dev 4.067
Gaus mean : 37.2447
Gaus Width: 5.5499
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch103, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 42.92
Std Dev 3.53
Gaus mean : 45.9398
Gaus Width: 5.6692
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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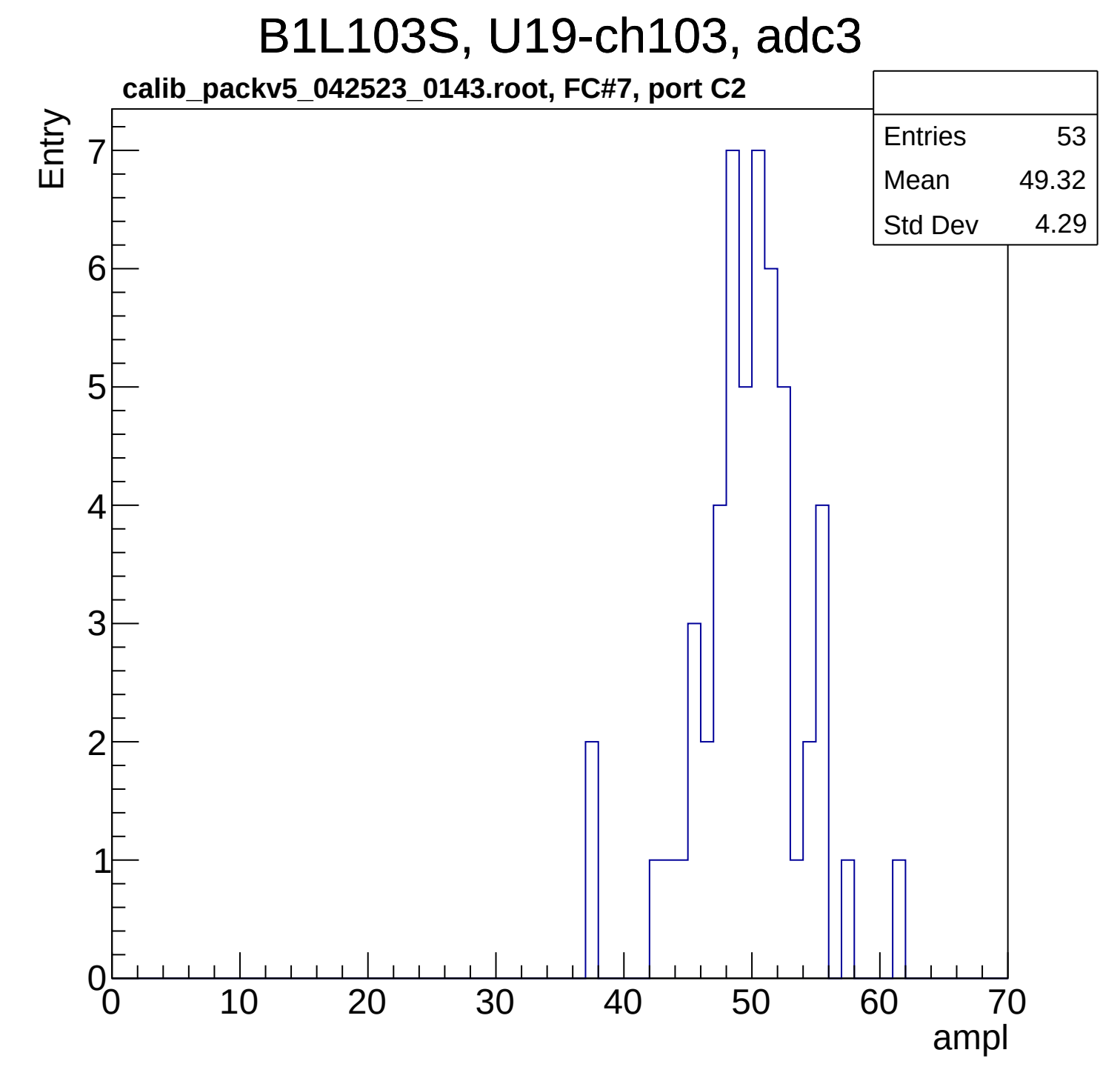
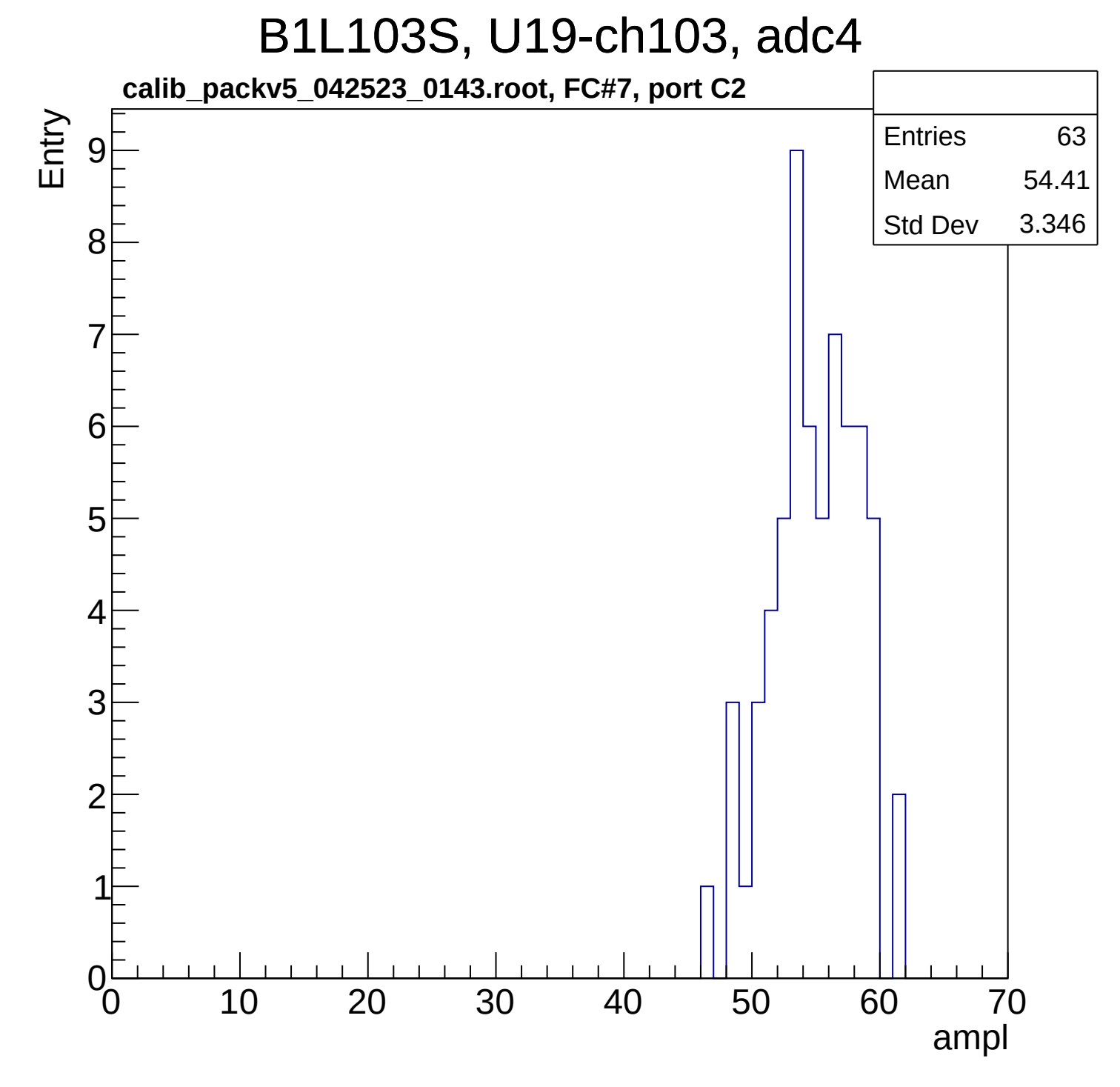

B1L103S, U19-ch103, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 49.32
Std Dev 4.29
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch103, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.41
Std Dev 3.346
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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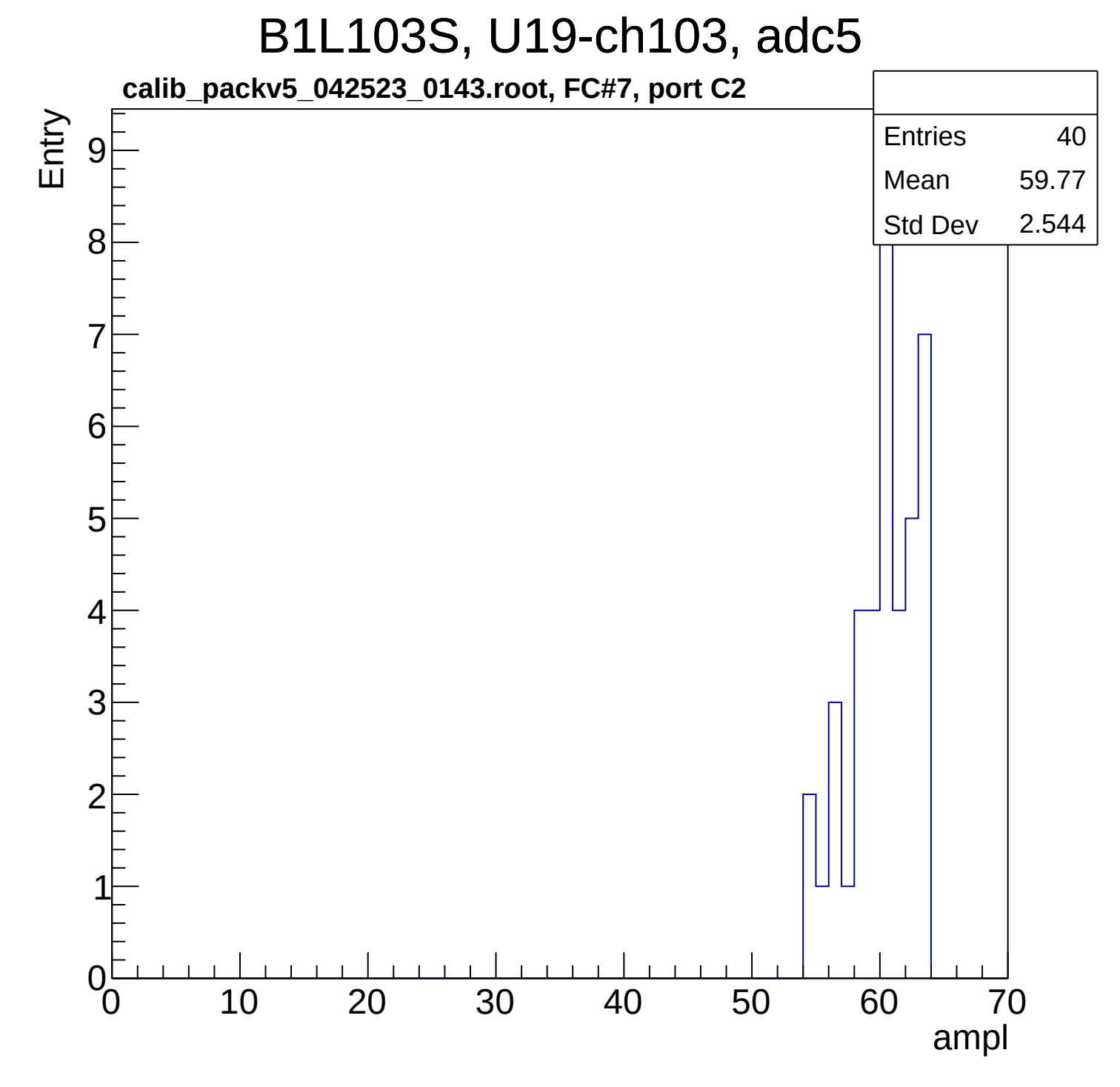
B1L103S, U19-ch103, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 59.77
Std Dev 2.544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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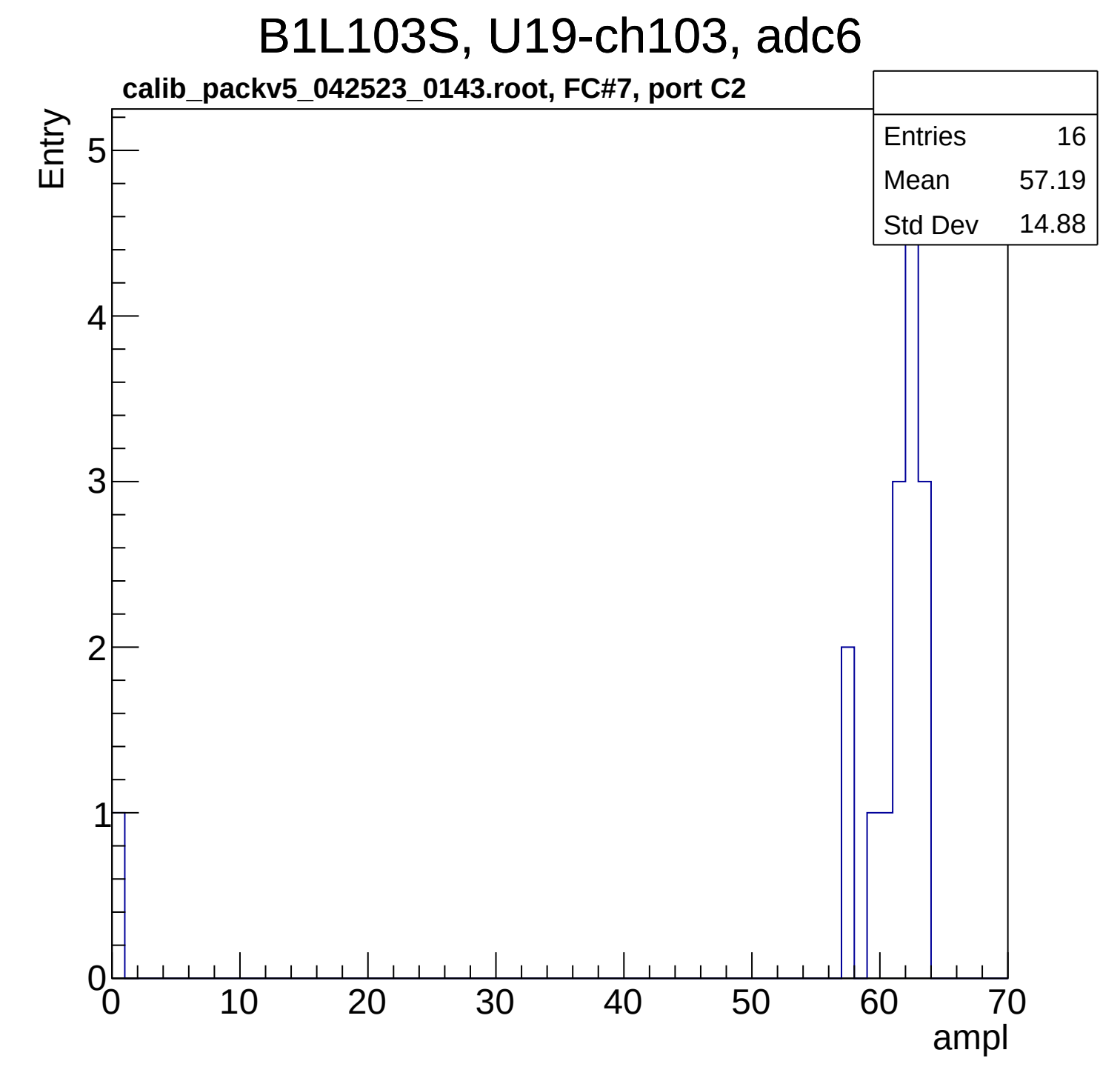
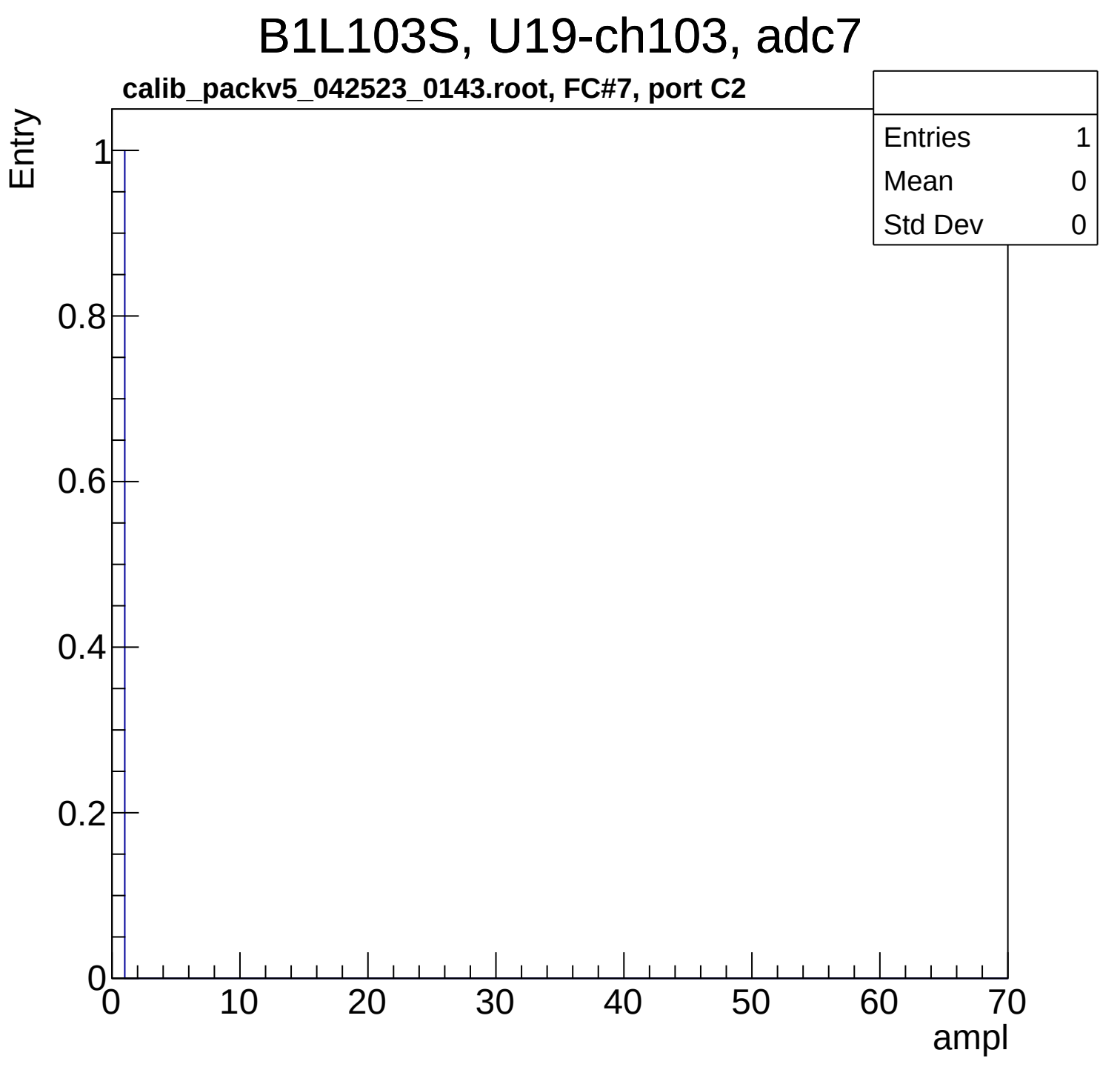

B1L103S, U19-ch103, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 57.19
Std Dev 14.88
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch103, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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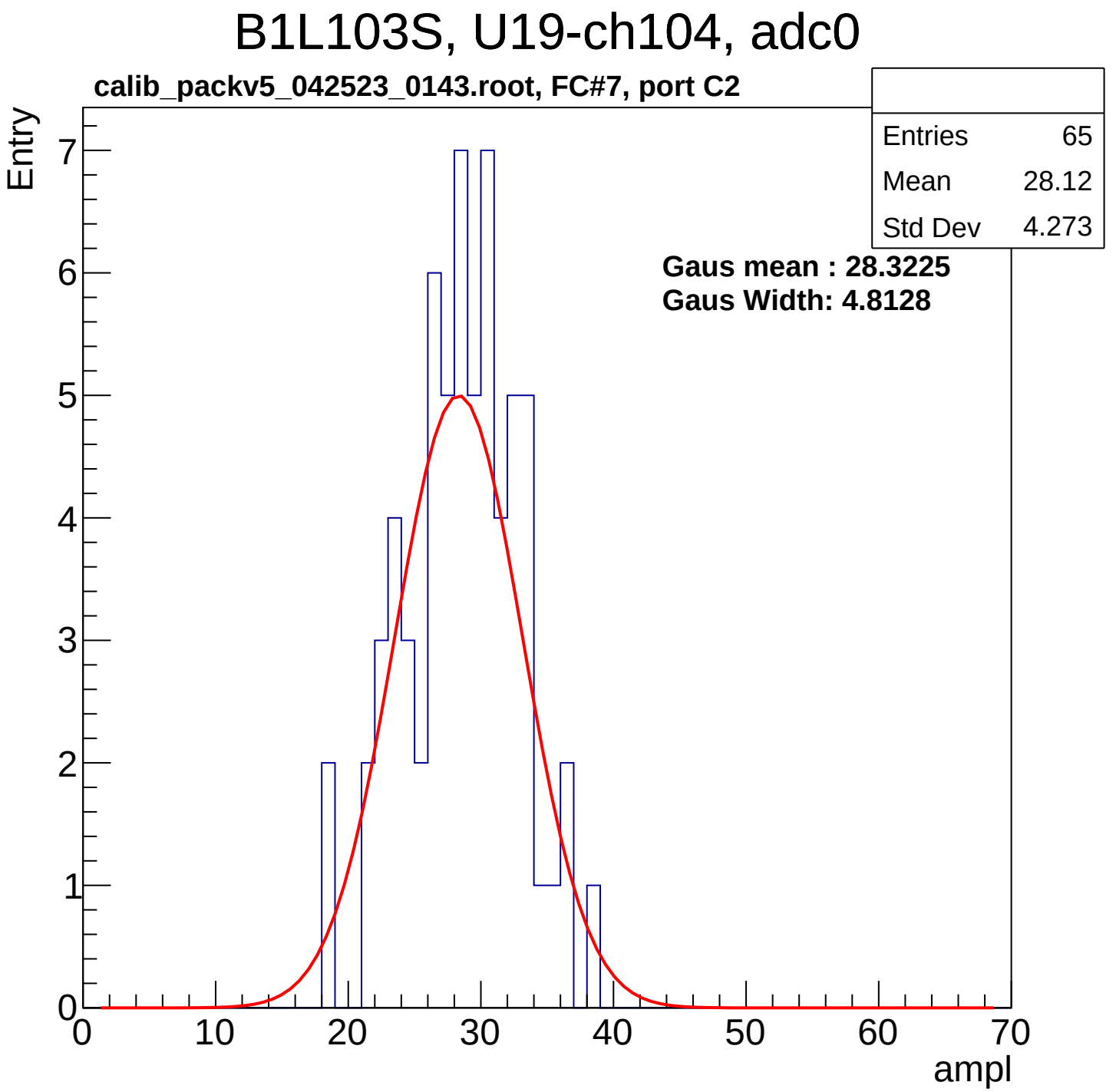
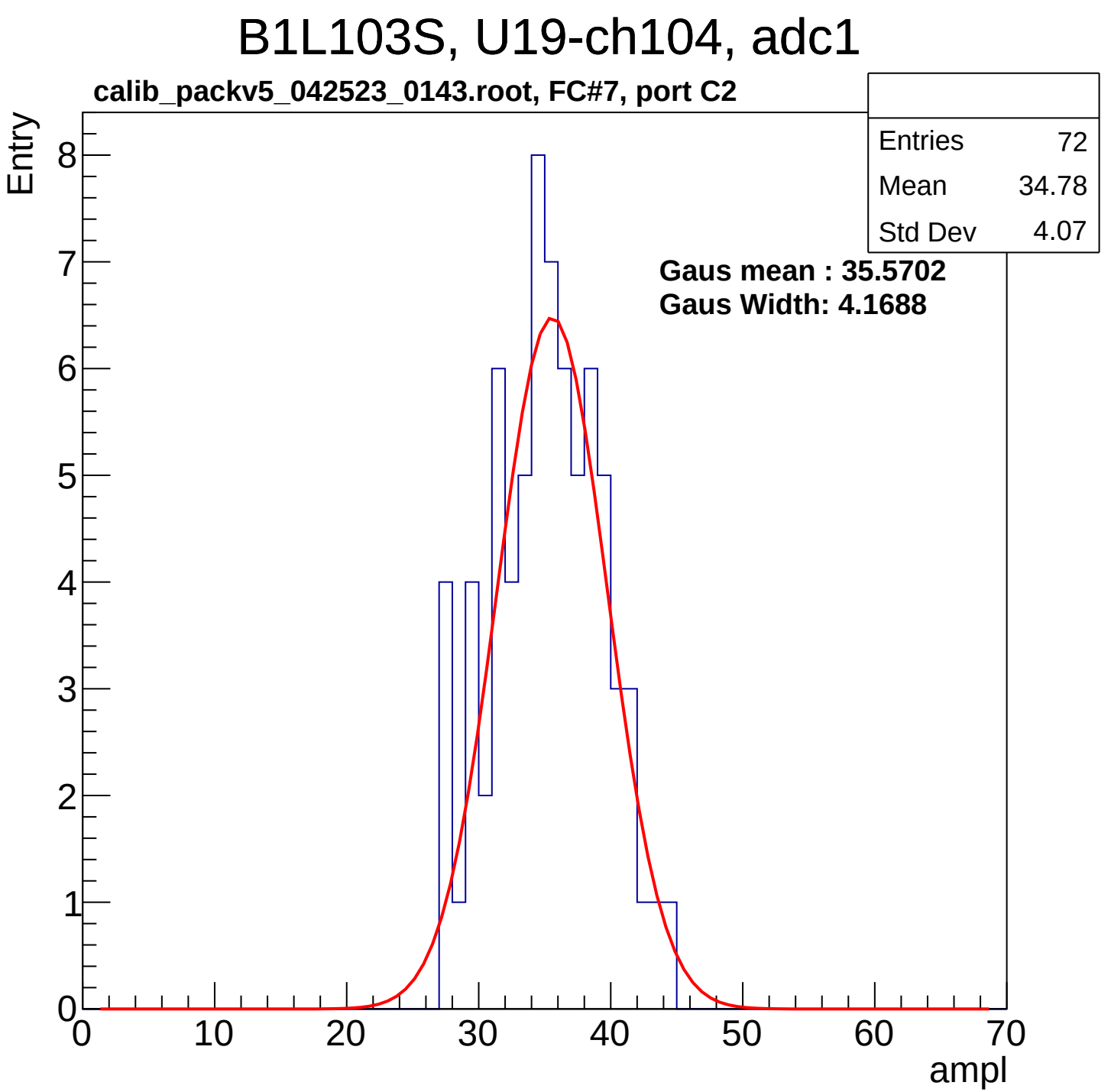
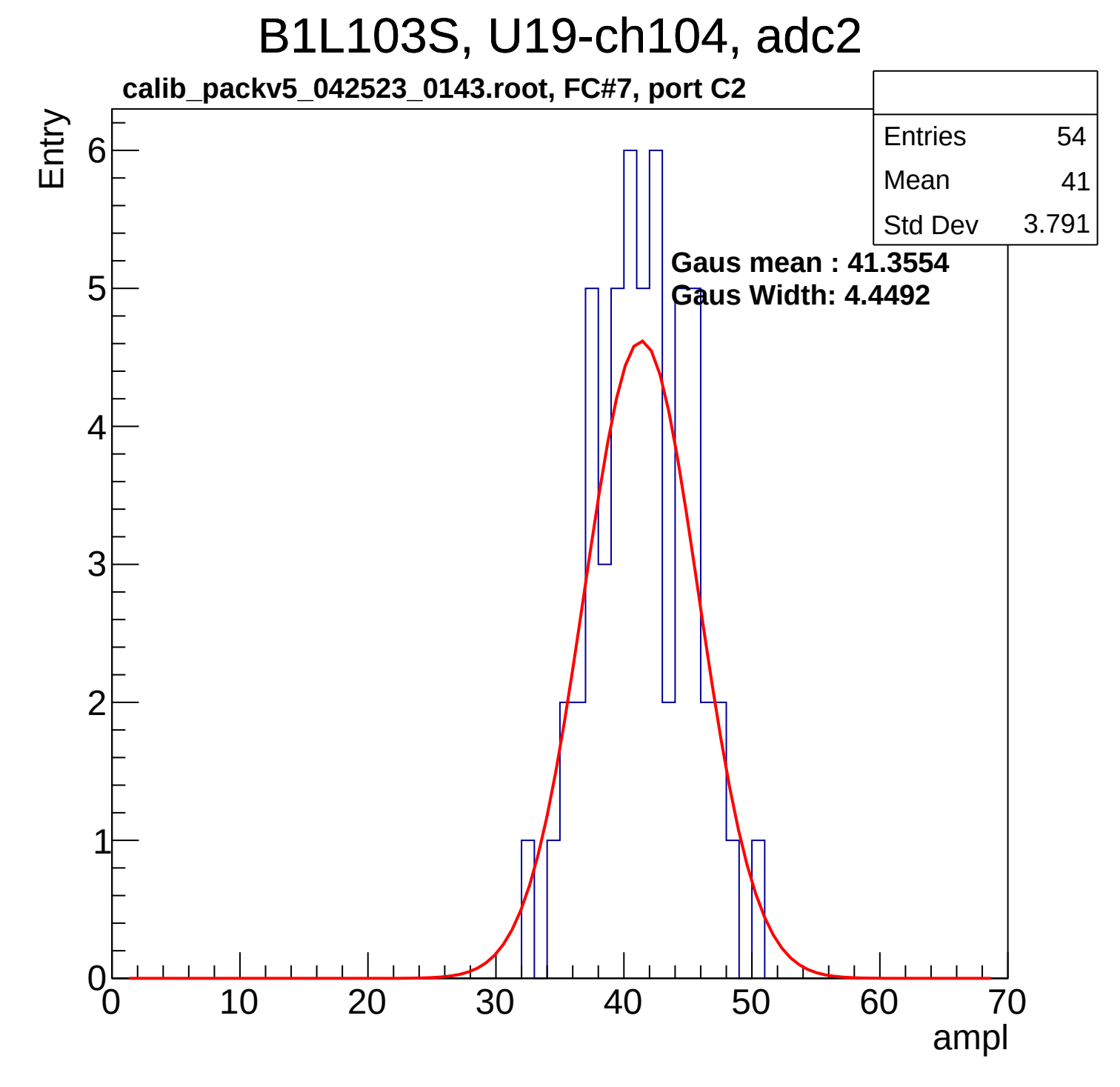
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch104, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 28.12
Std Dev 4.273
Gaus mean : 28.3225
Gaus Width: 4.8128
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch104, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 34.78
Std Dev 4.07
Gaus mean : 35.5702
Gaus Width: 4.1688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch104, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 41
Std Dev 3.791
Gaus mean : 41.3554
Gaus Width: 4.4492
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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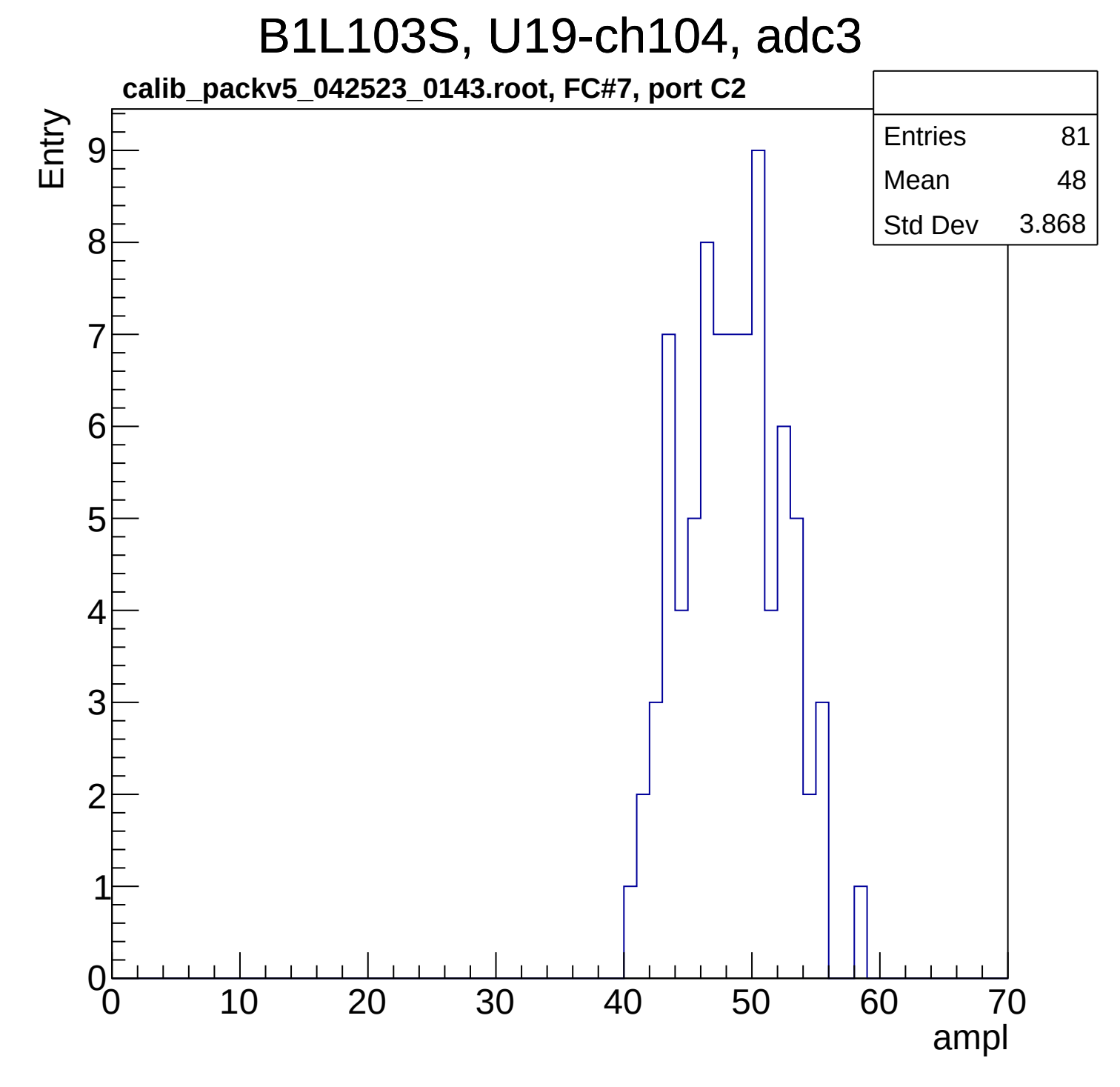
B1L103S, U19-ch104, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 48
Std Dev 3.868
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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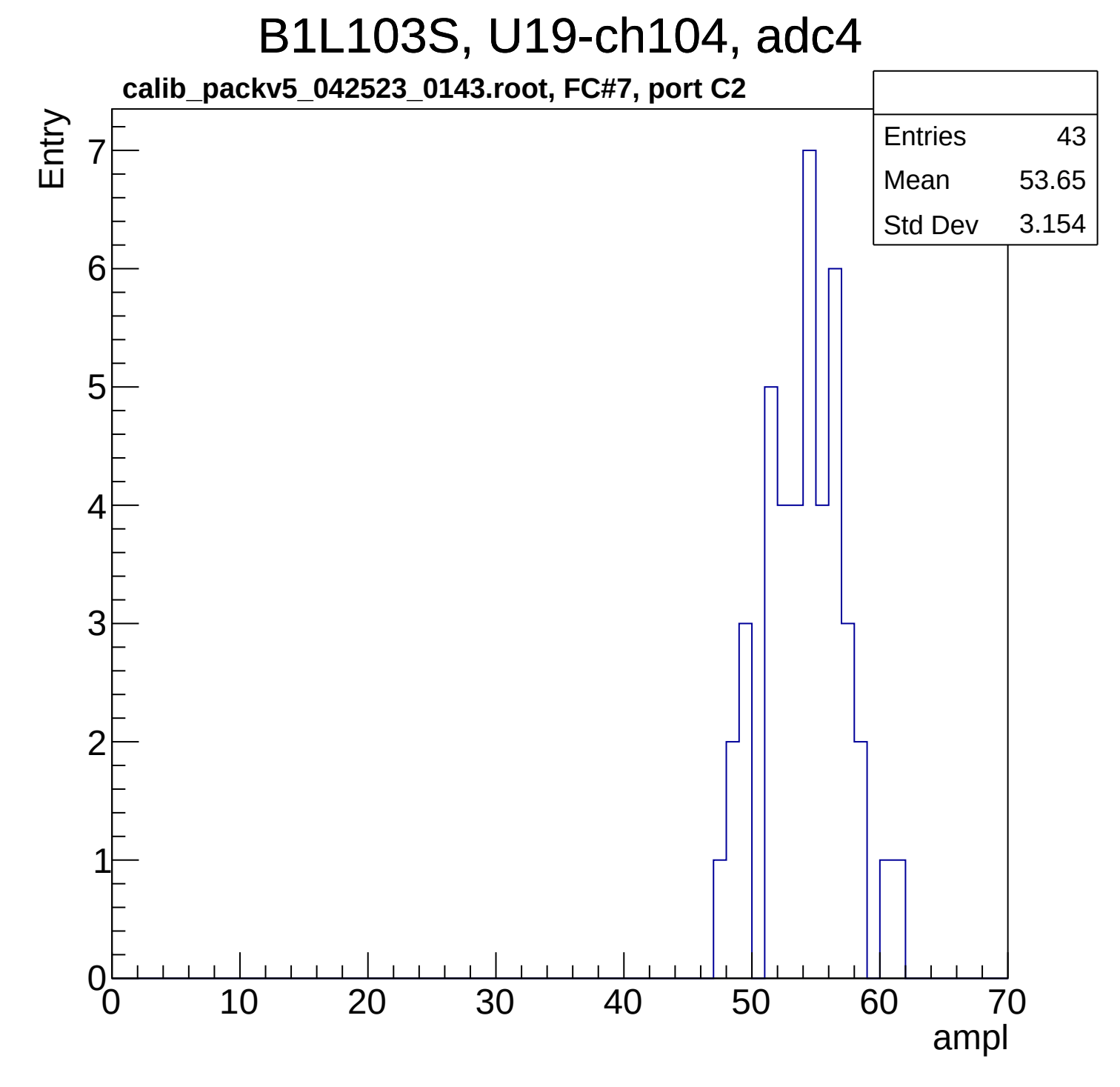
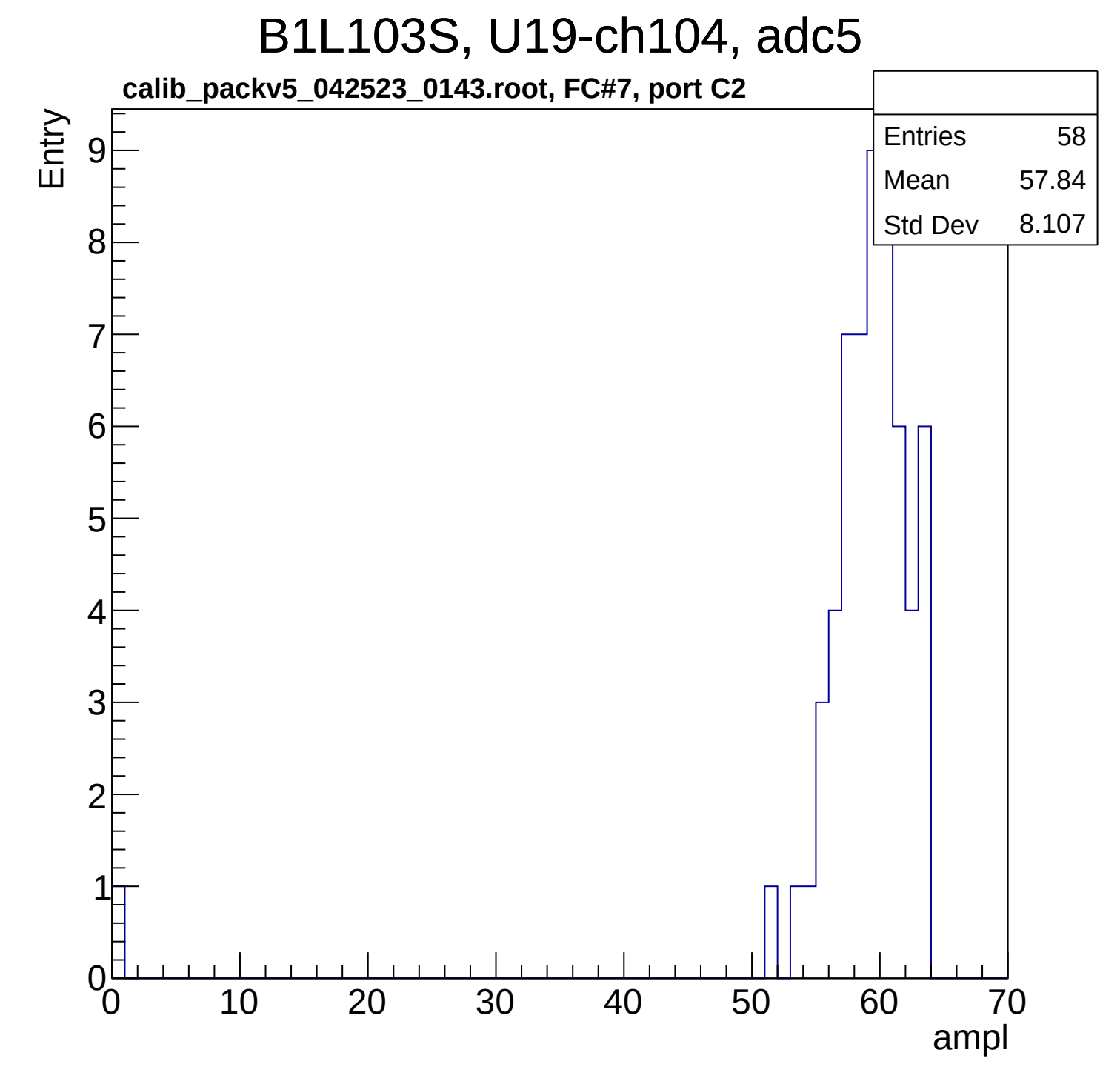

B1L103S, U19-ch104, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 53.65
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch104, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.84
Std Dev 8.107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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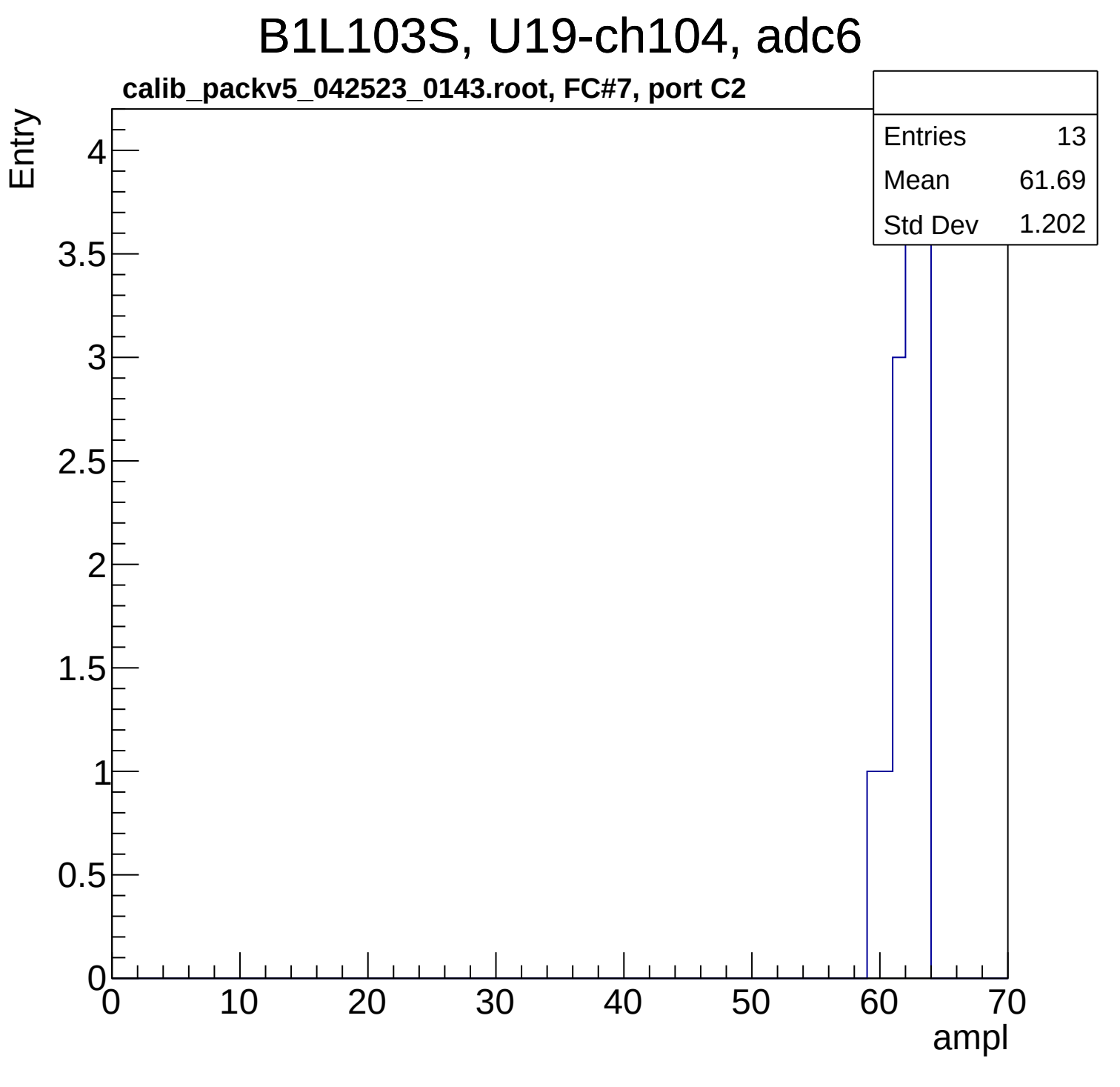
B1L103S, U19-ch104, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.69
Std Dev 1.202
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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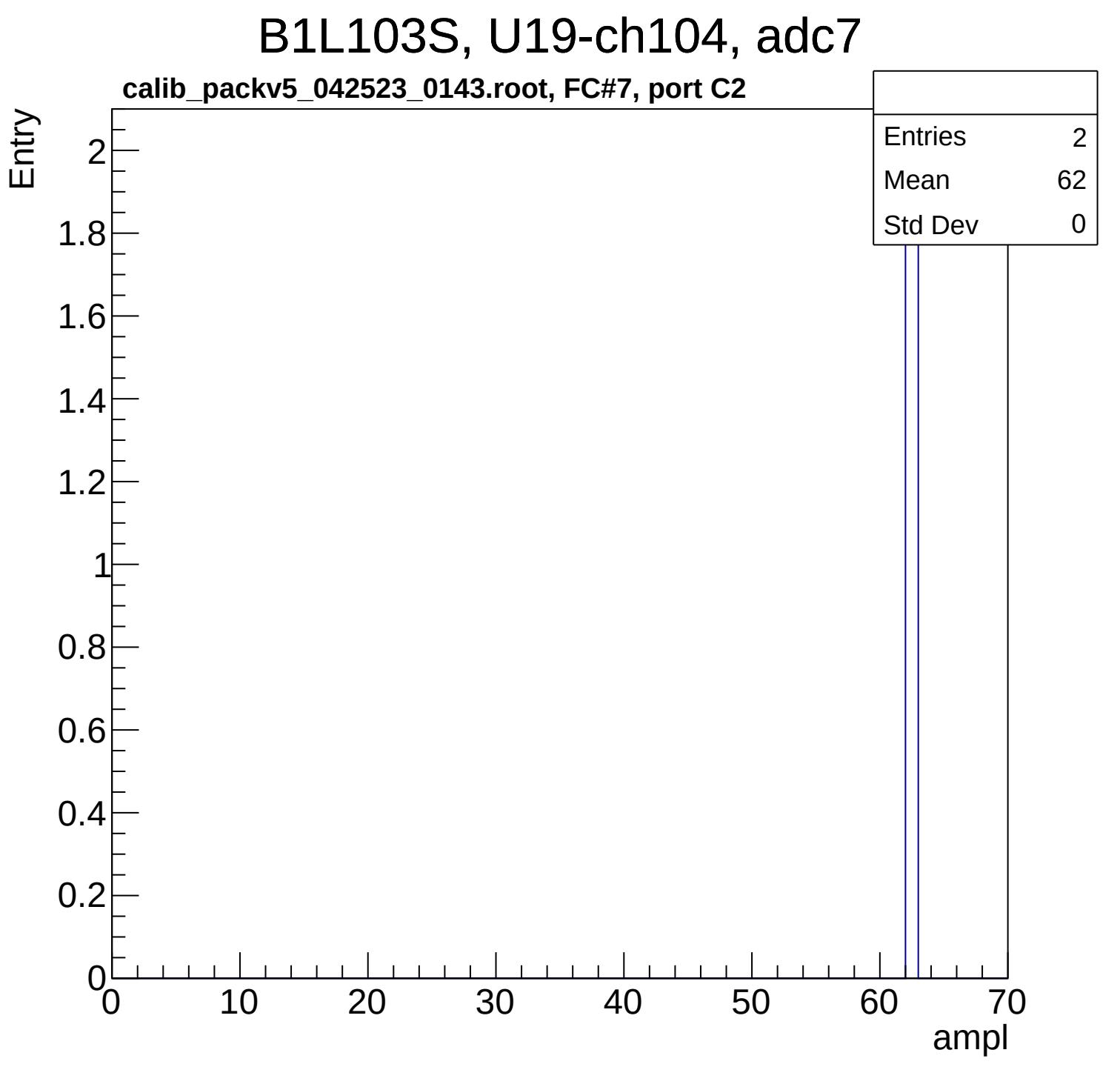
B1L103S, U19-ch104, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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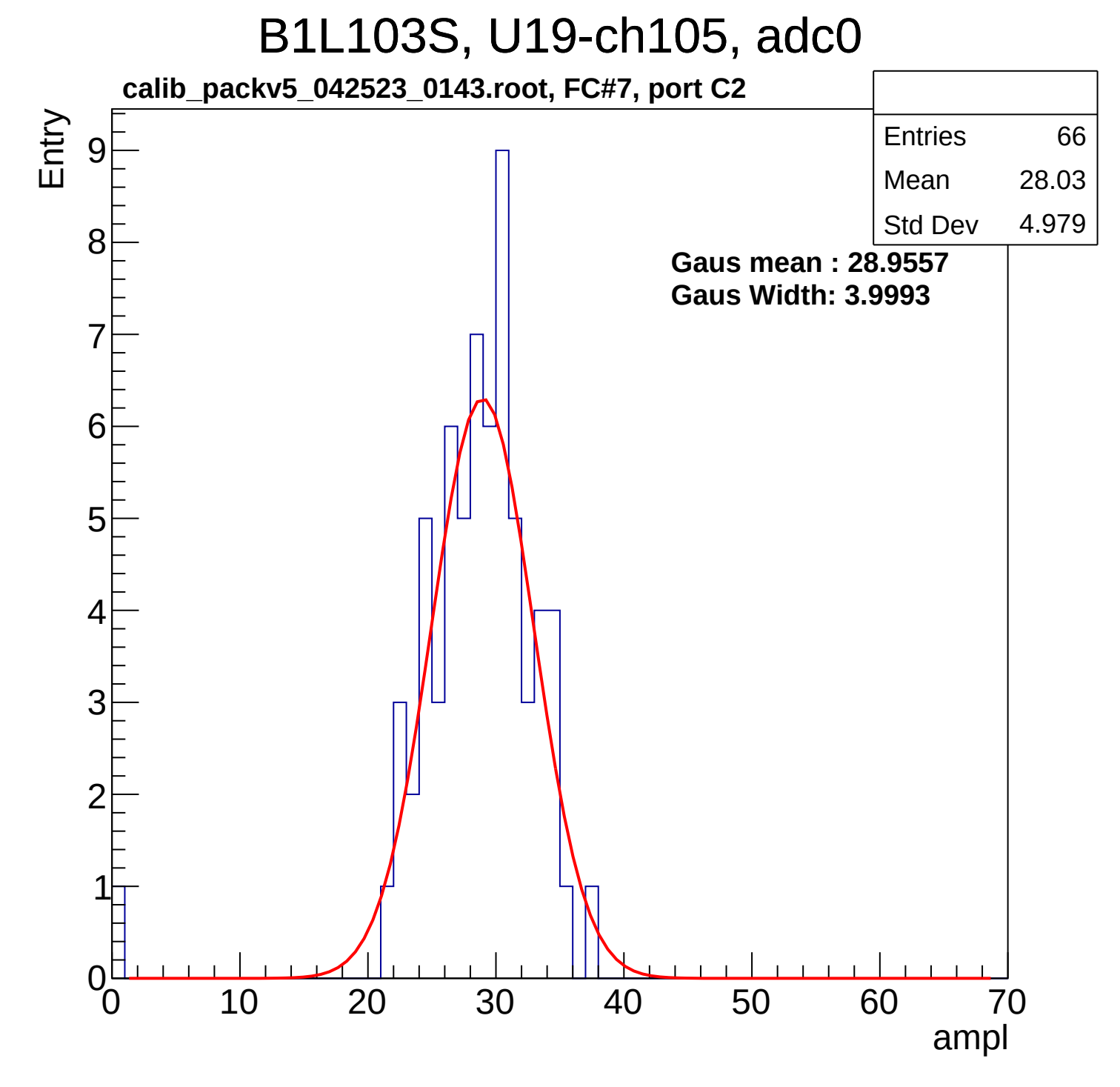
B1L103S, U19-ch105, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 28.03
Std Dev 4.979
Gaus mean : 28.9557
Gaus Width: 3.9993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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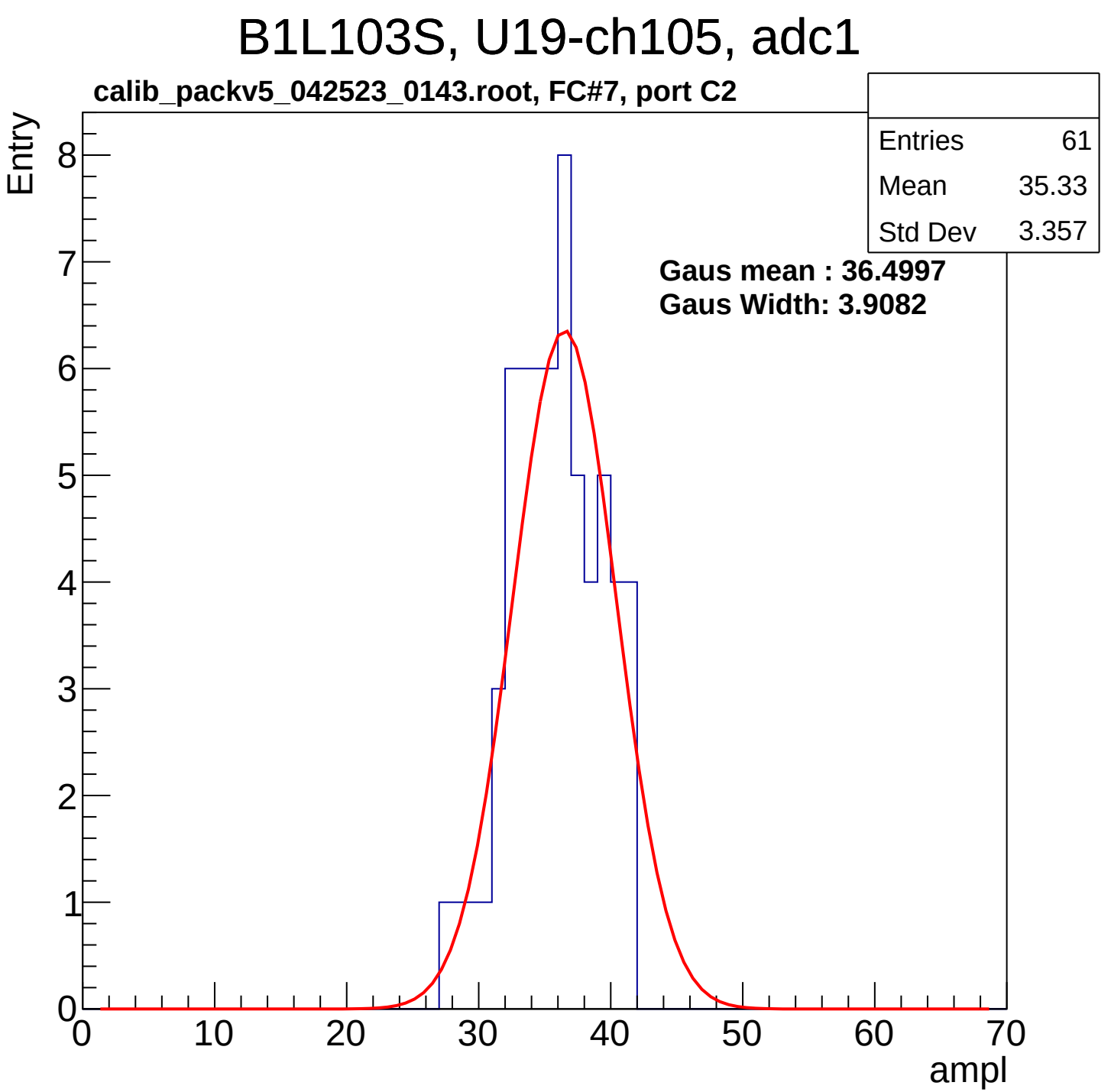
B1L103S, U19-ch105, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 35.33
Std Dev 3.357
Gaus mean : 36.4997
Gaus Width: 3.9082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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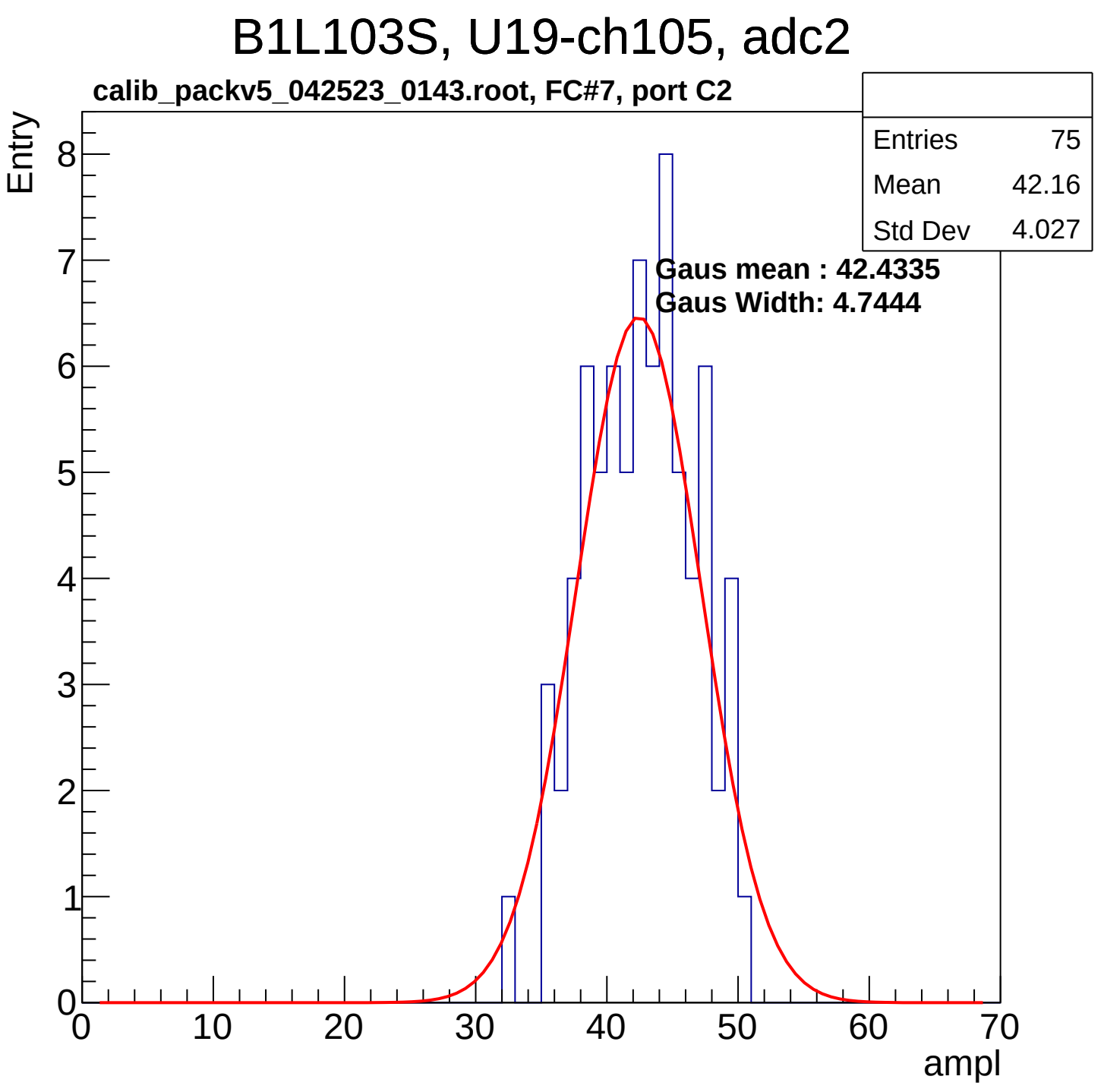
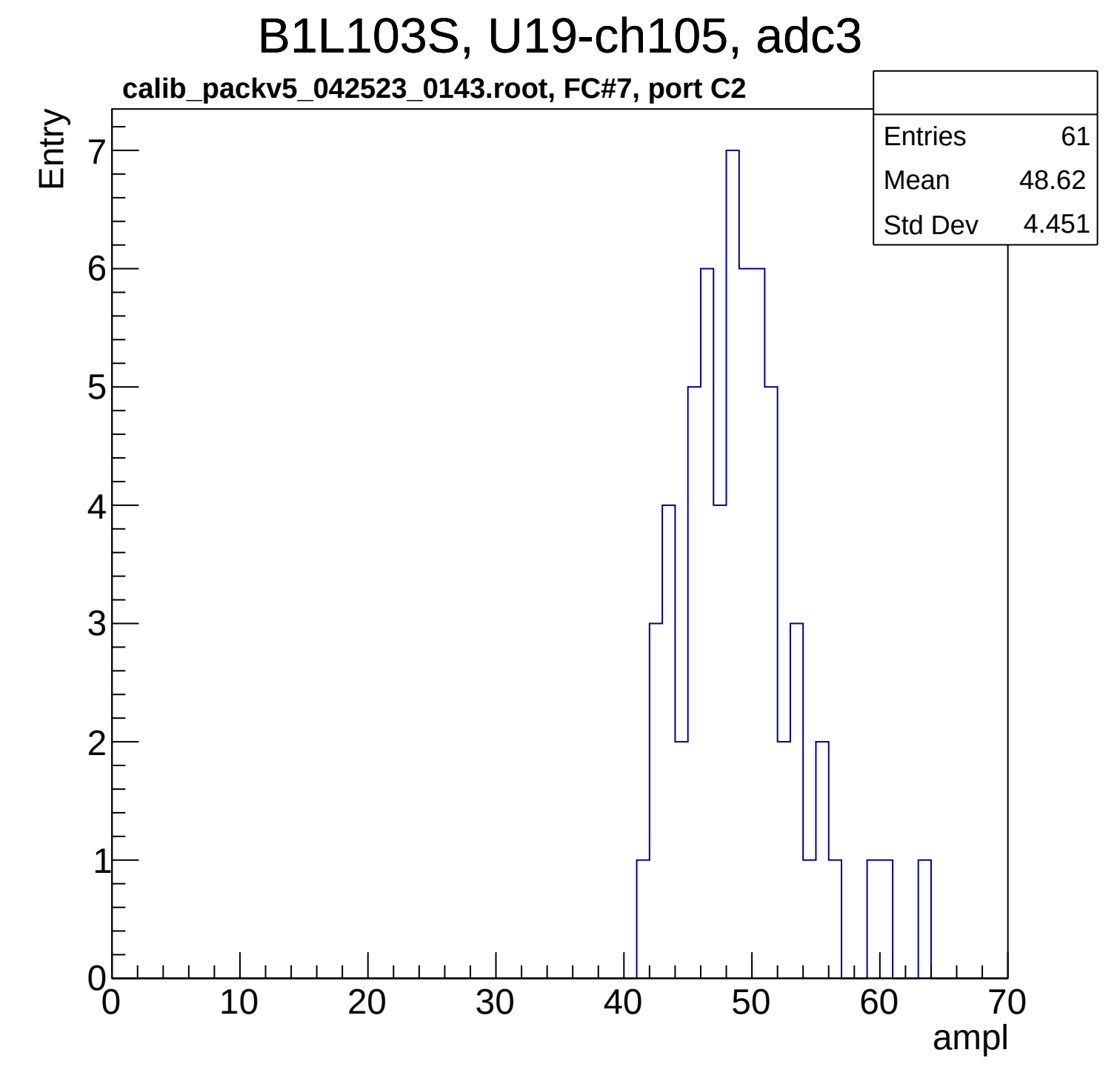
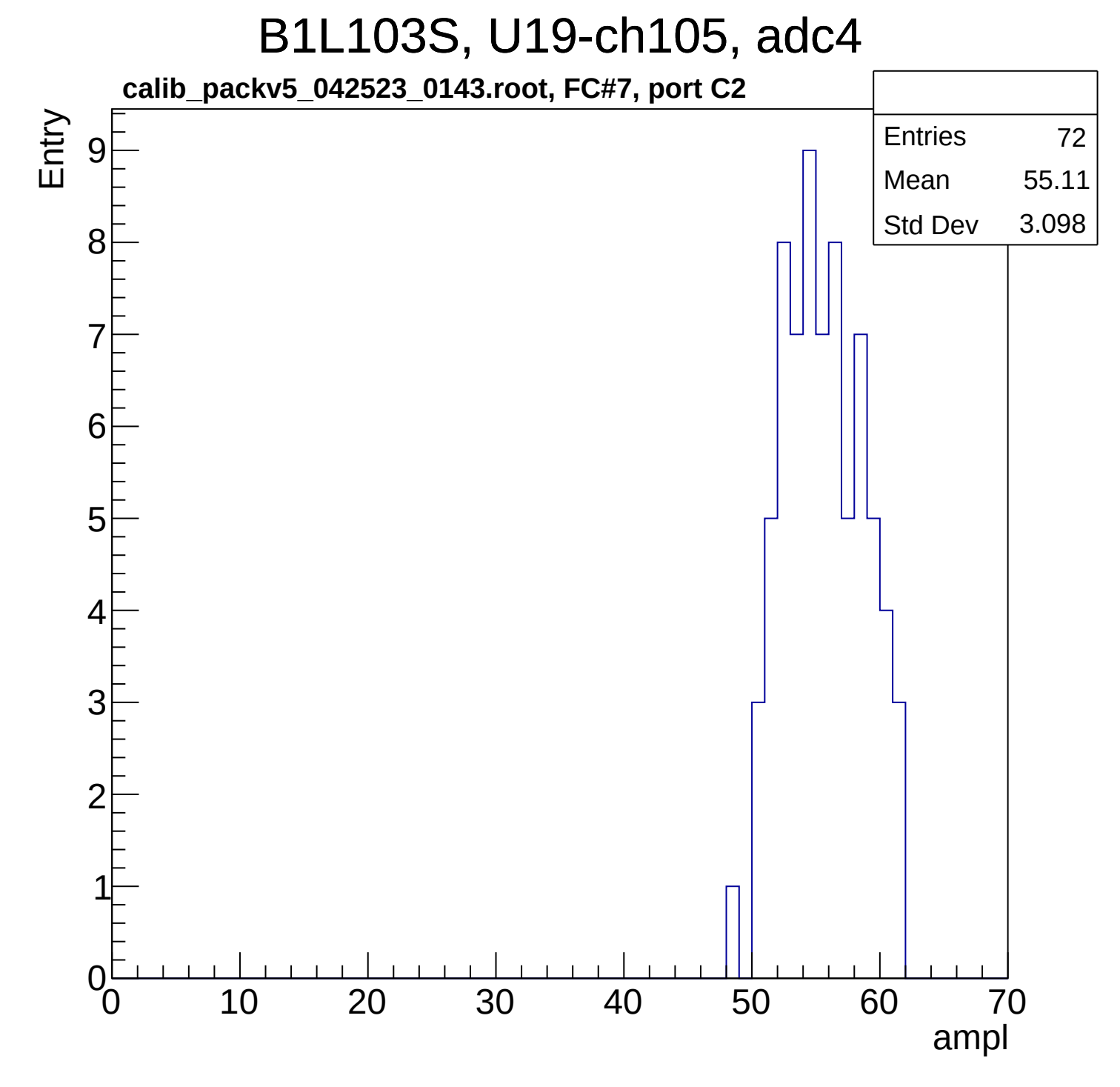
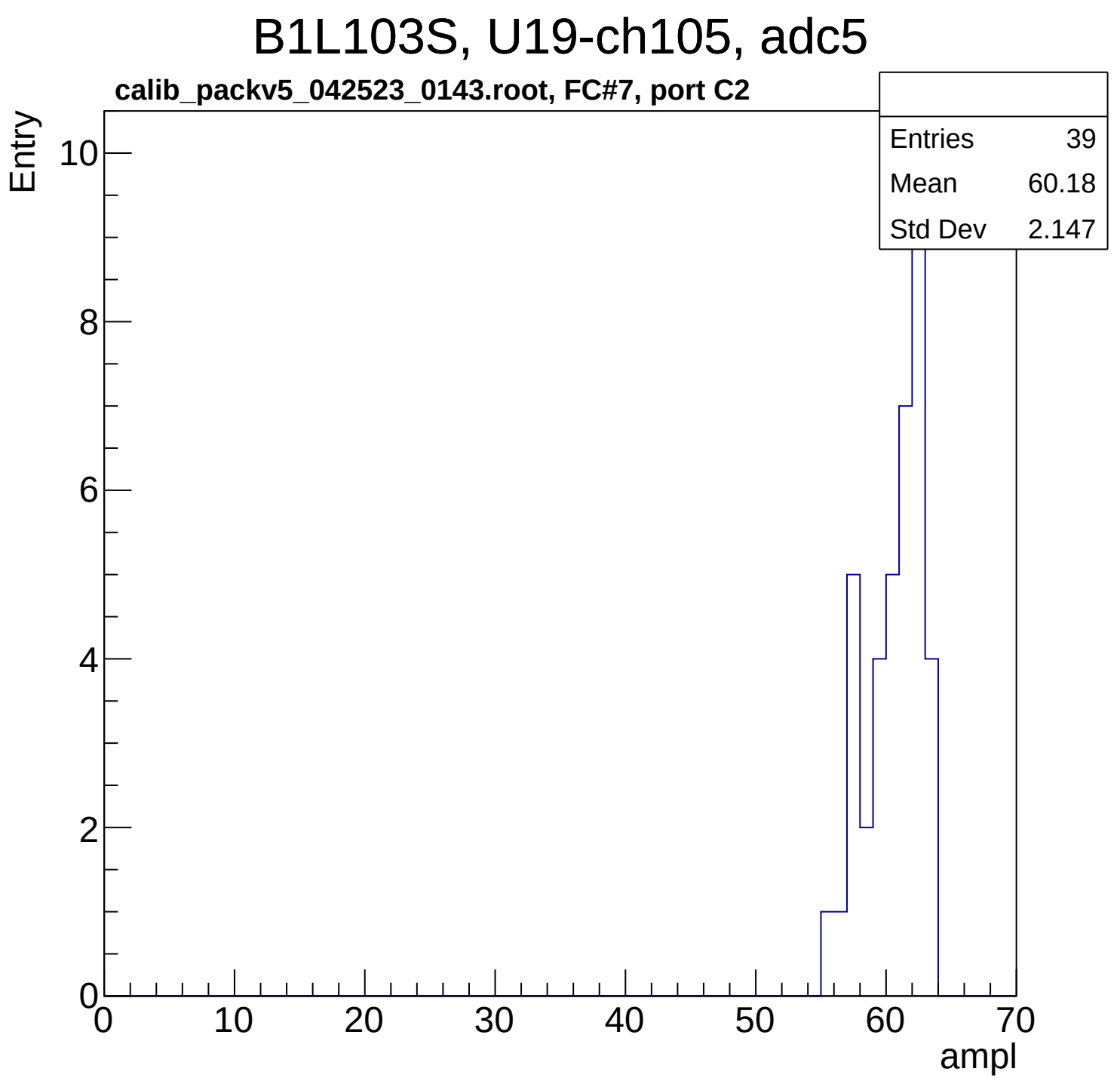
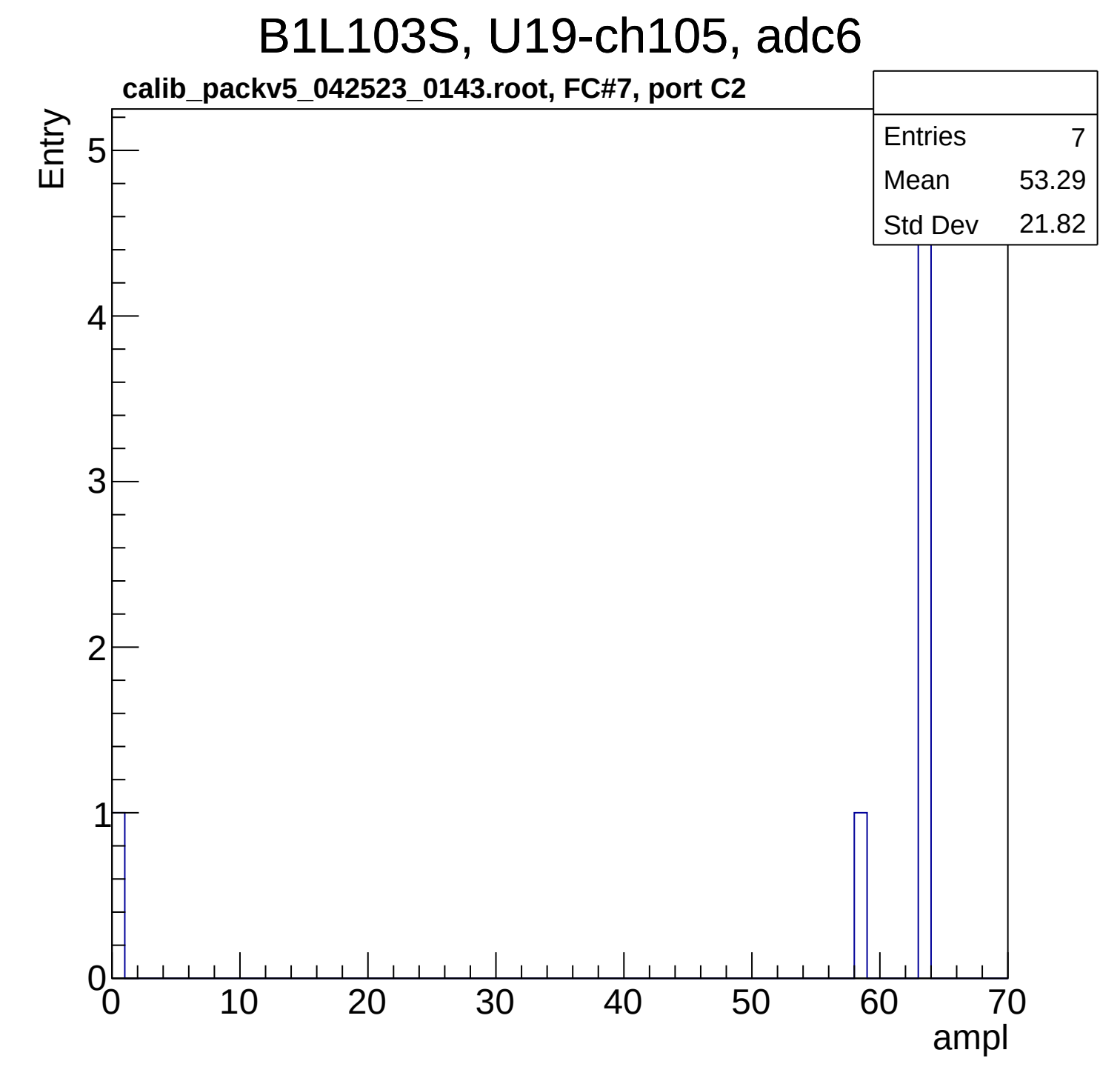
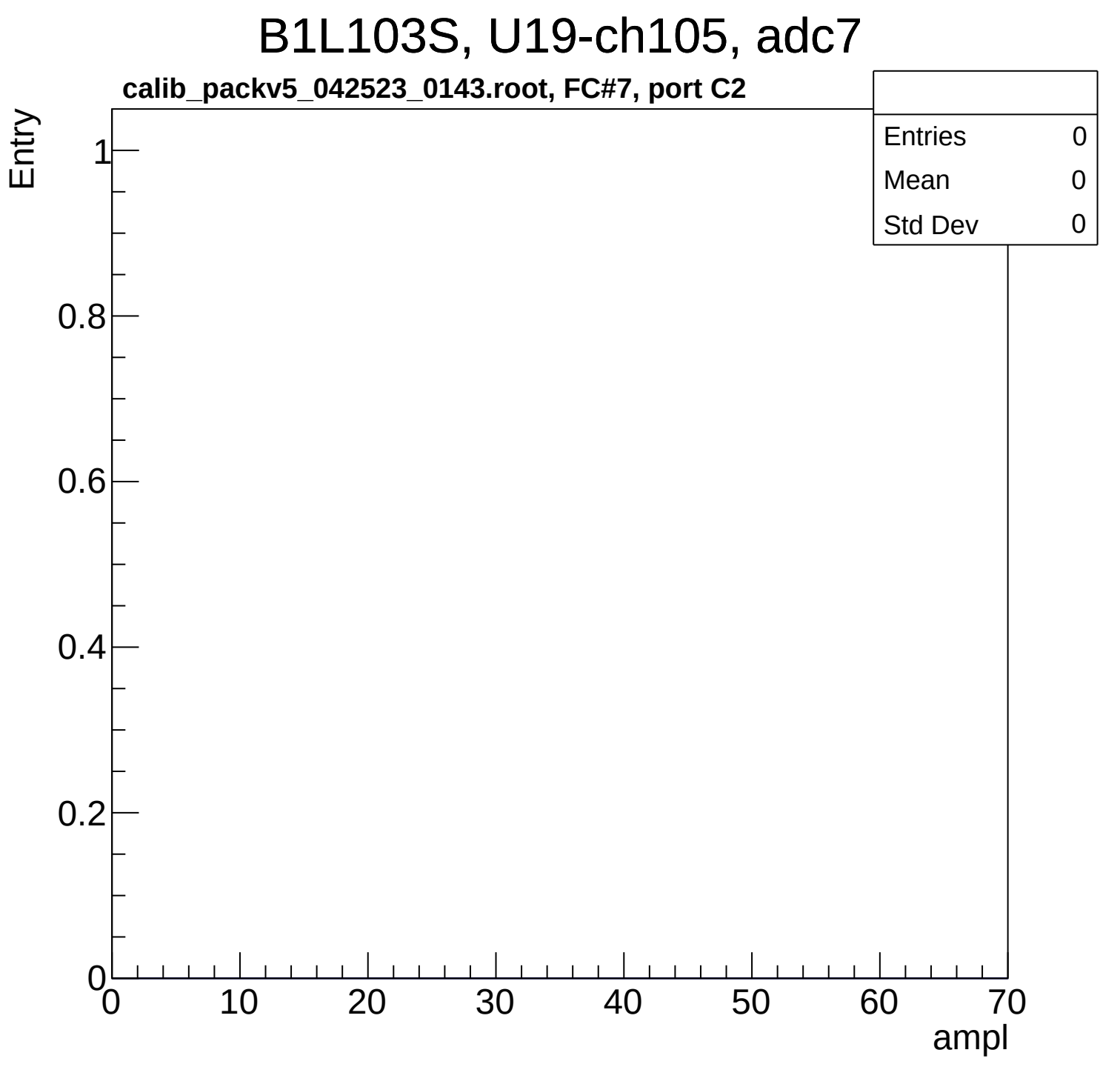

B1L103S, U19-ch105, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 42.16
Std Dev 4.027
Gaus mean : 42.4335
Gaus Width: 4.7444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

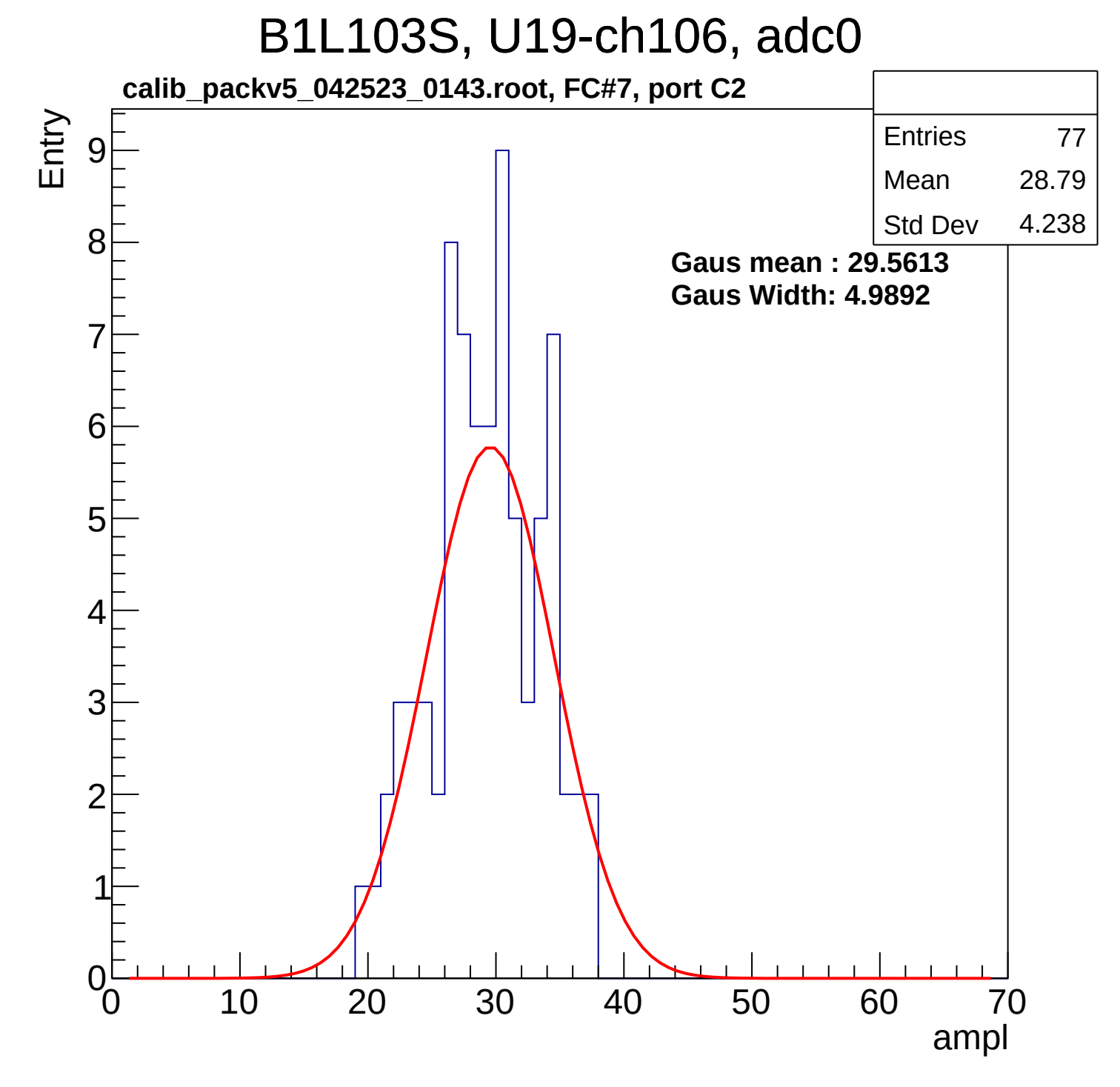

B1L103S, U19-ch105, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 48.62
Std Dev 4.451
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

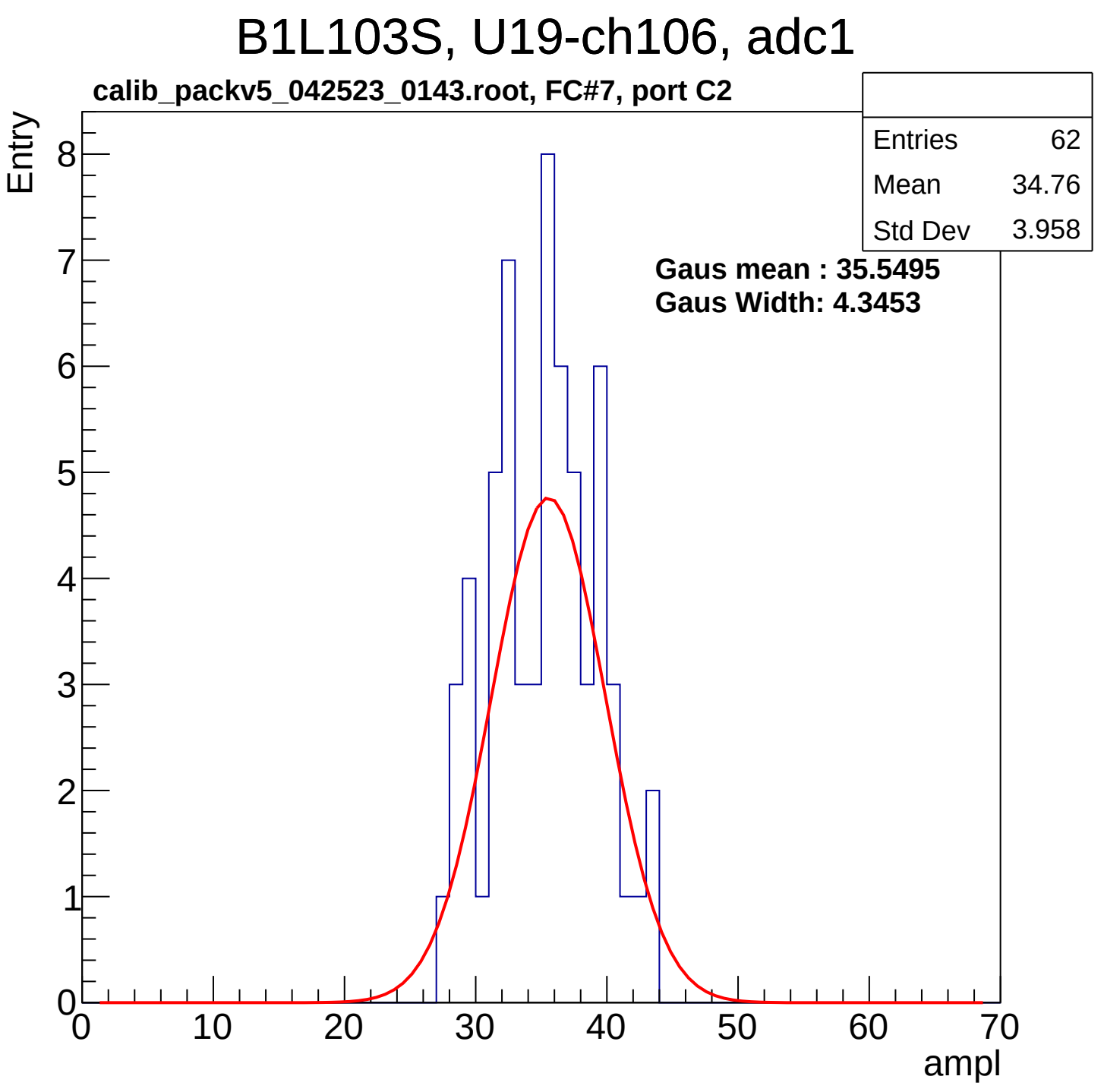

B1L103S, U19-ch105, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 55.11
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

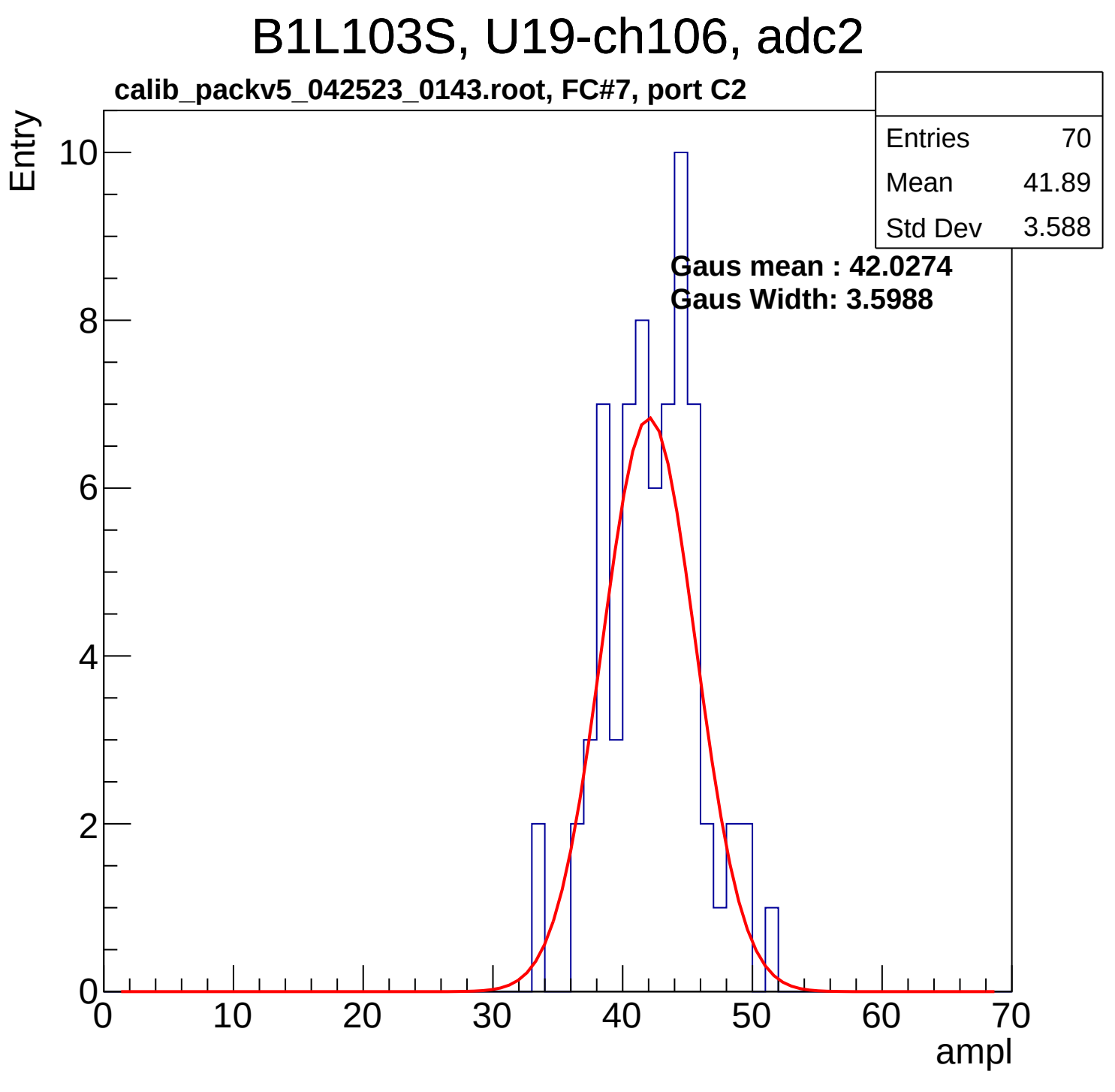

B1L103S, U19-ch105, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 60.18
Std Dev 2.147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

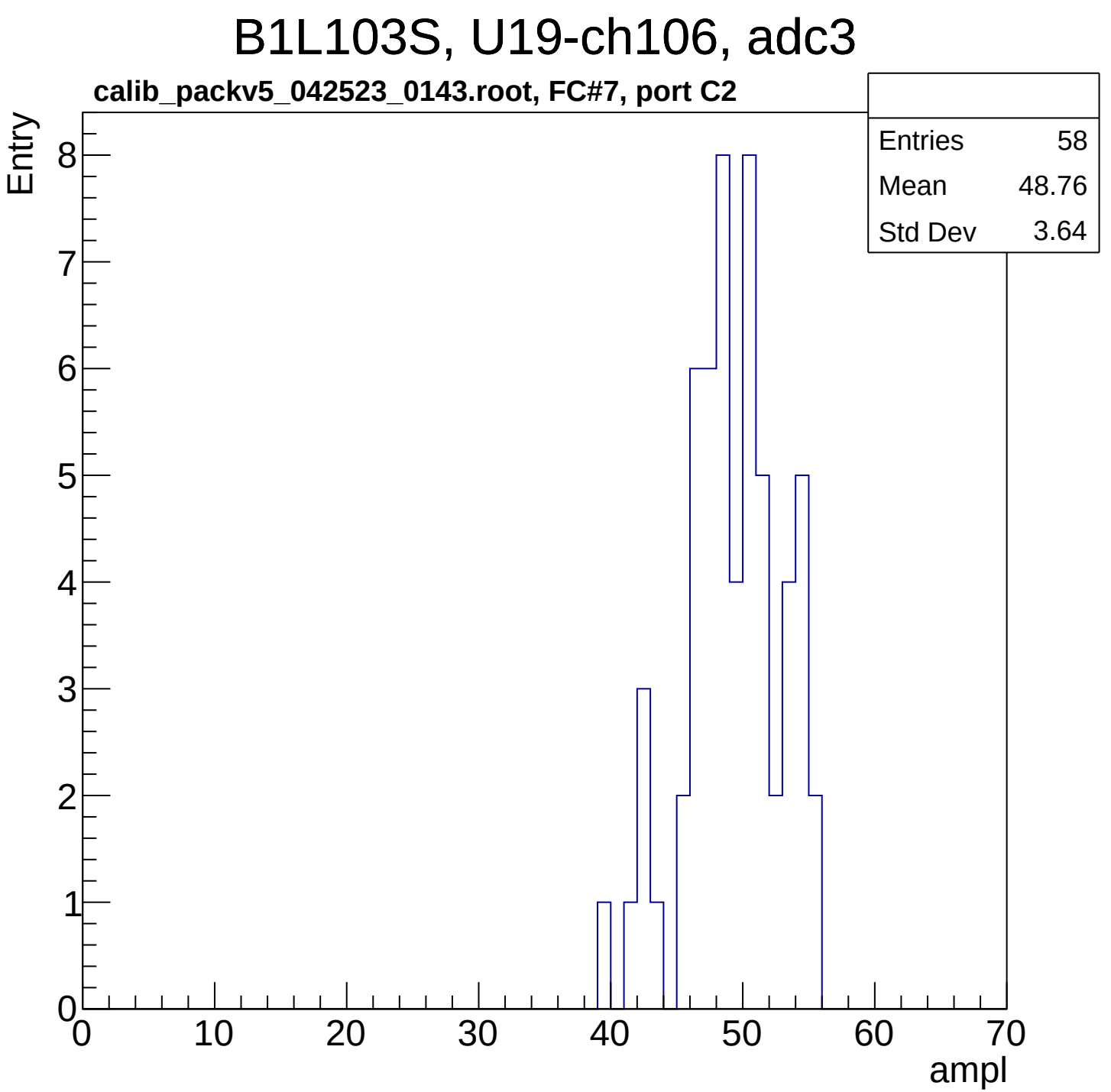

B1L103S, U19-ch105, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 53.29
Std Dev 21.82
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

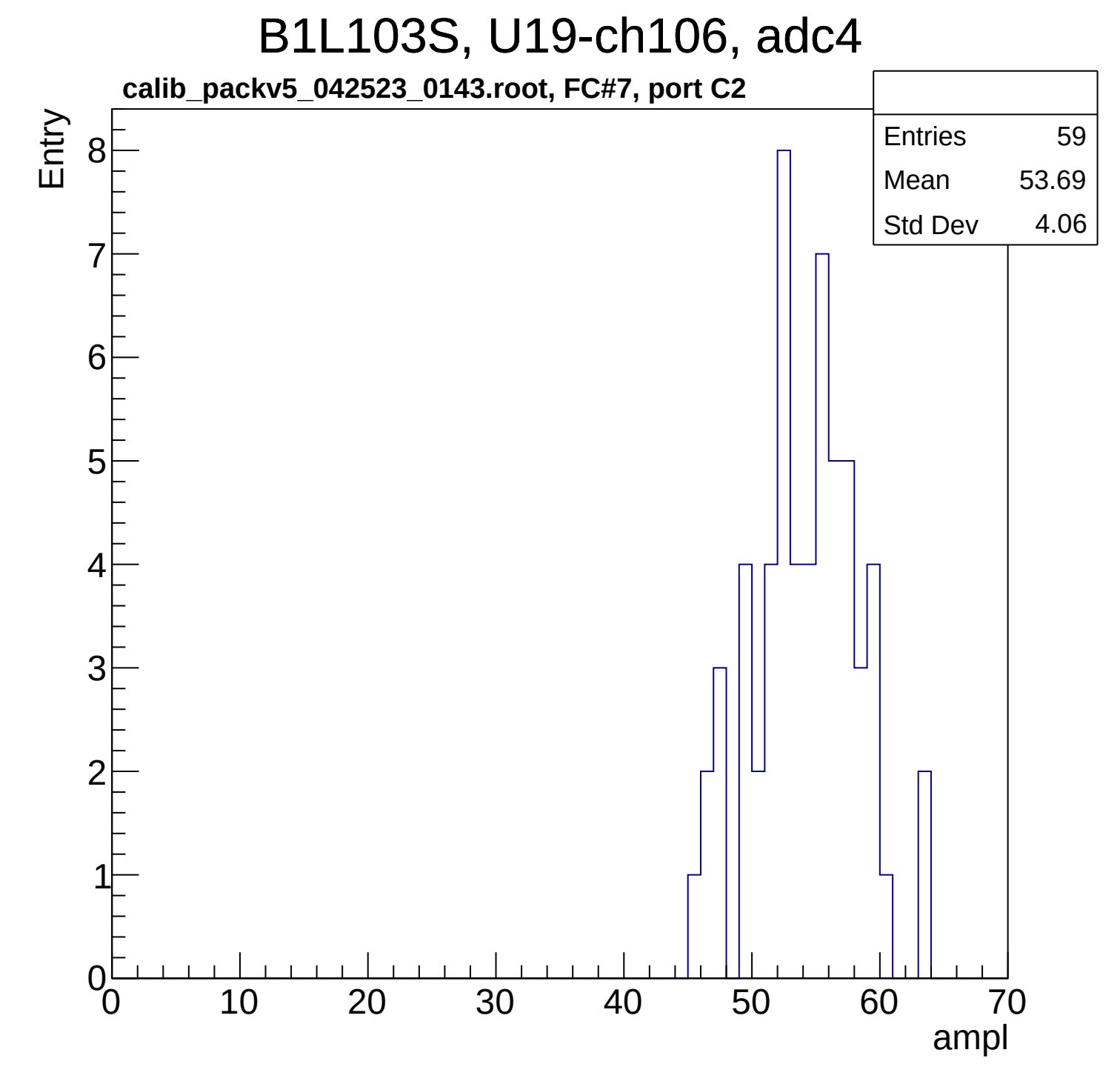

B1L103S, U19-ch105, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

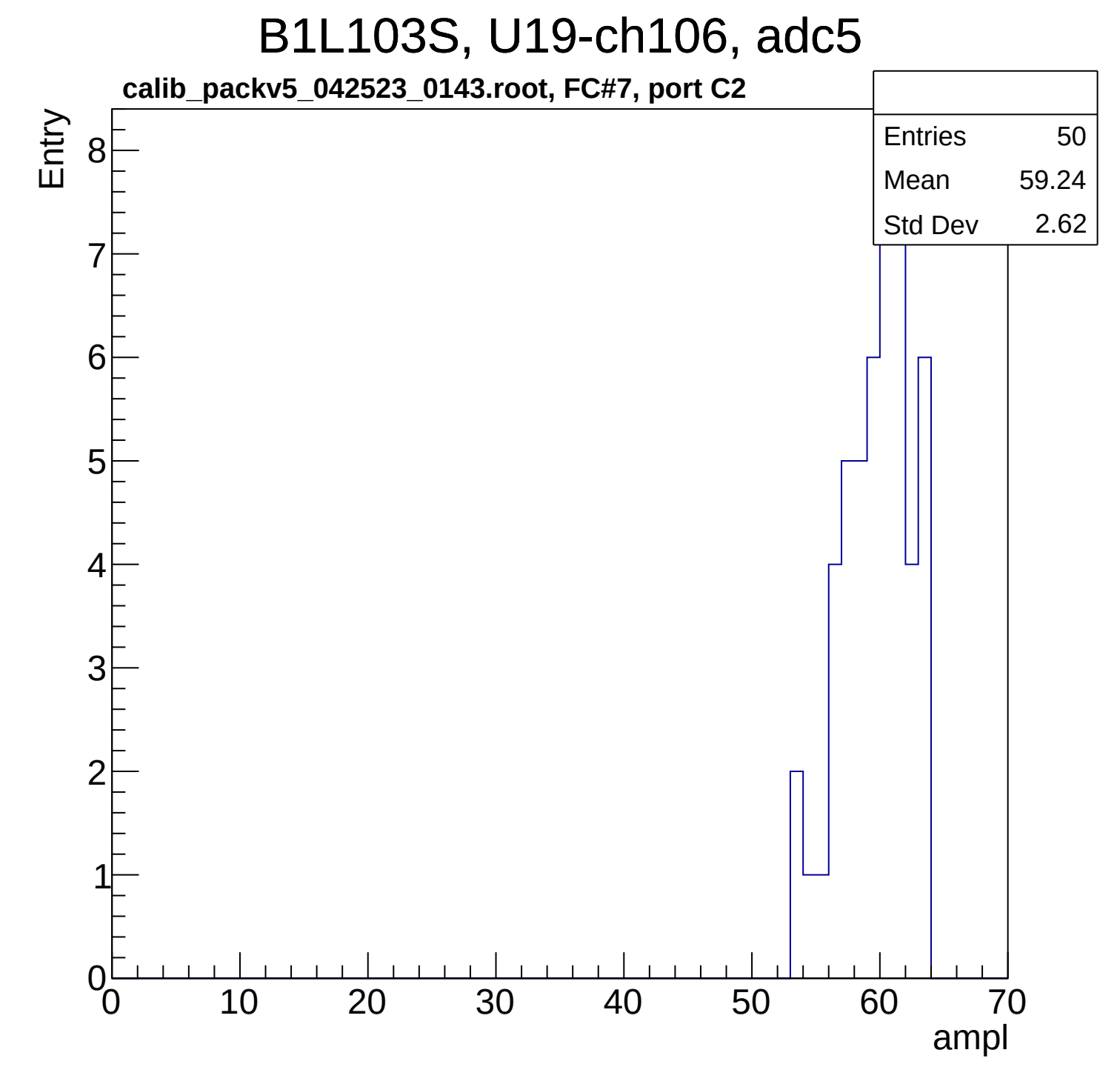

B1L103S, U19-ch106, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 28.79
Std Dev 4.238
Gaus mean : 29.5613
Gaus Width: 4.9892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

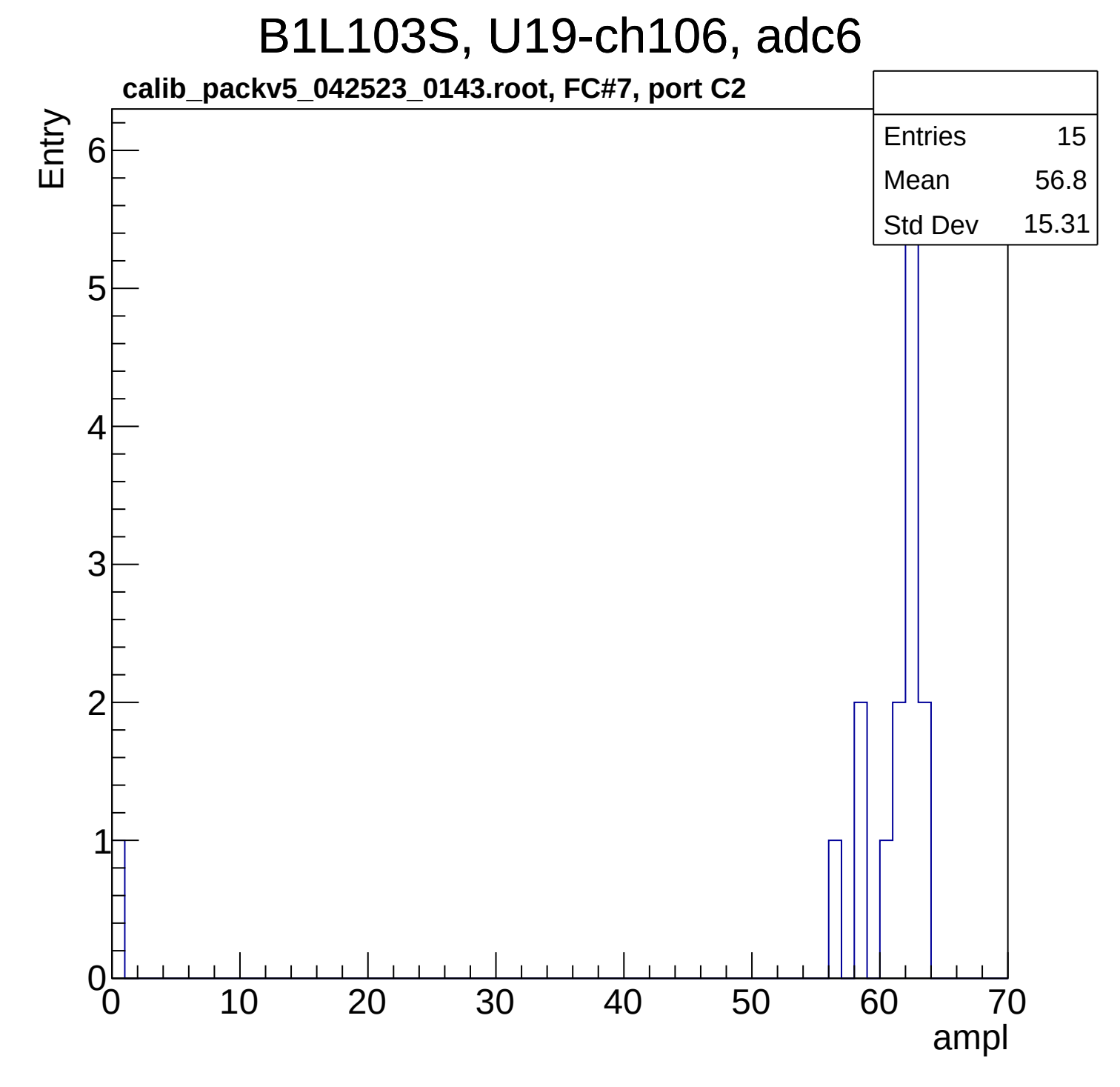

B1L103S, U19-ch106, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 34.76
Std Dev 3.958
Gaus mean : 35.5495
Gaus Width: 4.3453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

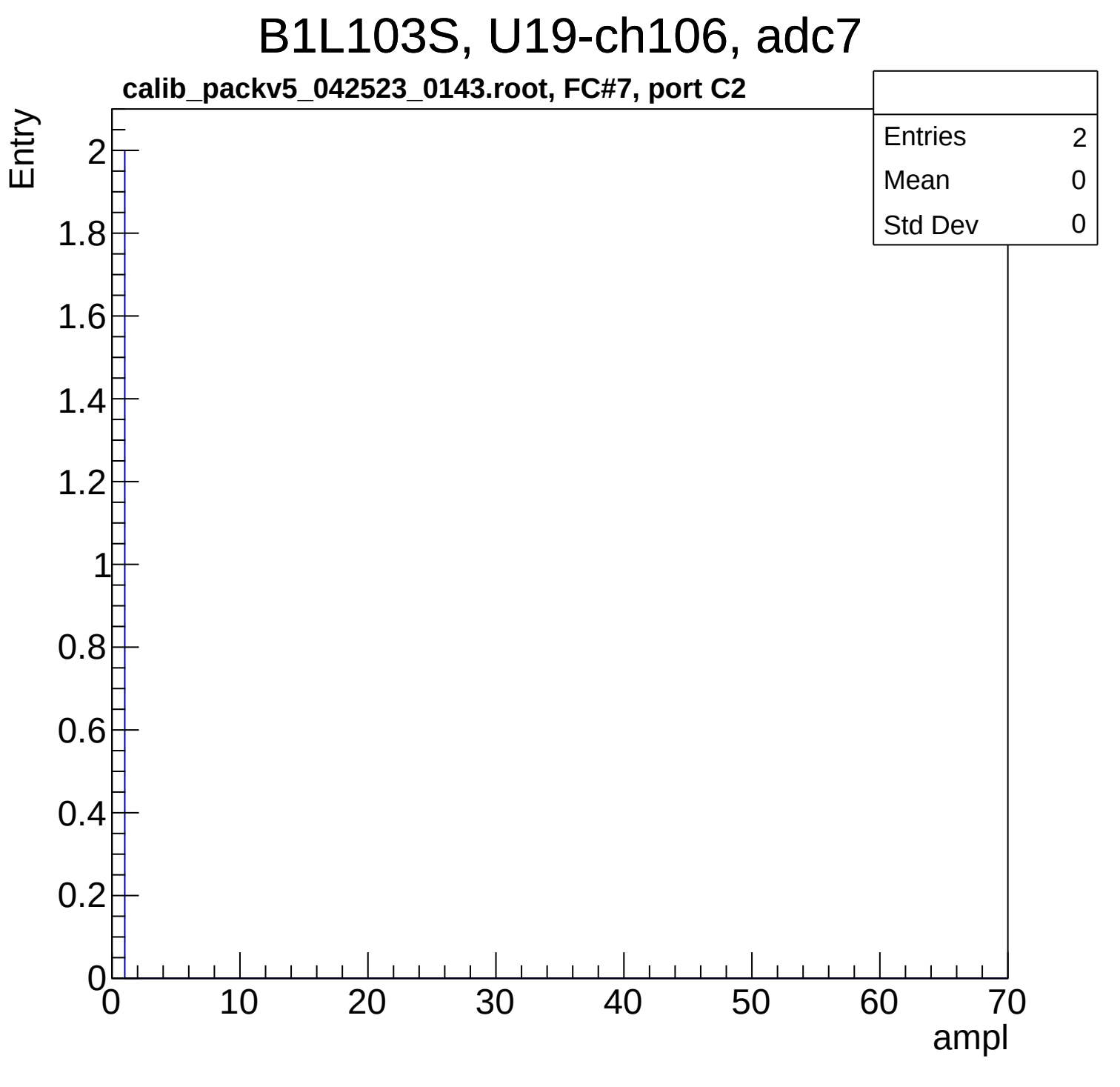

B1L103S, U19-ch106, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 41.89
Std Dev 3.588
Gaus mean : 42.0274
Gaus Width: 3.5988
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

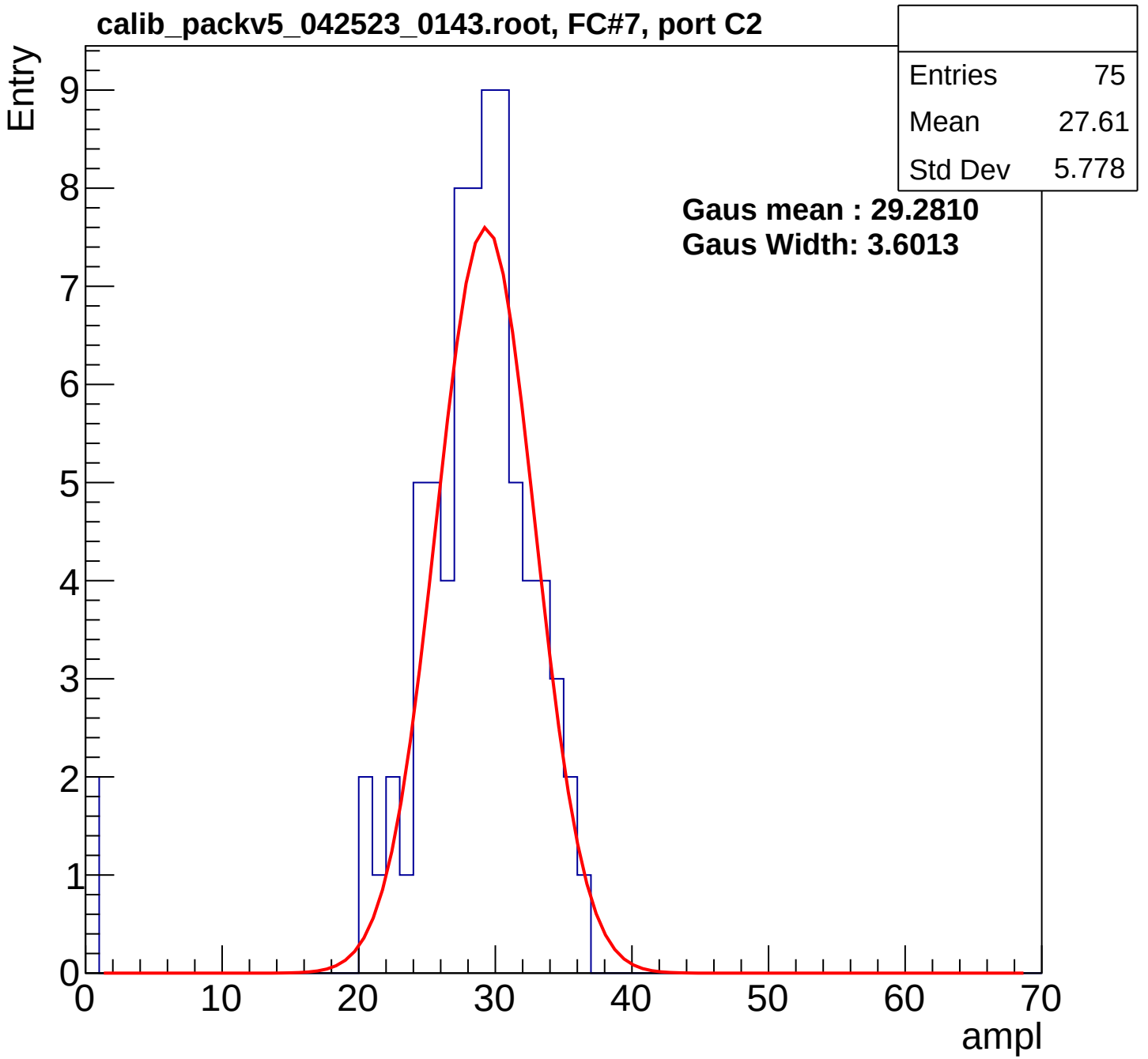

B1L103S, U19-ch106, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 48.76
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

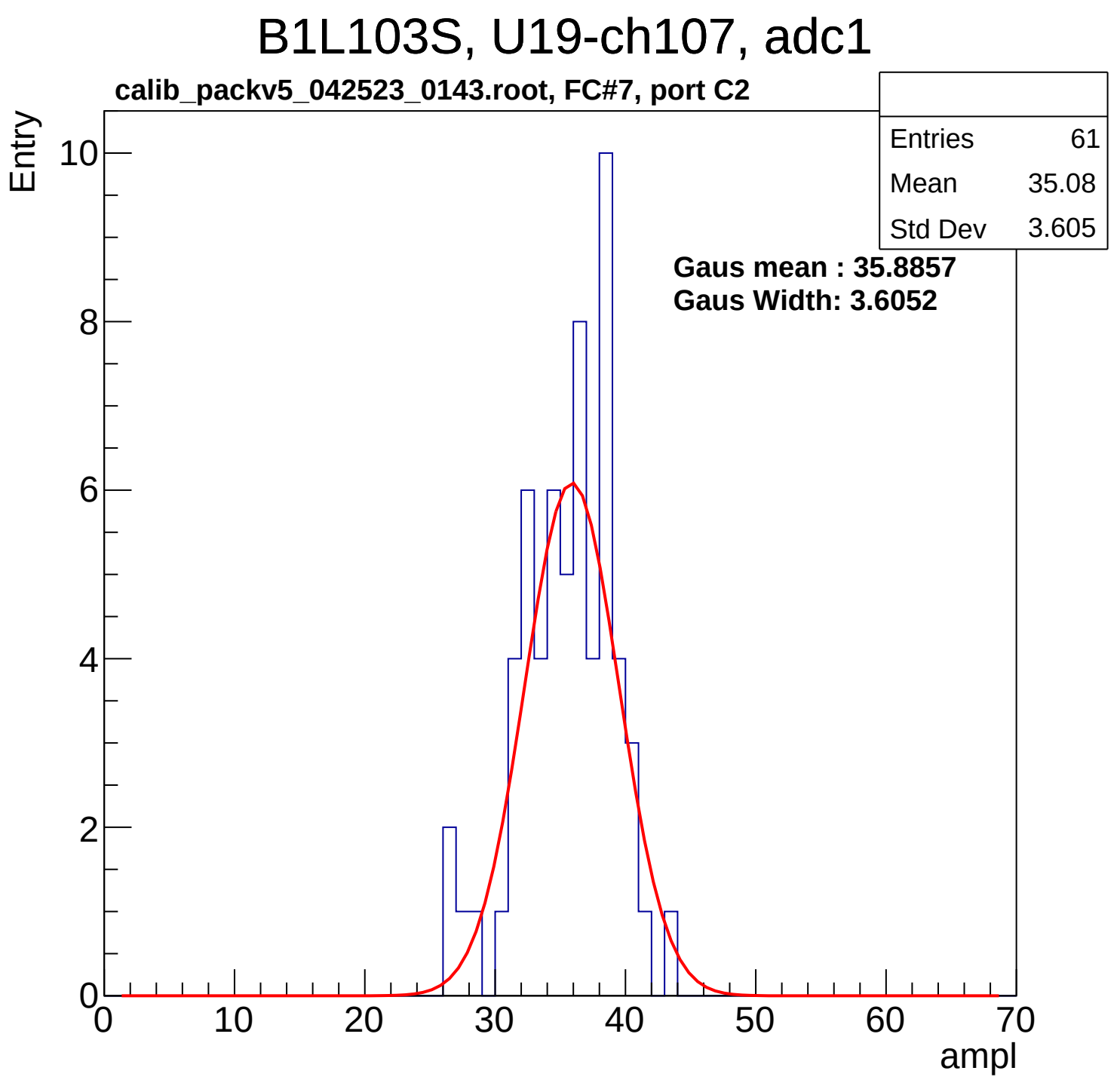

B1L103S, U19-ch106, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 53.69
Std Dev 4.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

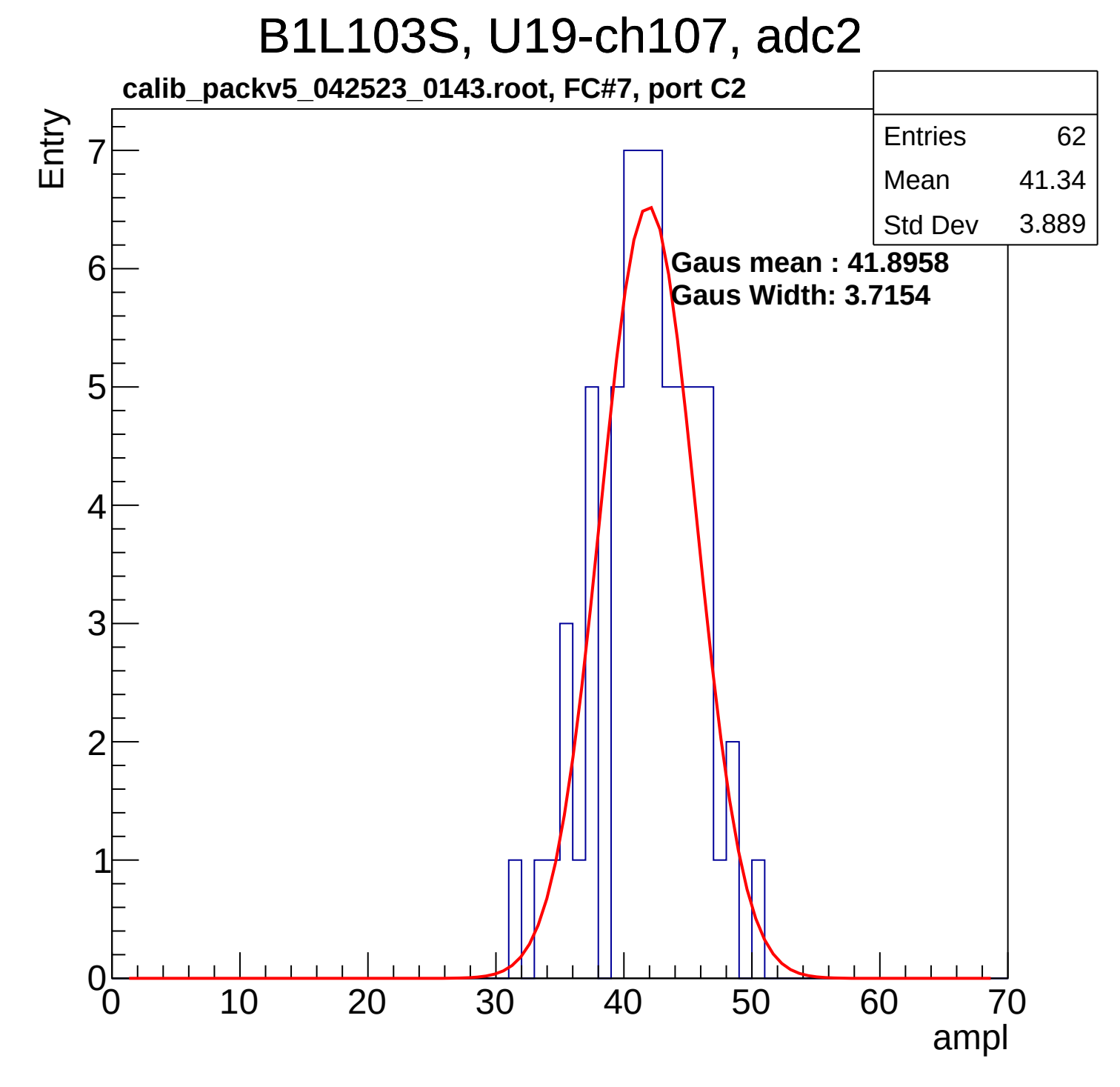

B1L103S, U19-ch106, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.24
Std Dev 2.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

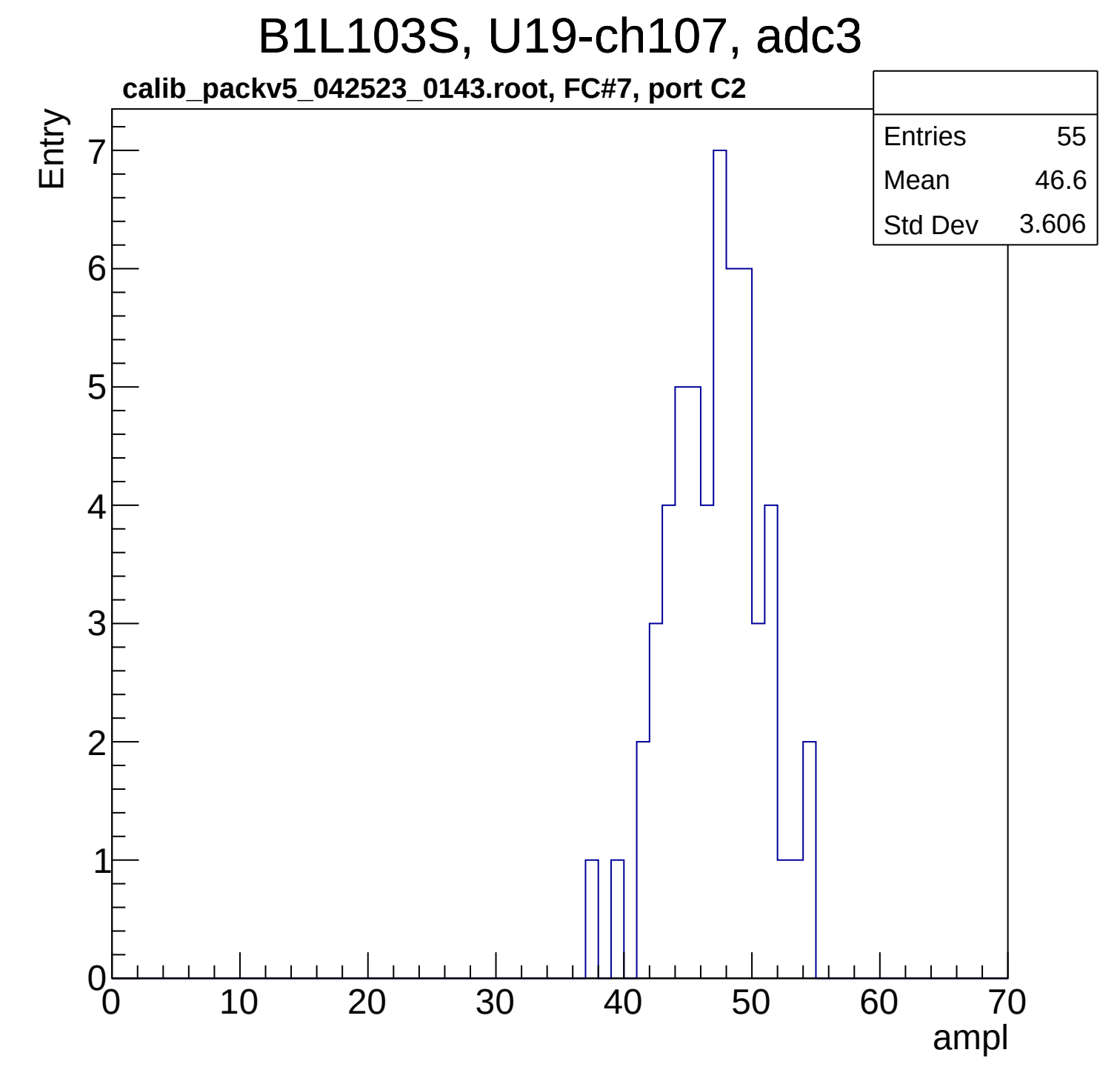

B1L103S, U19-ch106, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 56.8
Std Dev 15.31
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

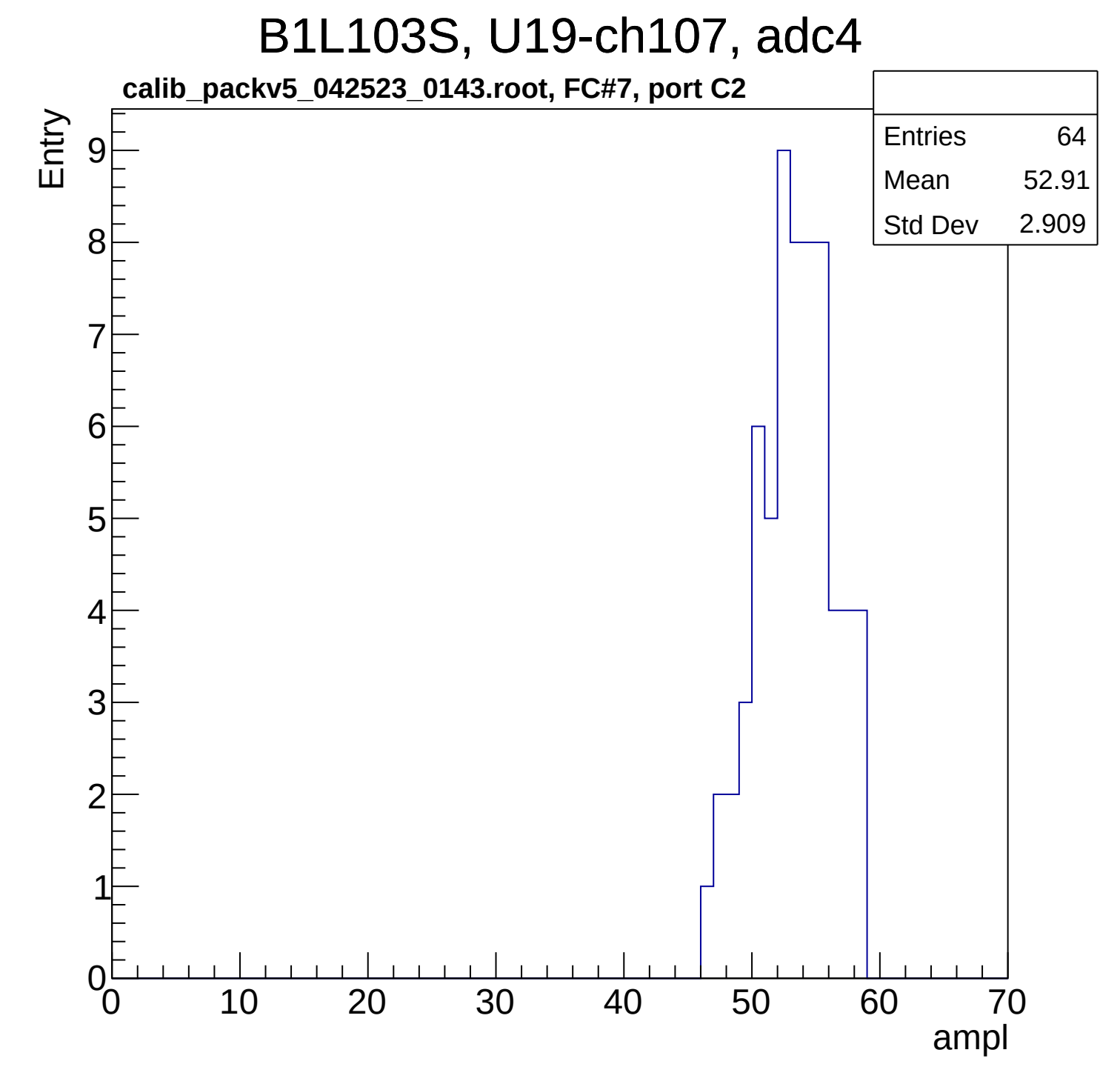

B1L103S, U19-ch106, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

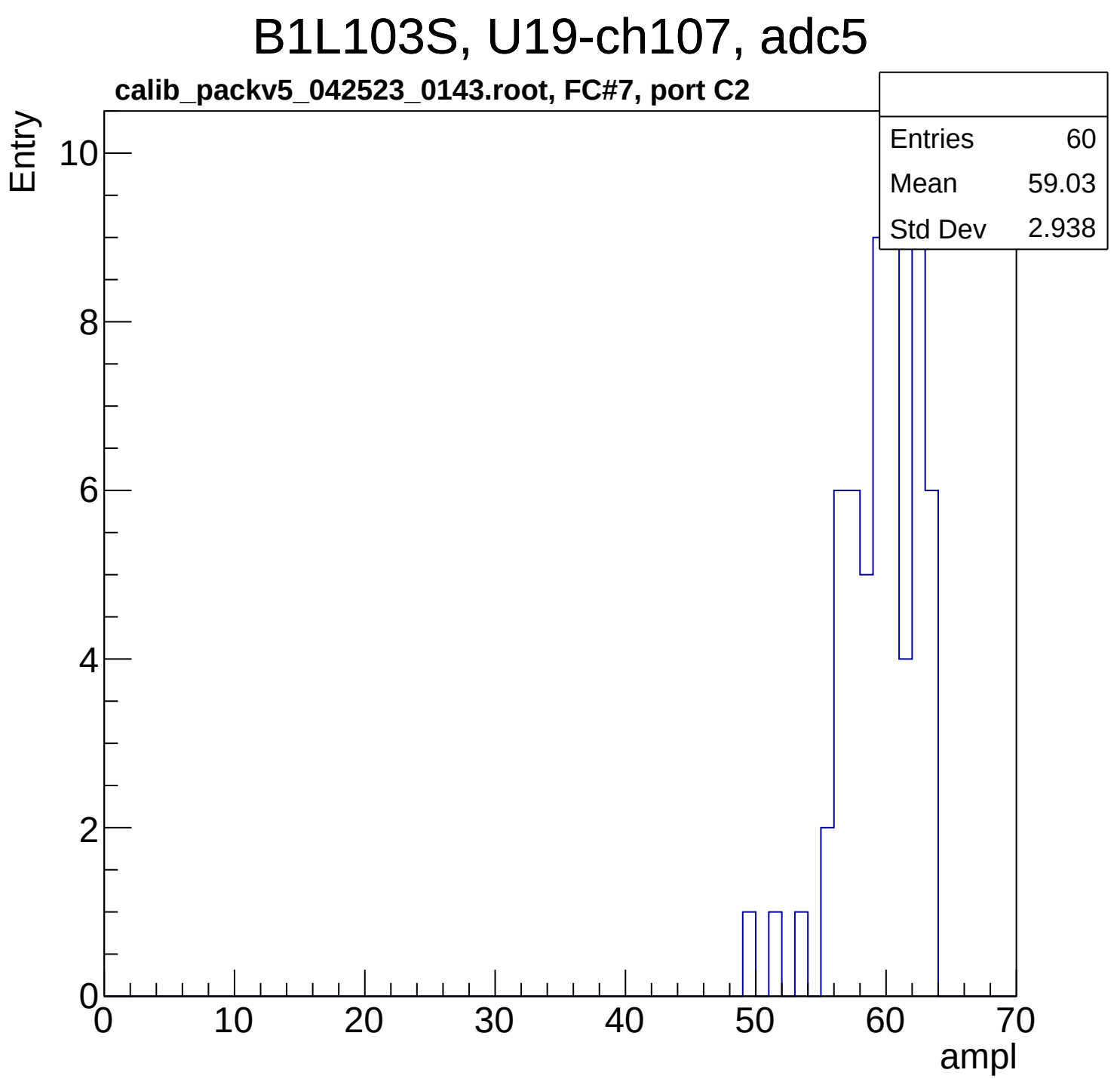

B1L103S, U19-ch107, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 27.61
Std Dev 5.778
Gaus mean : 29.2810
Gaus Width: 3.6013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

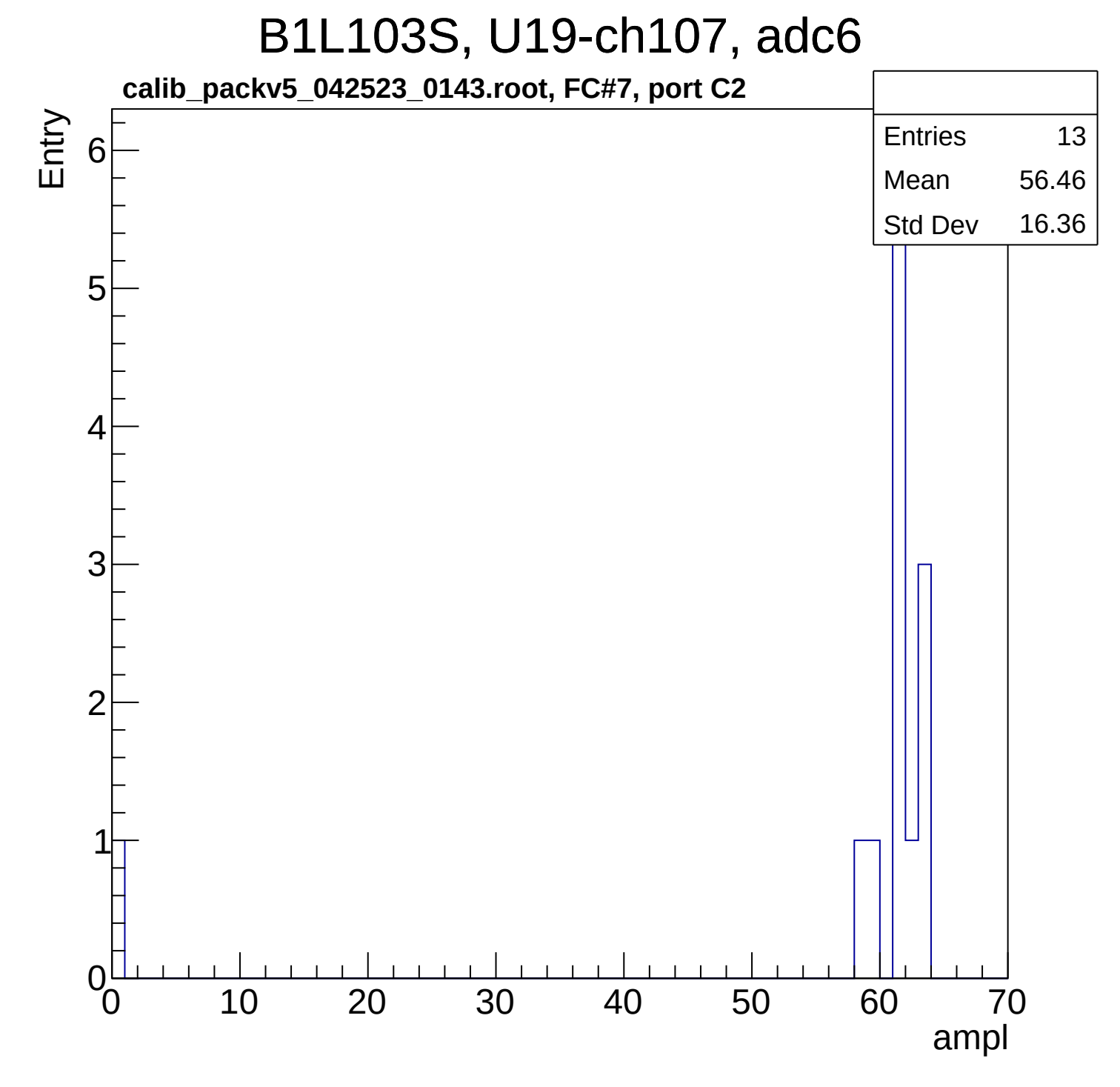

B1L103S, U19-ch107, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 35.08
Std Dev 3.605
Gaus mean : 35.8857
Gaus Width: 3.6052
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U19-ch107, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 41.34
Std Dev 3.889
Gaus mean : 41.8958
Gaus Width: 3.7154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch107, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 46.6
Std Dev 3.606
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch107, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 52.91
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch107, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 59.03
Std Dev 2.938
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch107, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.46
Std Dev 16.36
Entry
ampl
0
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2
3
4
5
6
0
10
20
30
40
50
60
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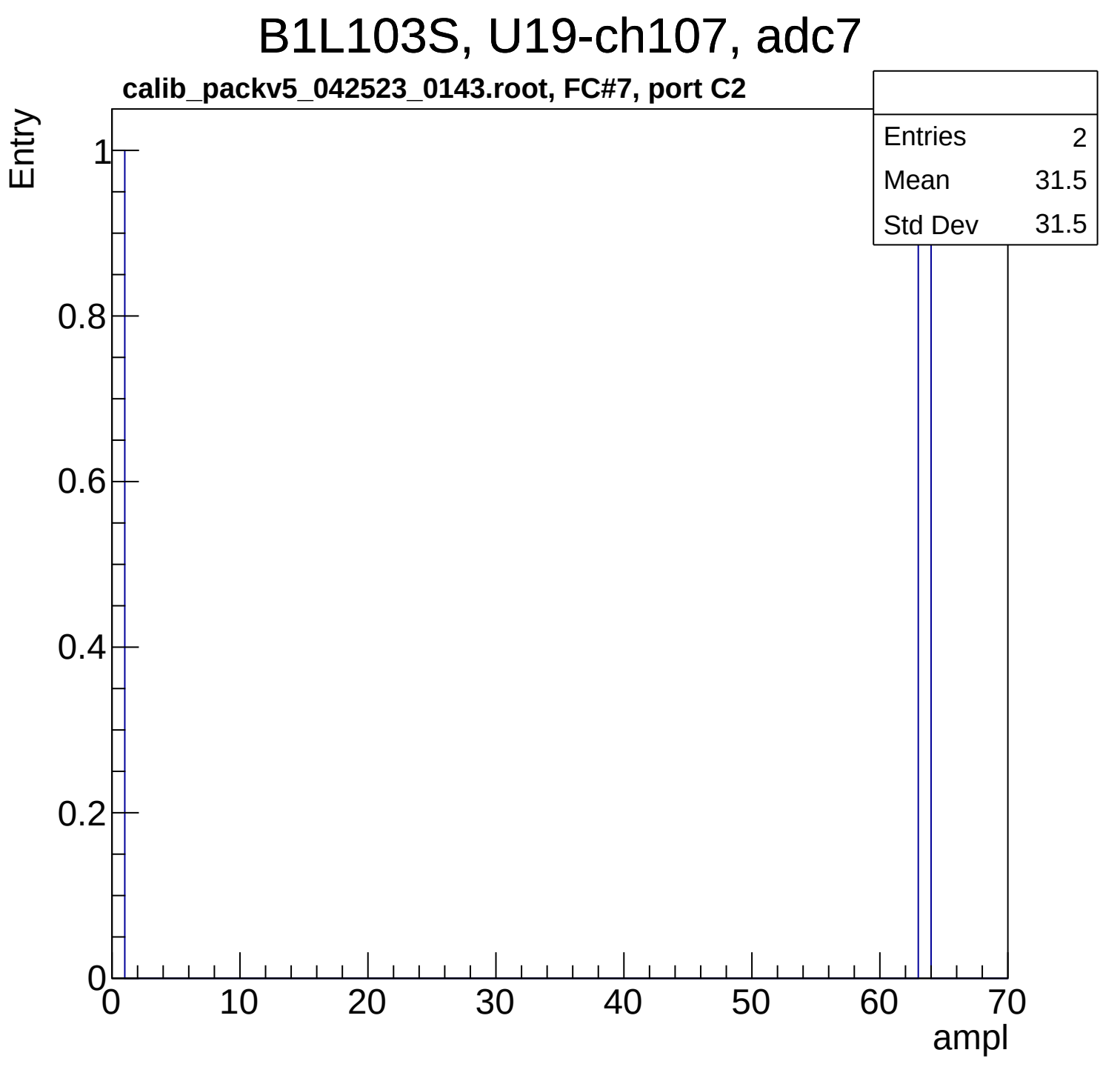
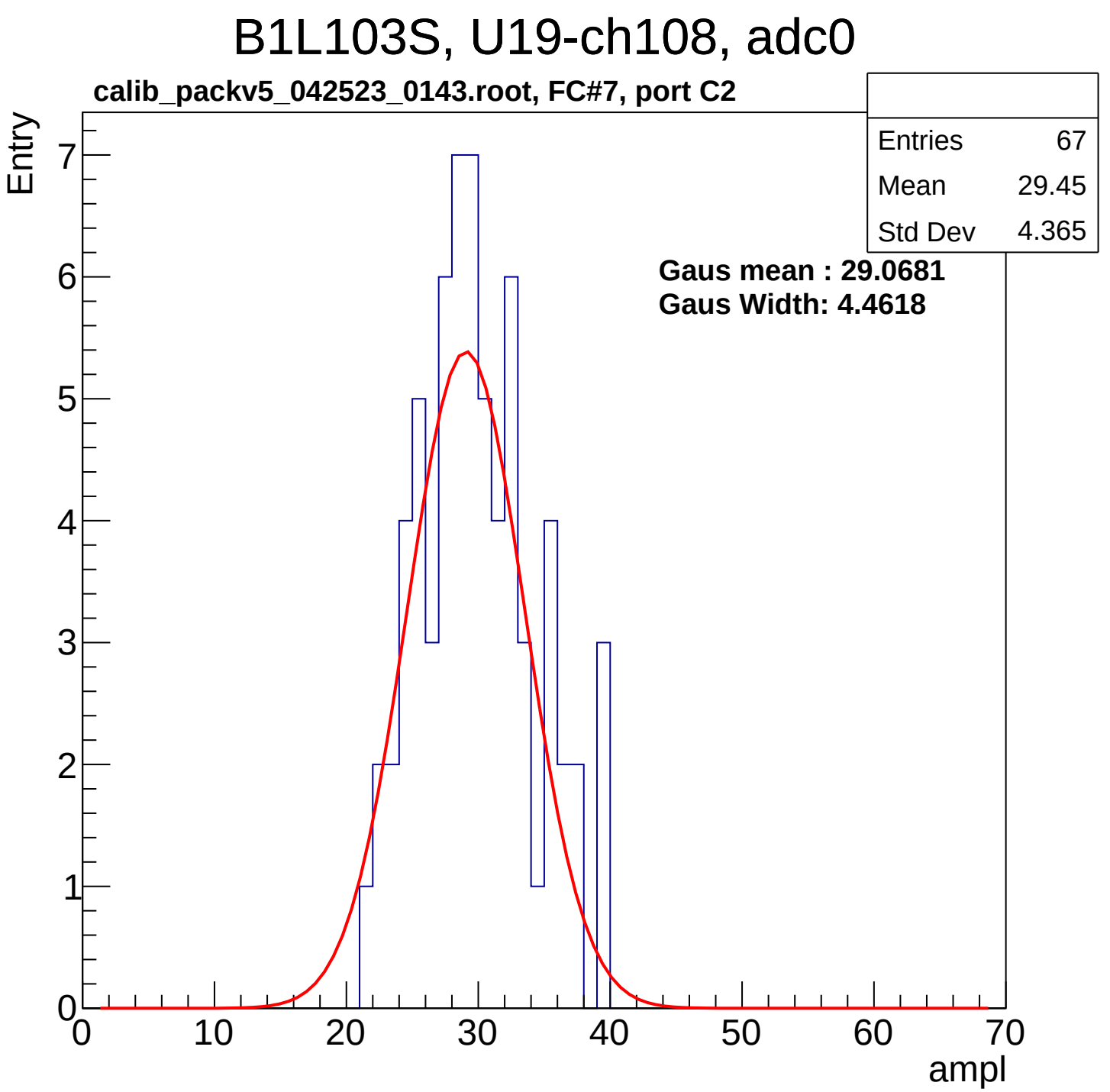
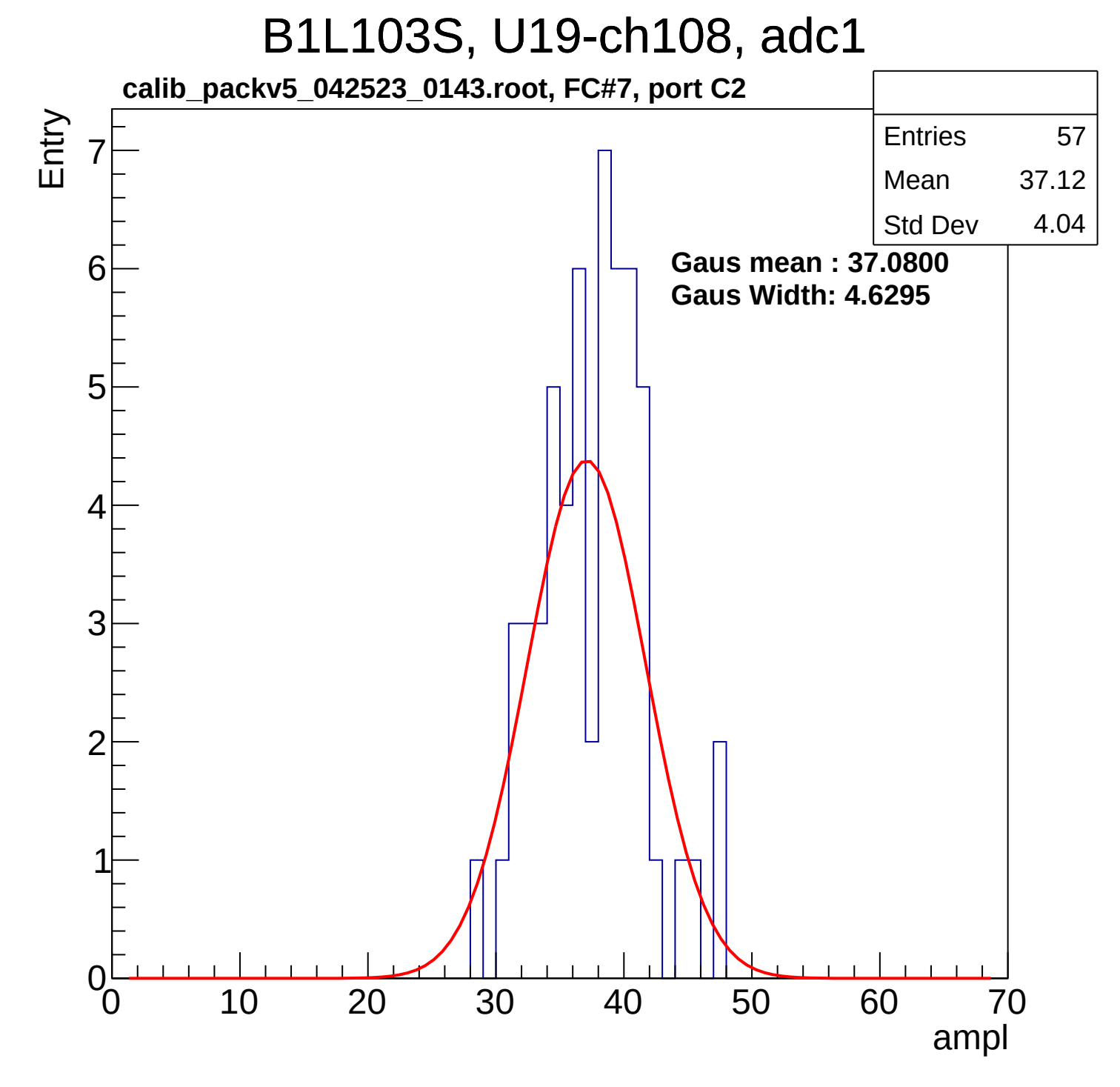

B1L103S, U19-ch107, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch108, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.45
Std Dev 4.365
Gaus mean : 29.0681
Gaus Width: 4.4618
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch108, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 37.12
Std Dev 4.04
Gaus mean : 37.0800
Gaus Width: 4.6295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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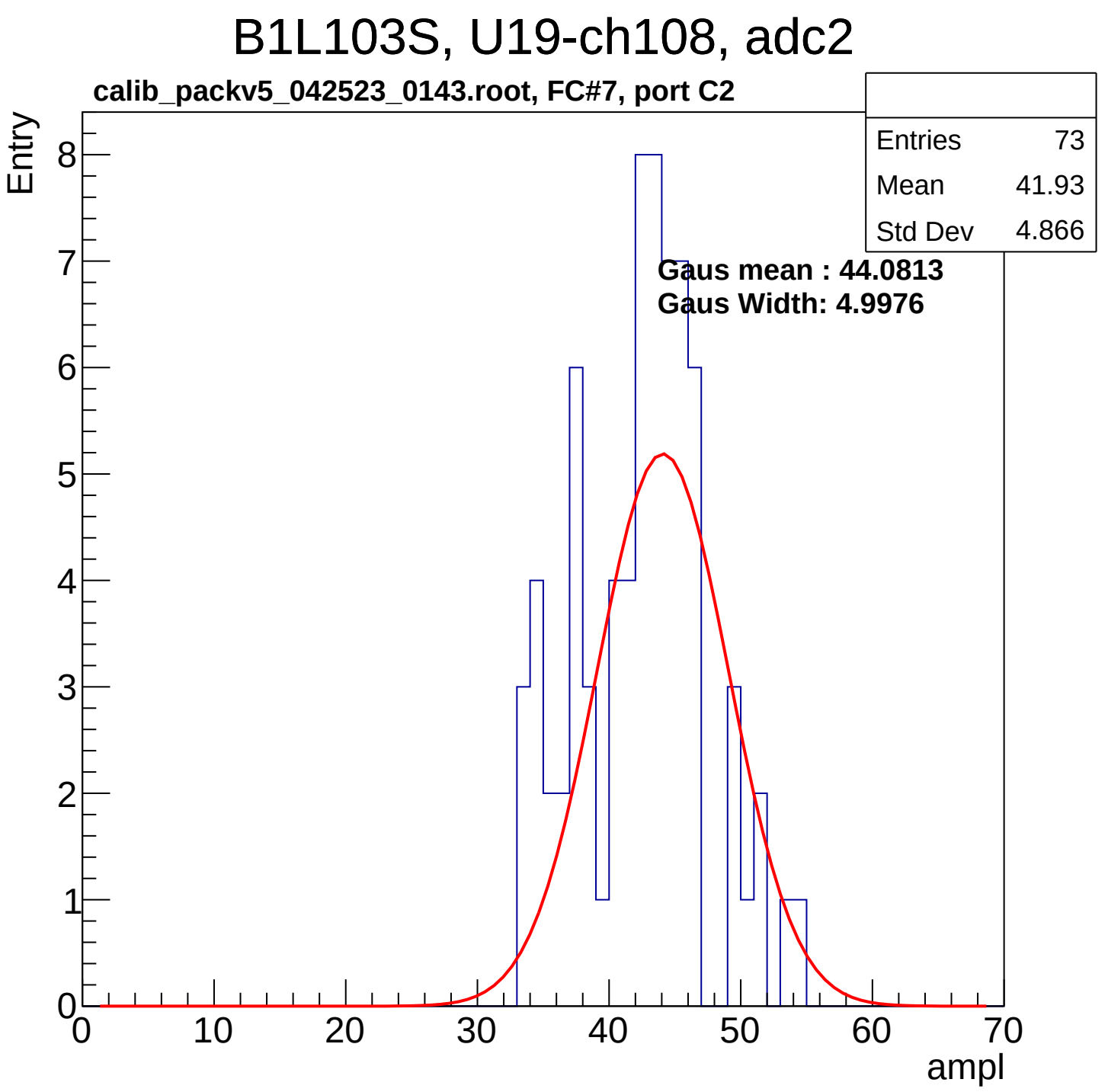
B1L103S, U19-ch108, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 41.93
Std Dev 4.866
Gaus mean : 44.0813
Gaus Width: 4.9976
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
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60
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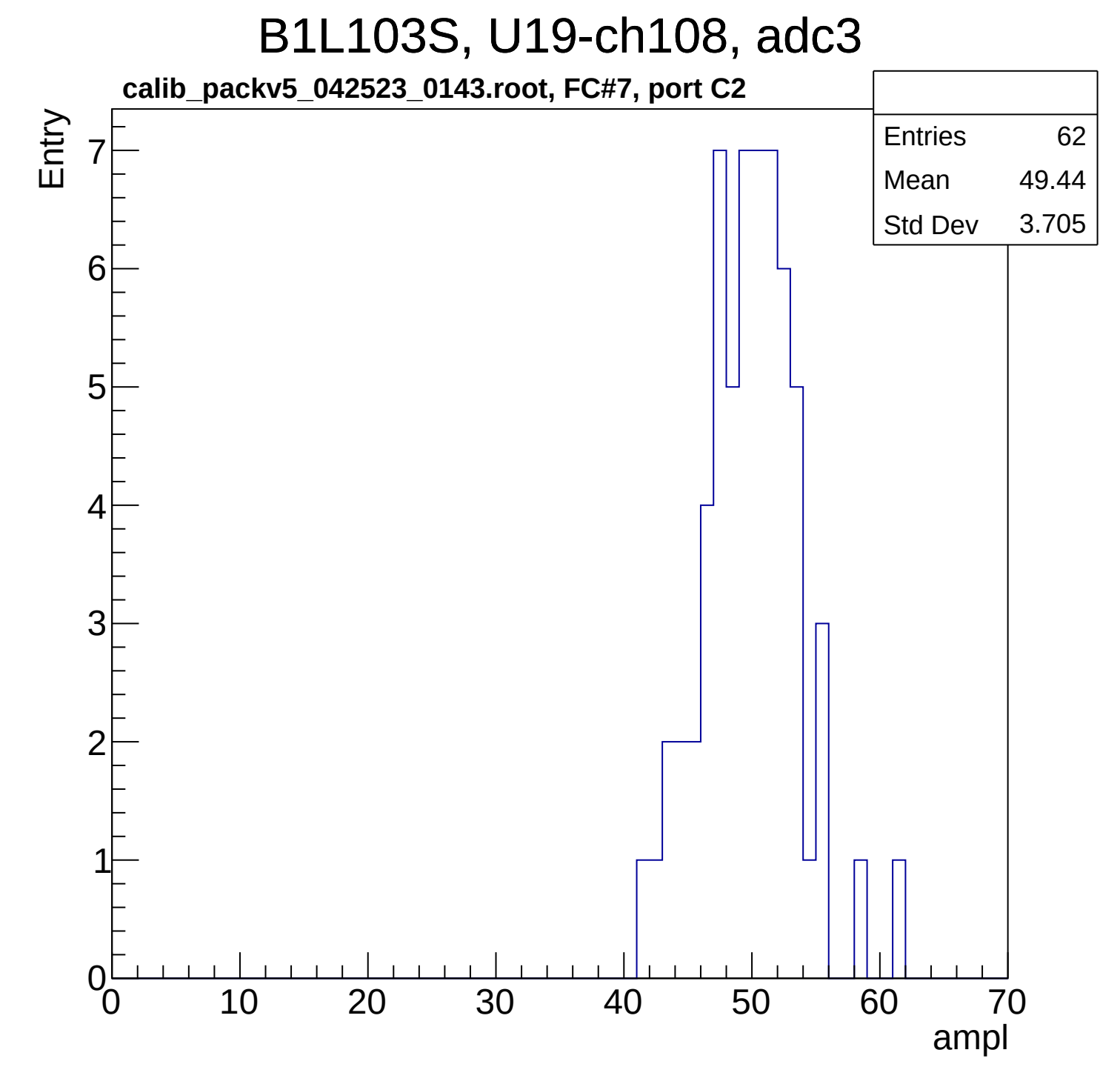
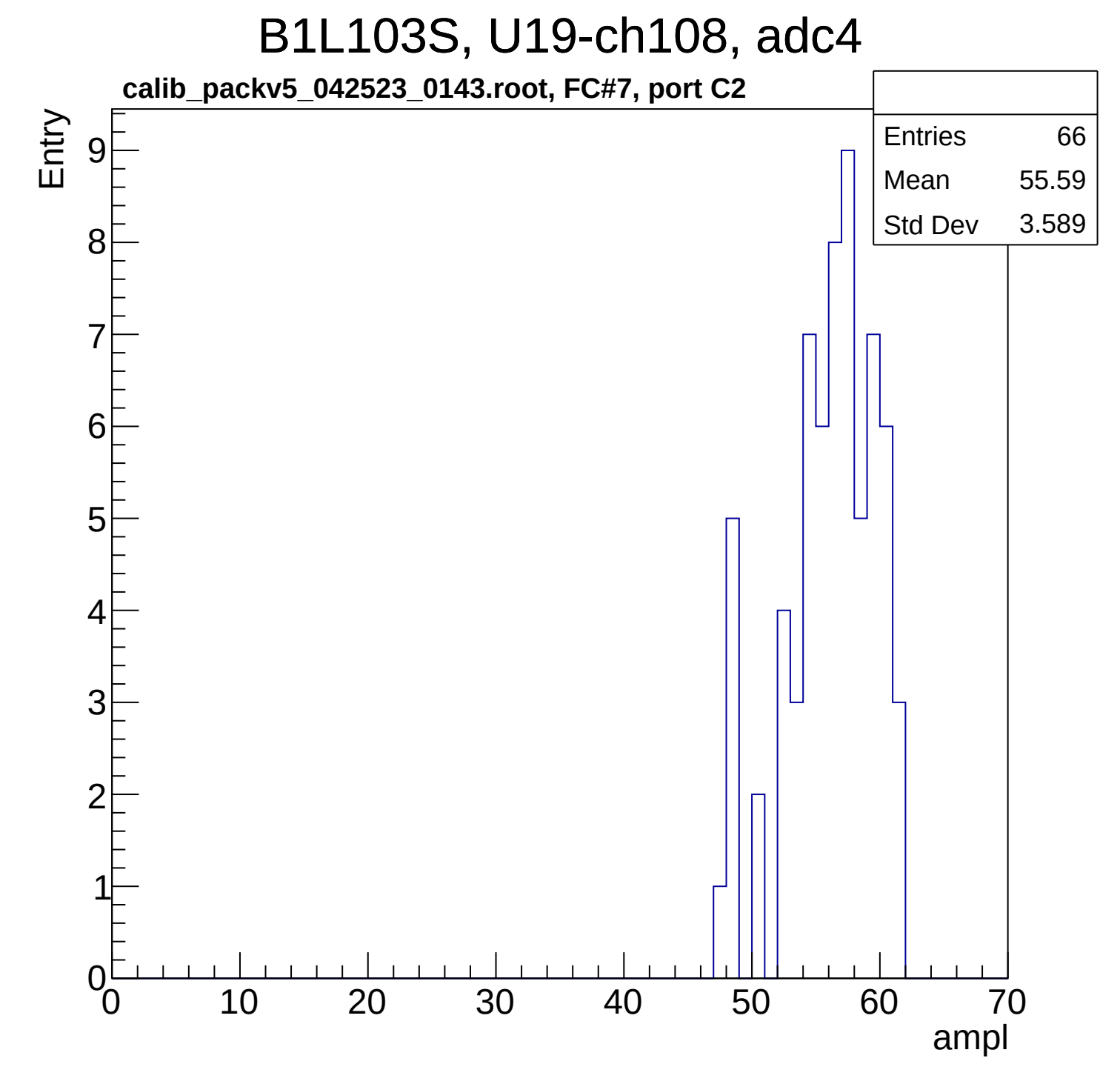

B1L103S, U19-ch108, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.44
Std Dev 3.705
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch108, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 55.59
Std Dev 3.589
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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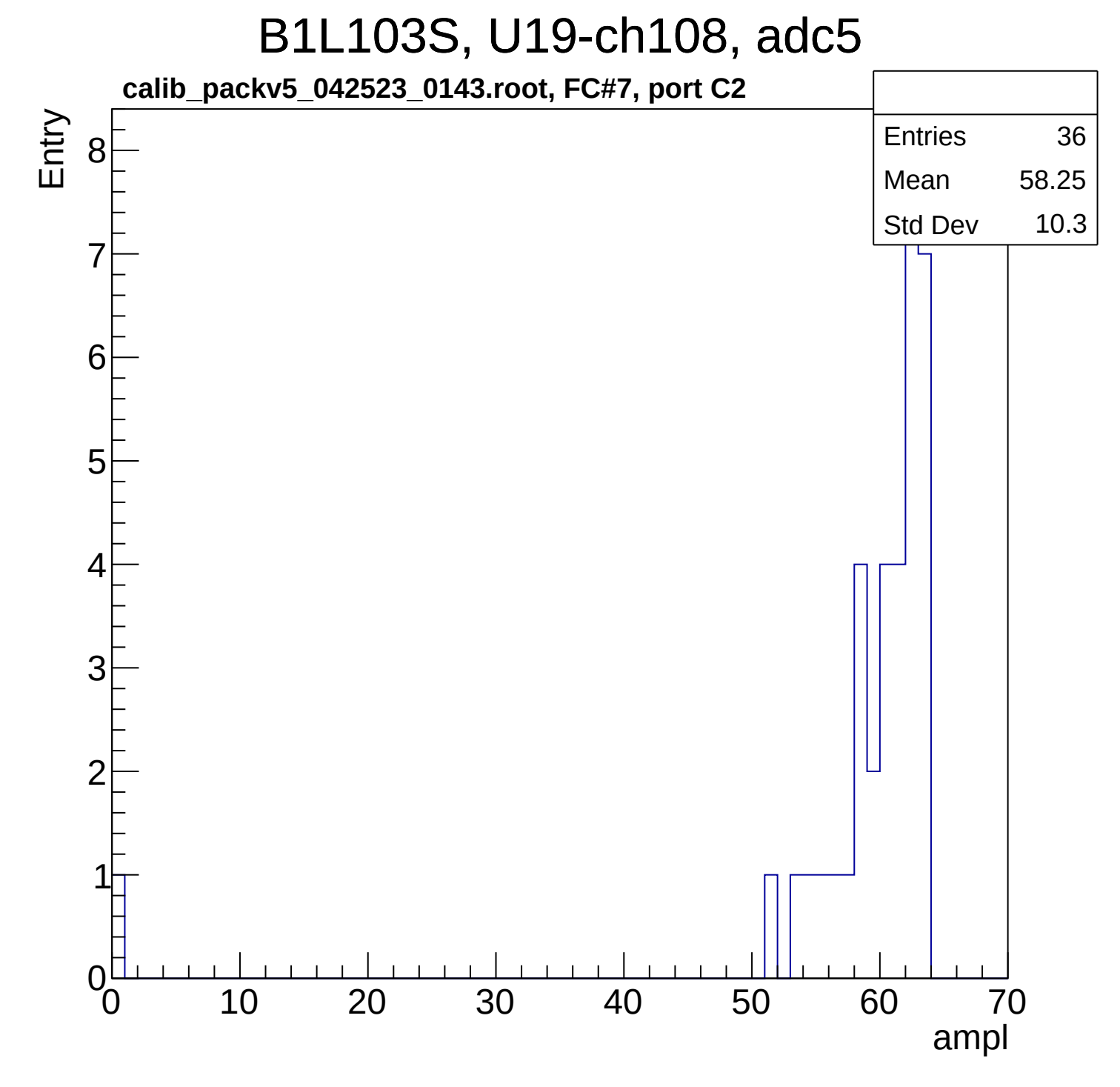
B1L103S, U19-ch108, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.25
Std Dev 10.3
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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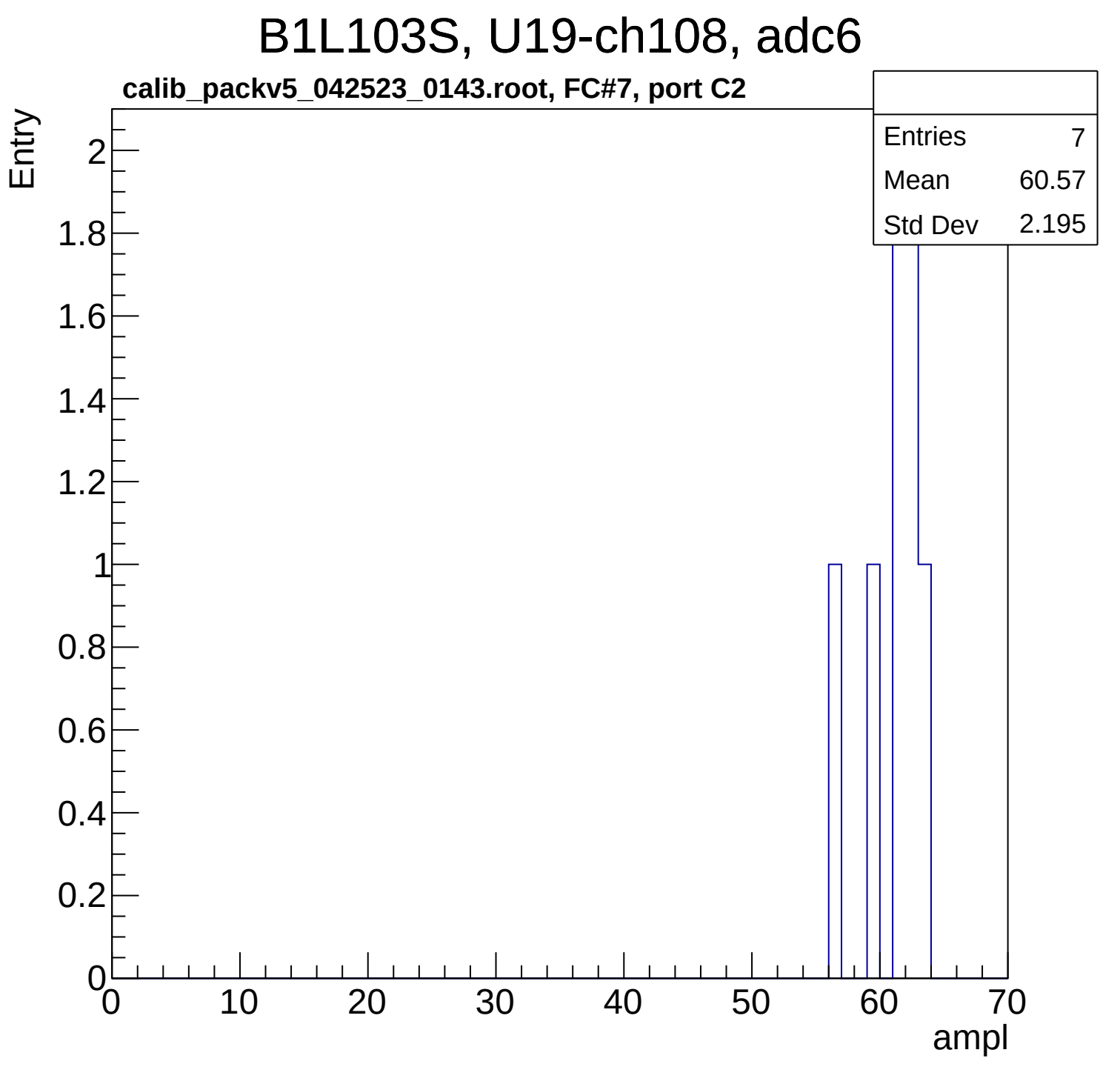
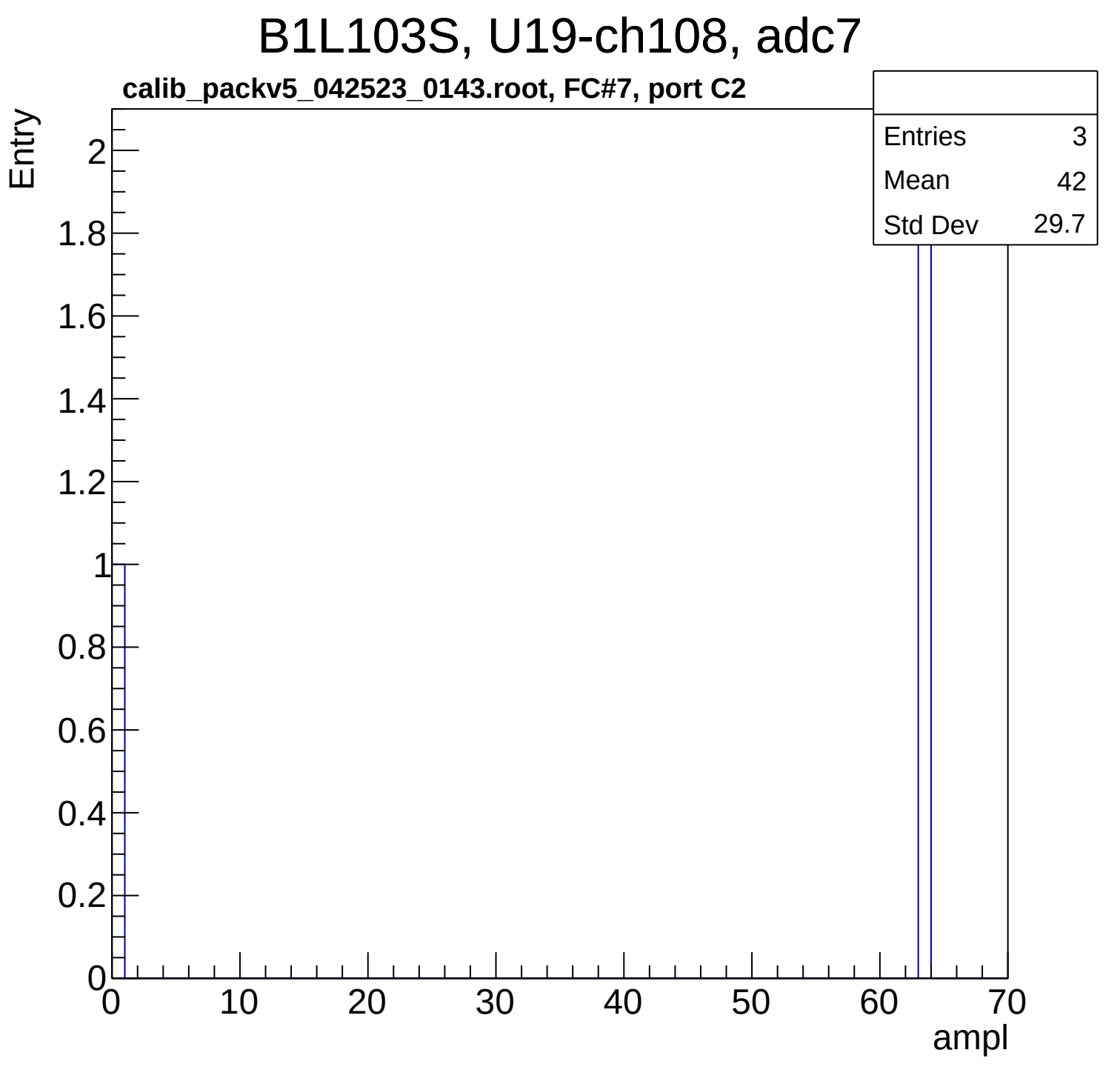
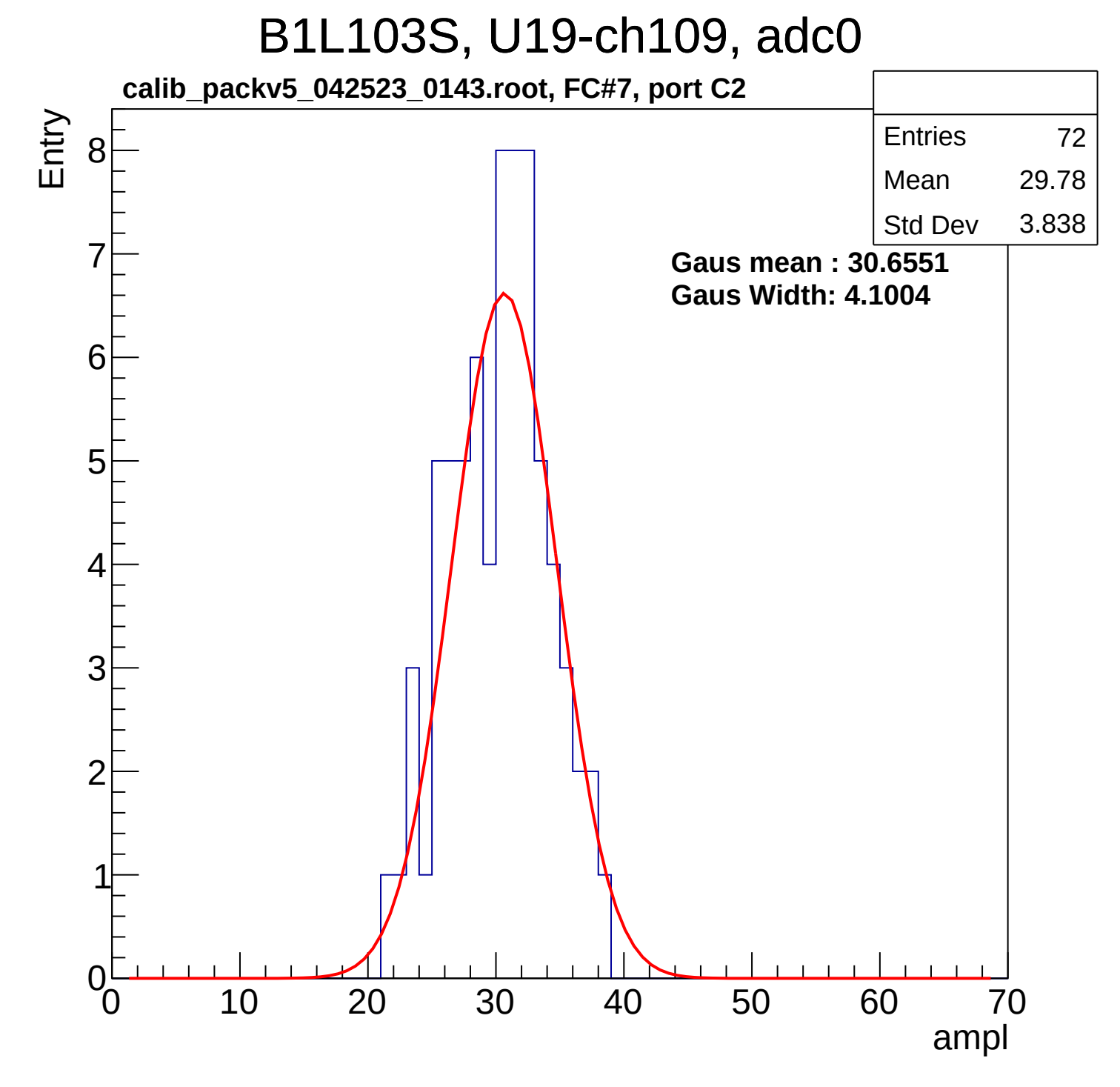
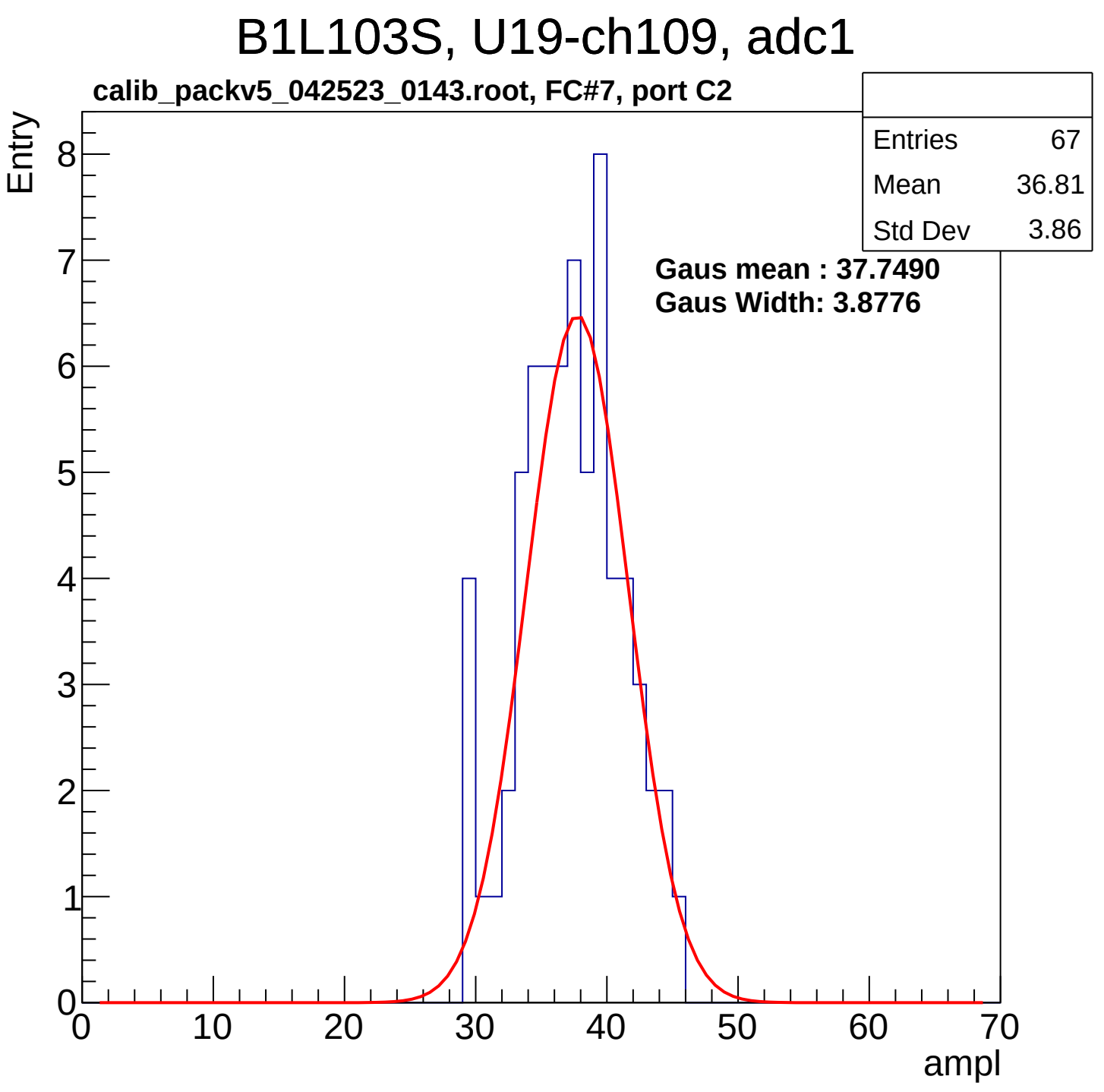
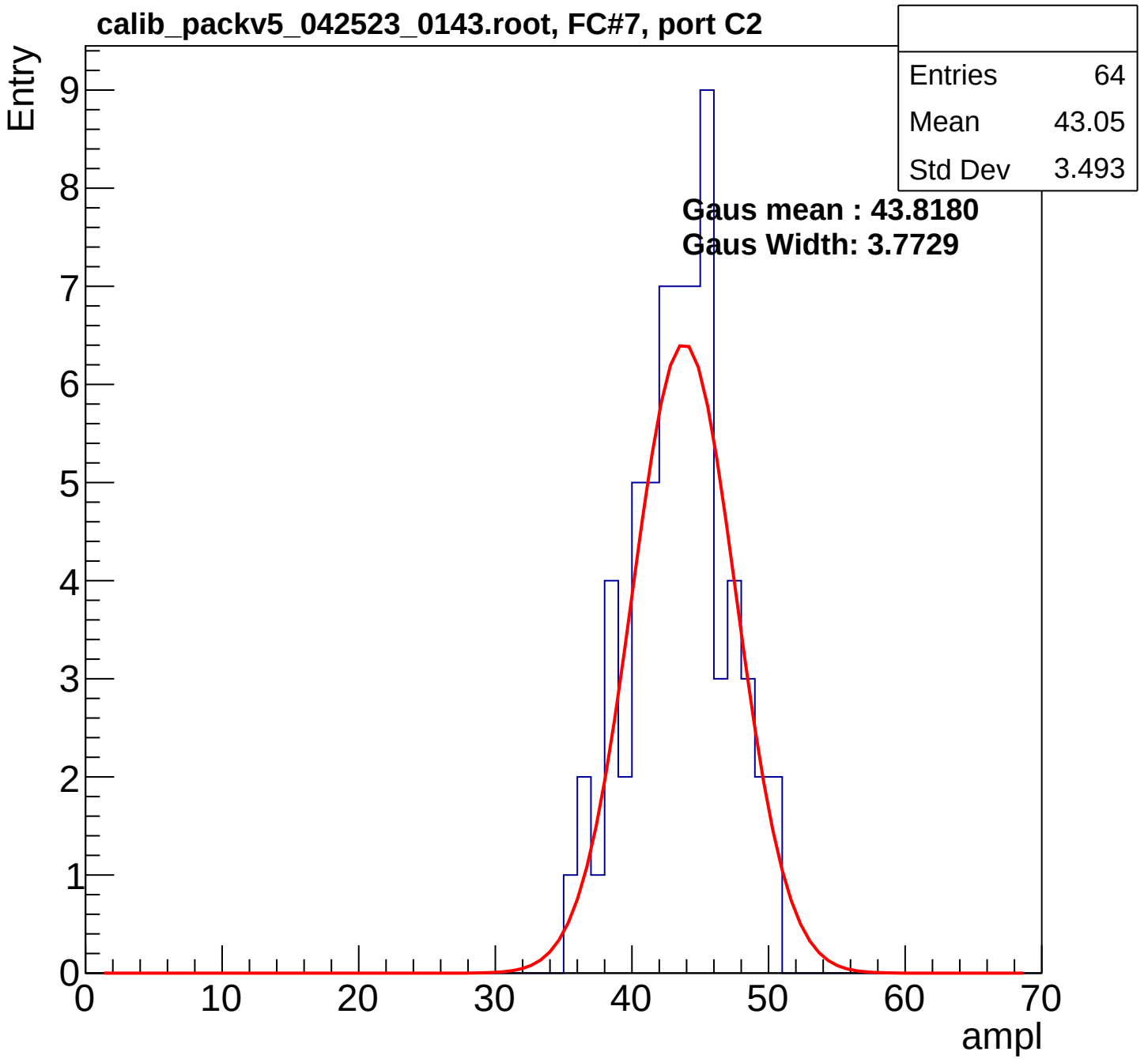

B1L103S, U19-ch108, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 60.57
Std Dev 2.195
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch108, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch109, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 29.78
Std Dev 3.838
Gaus mean : 30.6551
Gaus Width: 4.1004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch109, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.81
Std Dev 3.86
Gaus mean : 37.7490
Gaus Width: 3.8776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch109, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 43.05
Std Dev 3.493
Gaus mean : 43.8180
Gaus Width: 3.7729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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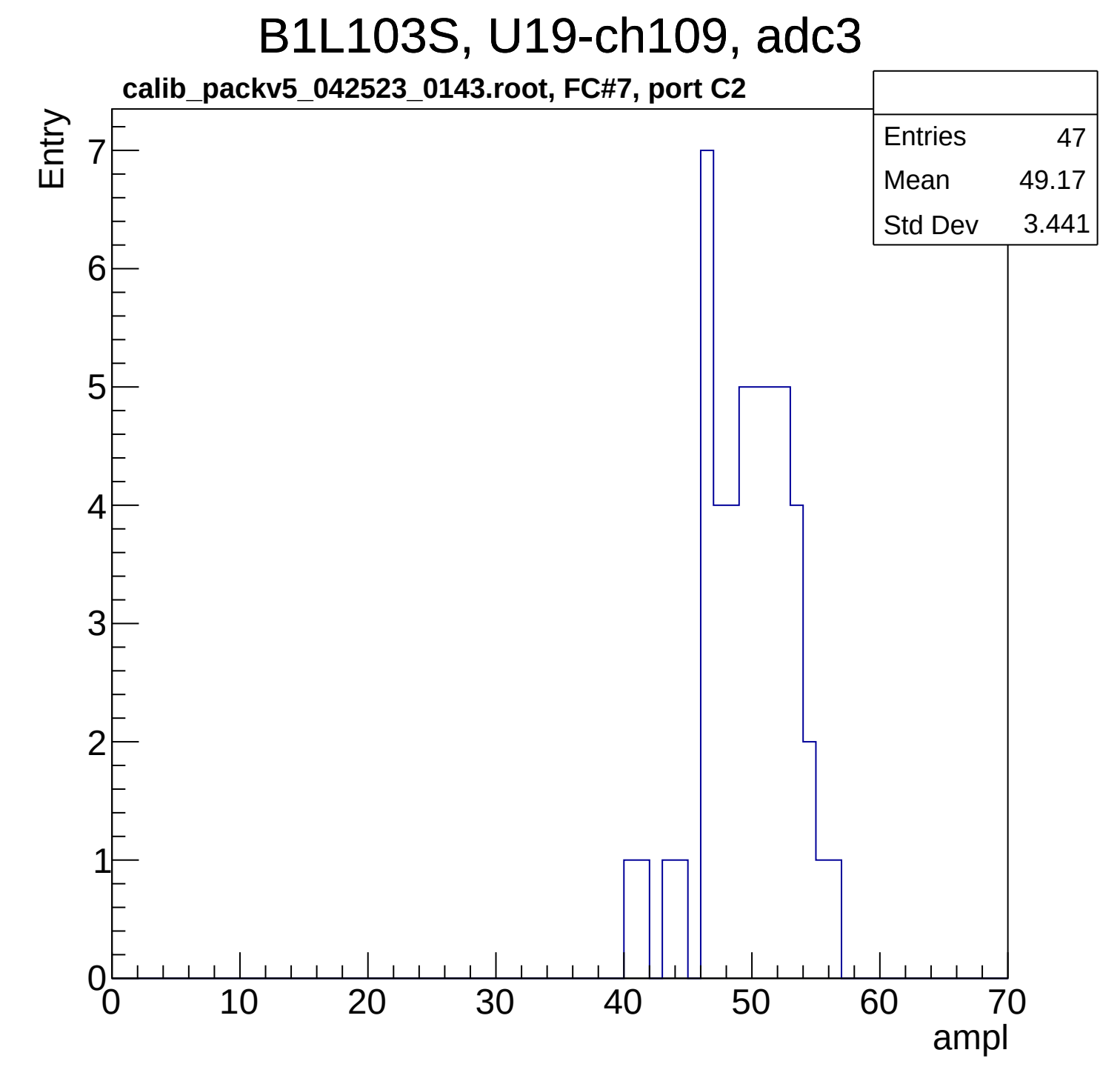
B1L103S, U19-ch109, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 49.17
Std Dev 3.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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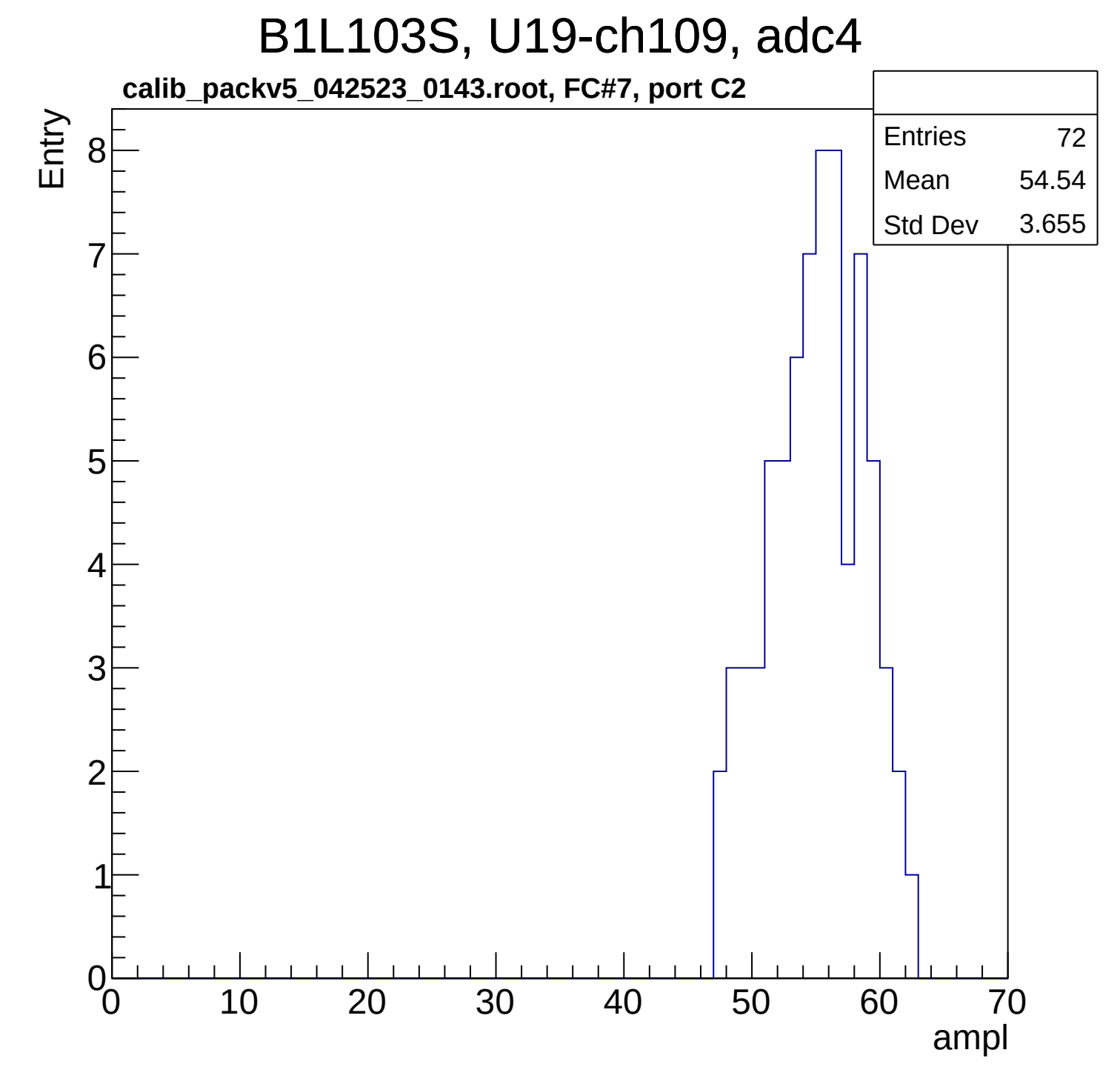
B1L103S, U19-ch109, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 54.54
Std Dev 3.655
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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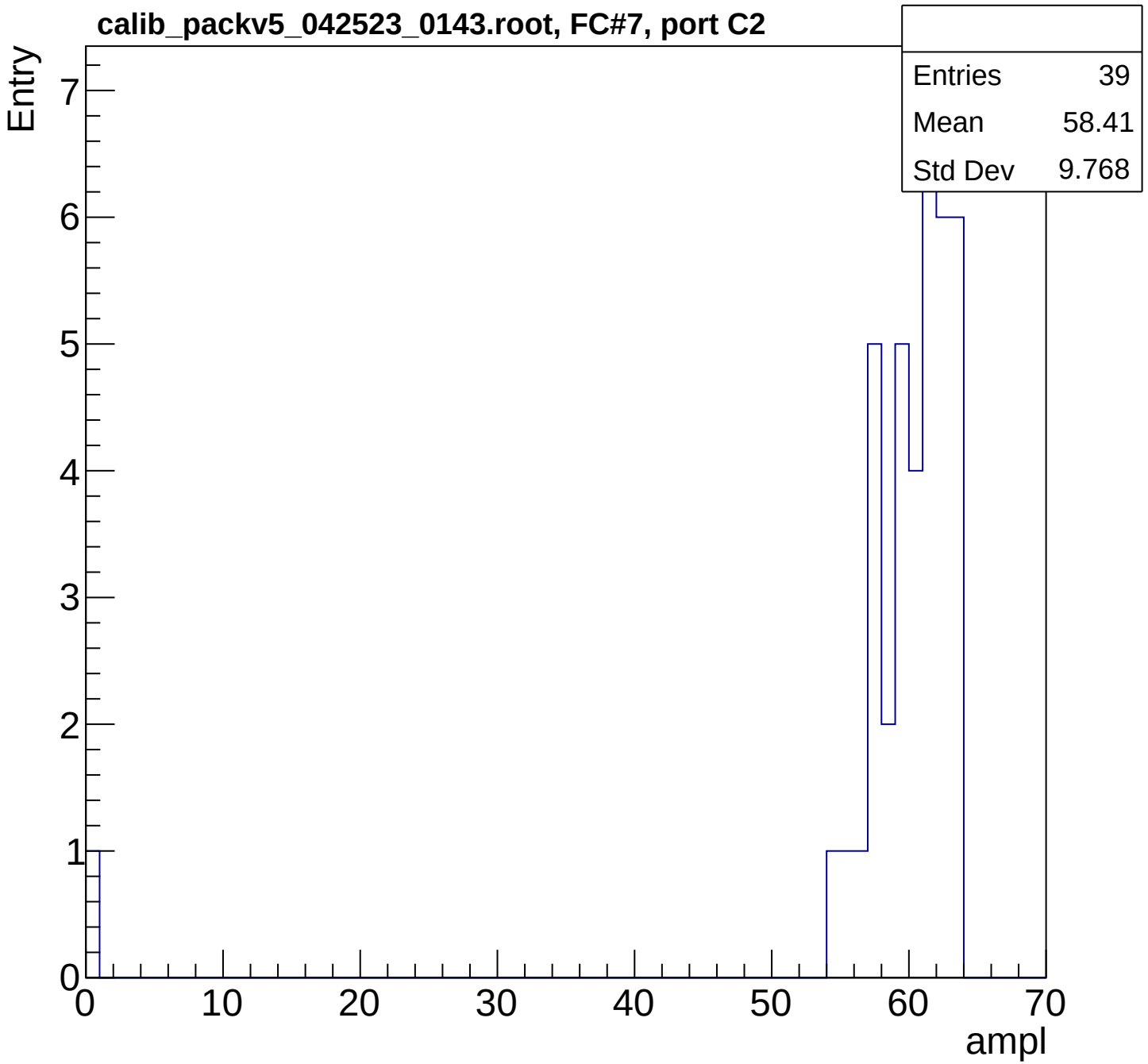
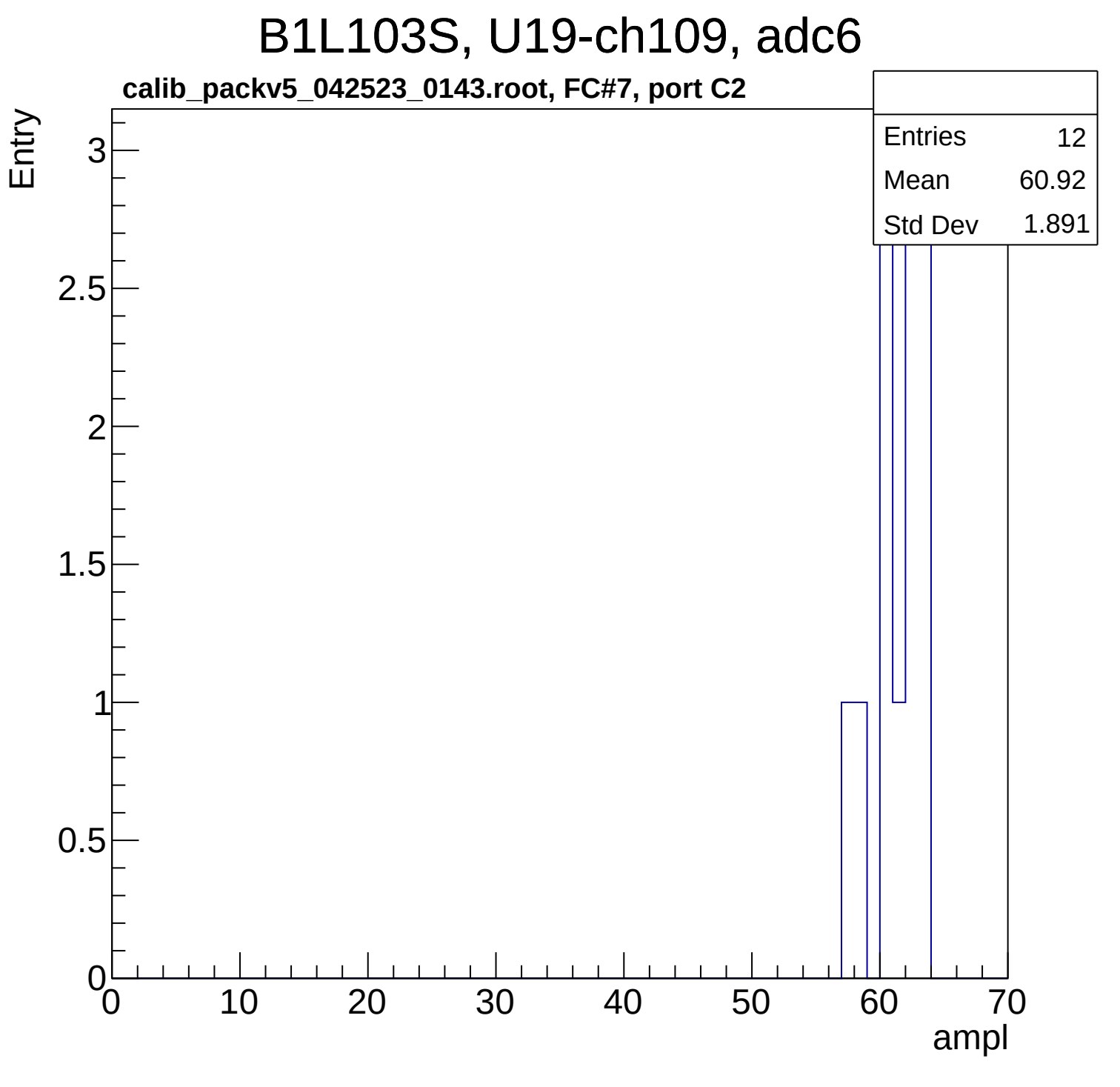

B1L103S, U19-ch109, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.41
Std Dev 9.768
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch109, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 60.92
Std Dev 1.891
Entry
ampl
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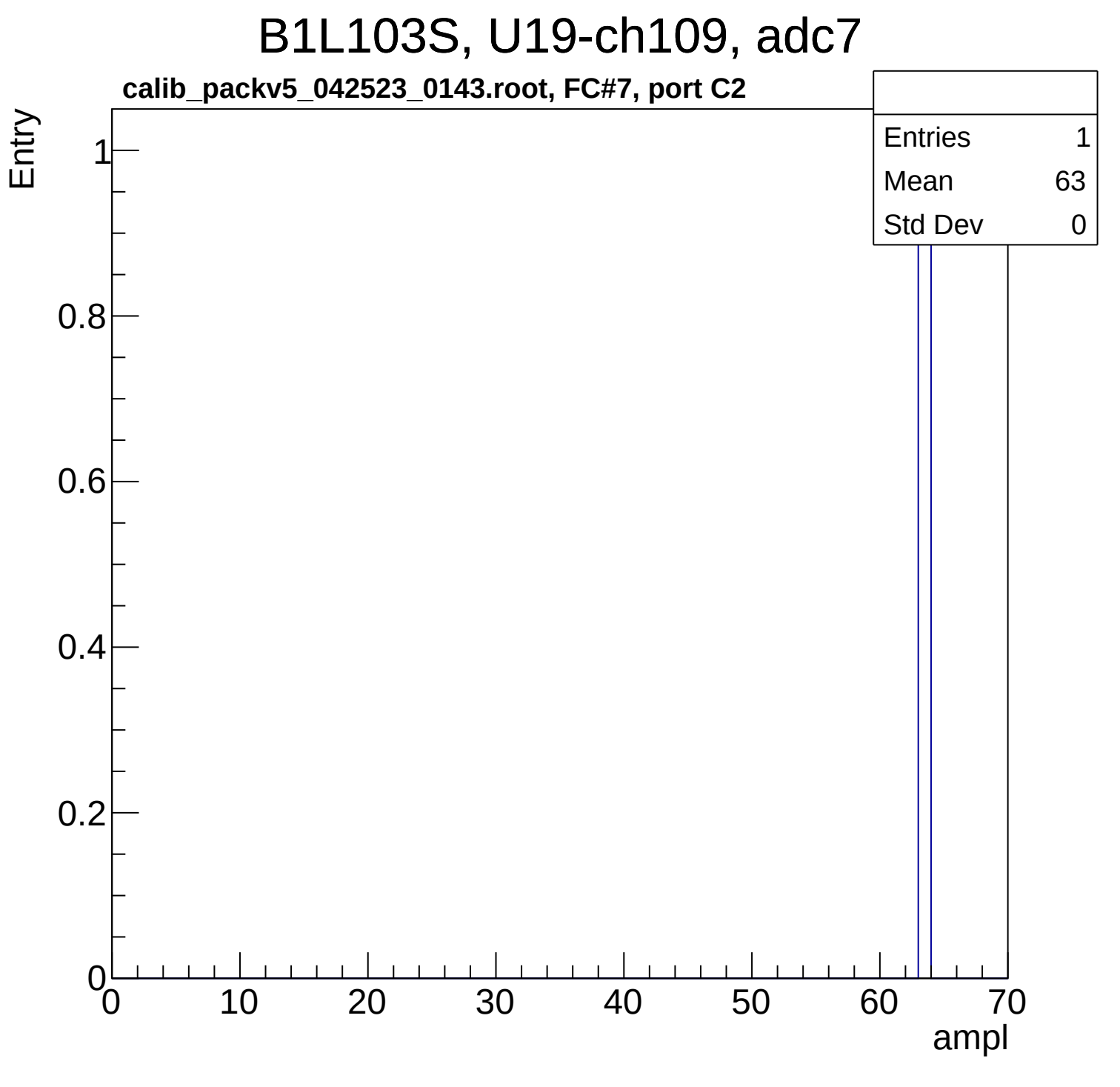
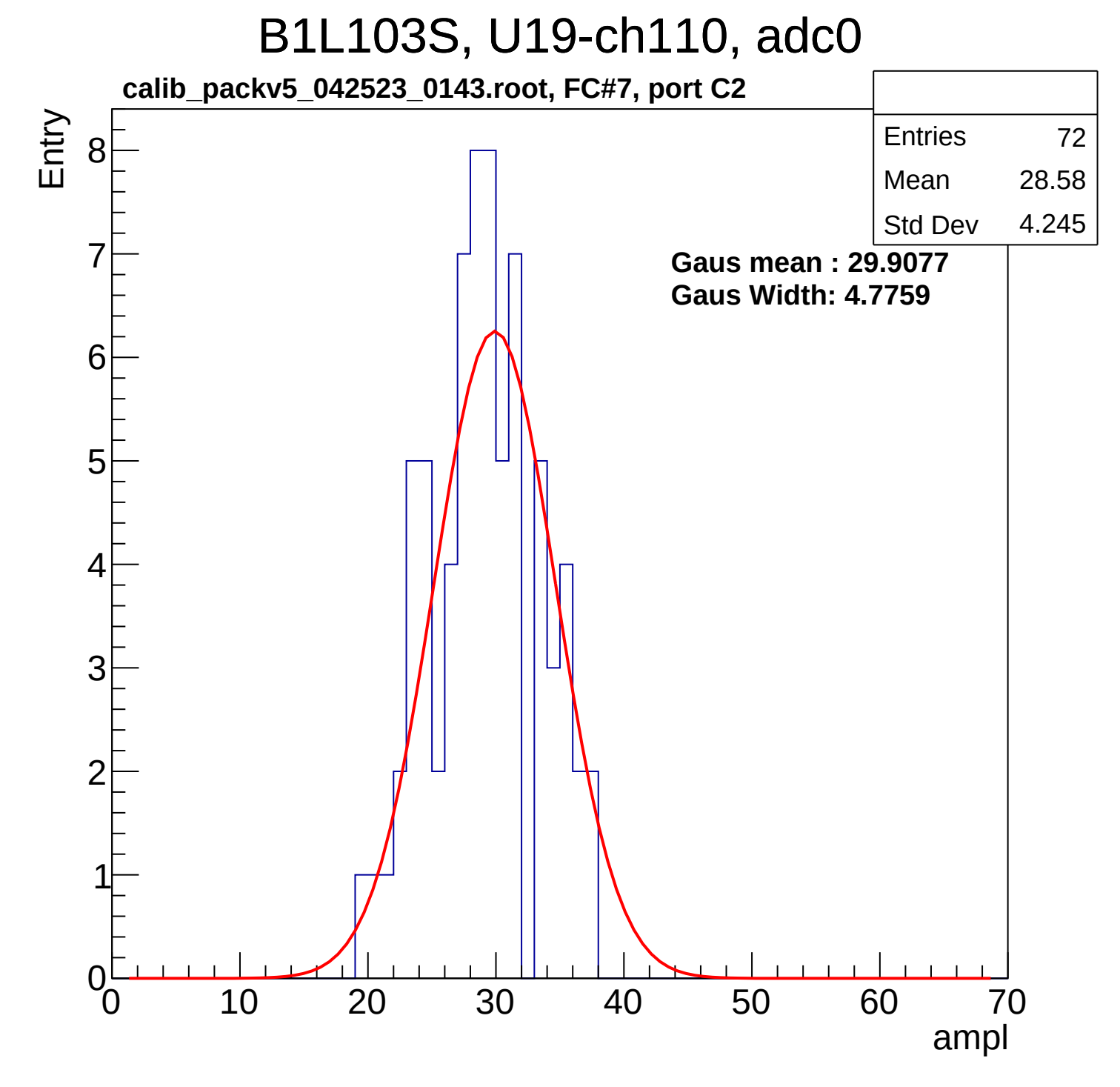
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch109, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch110, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 28.58
Std Dev 4.245
Gaus mean : 29.9077
Gaus Width: 4.7759
Entry
ampl
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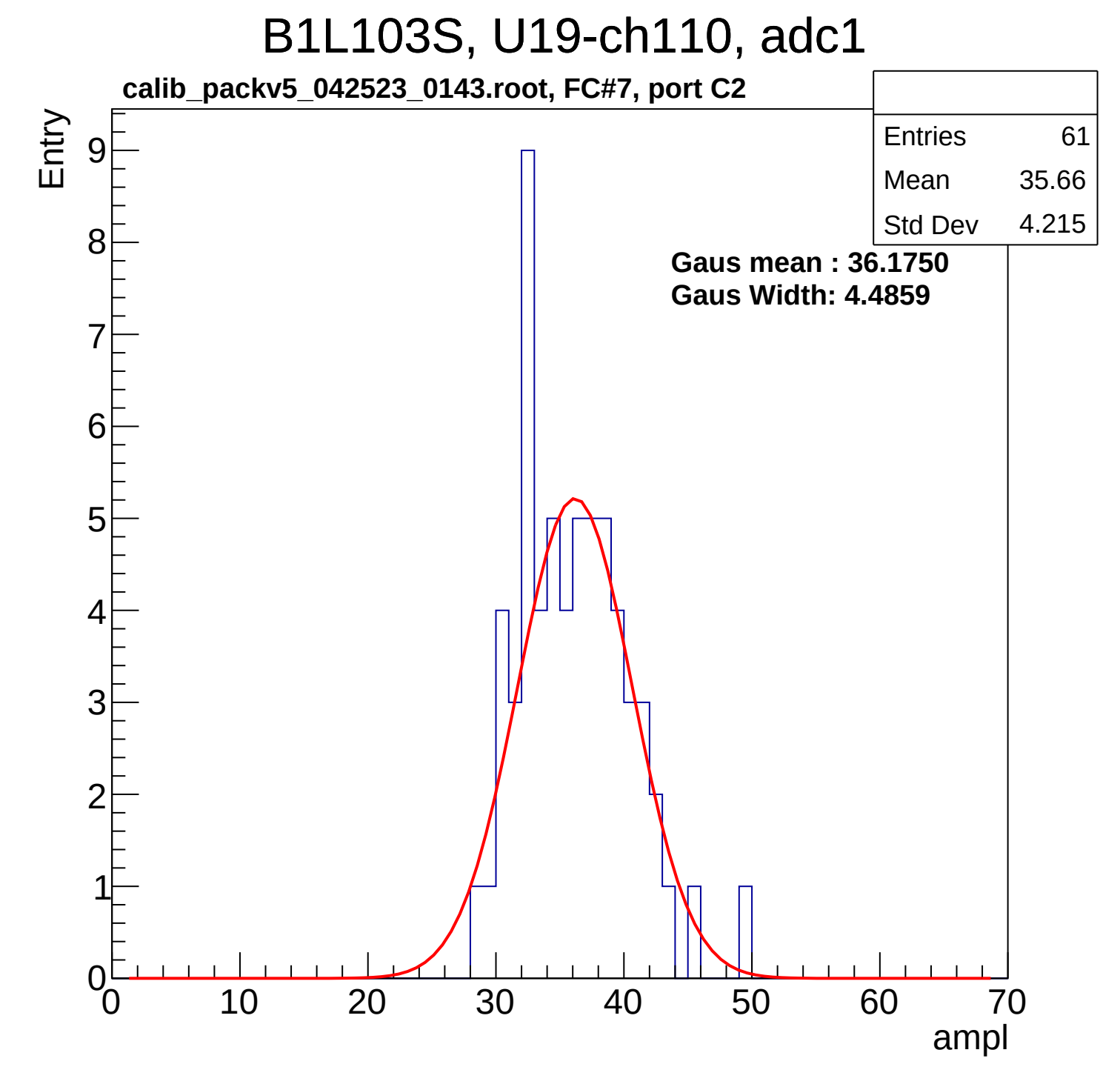
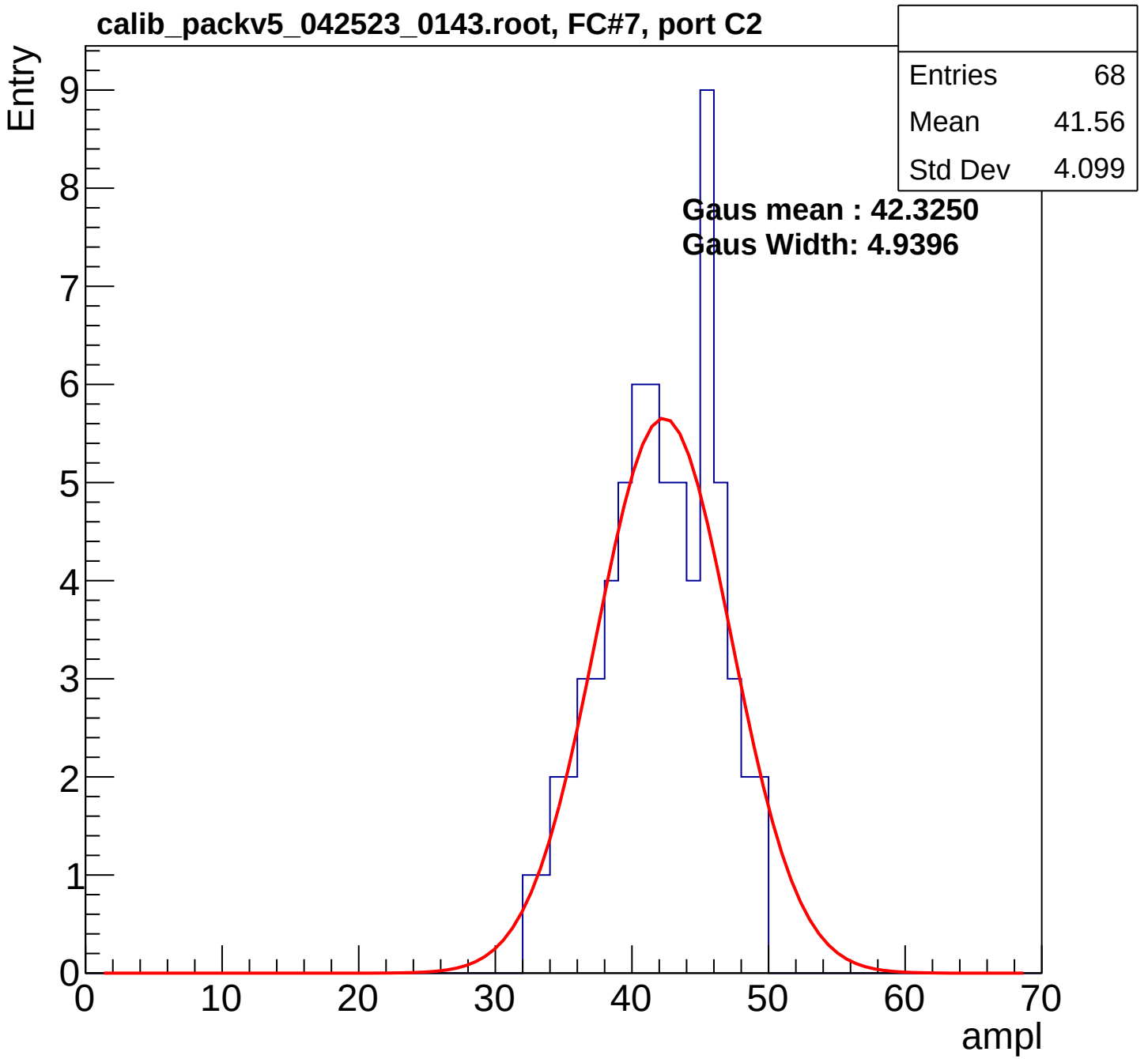
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch110, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 35.66
Std Dev 4.215
Gaus mean : 36.1750
Gaus Width: 4.4859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch110, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 41.56
Std Dev 4.099
Gaus mean : 42.3250
Gaus Width: 4.9396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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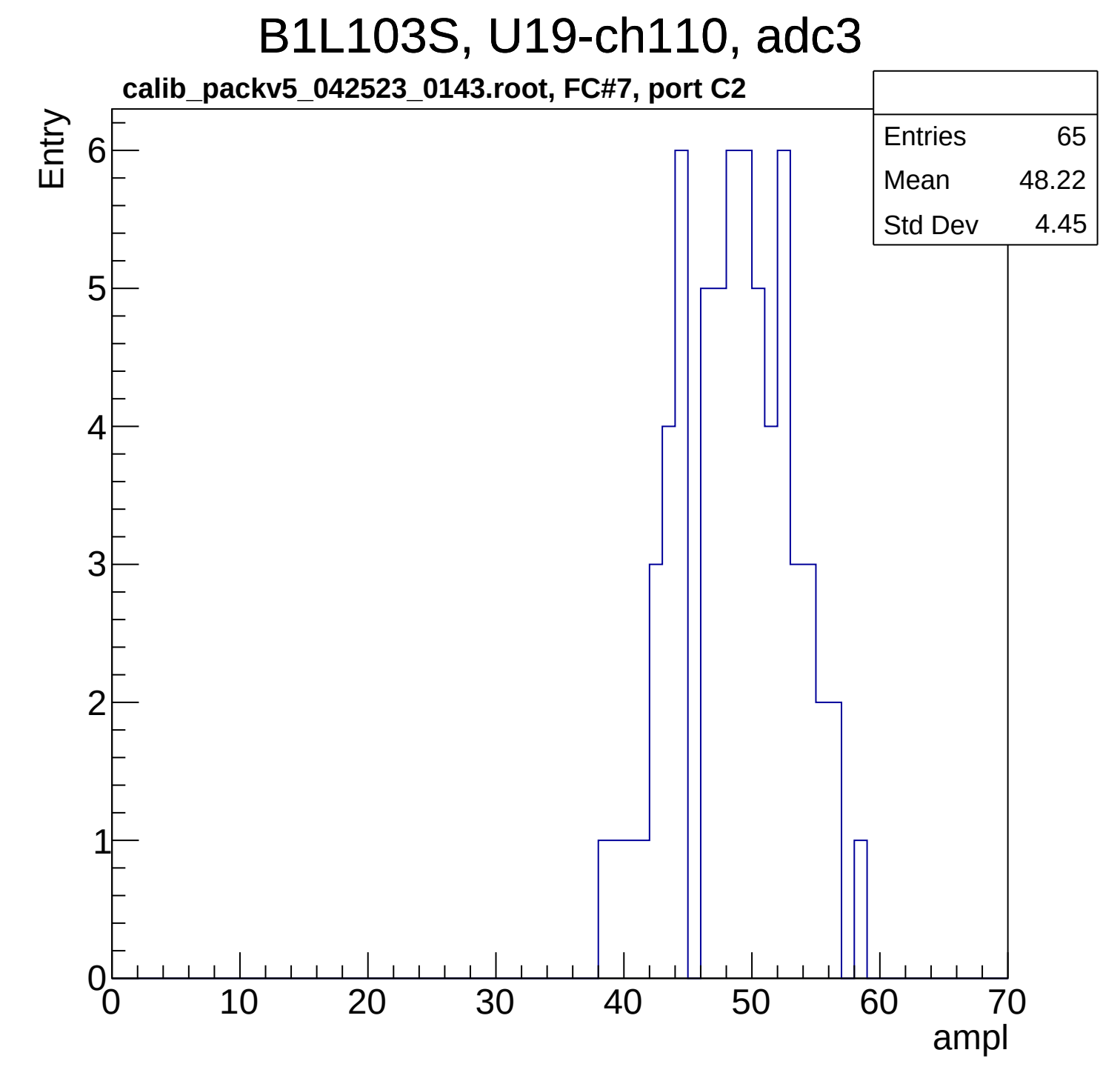
B1L103S, U19-ch110, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.22
Std Dev 4.45
Entry
ampl
0
1
2
3
4
5
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0
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20
30
40
50
60
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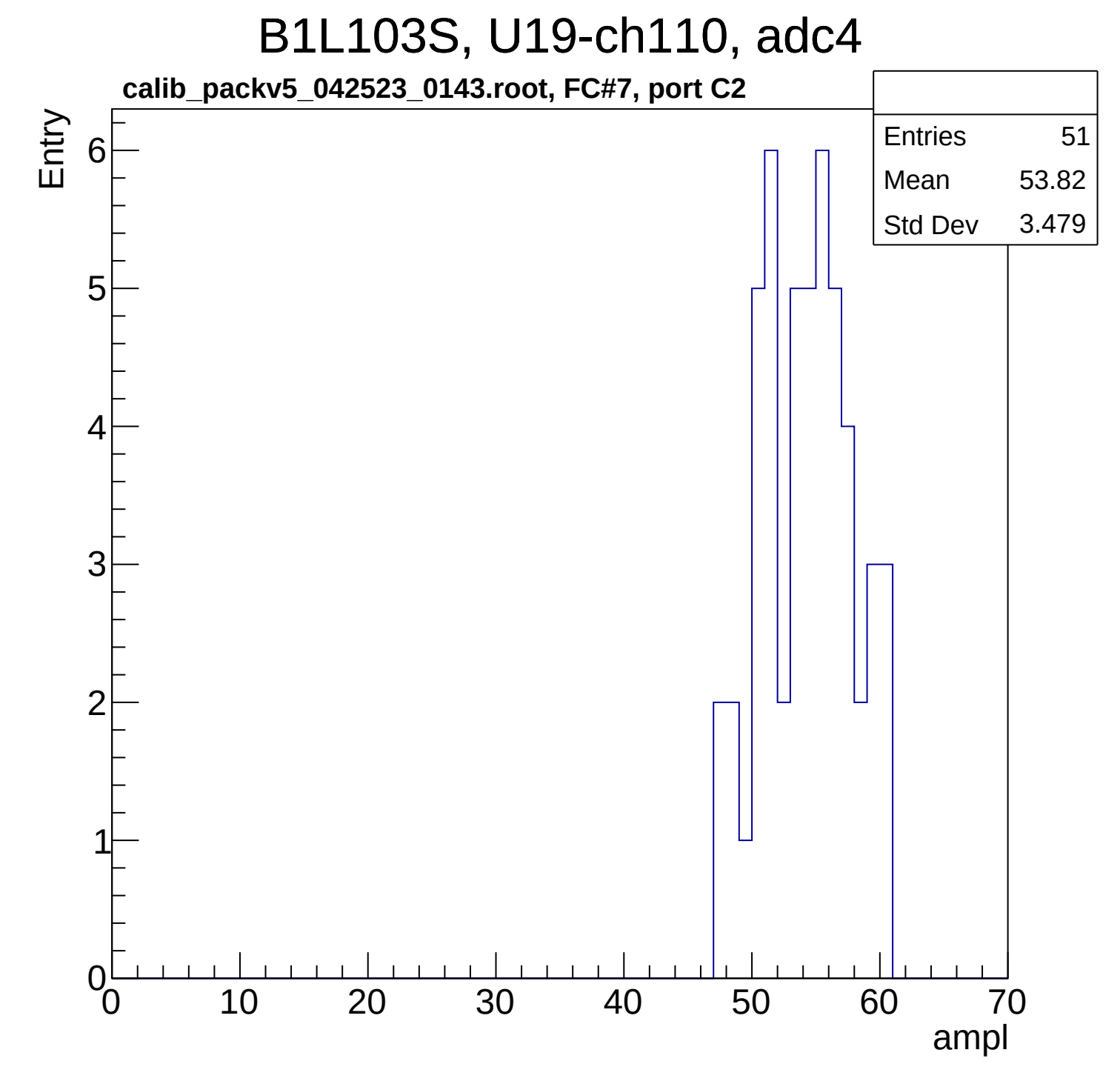
B1L103S, U19-ch110, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 53.82
Std Dev 3.479
Entry
ampl
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3
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5
6
0
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20
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40
50
60
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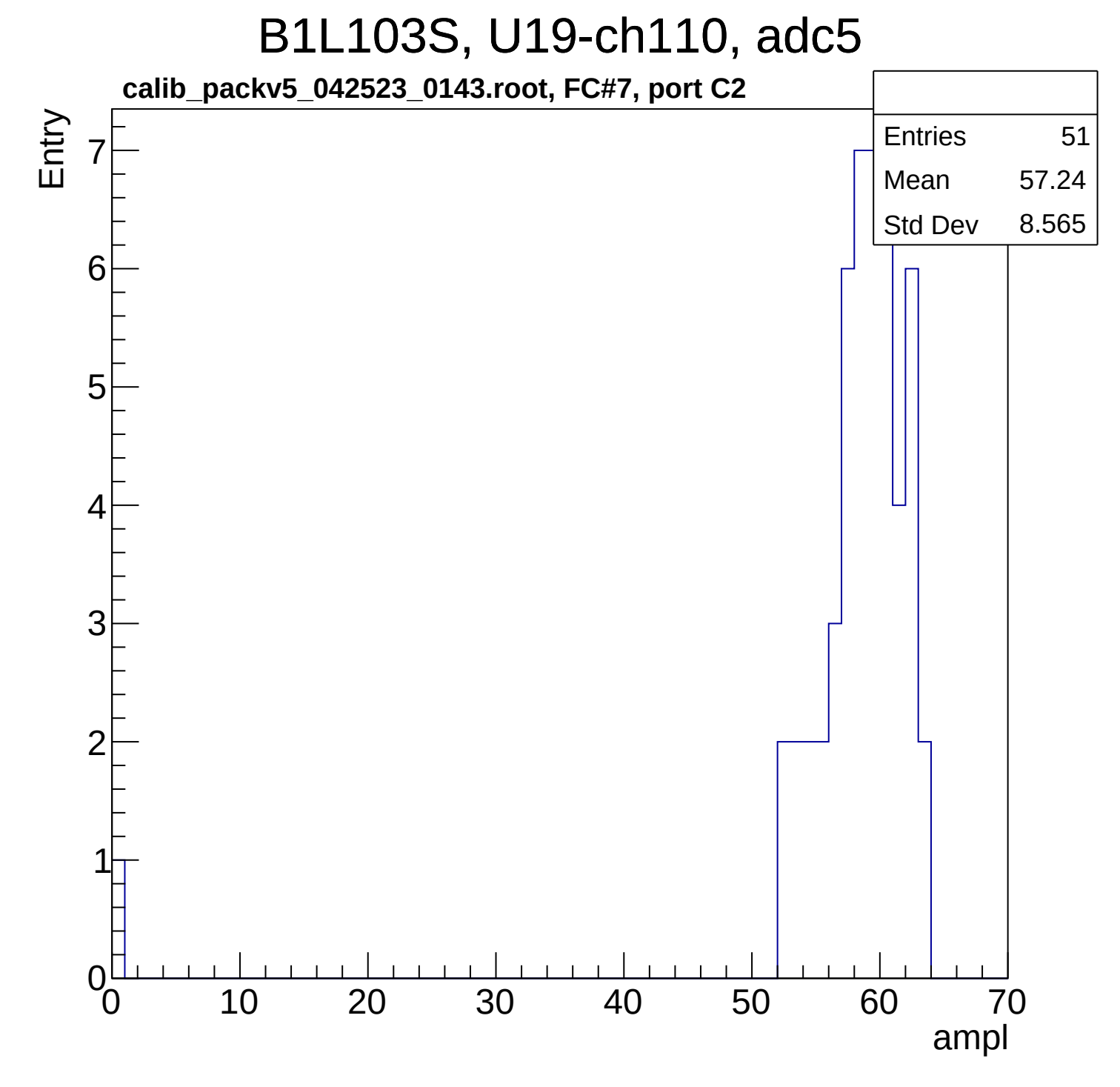
B1L103S, U19-ch110, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.24
Std Dev 8.565
Entry
ampl
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0
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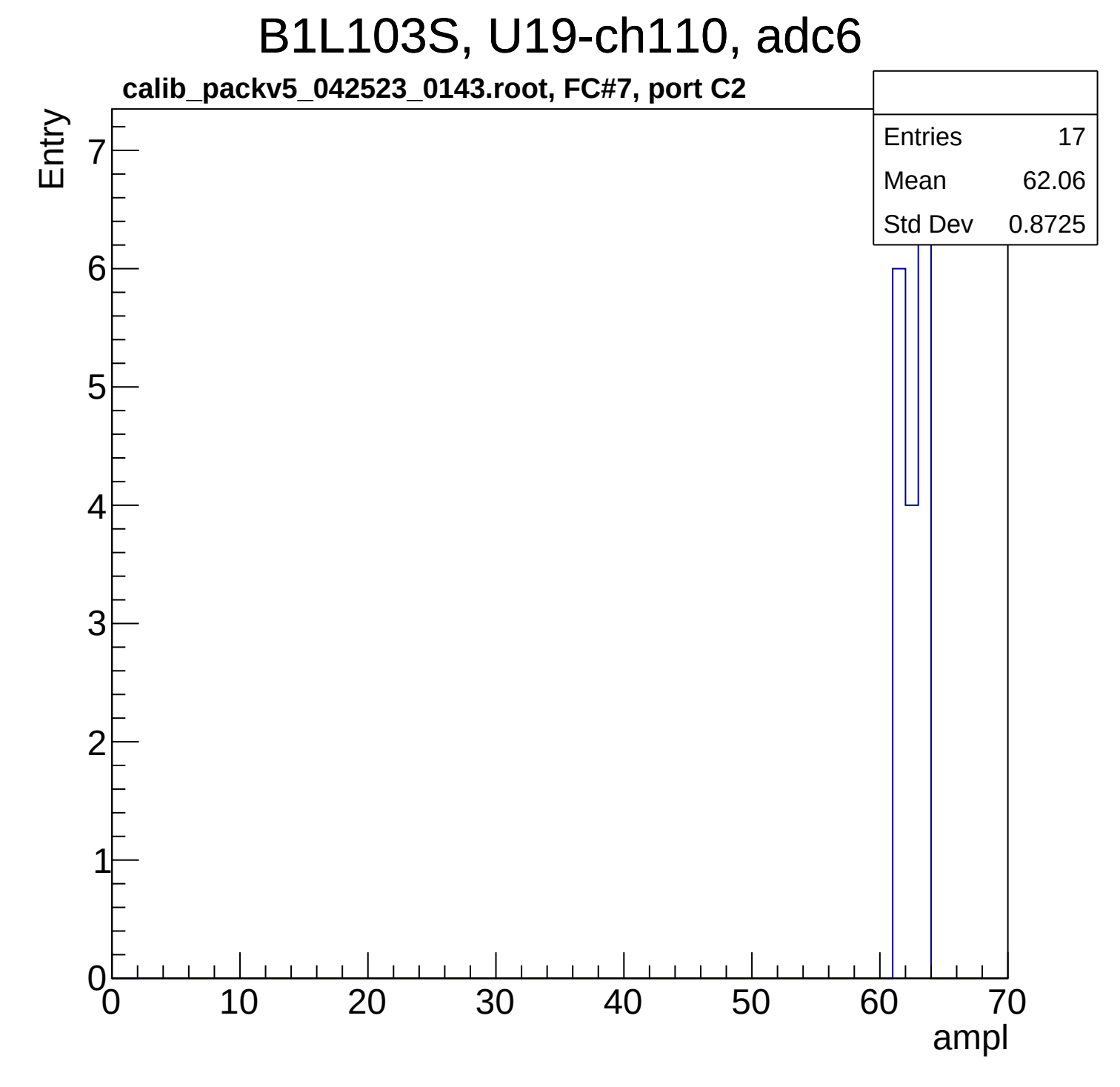
40
50
60
70


B1L103S, U19-ch110, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 62.06
Std Dev 0.8725
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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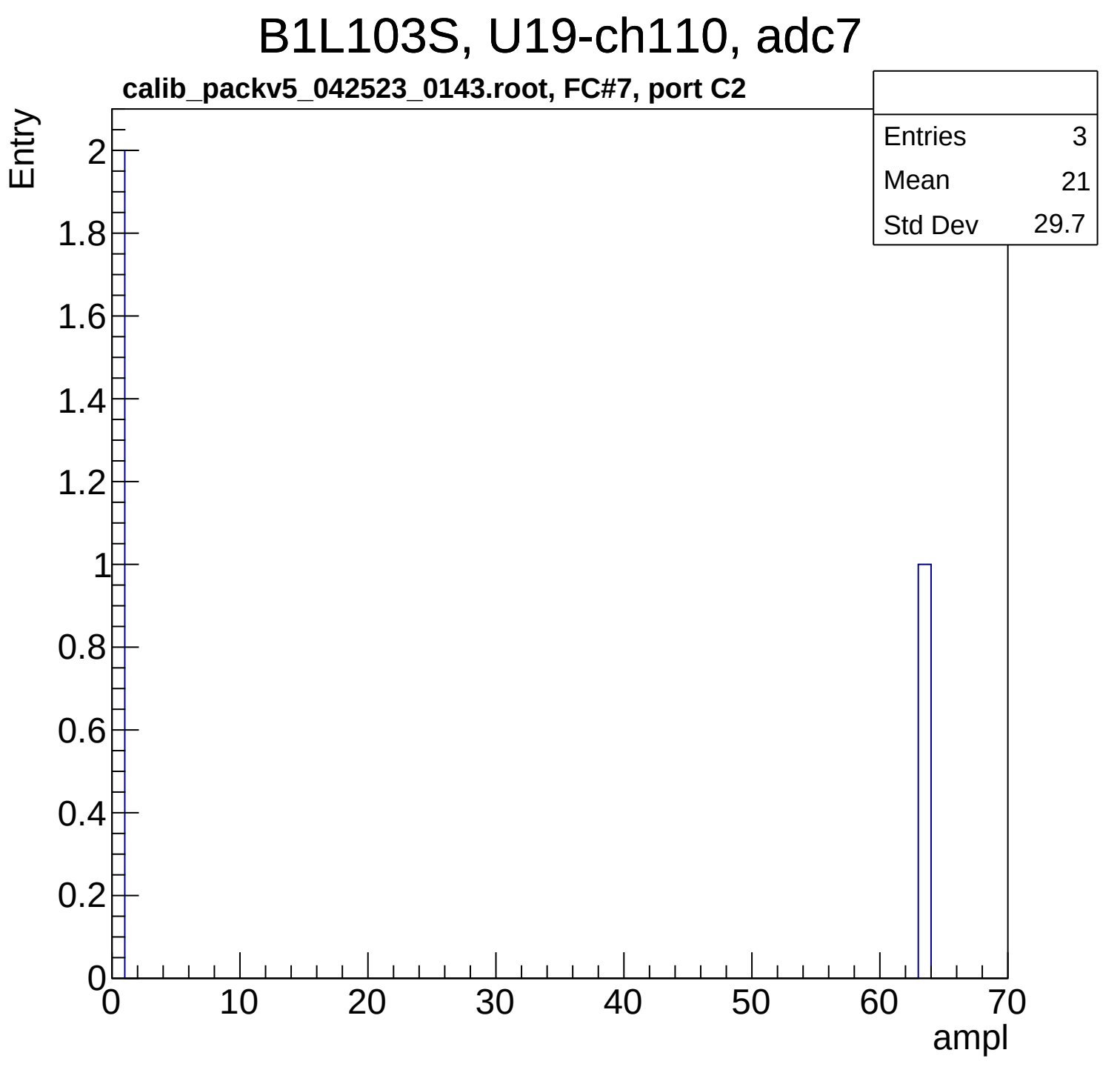
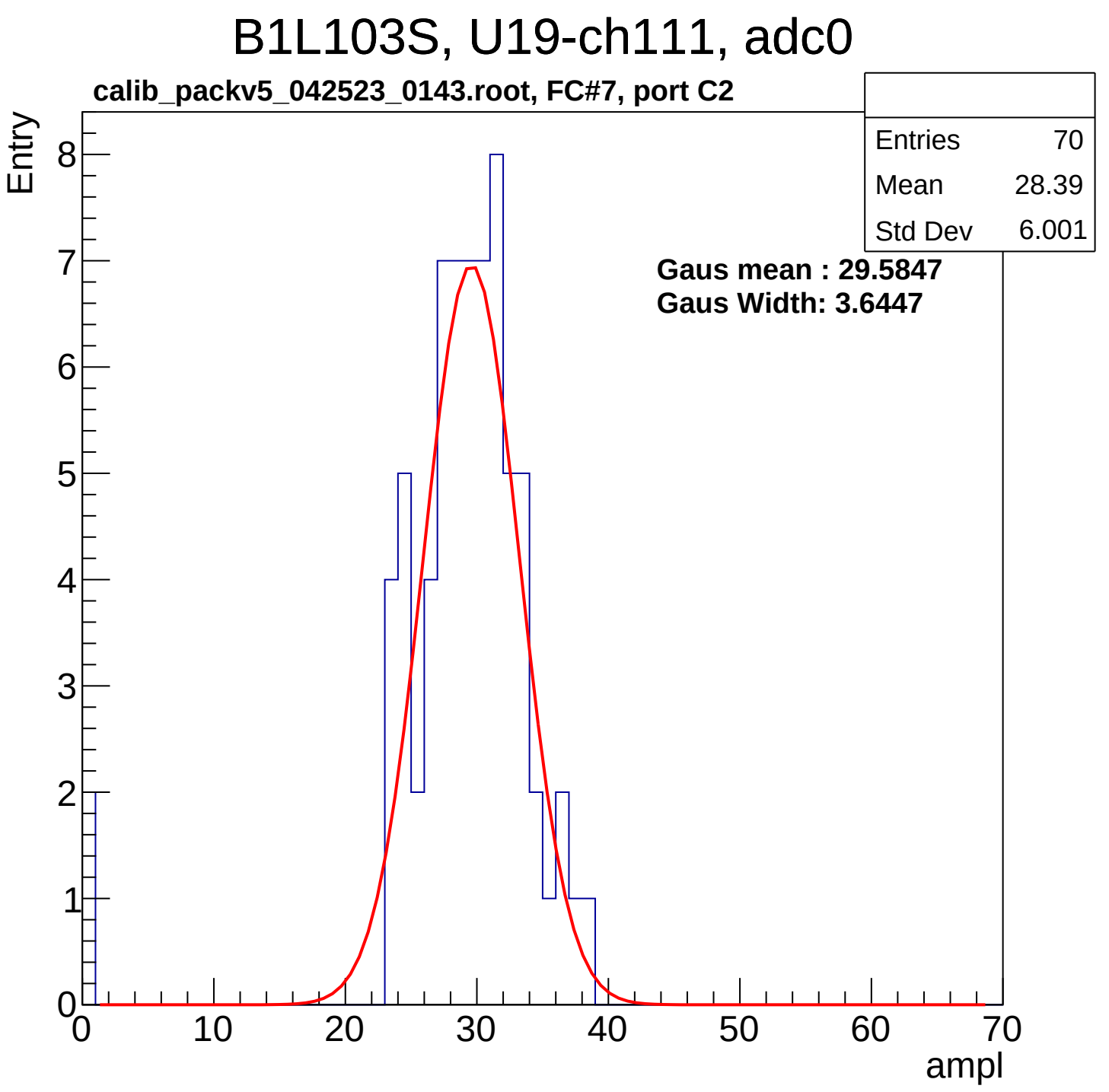

B1L103S, U19-ch110, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch111, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 28.39
Std Dev 6.001
Gaus mean : 29.5847
Gaus Width: 3.6447
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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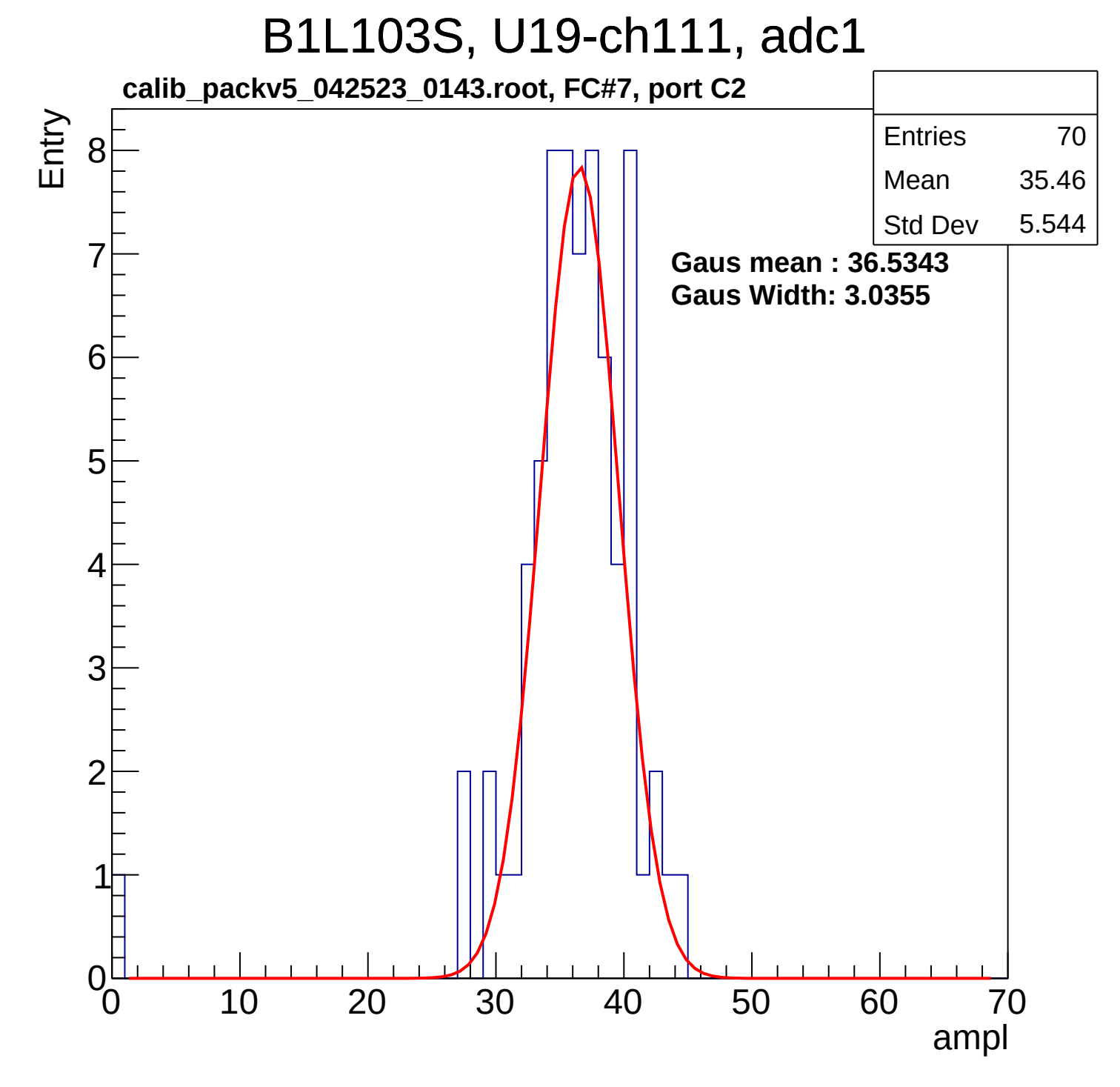
B1L103S, U19-ch111, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 35.46
Std Dev 5.544
Gaus mean : 36.5343
Gaus Width: 3.0355
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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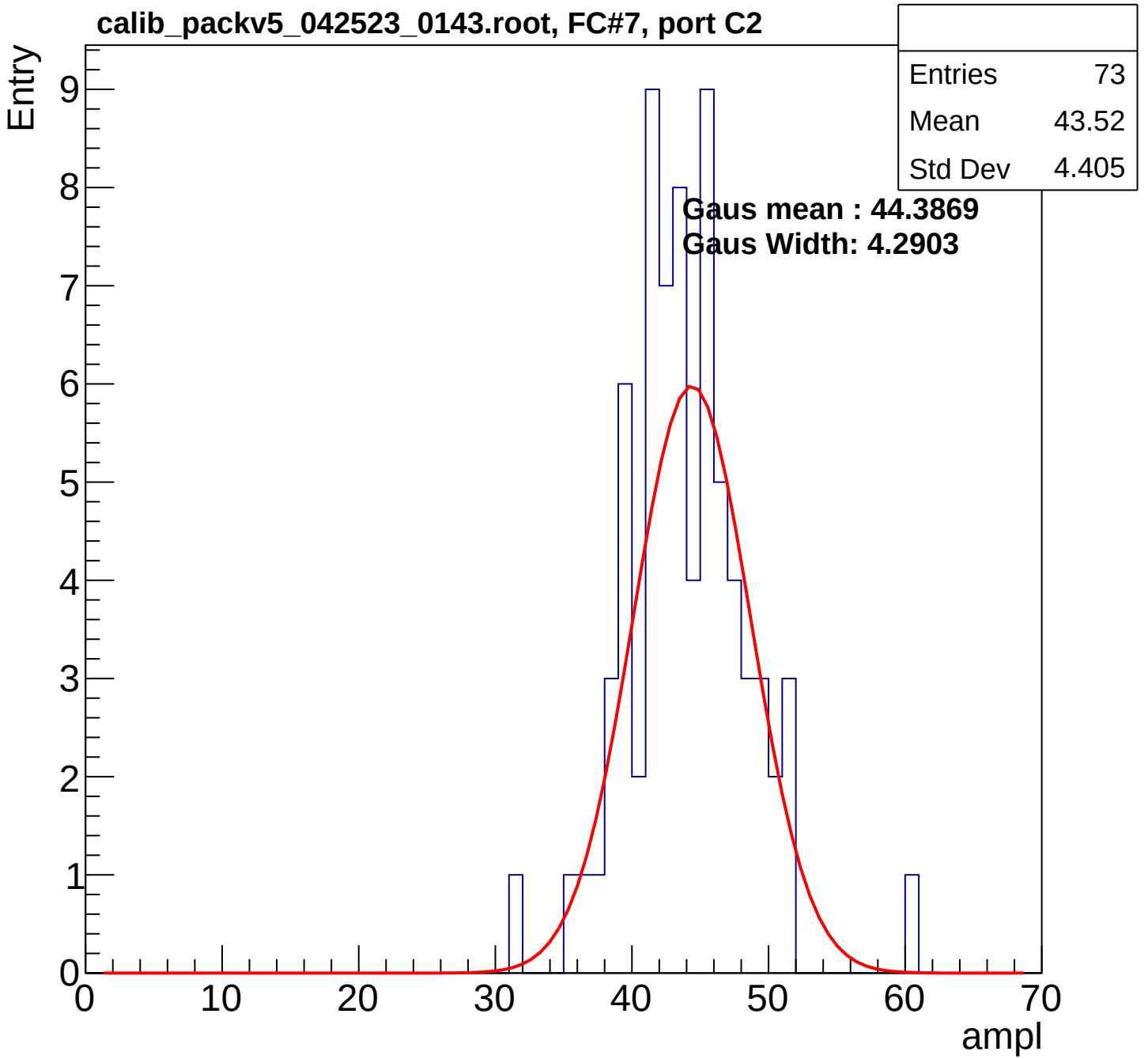
B1L103S, U19-ch111, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 43.52
Std Dev 4.405
Gaus mean : 44.3869
Gaus Width: 4.2903
Entry
ampl
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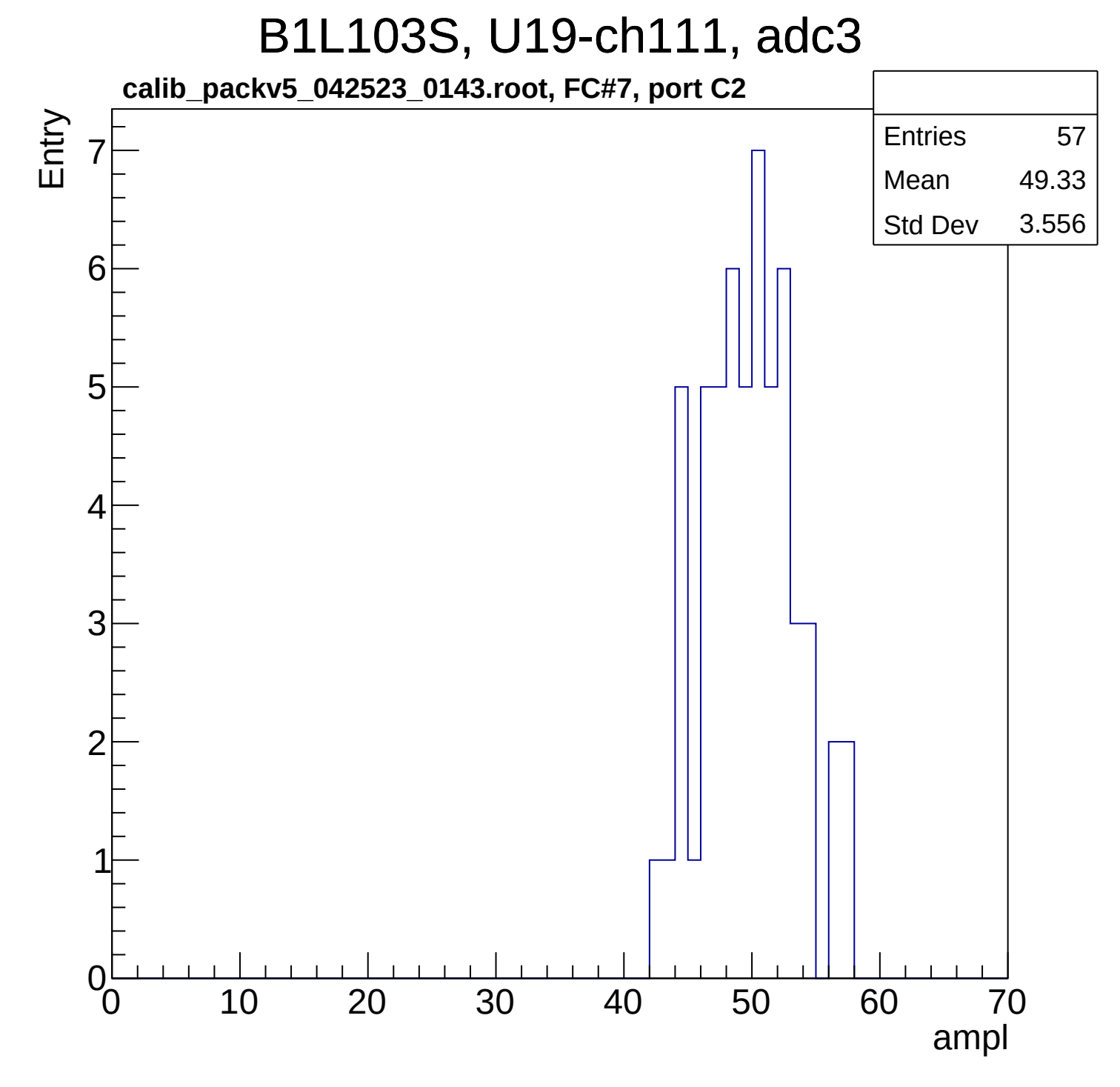
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch111, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 49.33
Std Dev 3.556
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
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60
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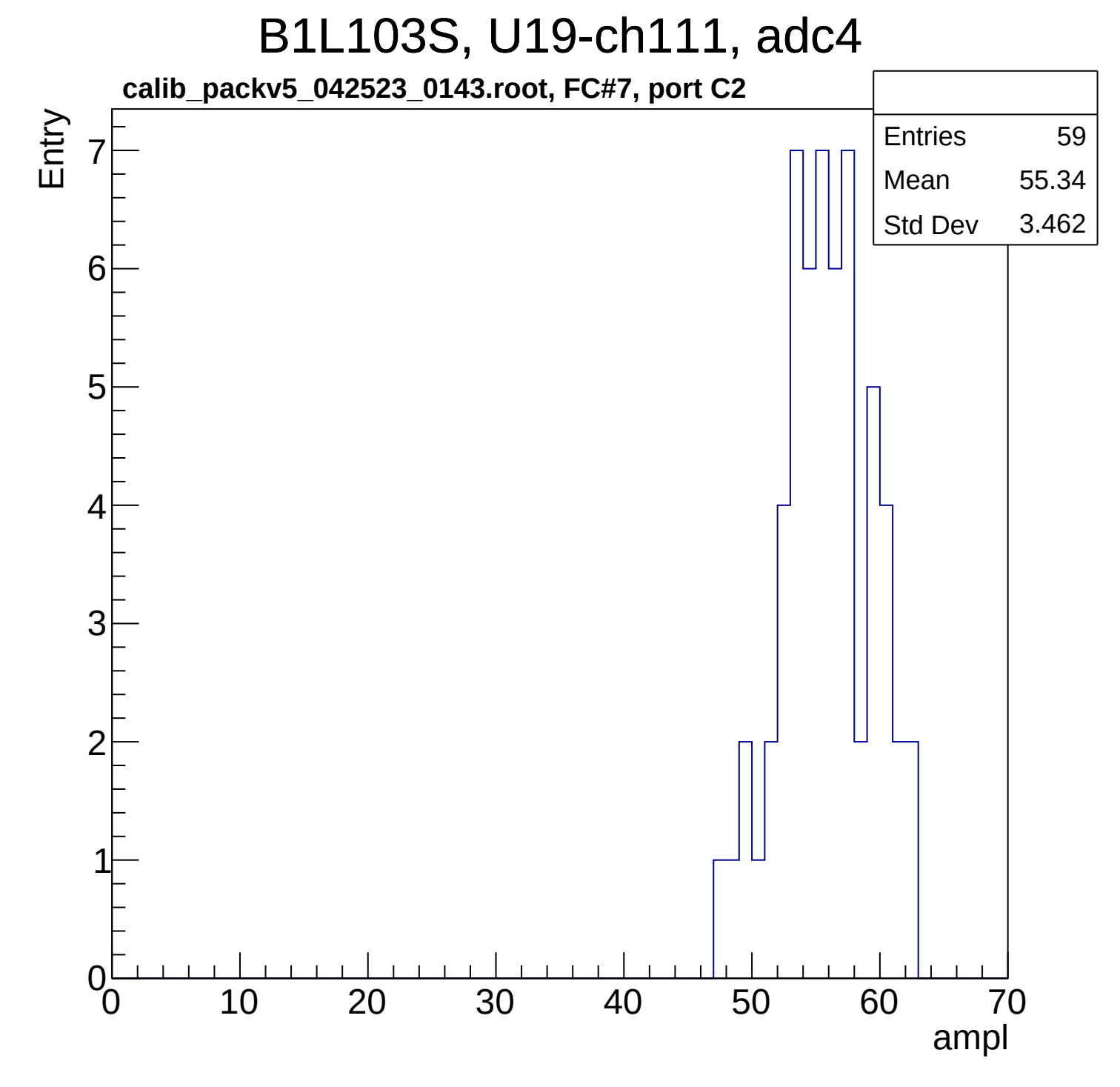
B1L103S, U19-ch111, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.34
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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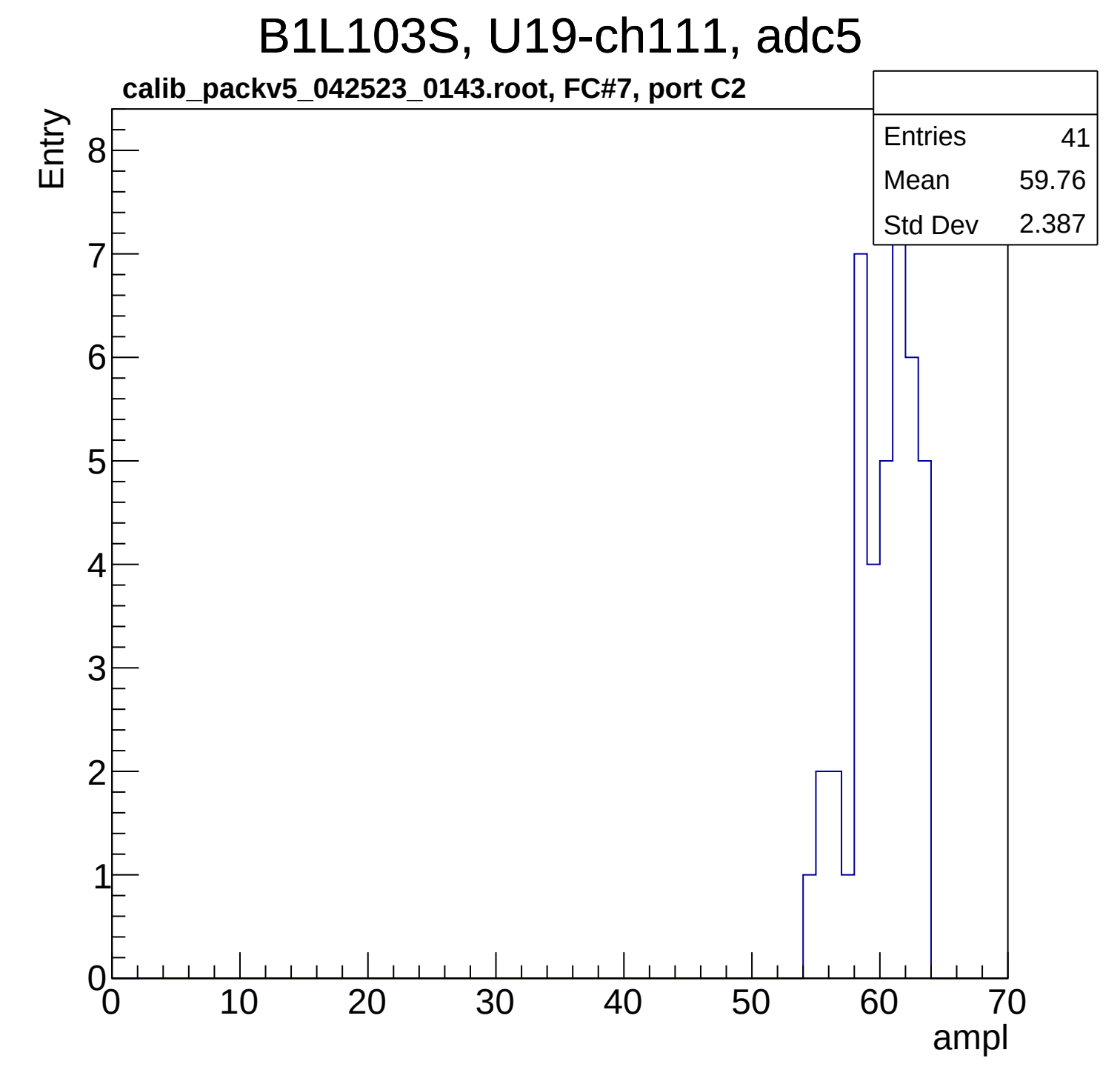
B1L103S, U19-ch111, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.76
Std Dev 2.387
Entry
ampl
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1
2
3
4
5
6
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0
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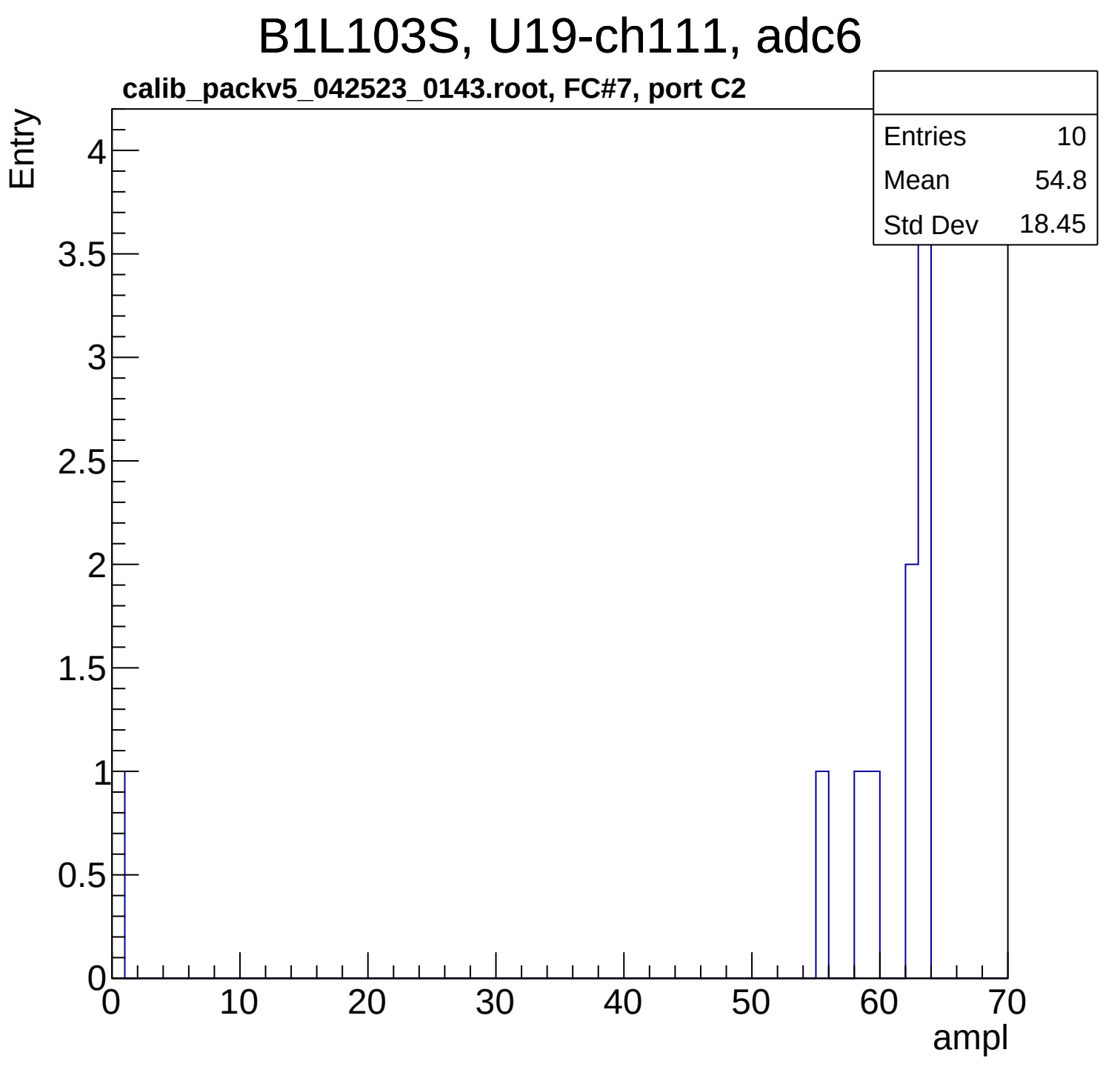
B1L103S, U19-ch111, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 54.8
Std Dev 18.45
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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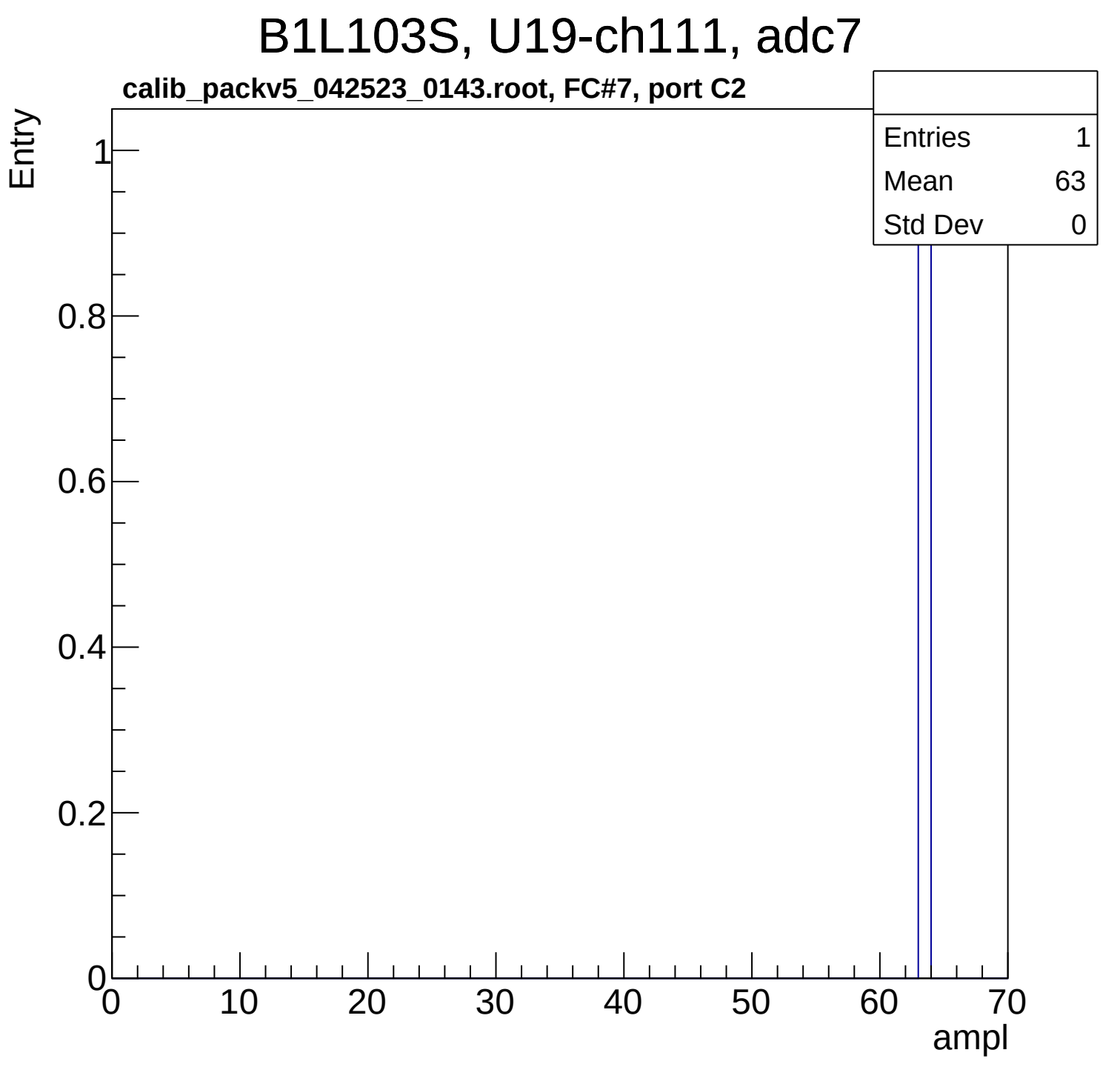
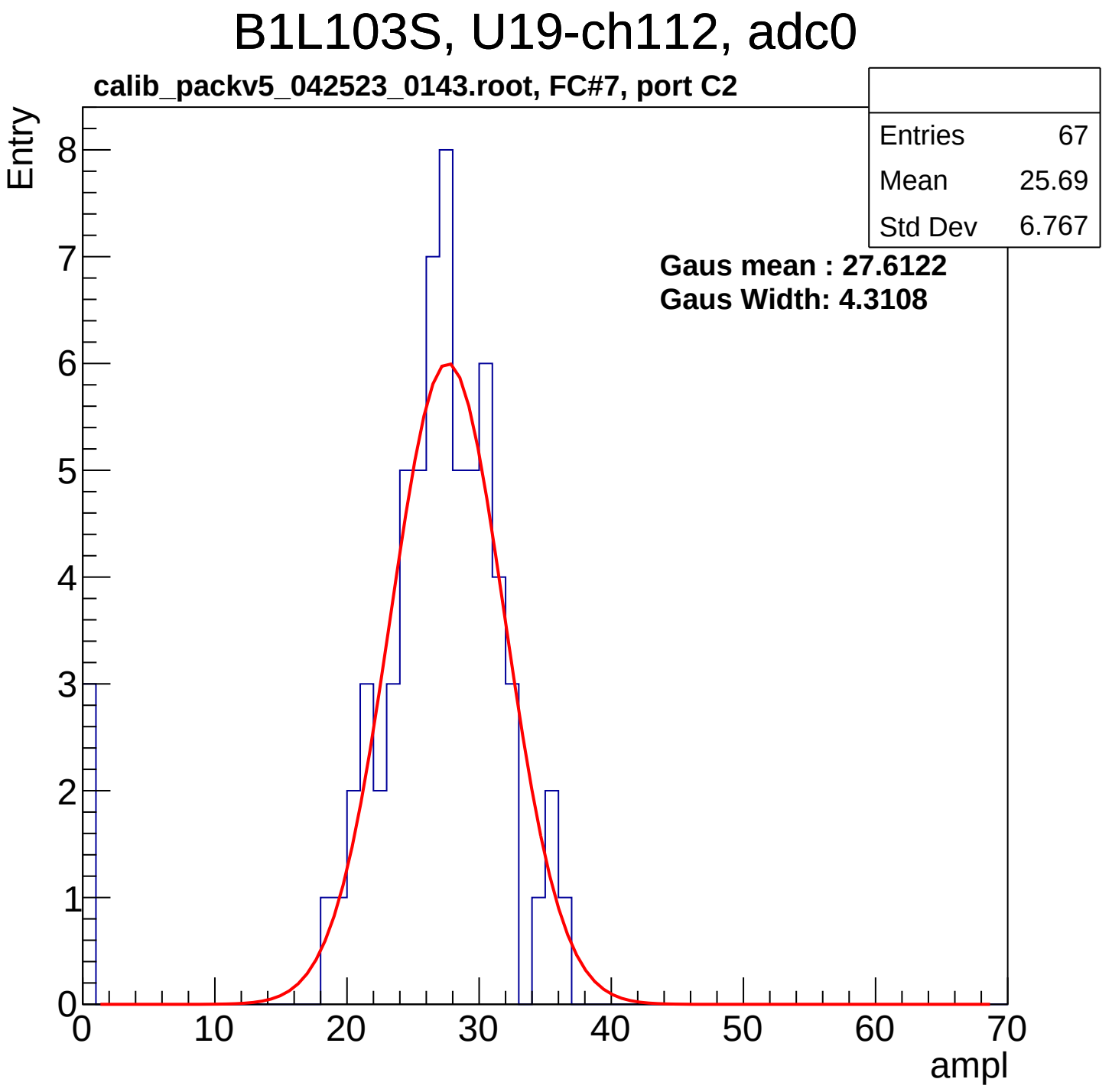

B1L103S, U19-ch111, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch112, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 25.69
Std Dev 6.767
Gaus mean : 27.6122
Gaus Width: 4.3108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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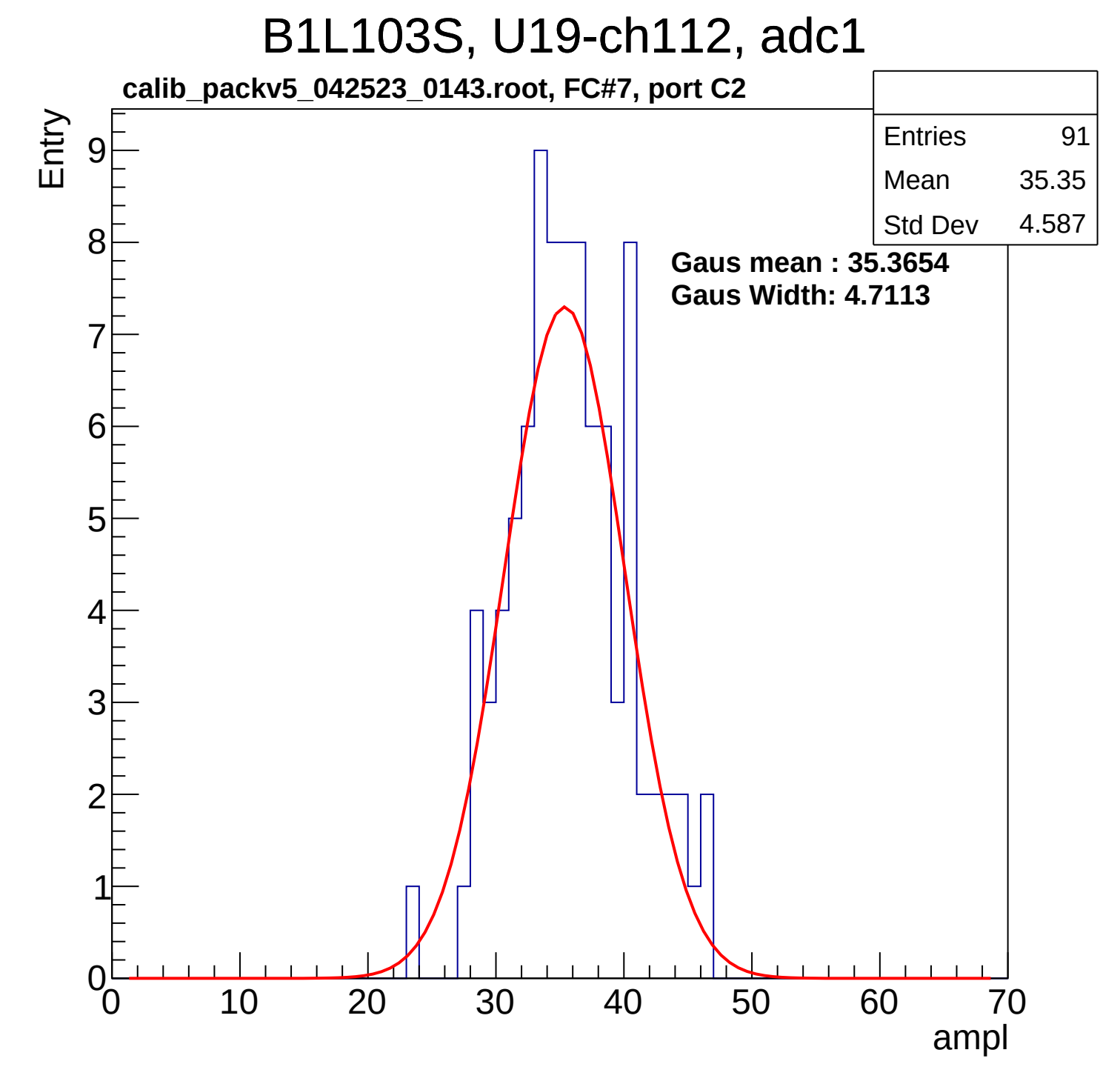
B1L103S, U19-ch112, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 35.35
Std Dev 4.587
Gaus mean : 35.3654
Gaus Width: 4.7113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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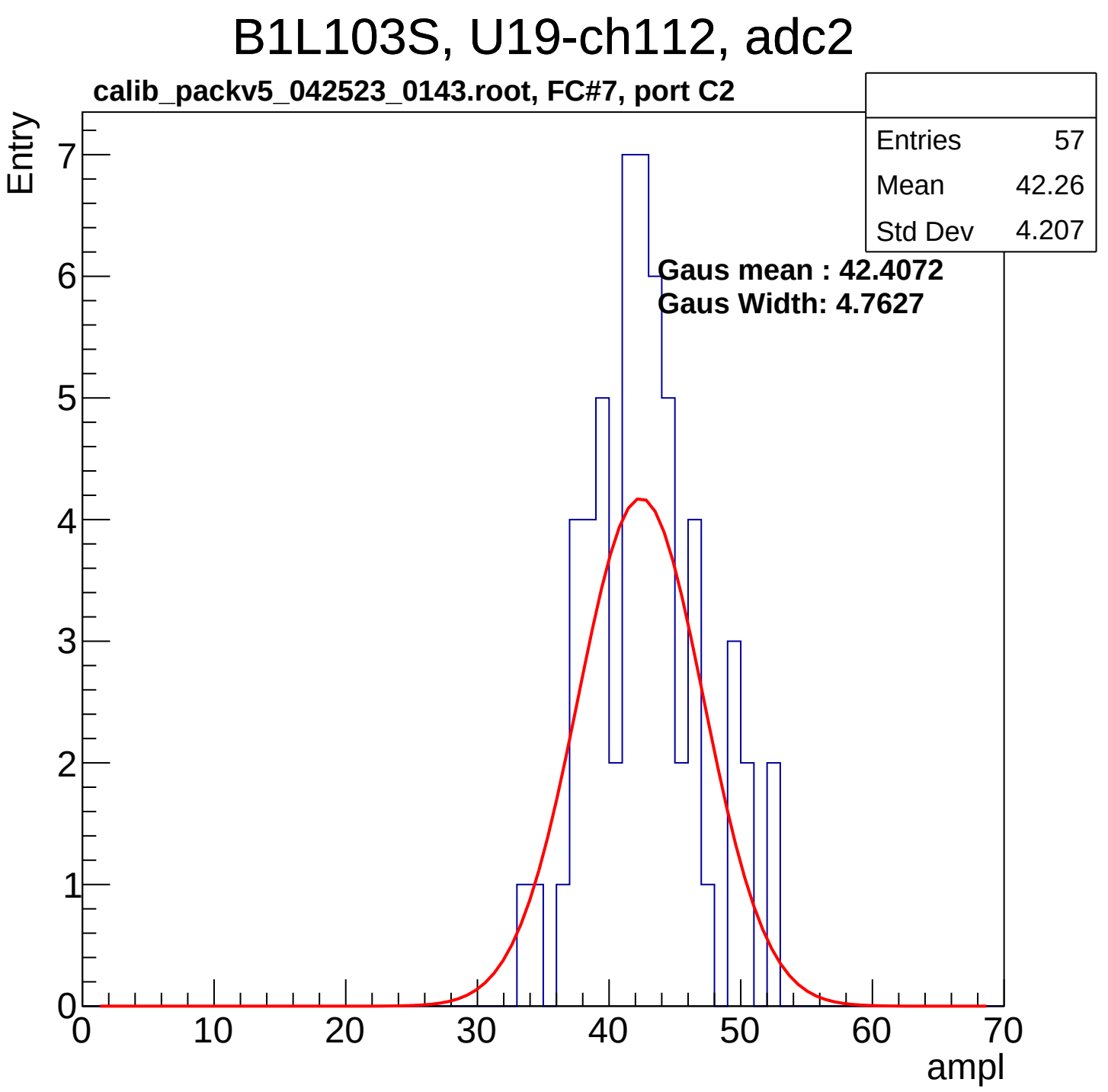
B1L103S, U19-ch112, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.26
Std Dev 4.207
Gaus mean : 42.4072
Gaus Width: 4.7627
Entry
ampl
0
1
2
3
4
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60
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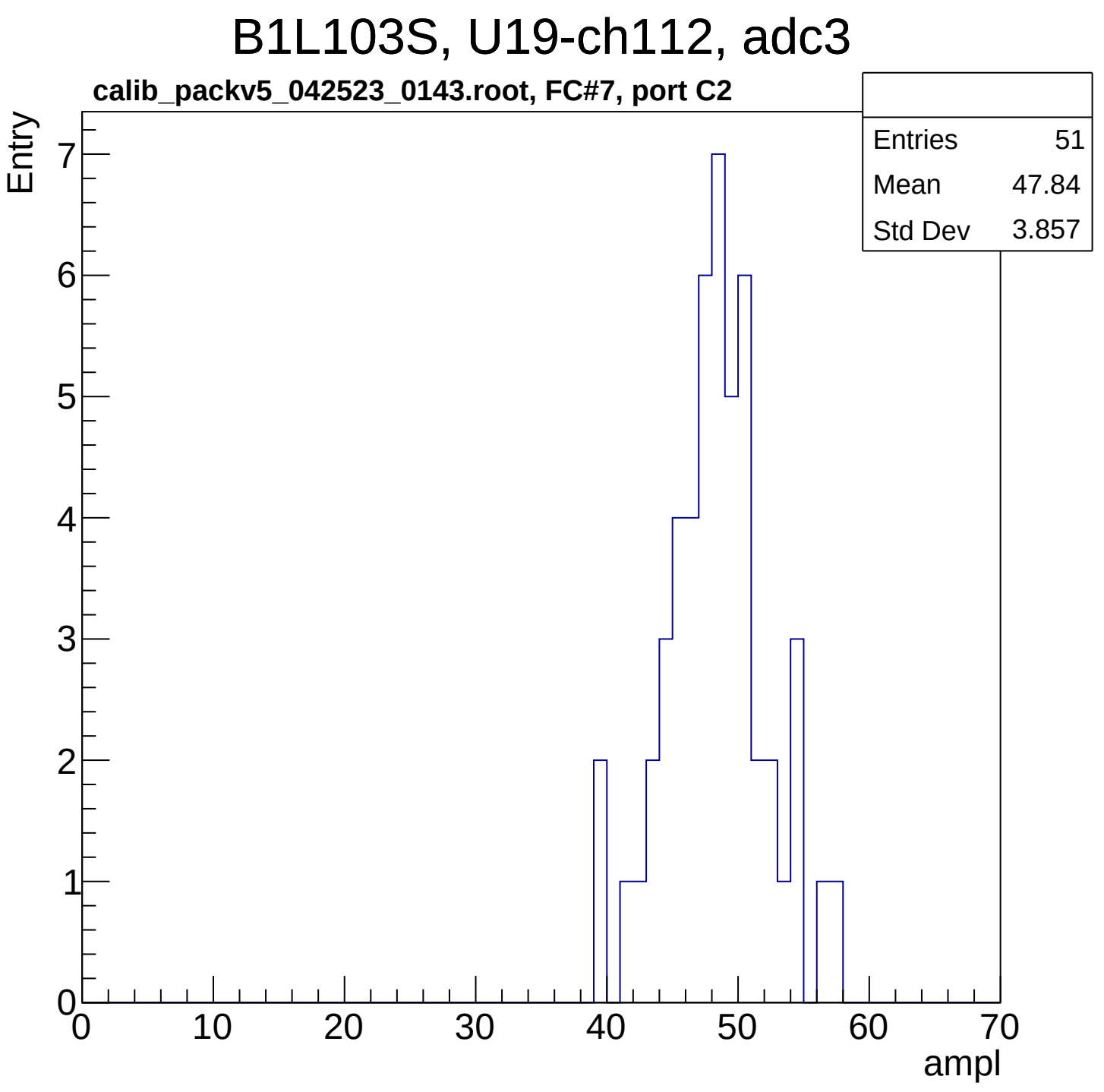
B1L103S, U19-ch112, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 47.84
Std Dev 3.857
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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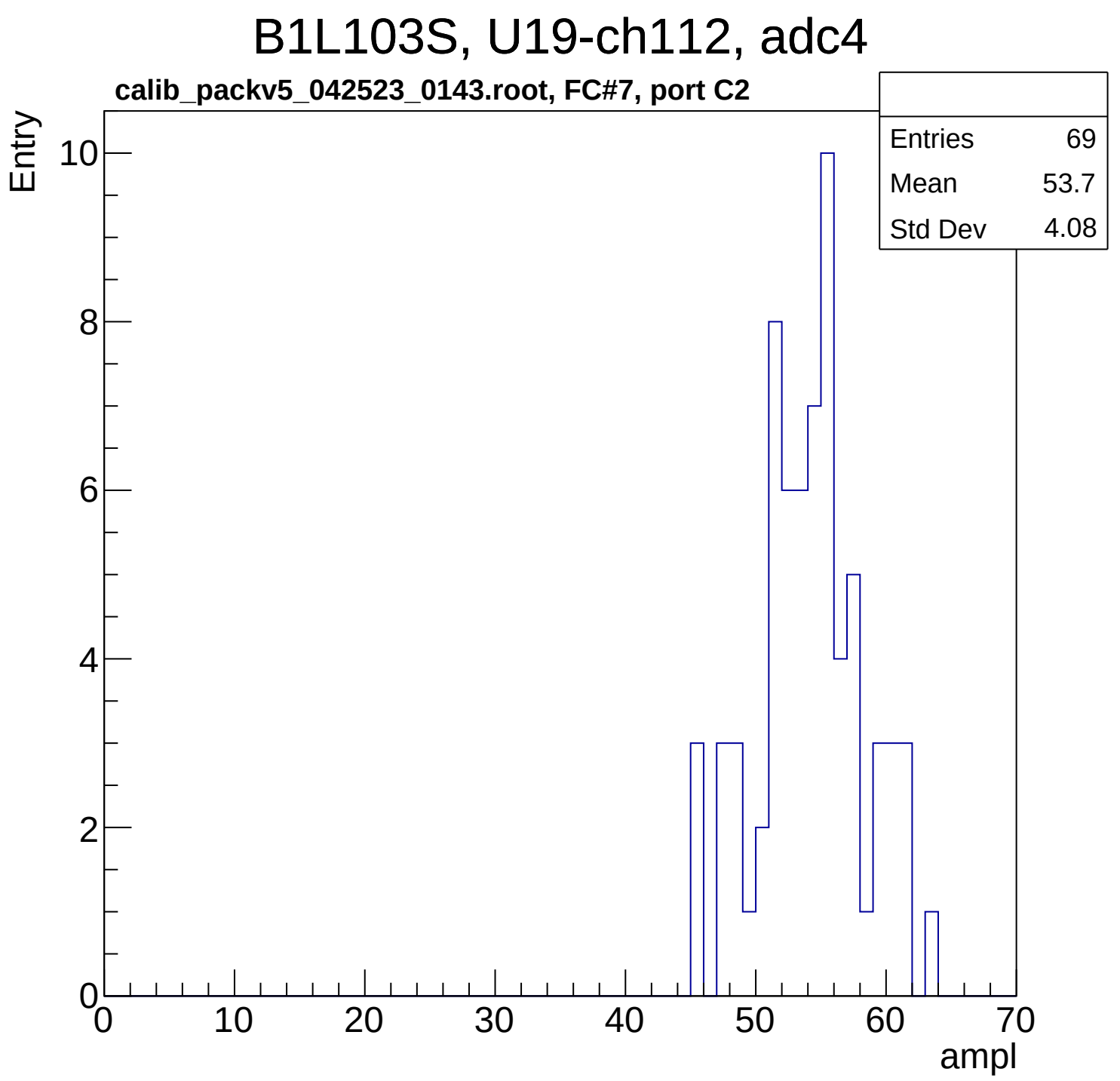
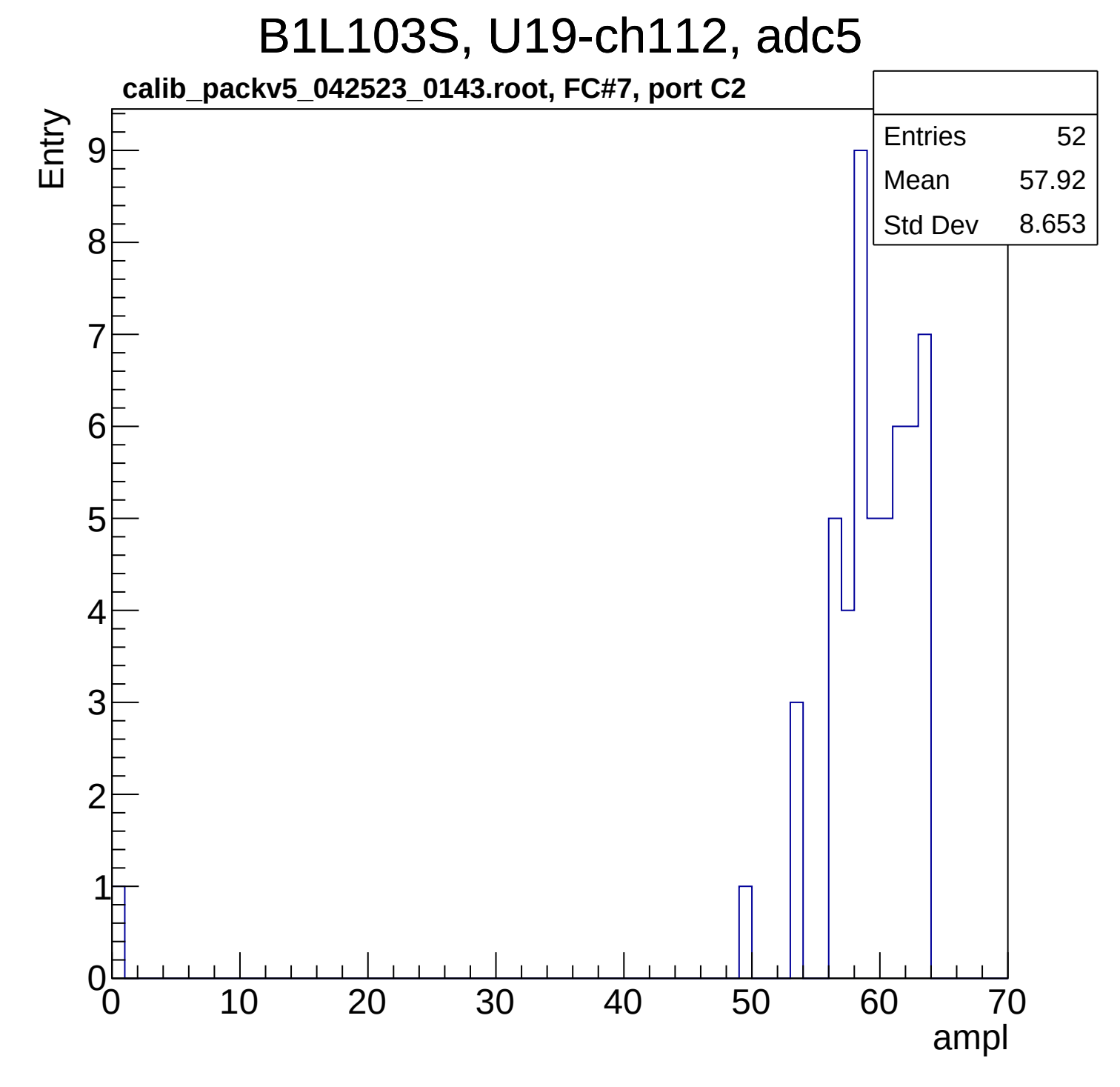

B1L103S, U19-ch112, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 53.7
Std Dev 4.08
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch112, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.92
Std Dev 8.653
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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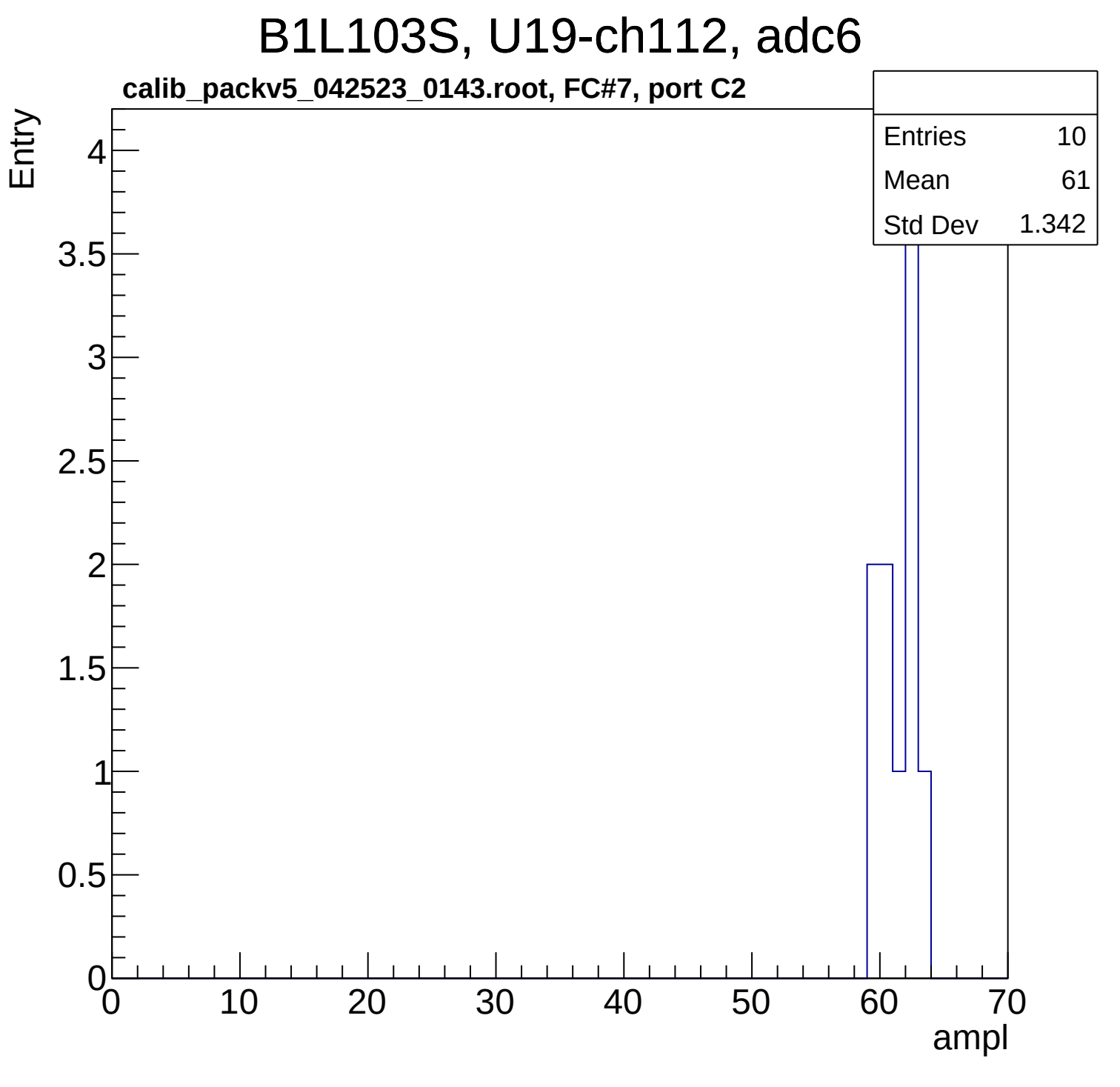
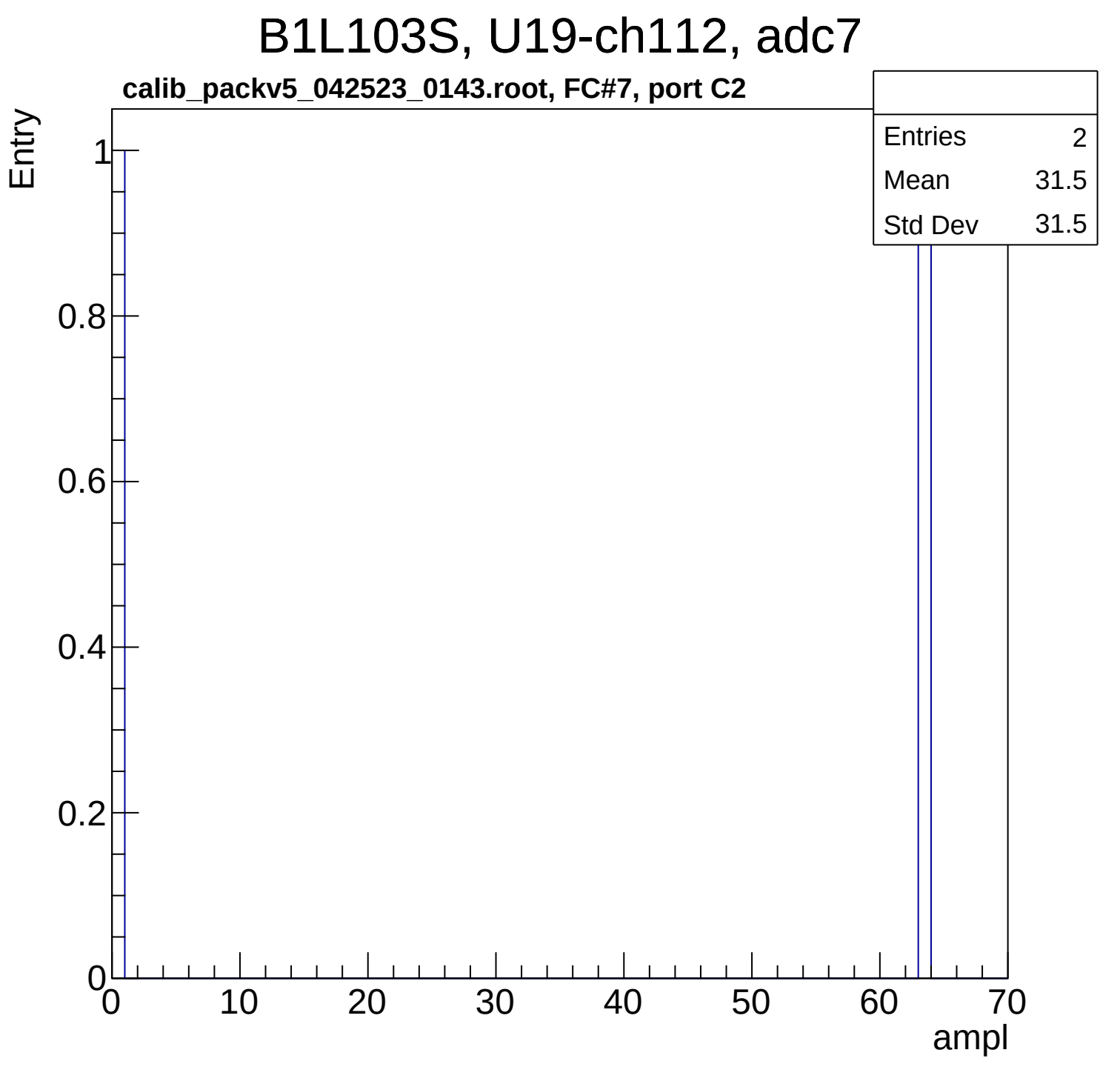
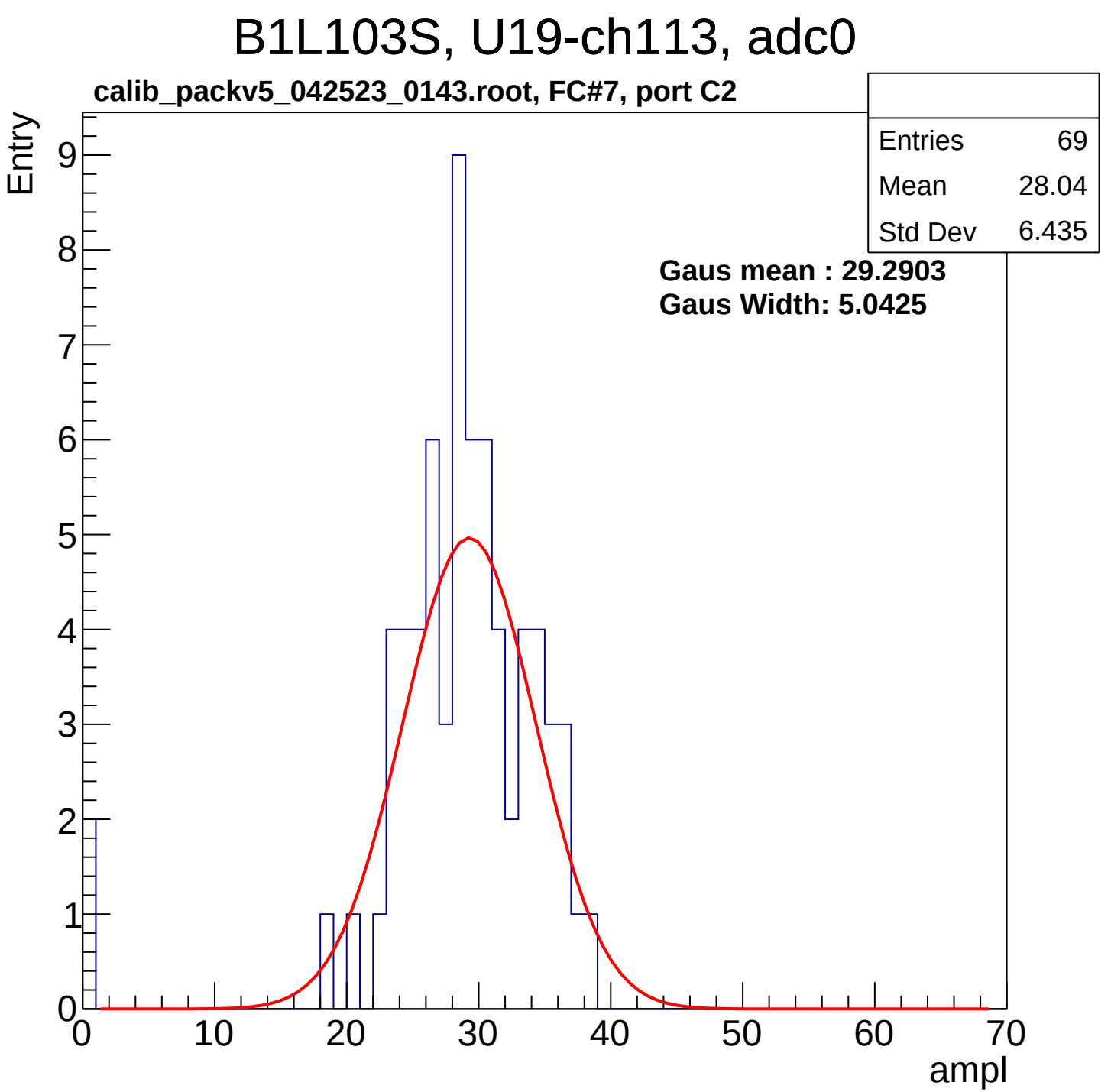
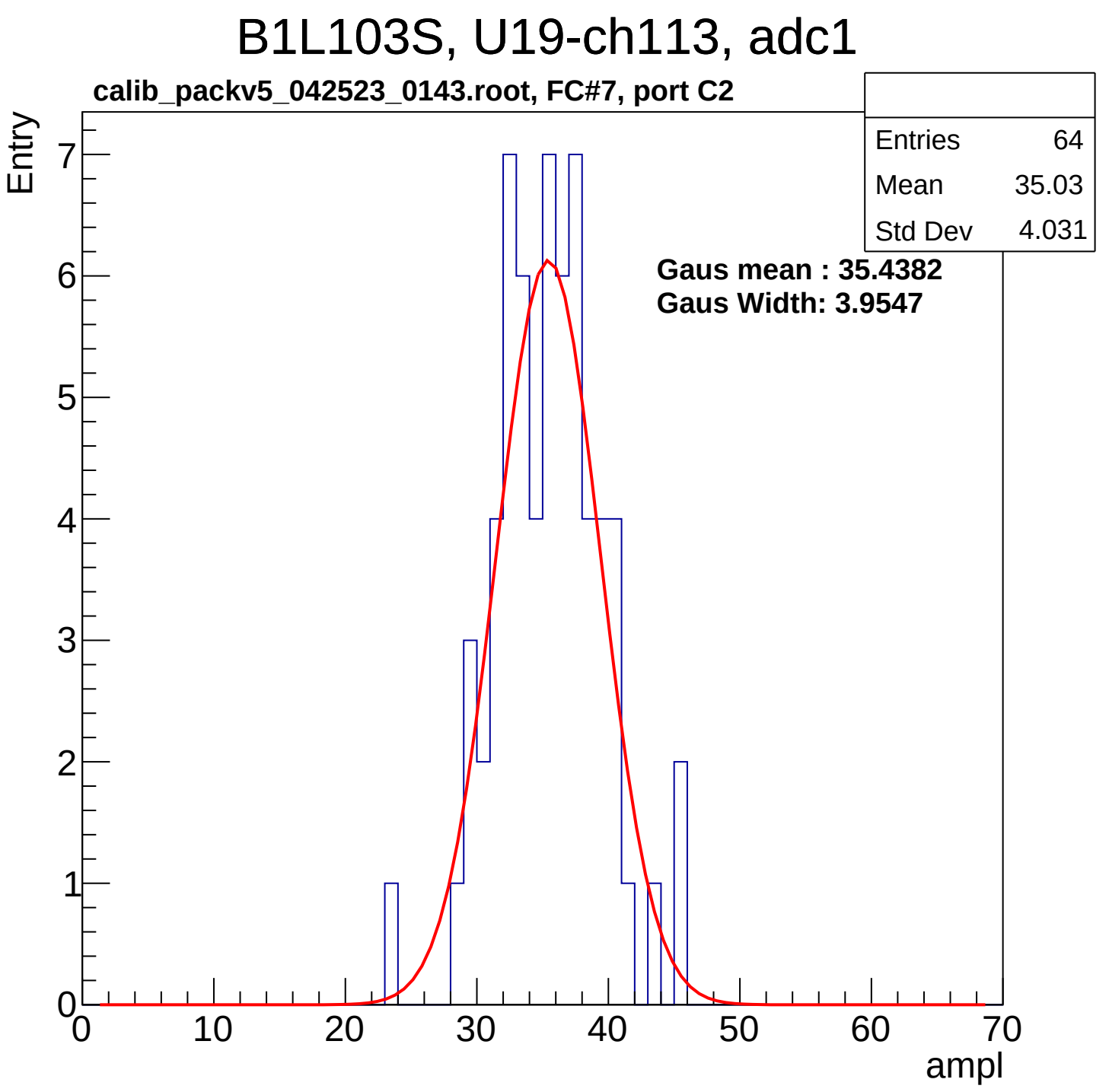
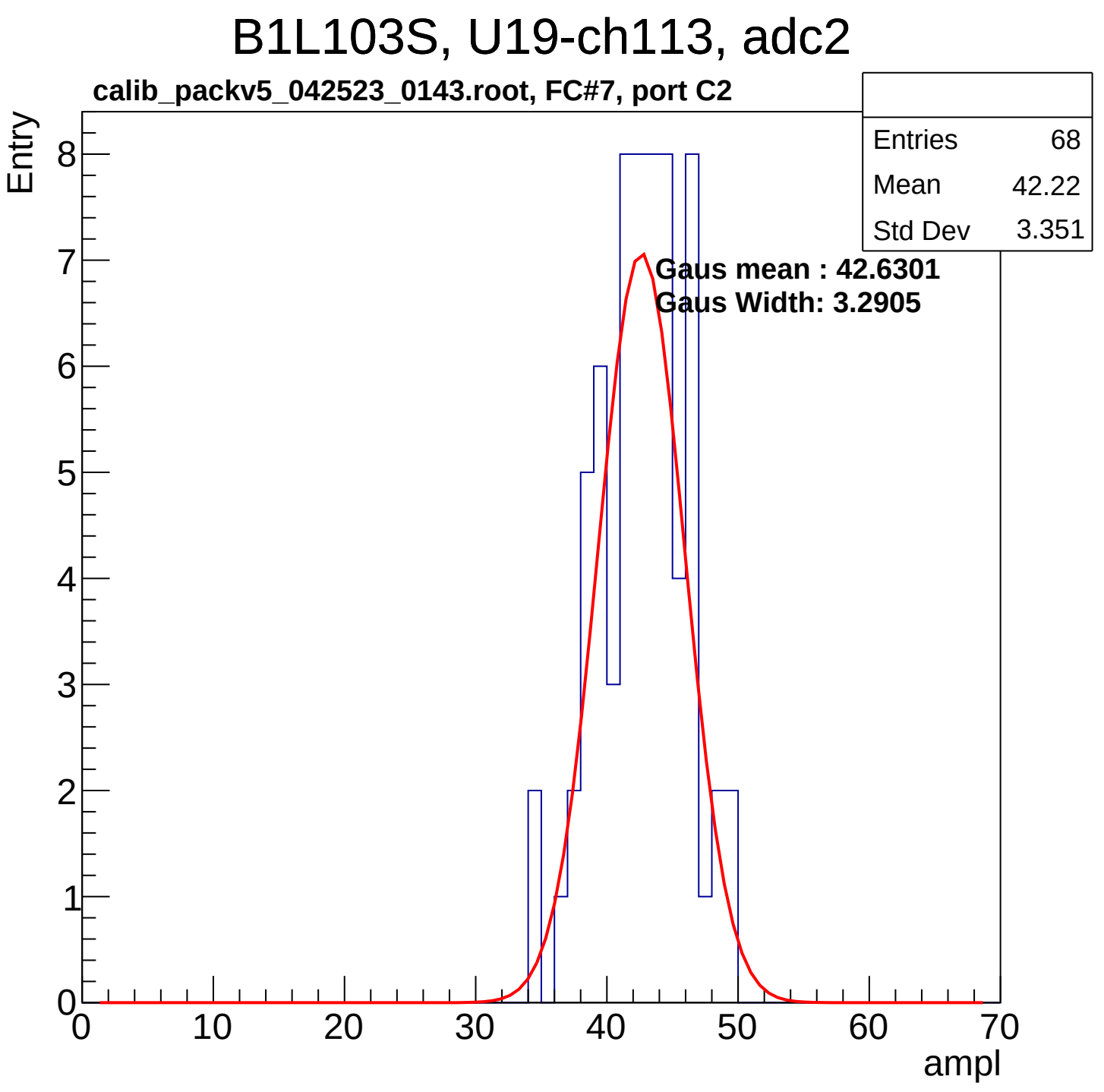
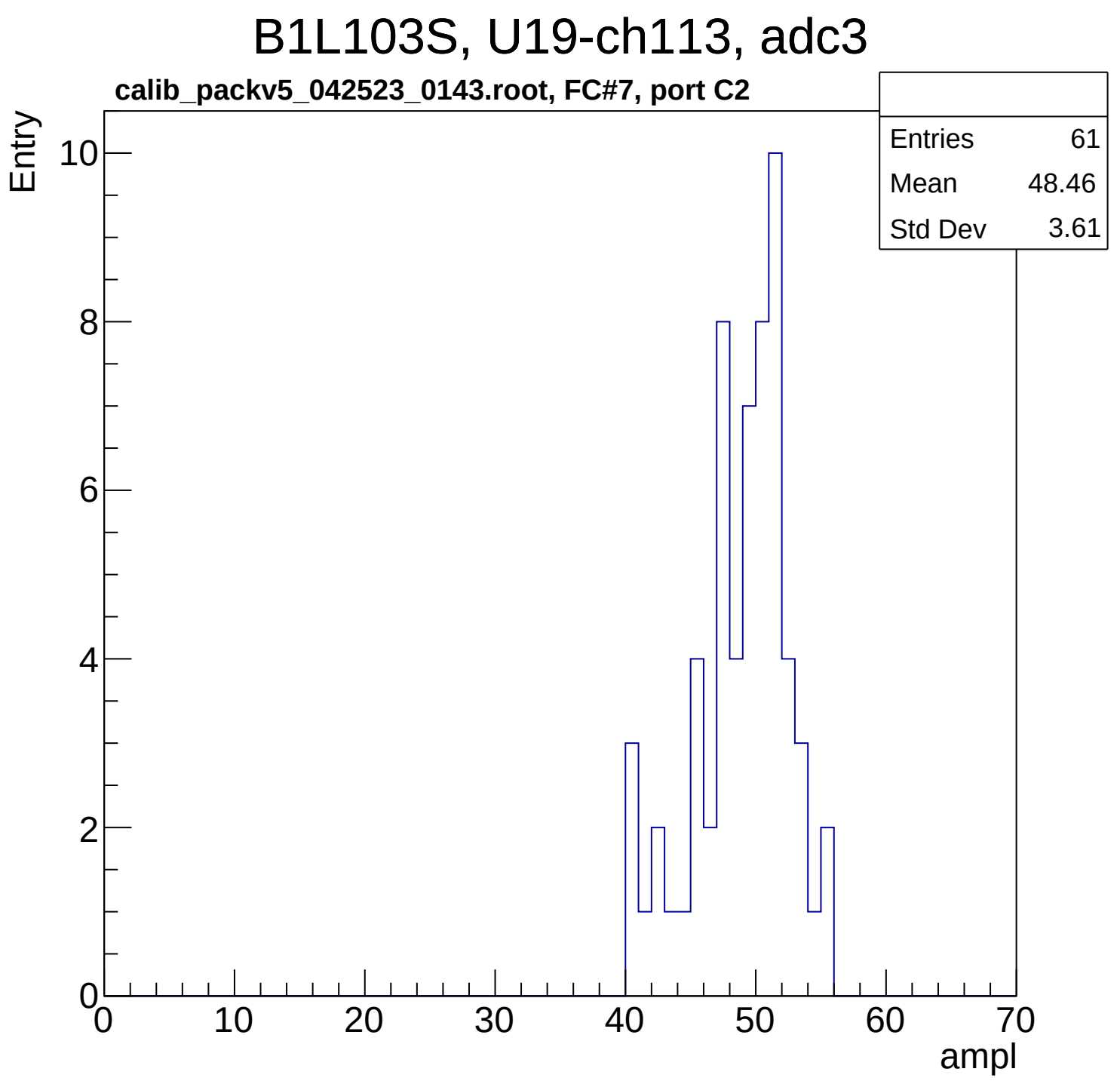

B1L103S, U19-ch112, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61
Std Dev 1.342
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

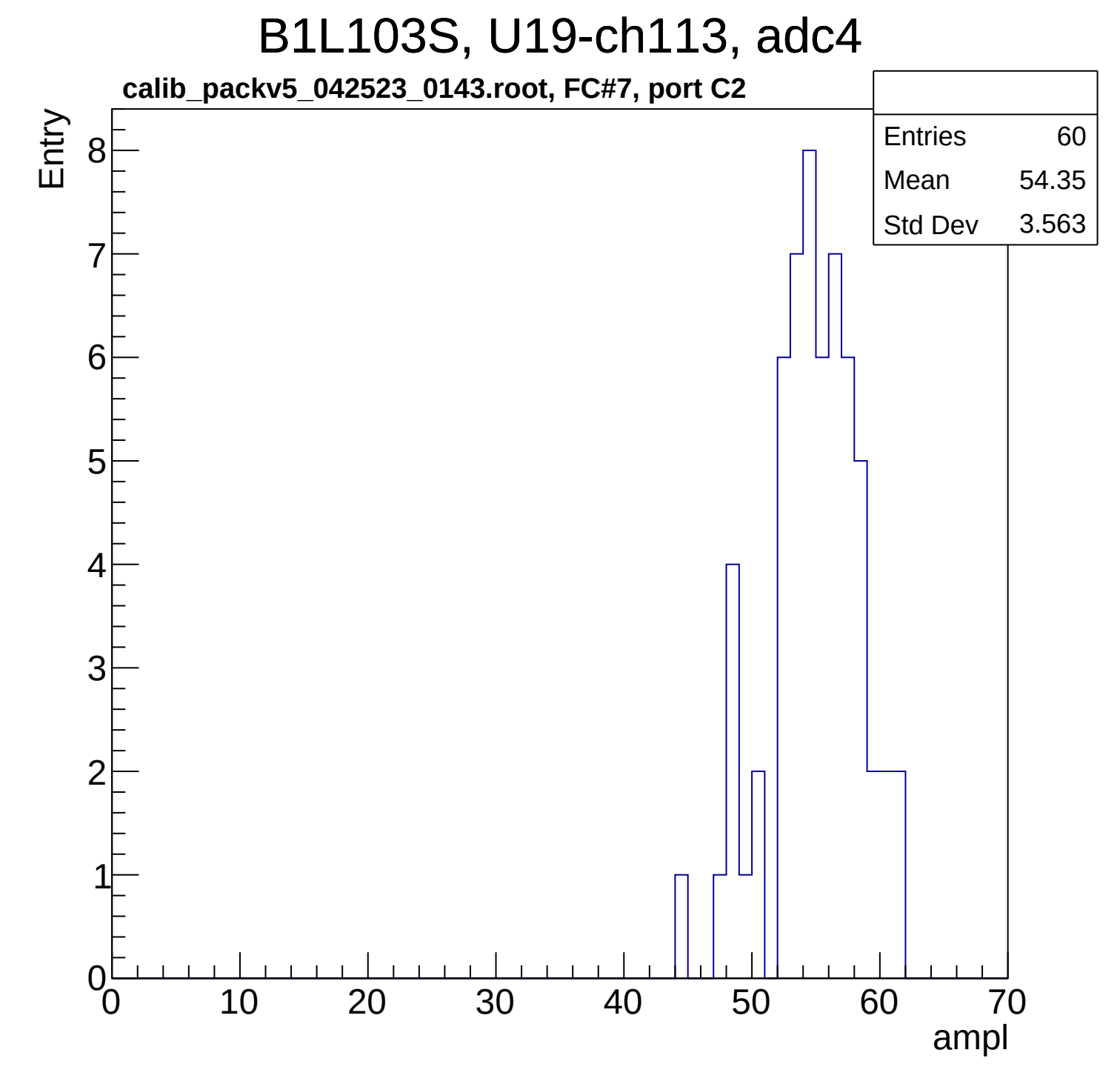

B1L103S, U19-ch112, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

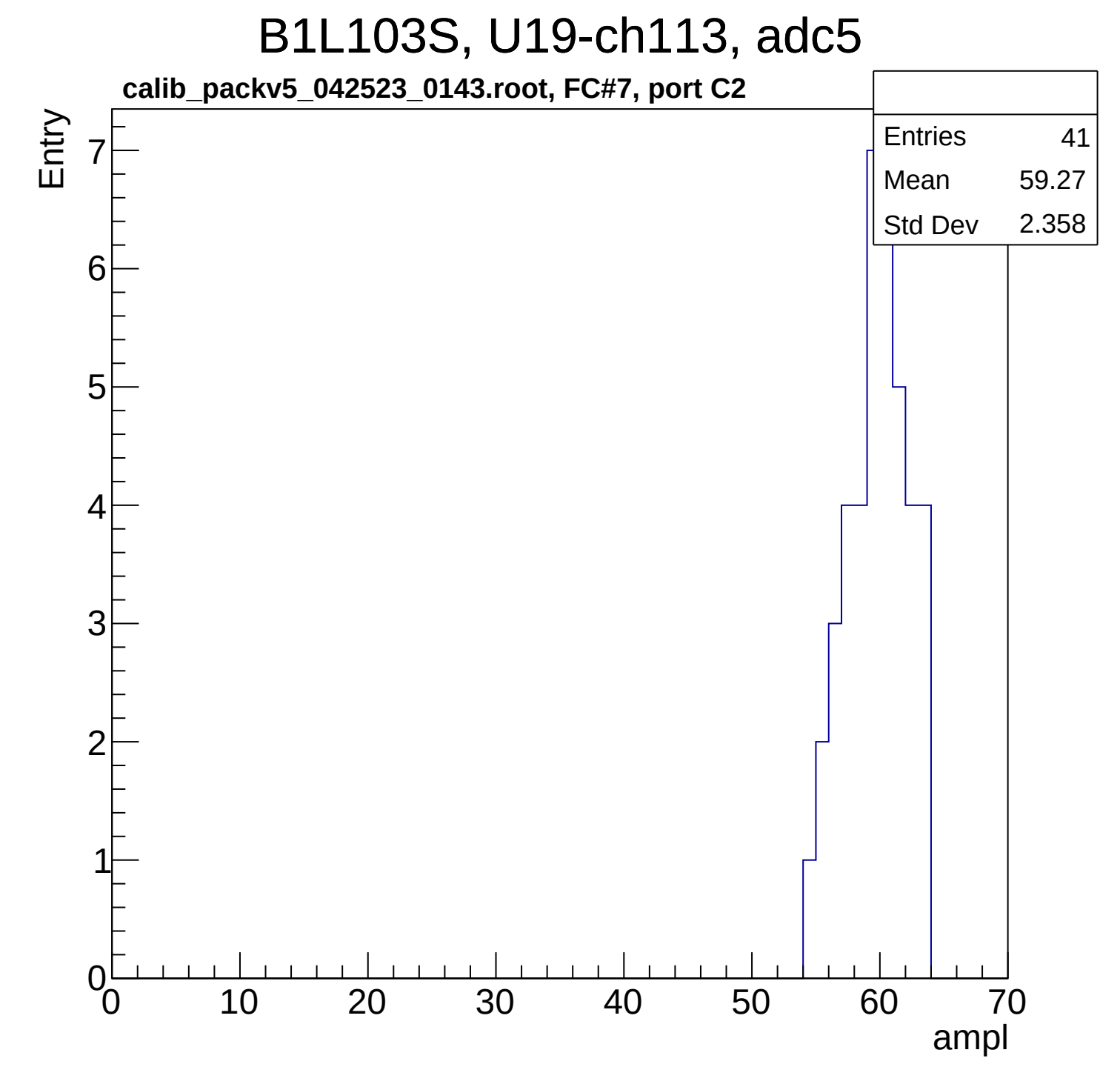

B1L103S, U19-ch113, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.04
Std Dev 6.435
Gaus mean : 29.2903
Gaus Width: 5.0425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

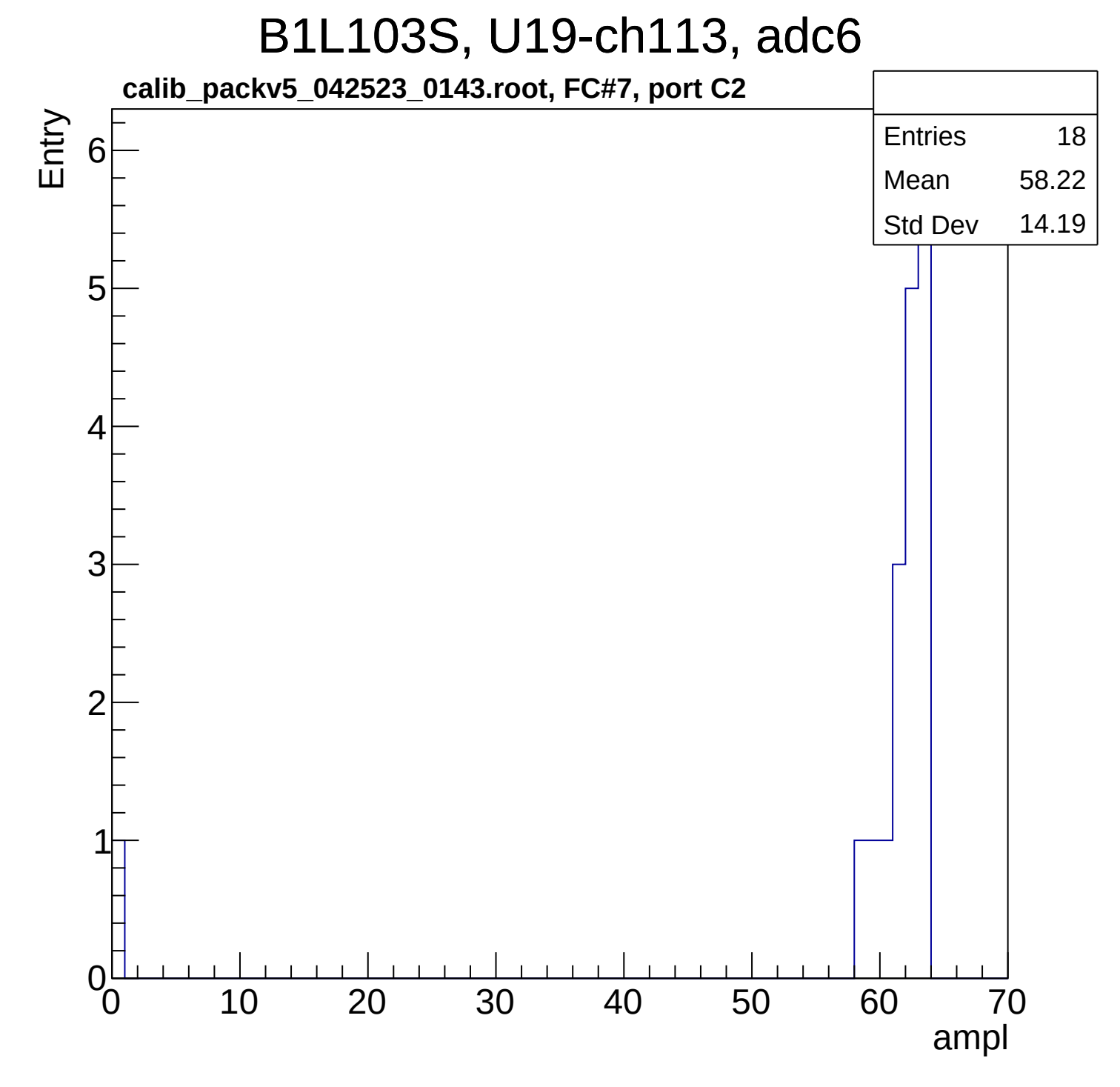

B1L103S, U19-ch113, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 35.03
Std Dev 4.031
Gaus mean : 35.4382
Gaus Width: 3.9547
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

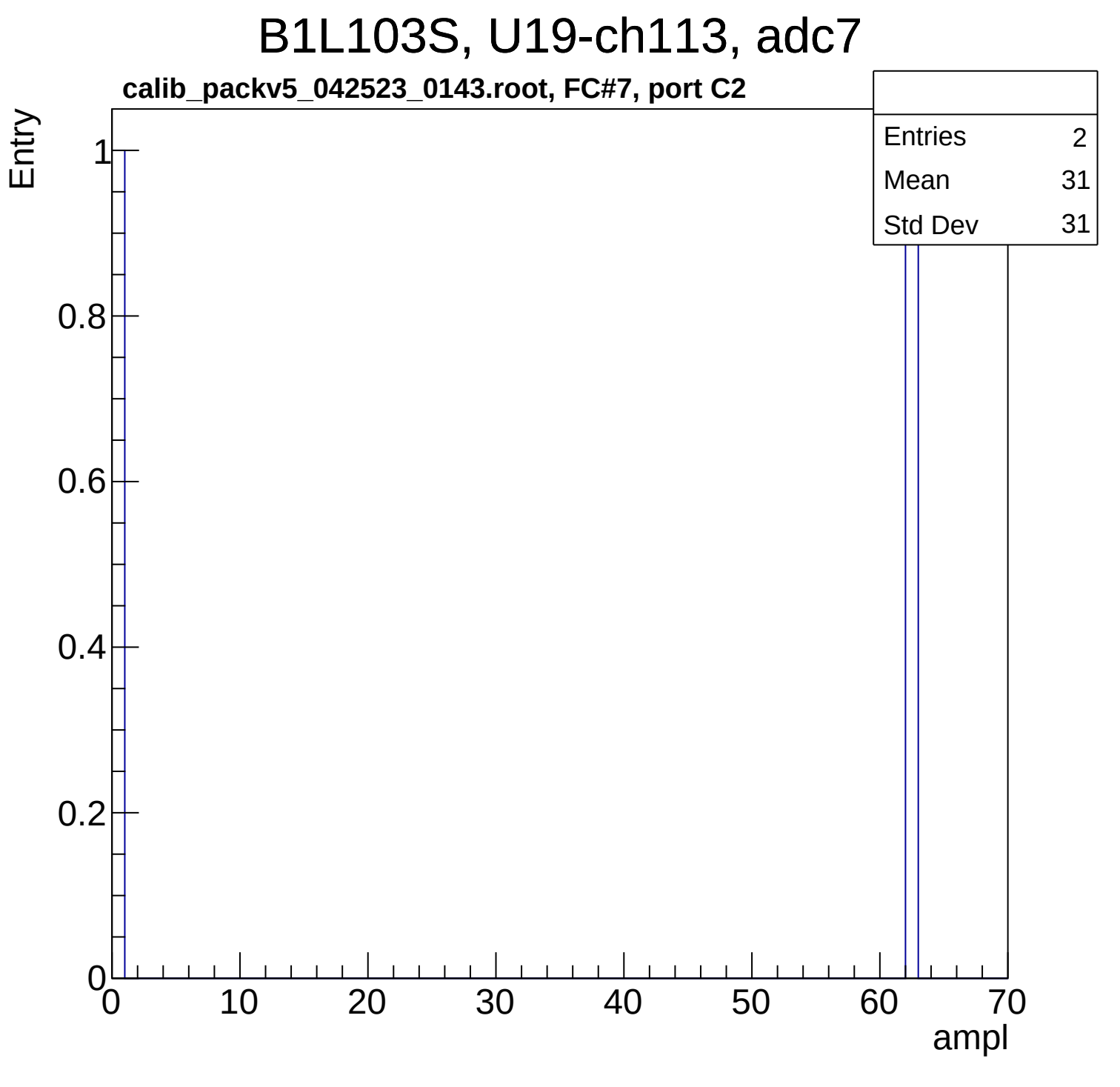

B1L103S, U19-ch113, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 42.22
Std Dev 3.351
Gaus mean : 42.6301
Gaus Width: 3.2905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch113, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 48.46
Std Dev 3.61
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch113, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 54.35
Std Dev 3.563
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch113, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.27
Std Dev 2.358
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch113, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 58.22
Std Dev 14.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch113, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
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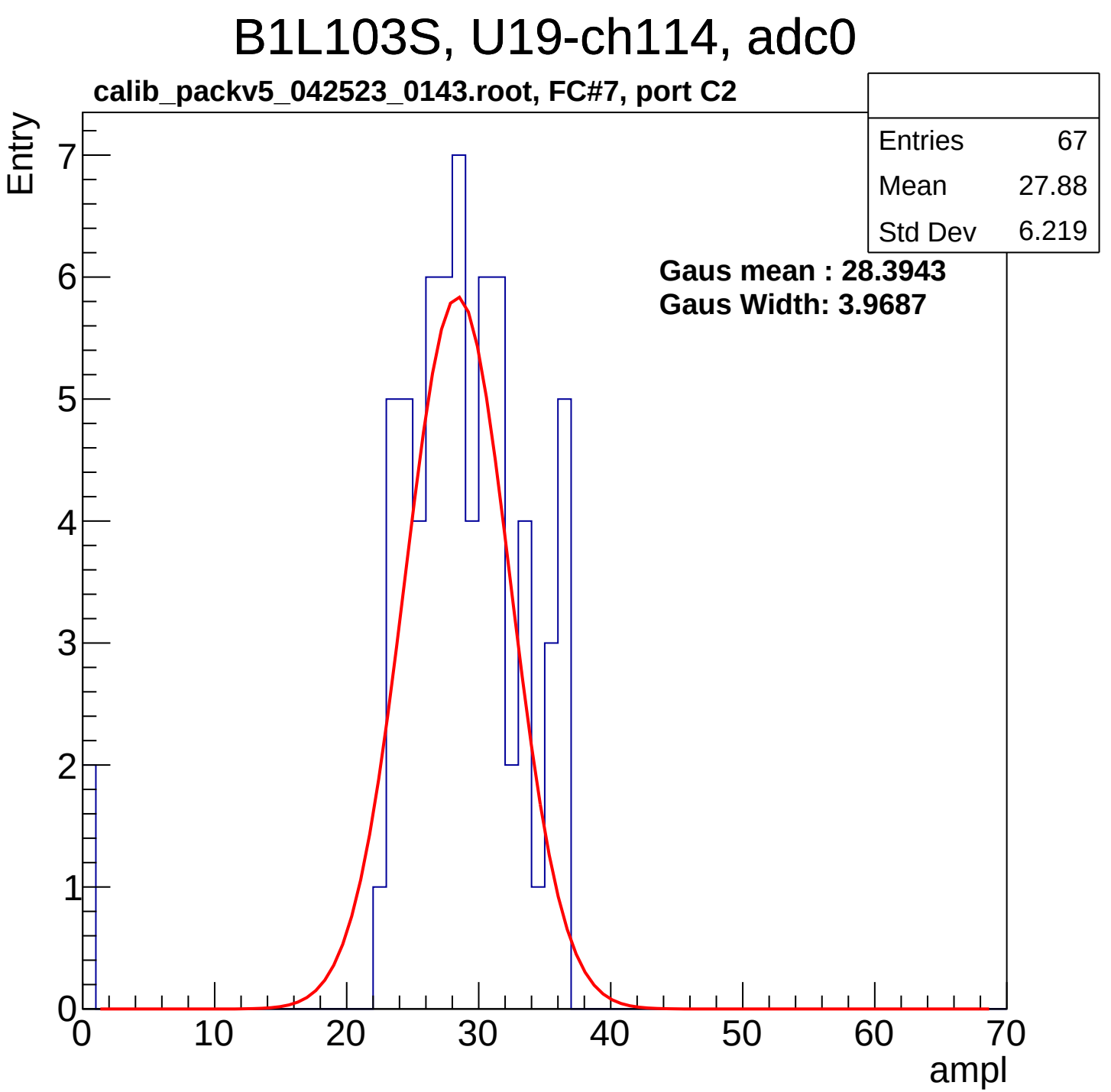
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch114, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 27.88
Std Dev 6.219
Gaus mean : 28.3943
Gaus Width: 3.9687
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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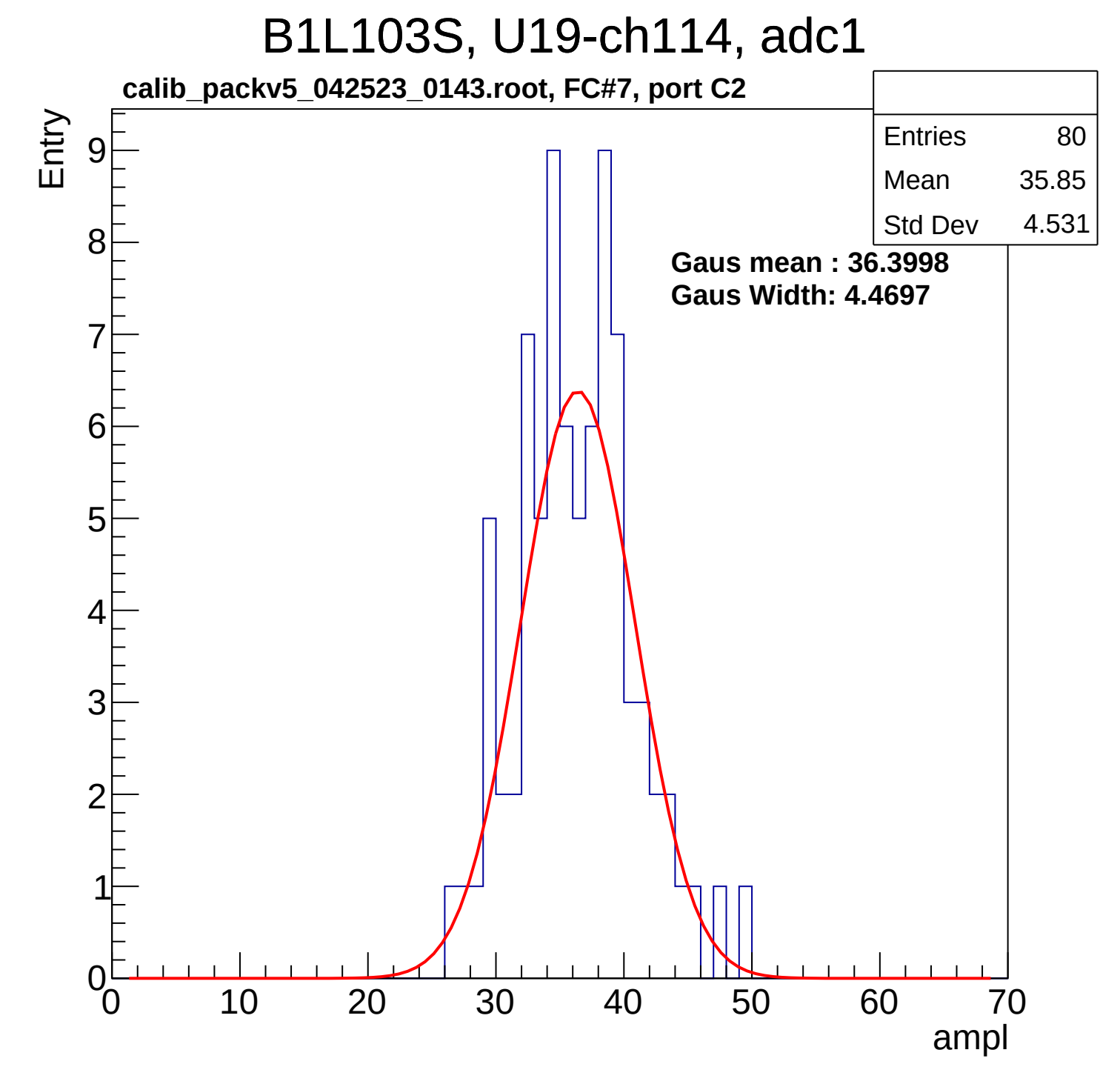
B1L103S, U19-ch114, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 35.85
Std Dev 4.531
Gaus mean : 36.3998
Gaus Width: 4.4697
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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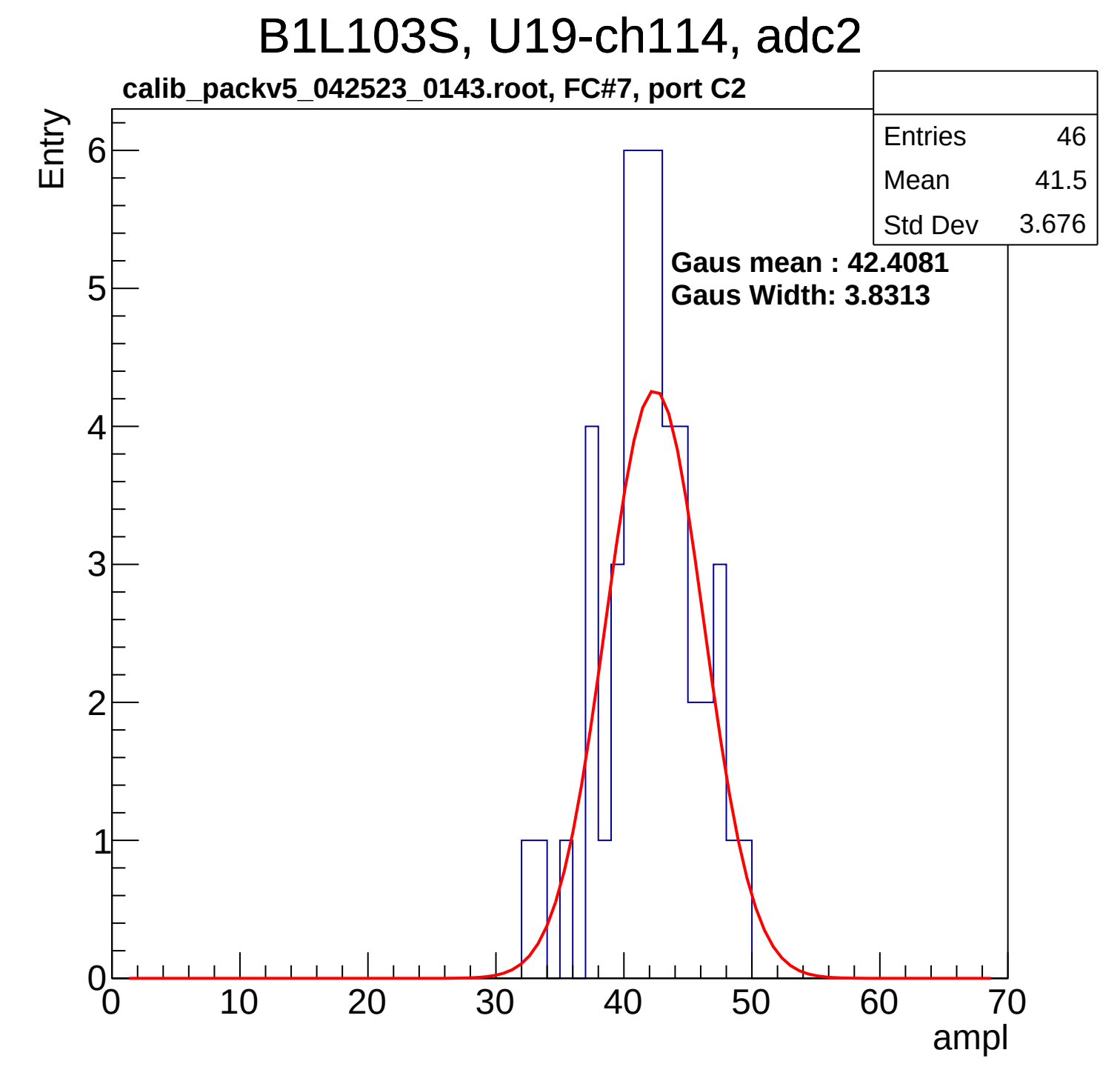
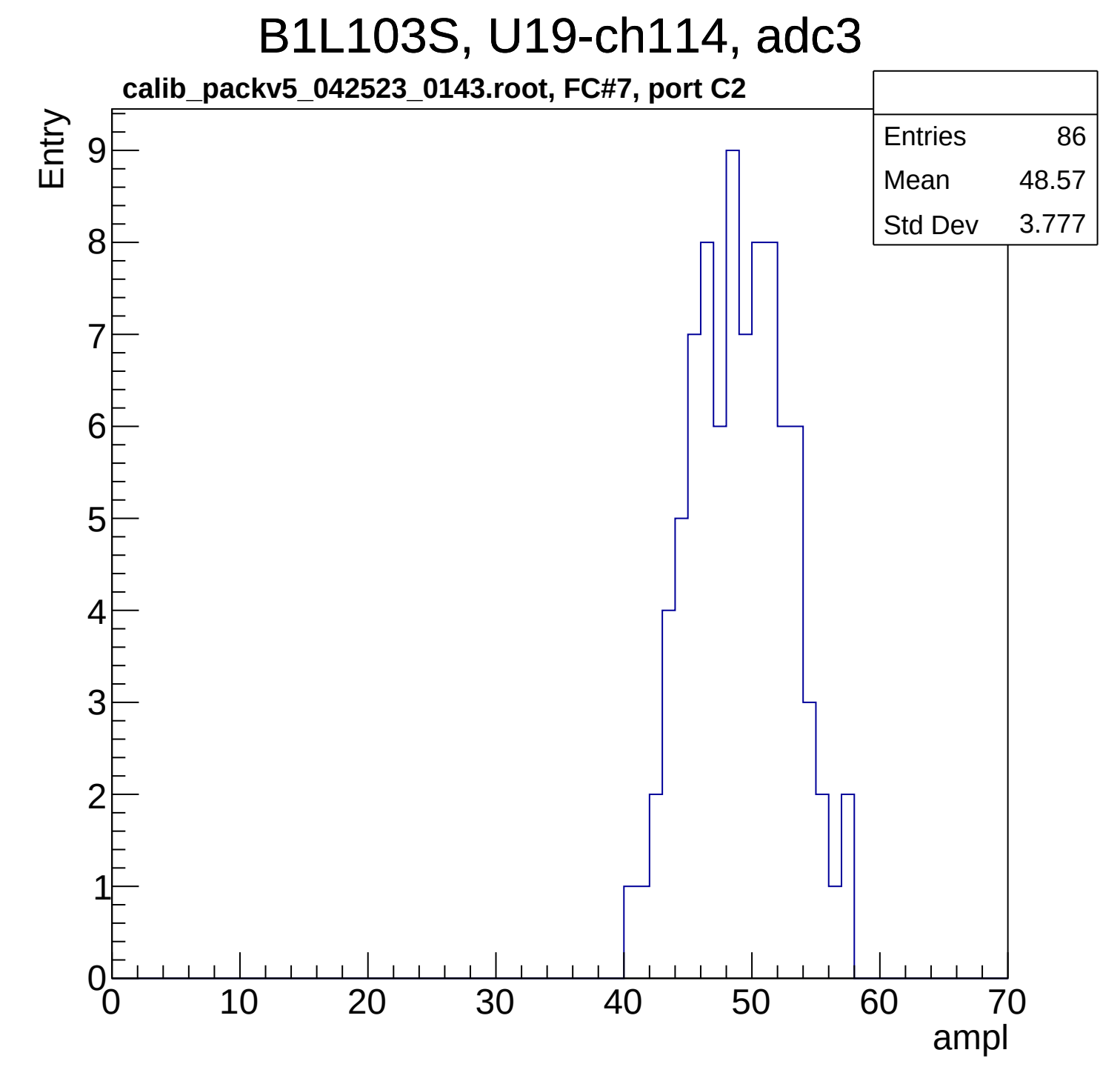

B1L103S, U19-ch114, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 41.5
Std Dev 3.676
Gaus mean : 42.4081
Gaus Width: 3.8313
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch114, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 48.57
Std Dev 3.777
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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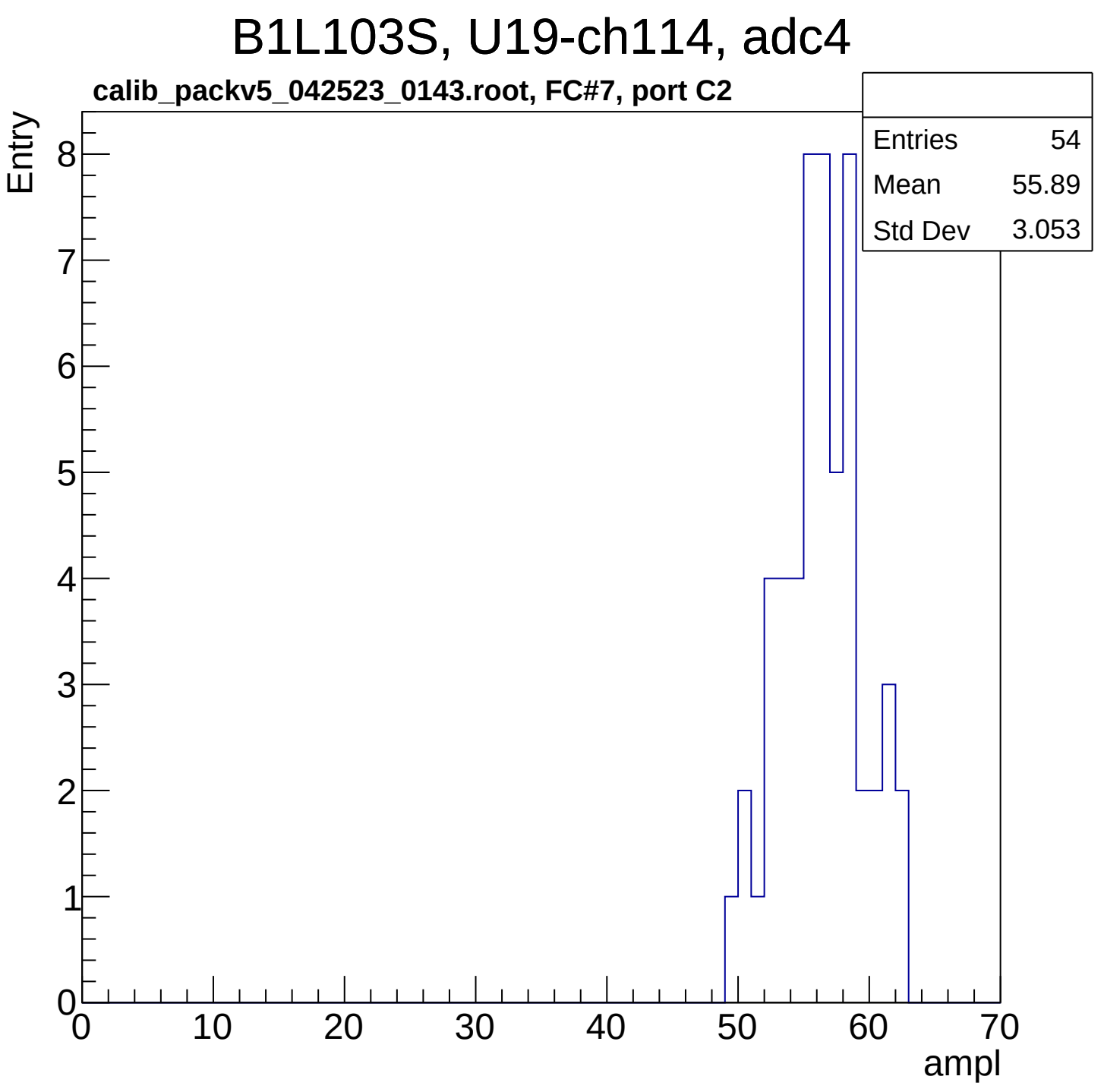
B1L103S, U19-ch114, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.89
Std Dev 3.053
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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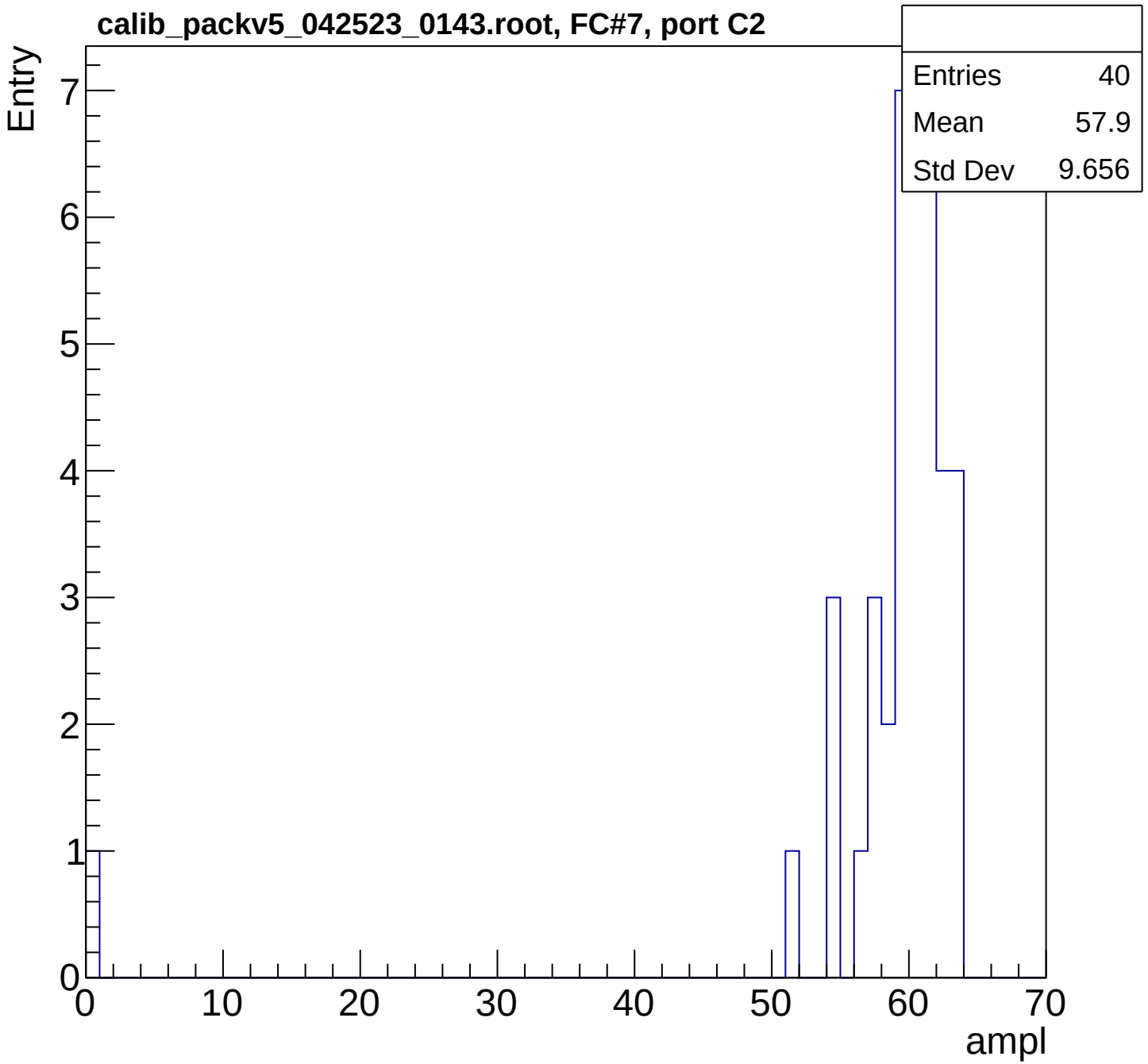
B1L103S, U19-ch114, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 57.9
Std Dev 9.656
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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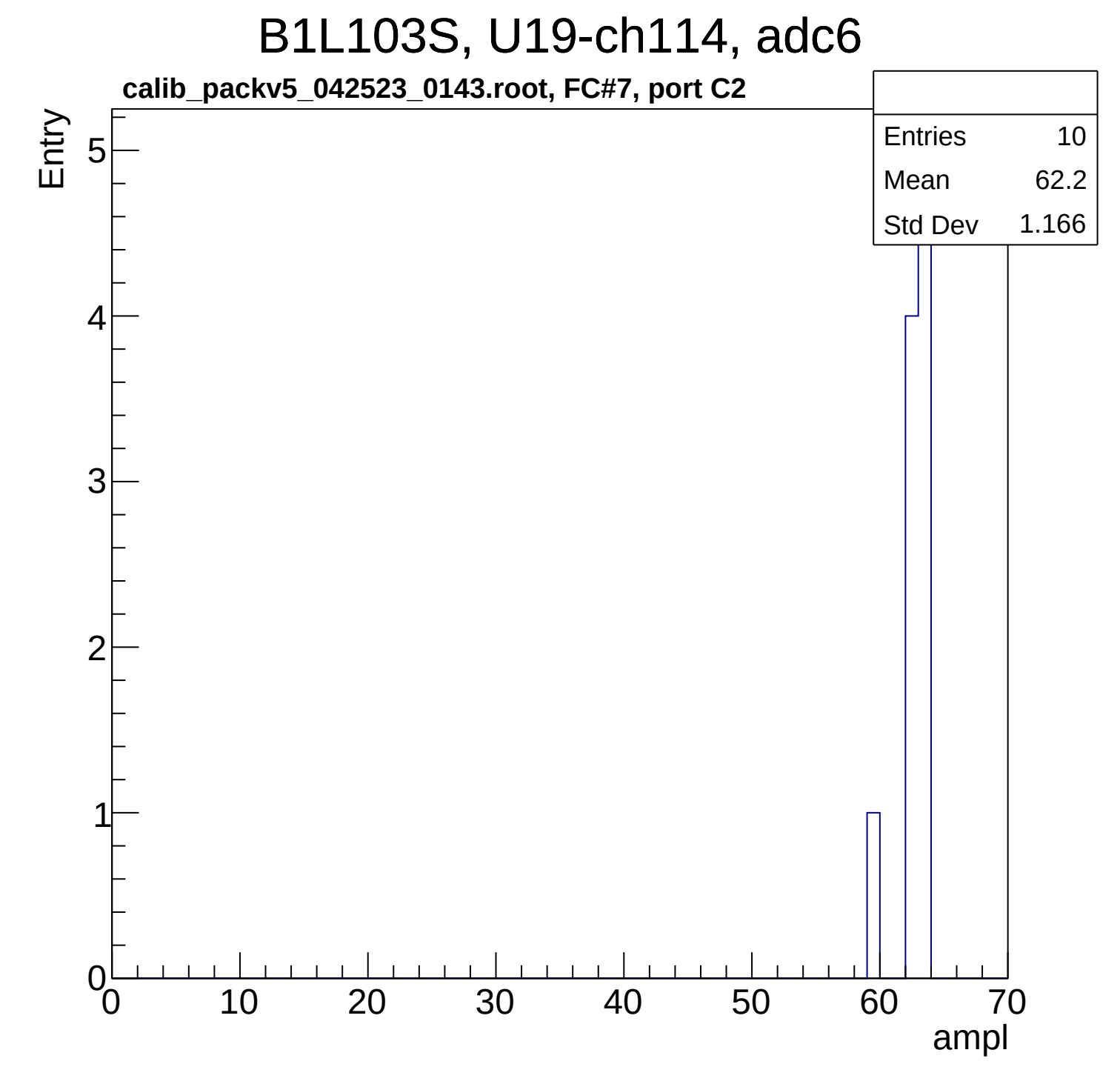
B1L103S, U19-ch114, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 62.2
Std Dev 1.166
Entry
ampl
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2
3
4
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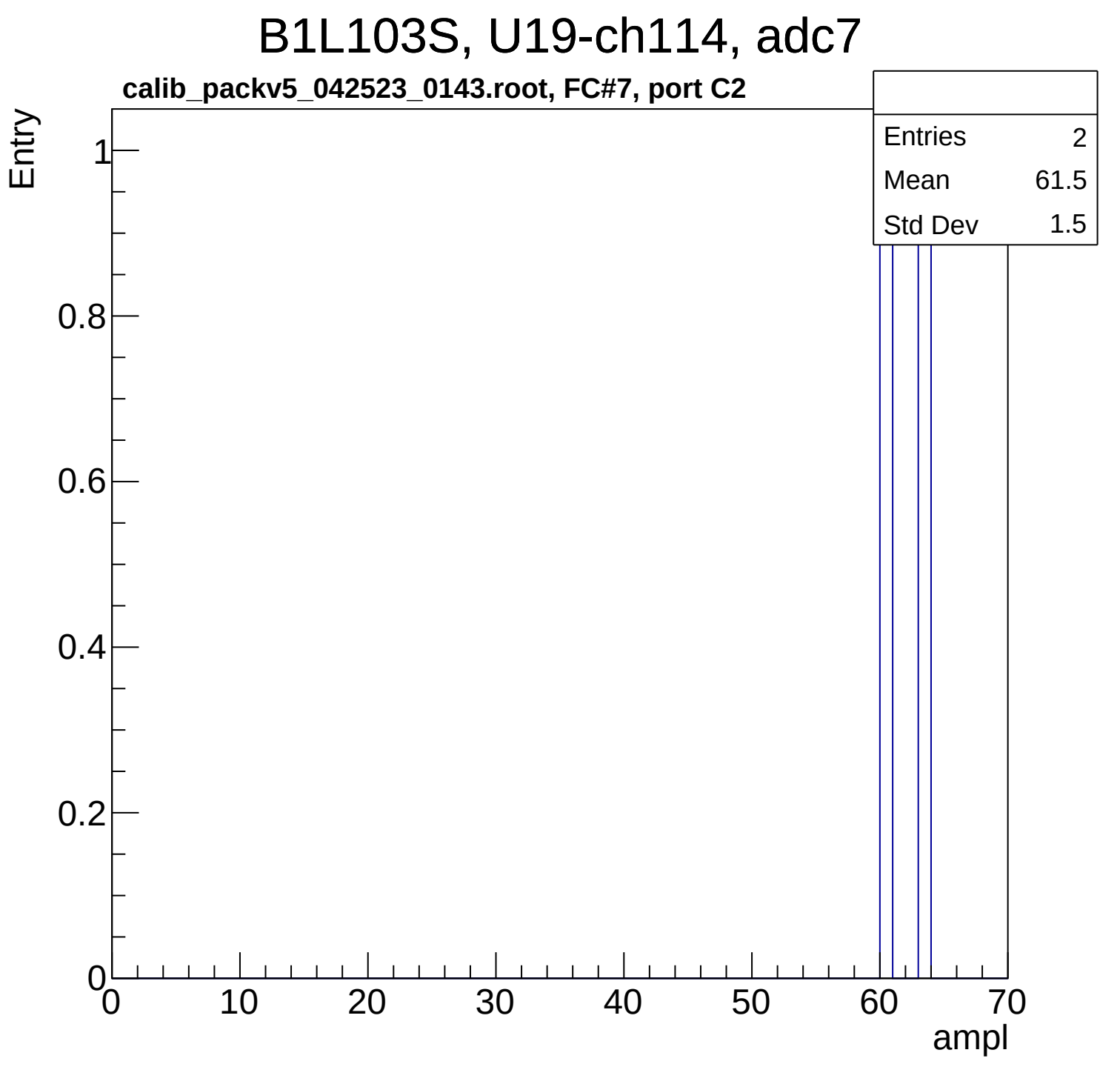
0
10
20
30
40
50
60
70


B1L103S, U19-ch114, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
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0.4
0.6
0.8
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0
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60
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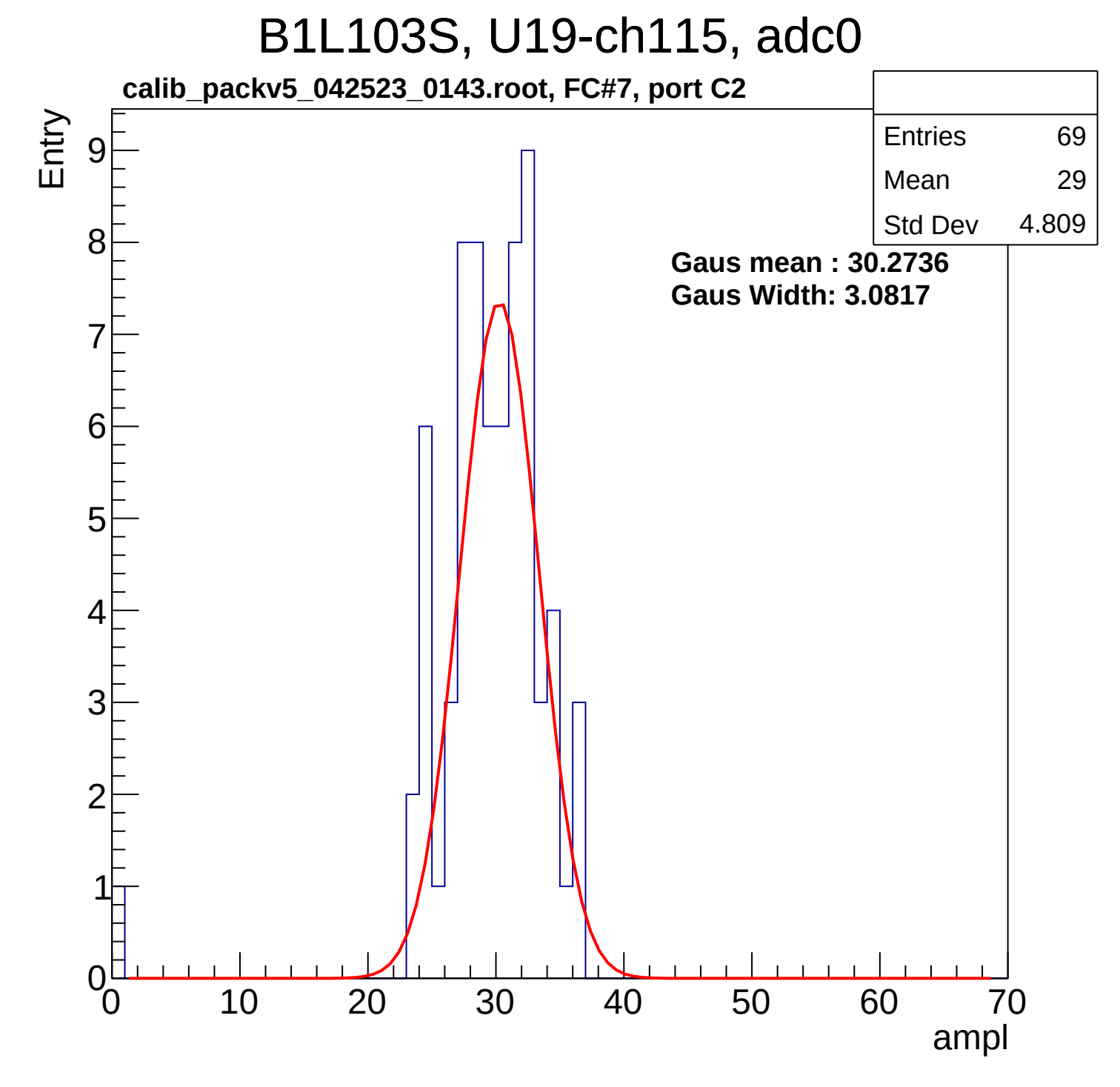
B1L103S, U19-ch115, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 29
Std Dev 4.809
Gaus mean : 30.2736
Gaus Width: 3.0817
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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50
60
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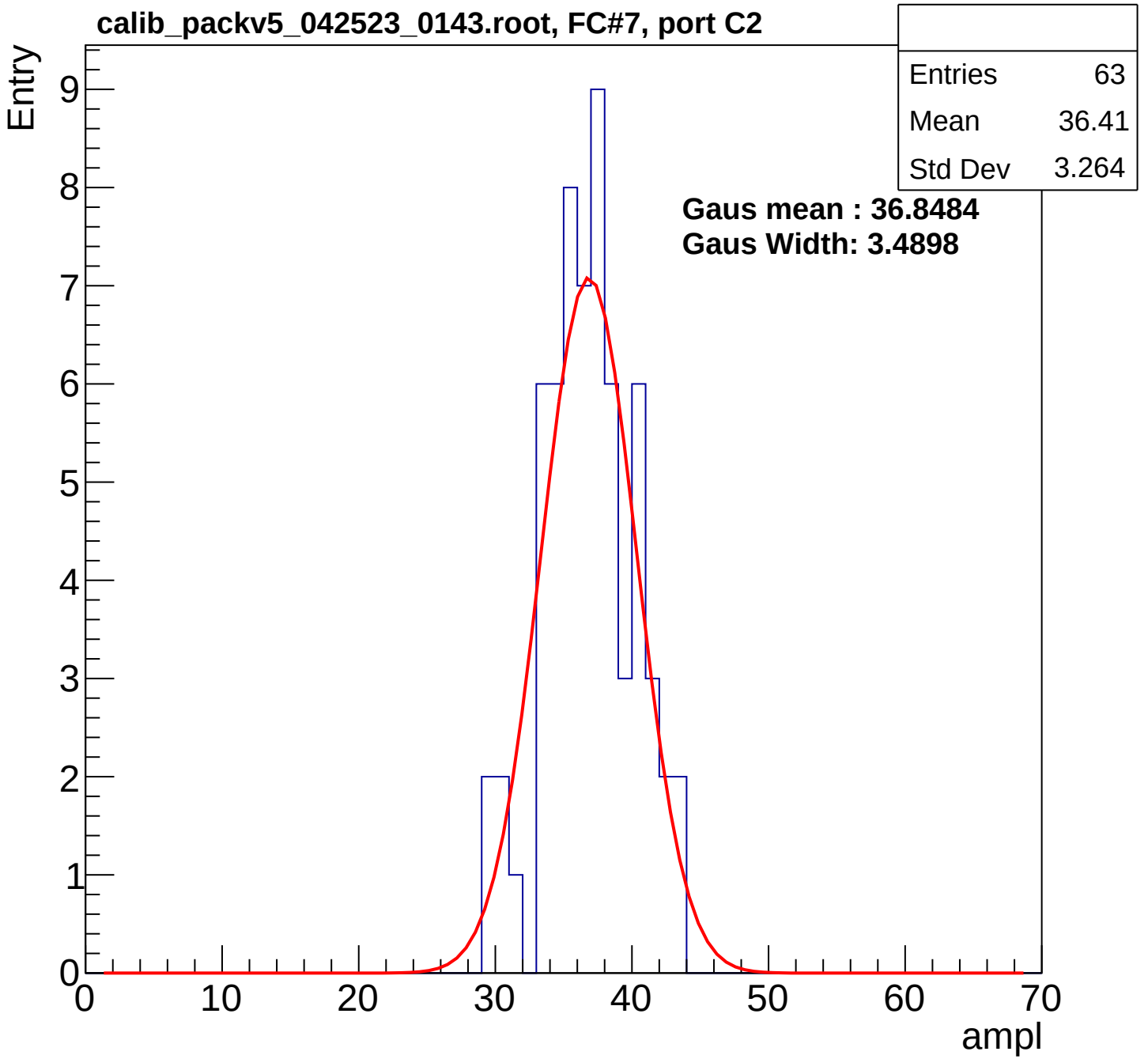
B1L103S, U19-ch115, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 36.41
Std Dev 3.264
Gaus mean : 36.8484
Gaus Width: 3.4898
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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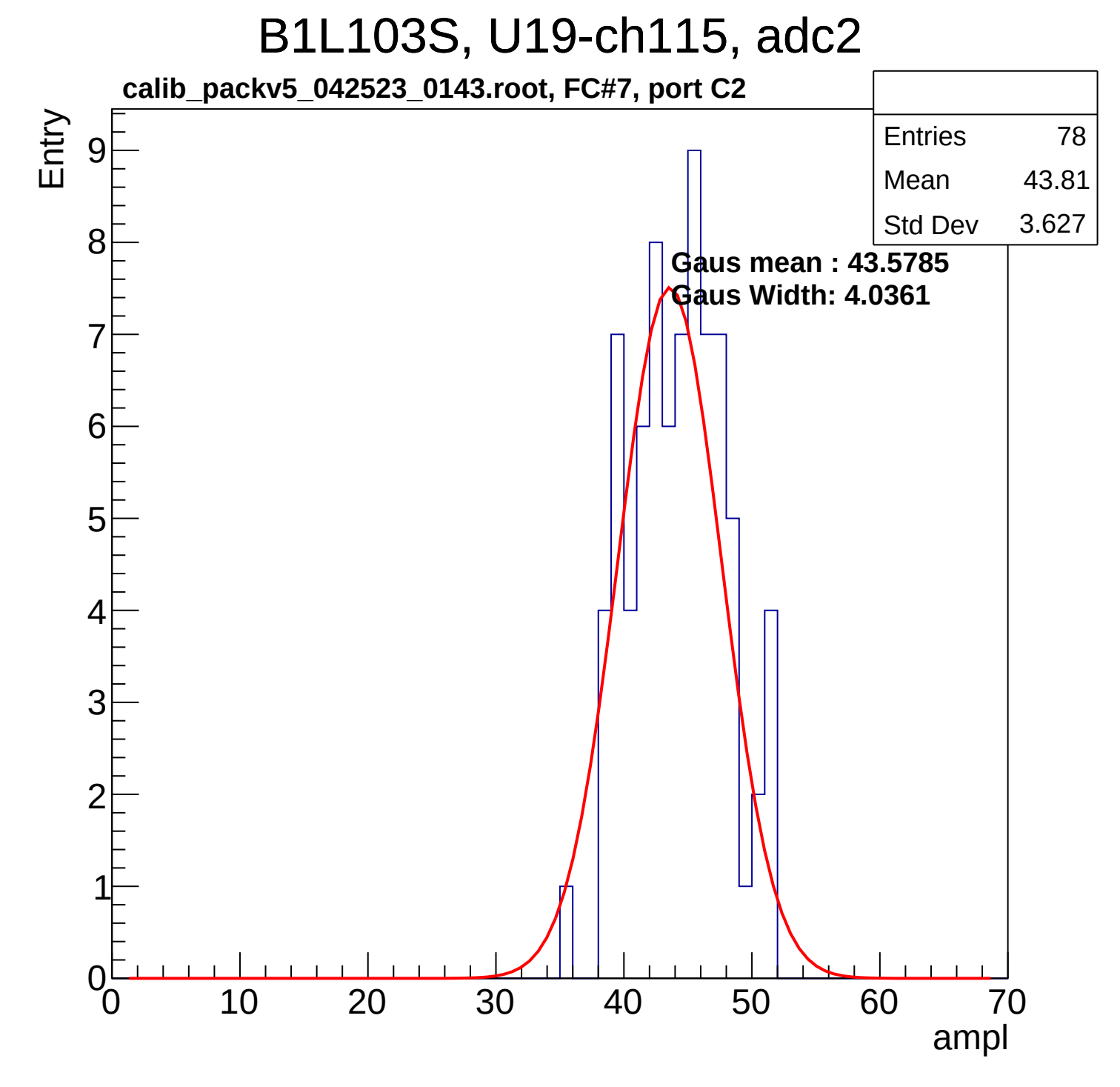
B1L103S, U19-ch115, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 43.81
Std Dev 3.627
Gaus mean : 43.5785
Gaus Width: 4.0361
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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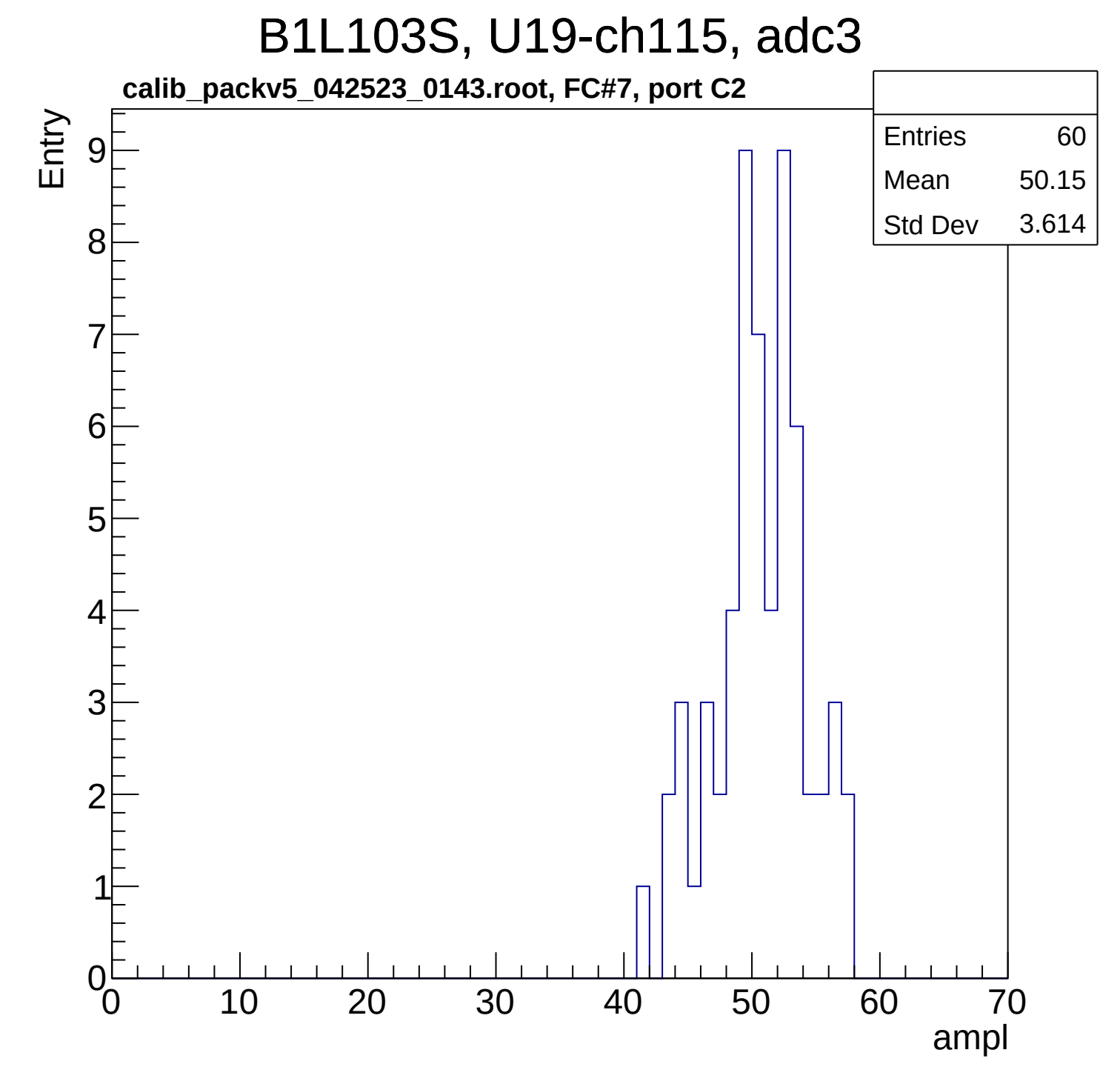
B1L103S, U19-ch115, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.15
Std Dev 3.614
Entry
ampl
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5
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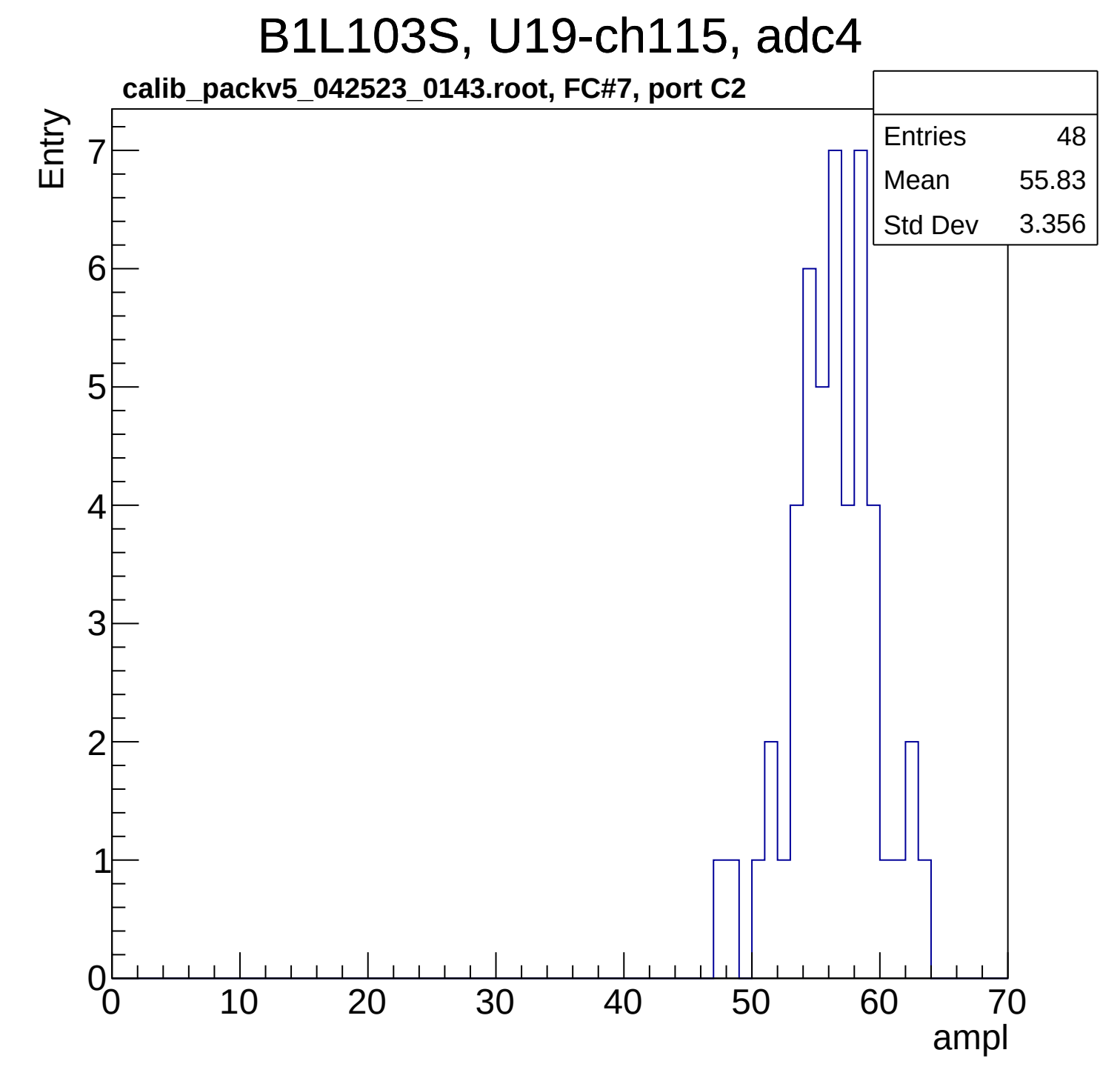
B1L103S, U19-ch115, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 55.83
Std Dev 3.356
Entry
ampl
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3
4
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50
60
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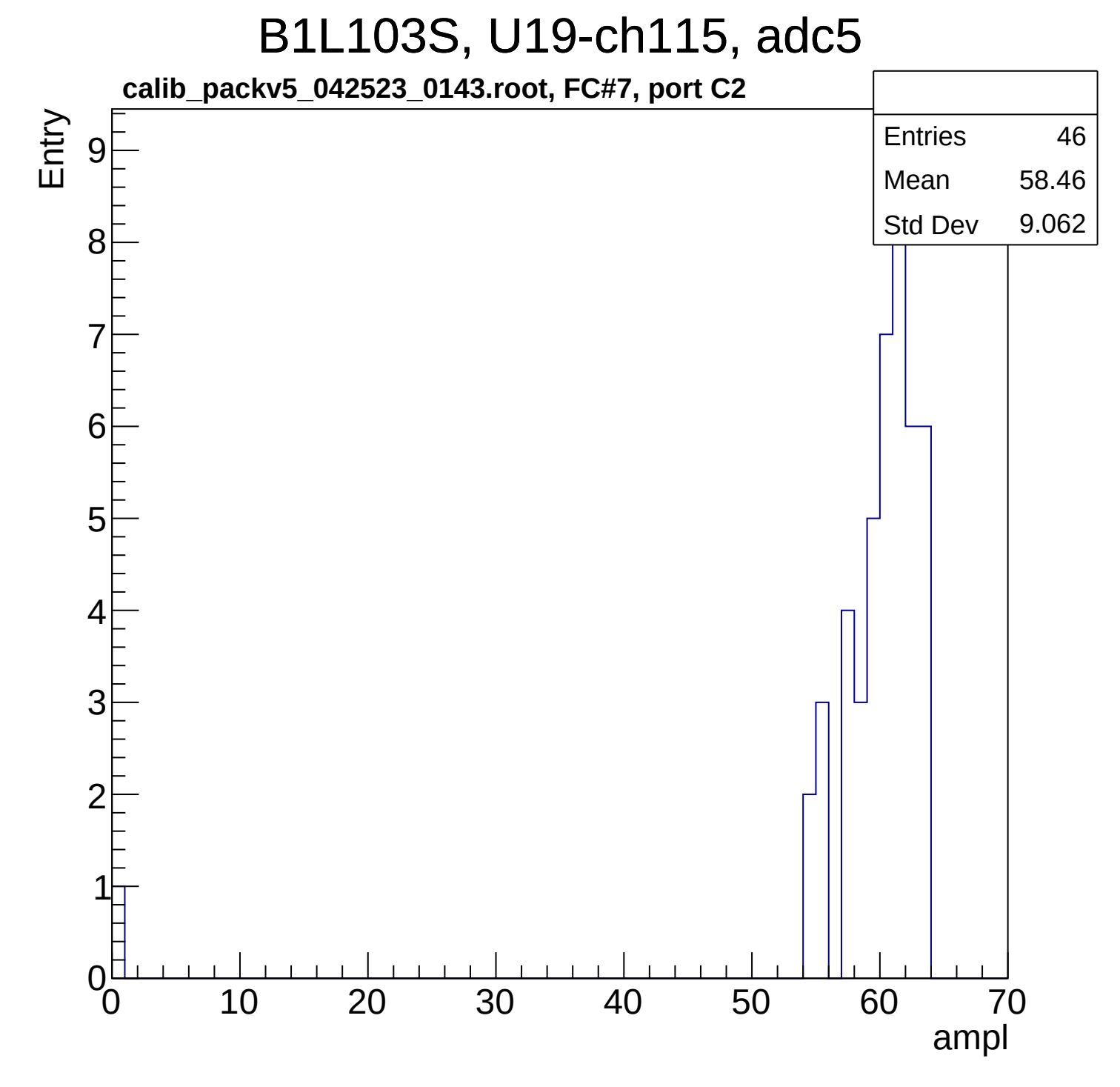
B1L103S, U19-ch115, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.46
Std Dev 9.062
Entry
ampl
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3
4
5
6
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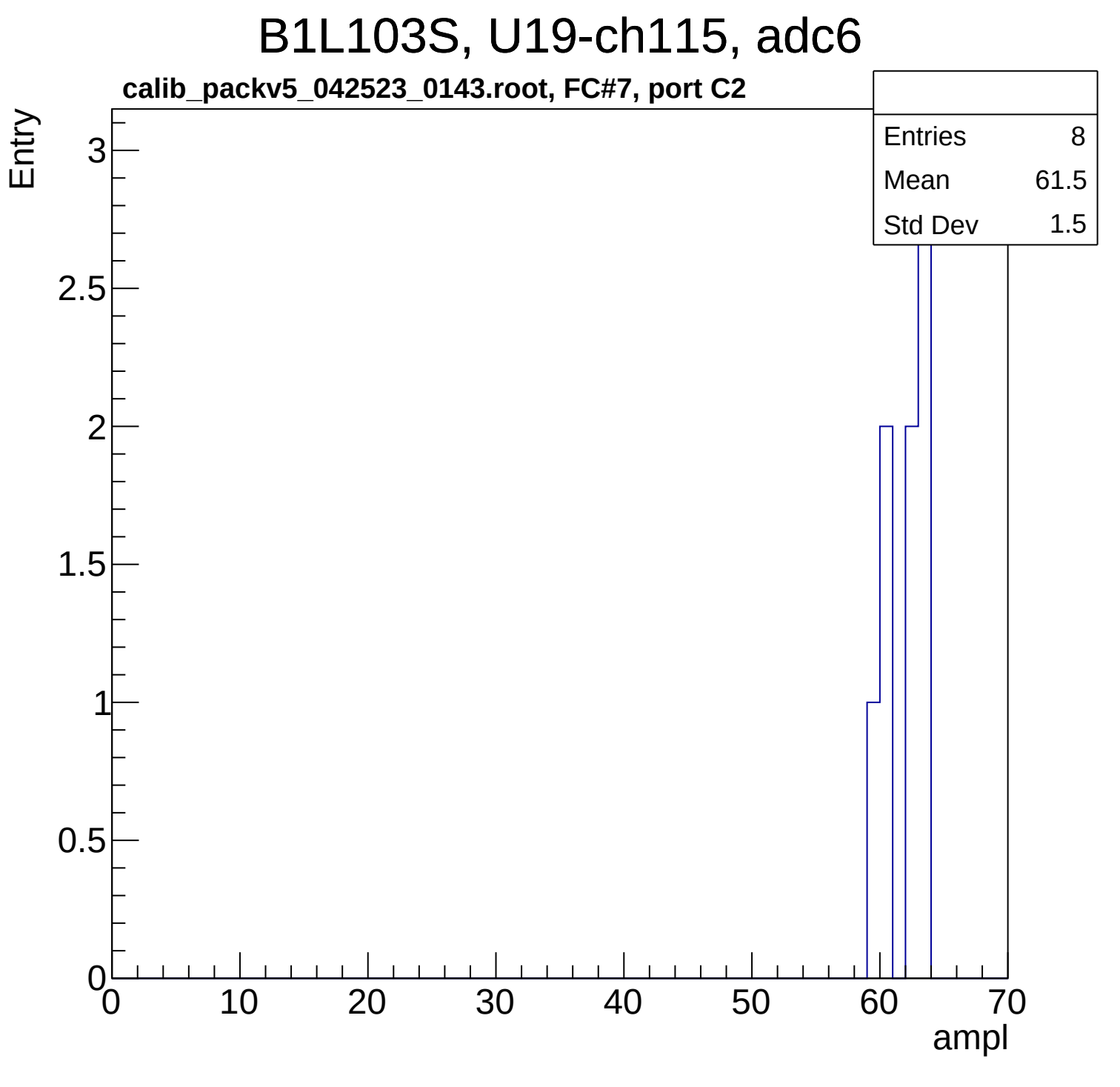
B1L103S, U19-ch115, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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60
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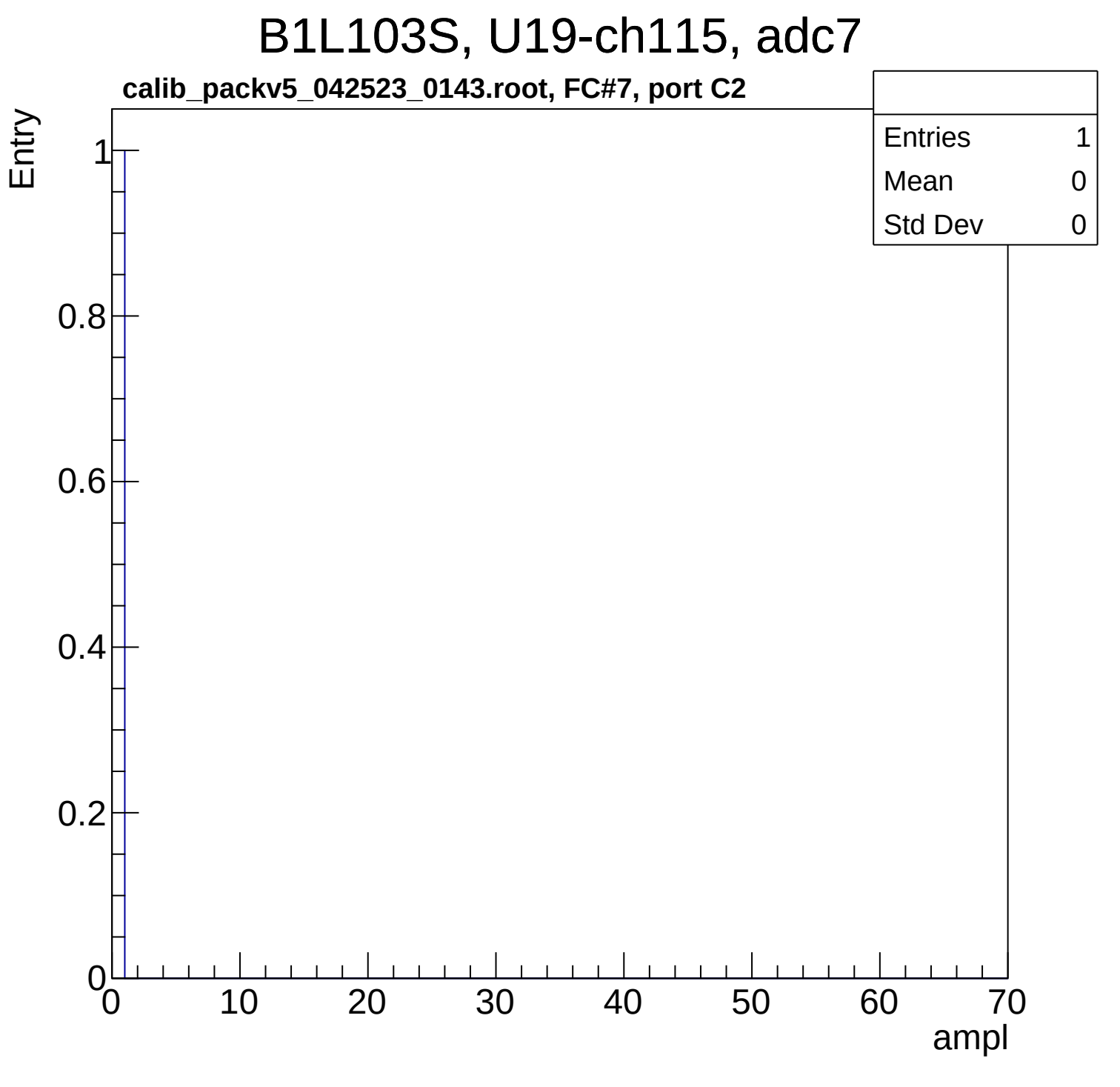
B1L103S, U19-ch115, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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10
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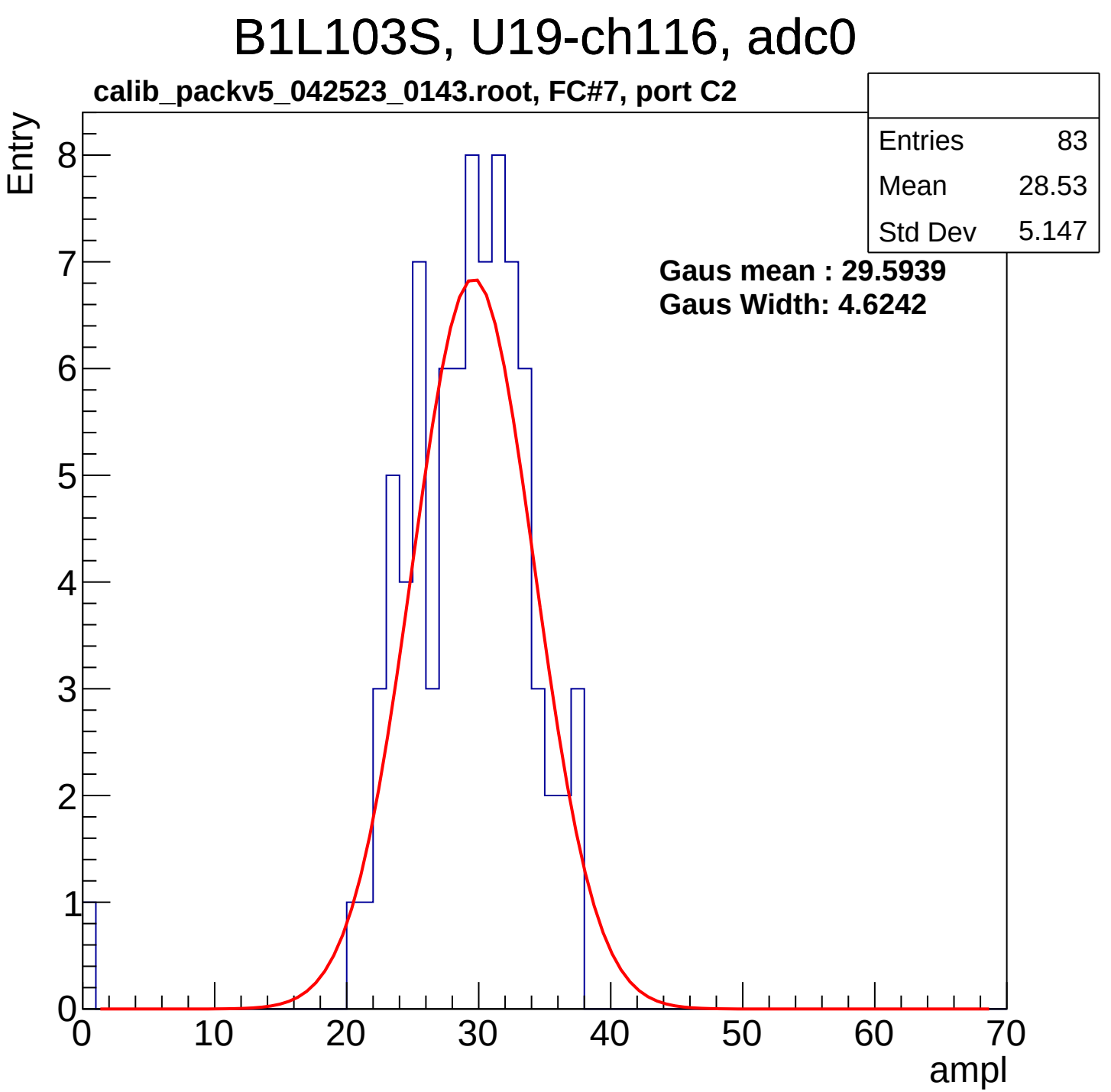
50
60
70
ampl


B1L103S, U19-ch116, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 28.53
Std Dev 5.147
Gaus mean : 29.5939
Gaus Width: 4.6242
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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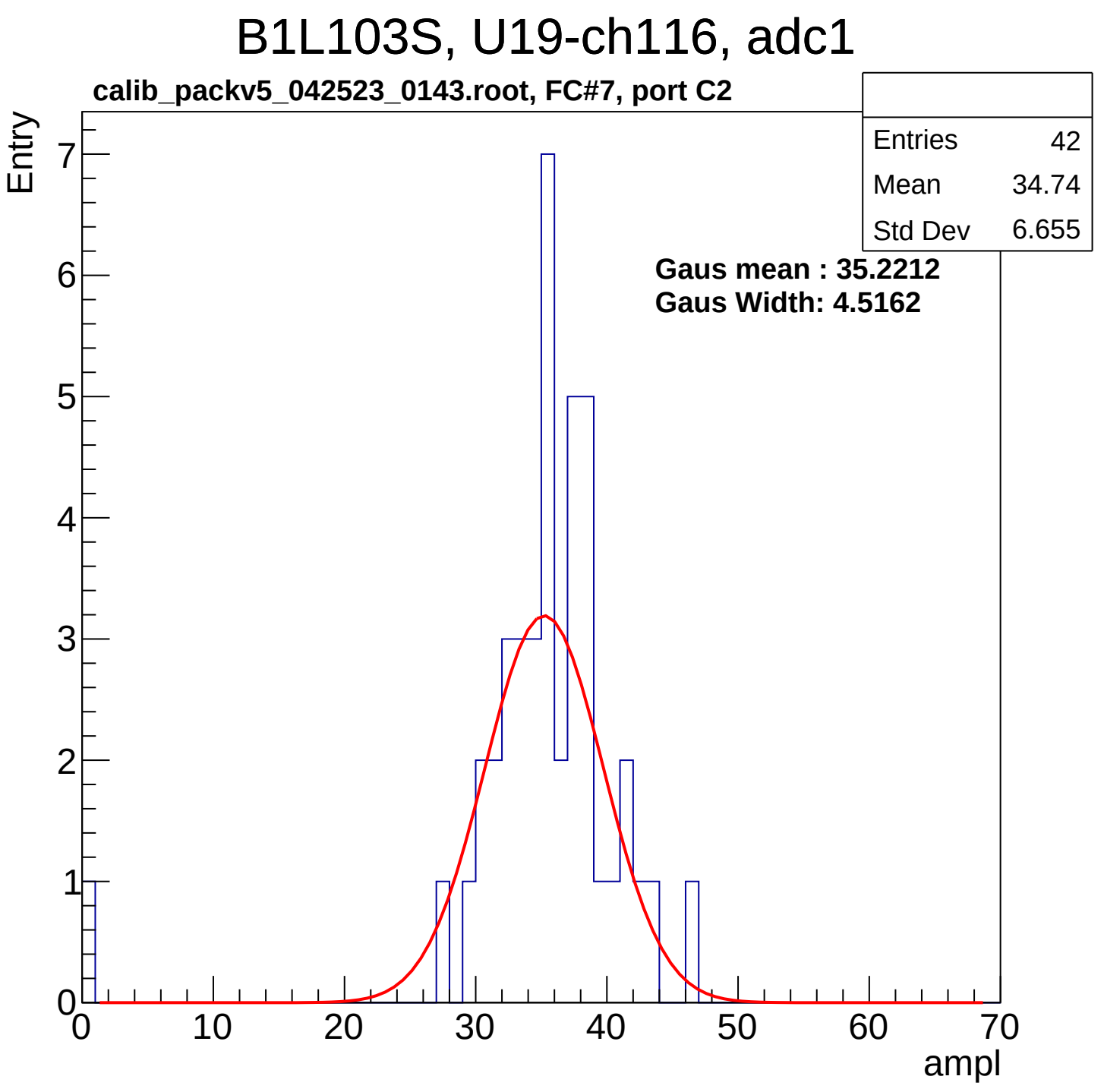
B1L103S, U19-ch116, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 34.74
Std Dev 6.655
Gaus mean : 35.2212
Gaus Width: 4.5162
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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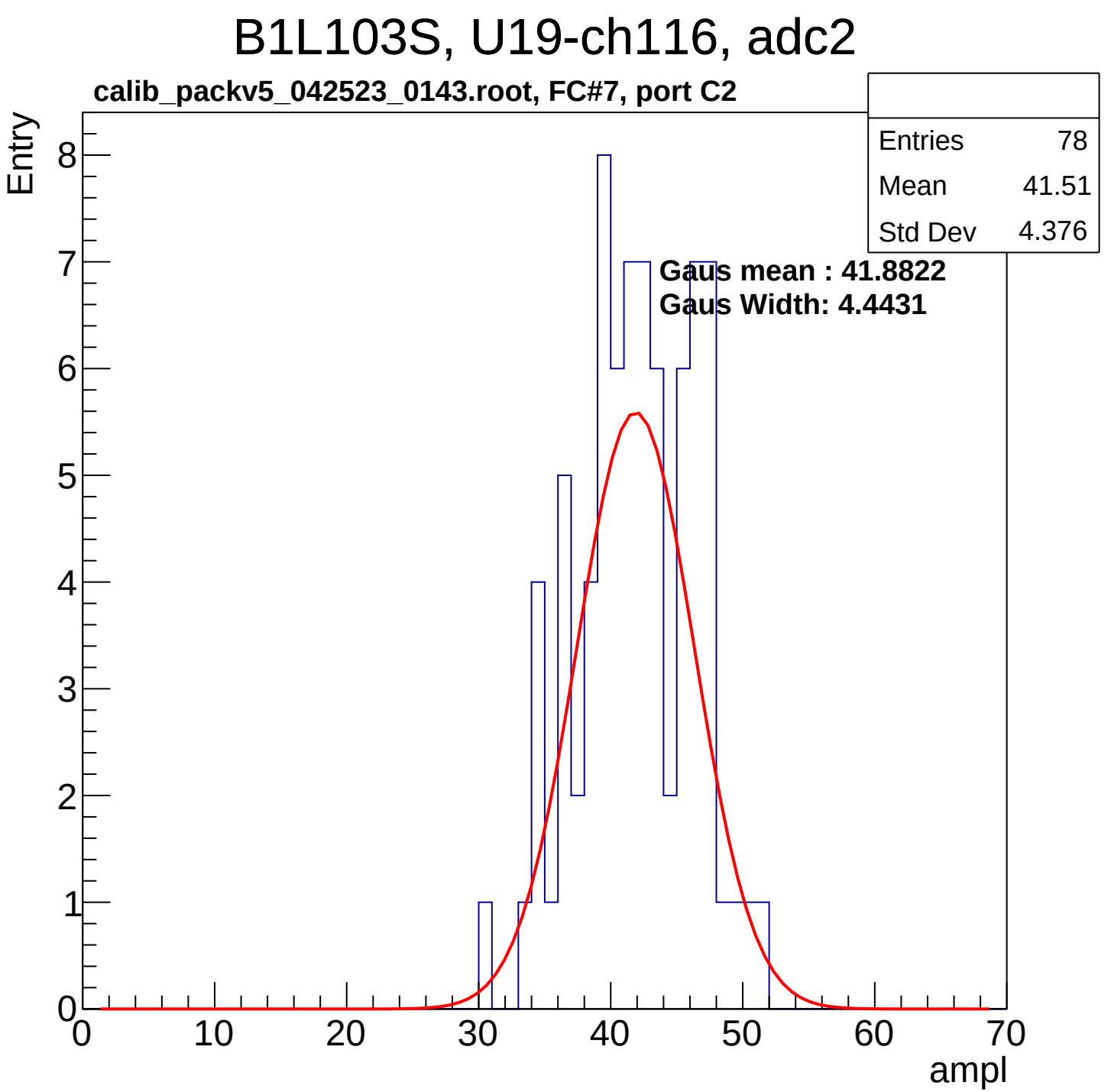
B1L103S, U19-ch116, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 41.51
Std Dev 4.376
Gaus mean : 41.8822
Gaus Width: 4.4431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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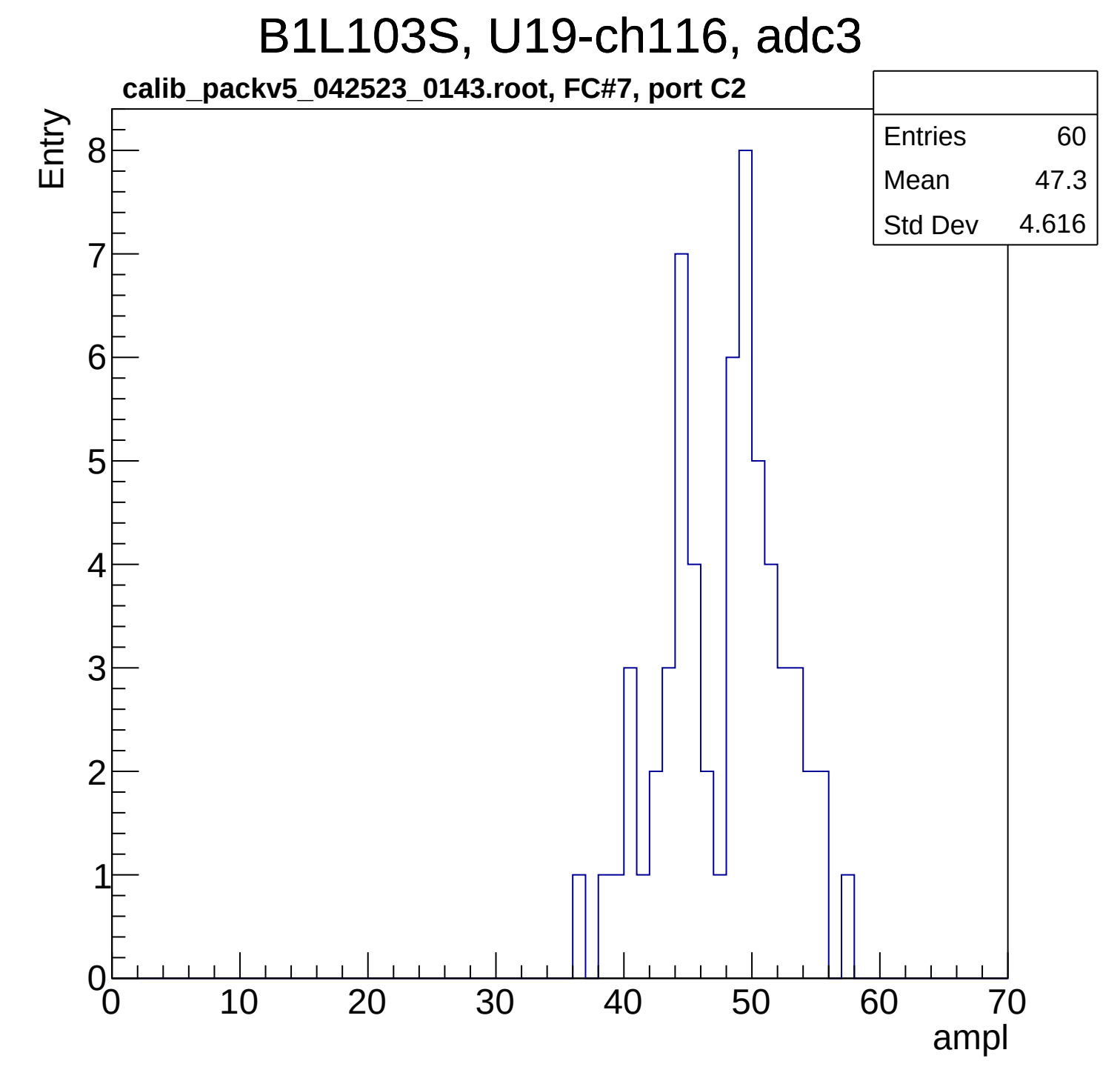
B1L103S, U19-ch116, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 47.3
Std Dev 4.616
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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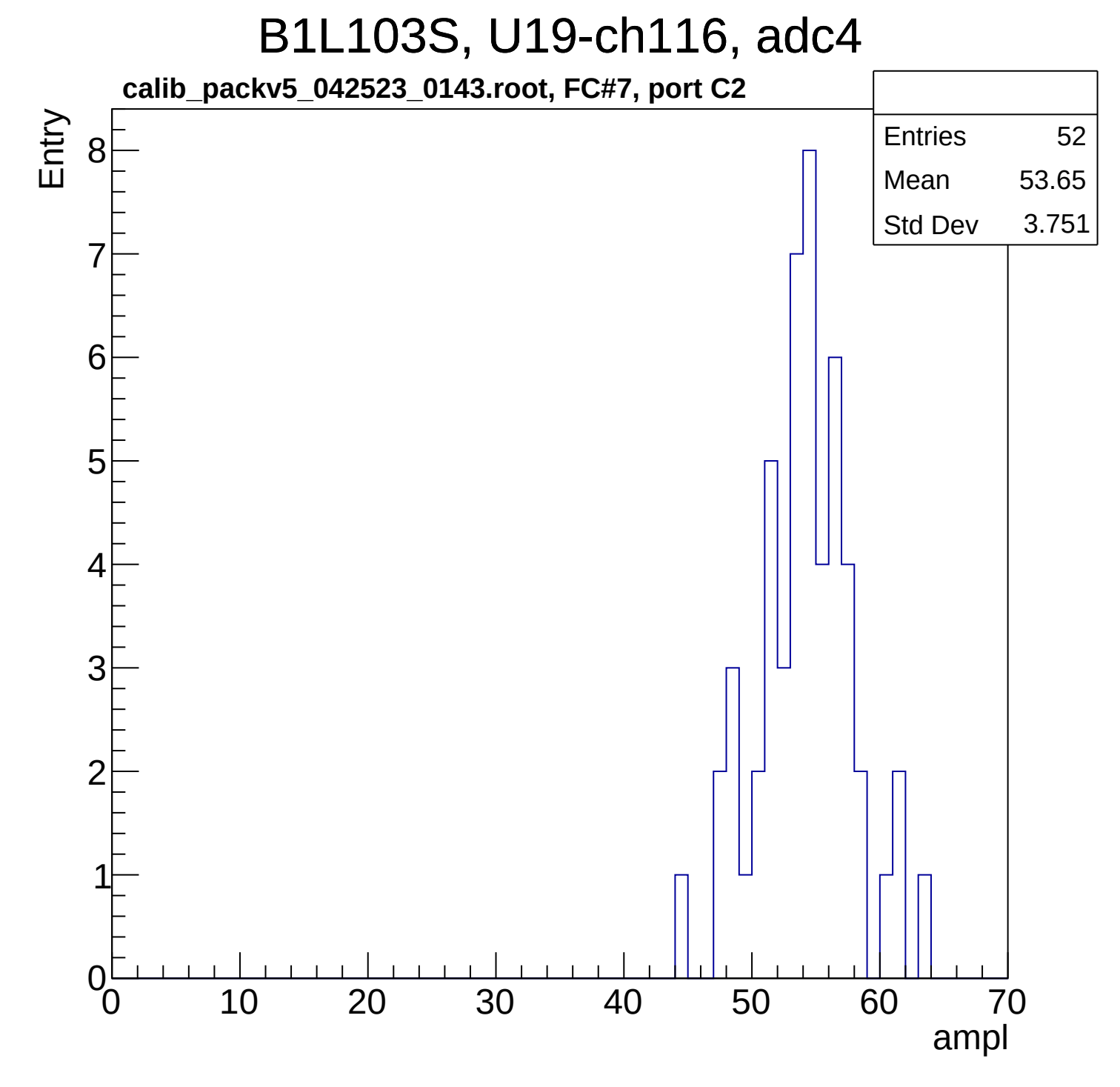
B1L103S, U19-ch116, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 53.65
Std Dev 3.751
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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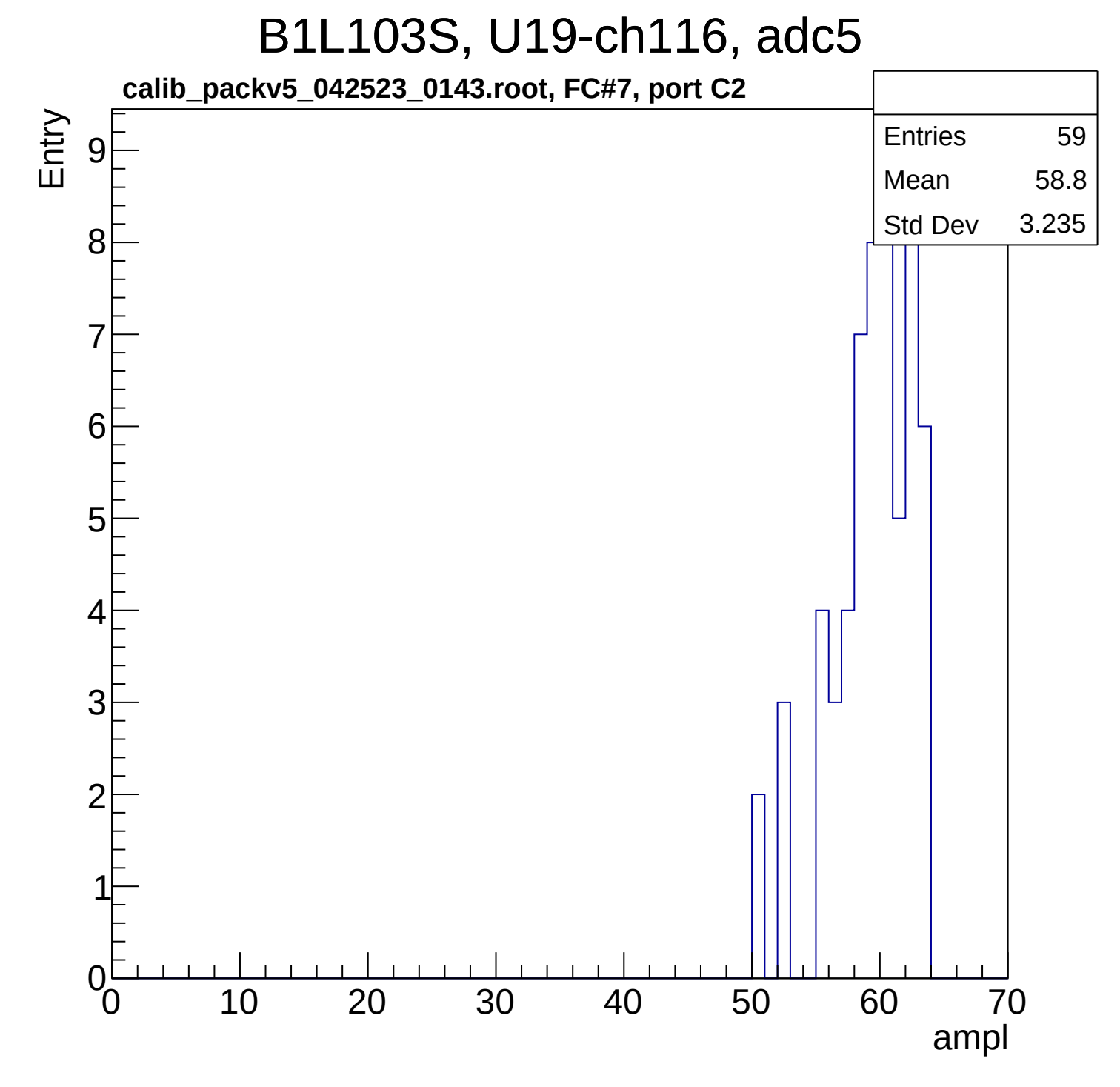
B1L103S, U19-ch116, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.8
Std Dev 3.235
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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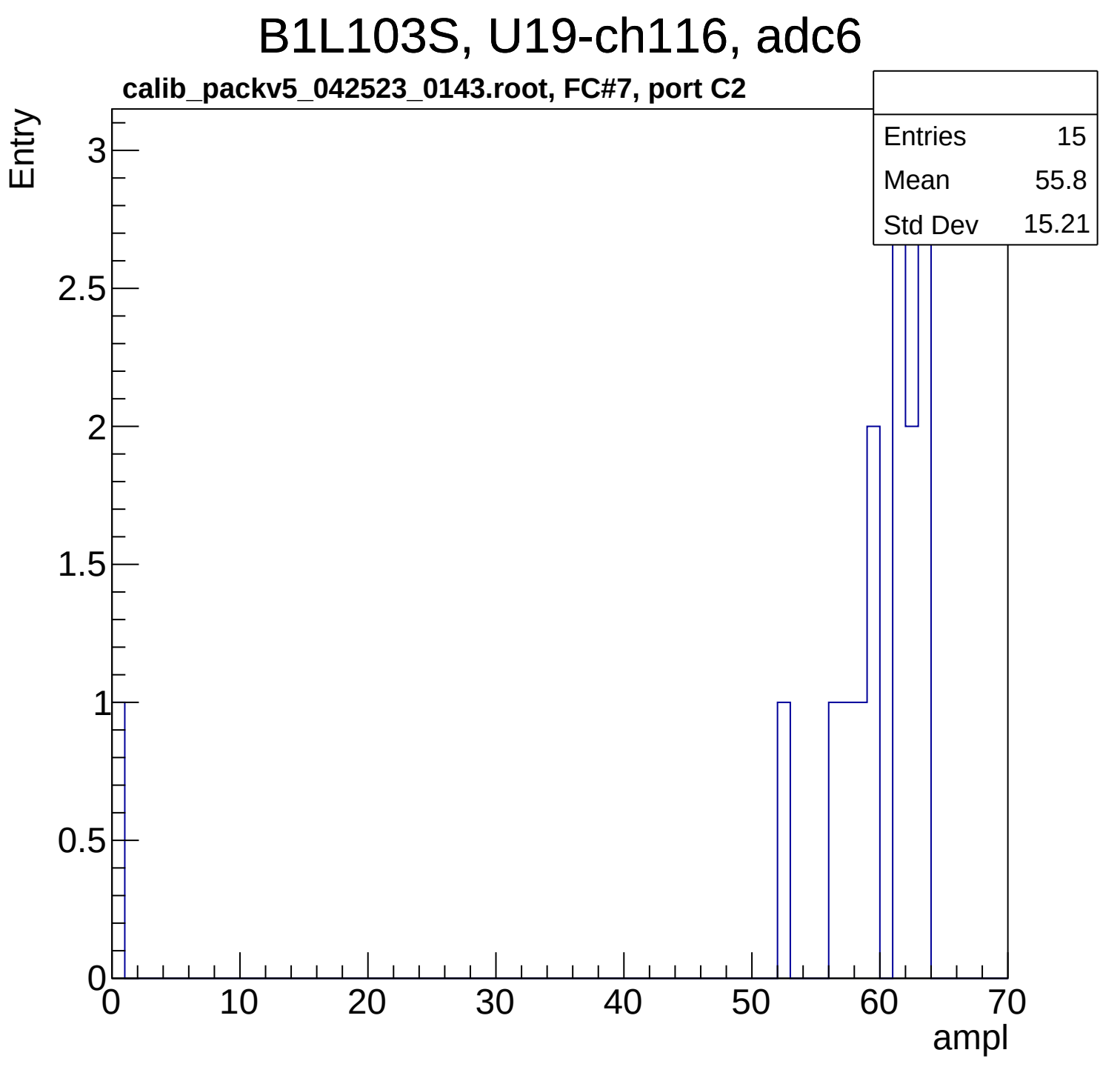
B1L103S, U19-ch116, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 55.8
Std Dev 15.21
Entry
ampl
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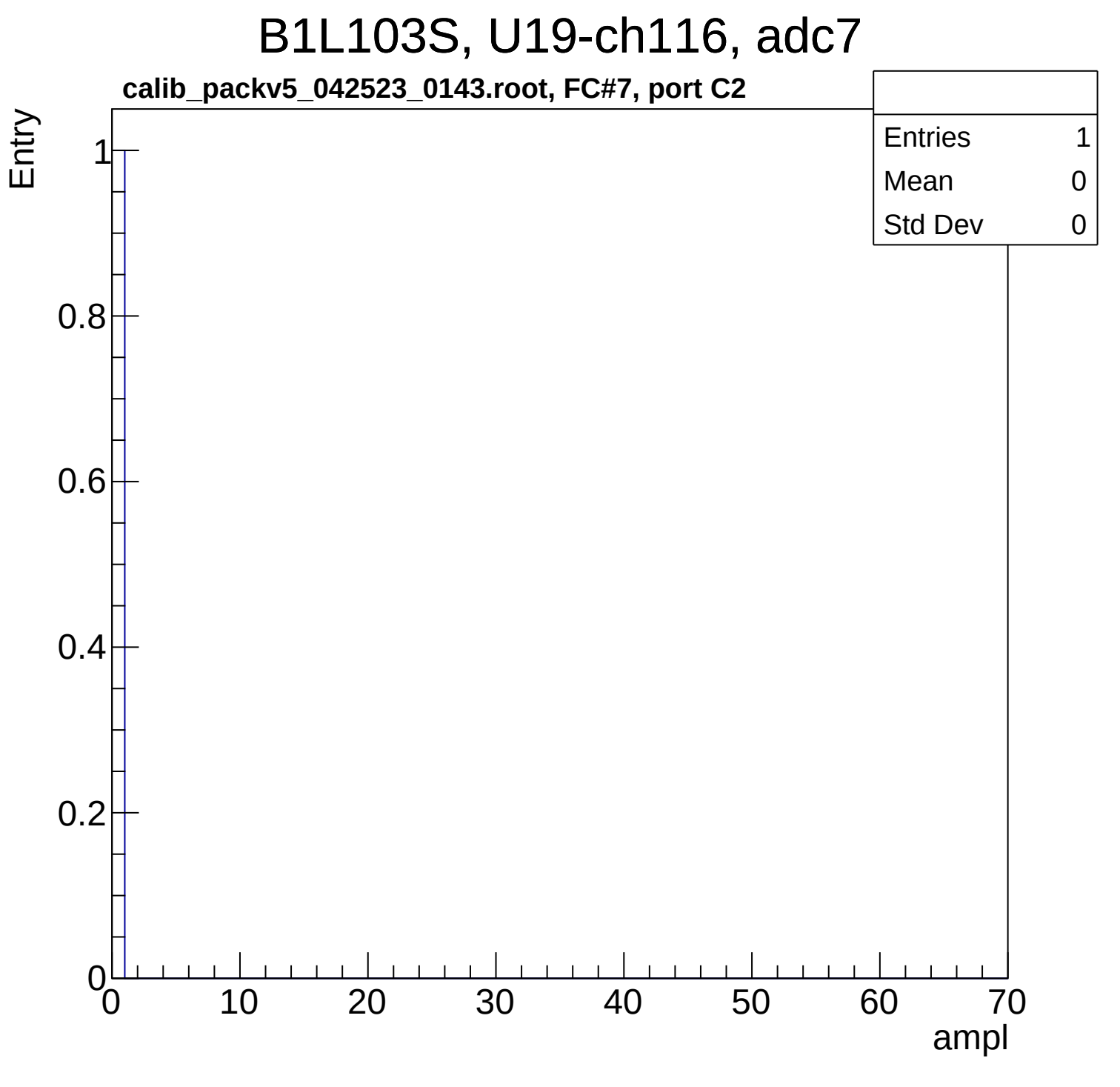
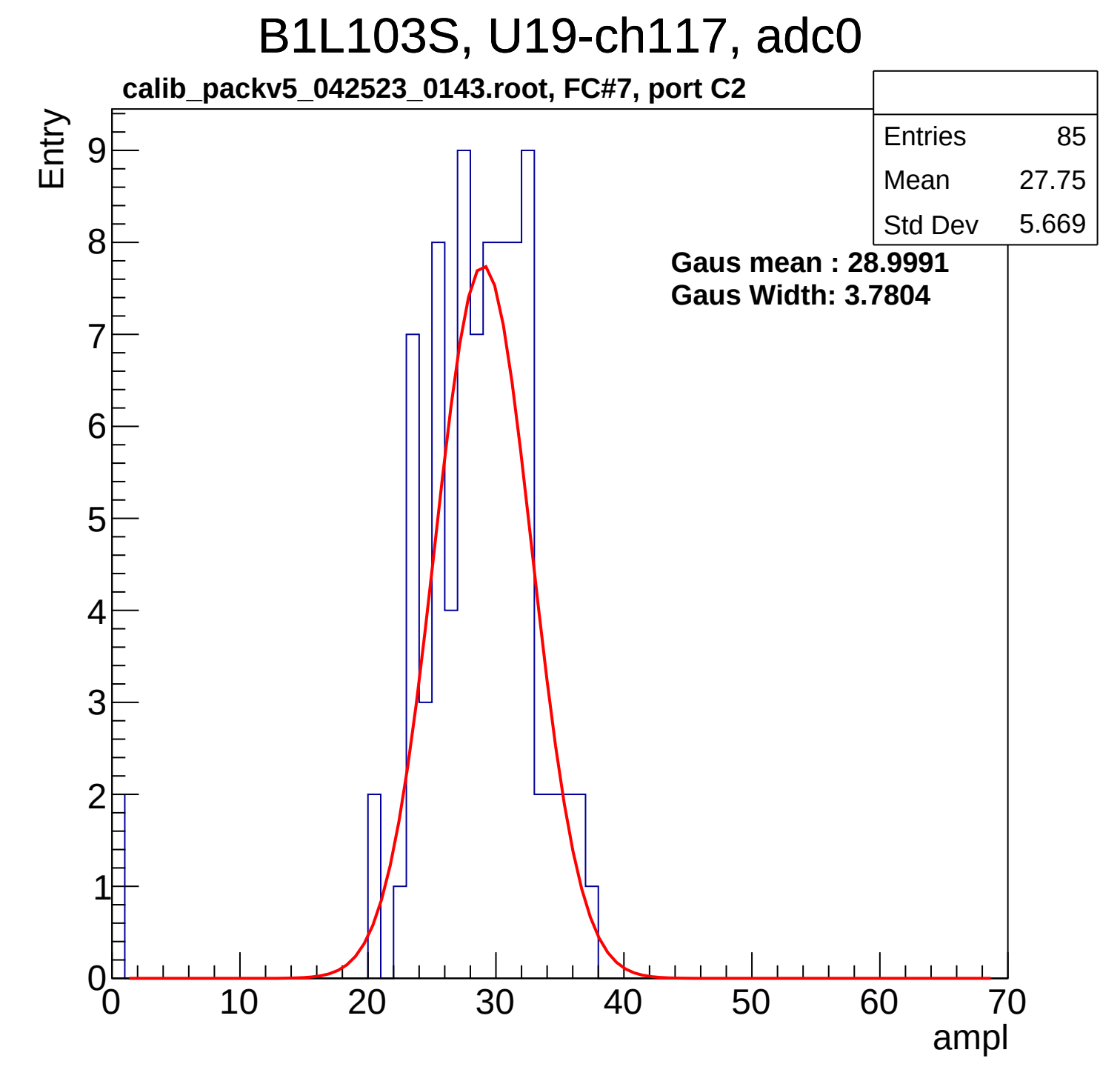
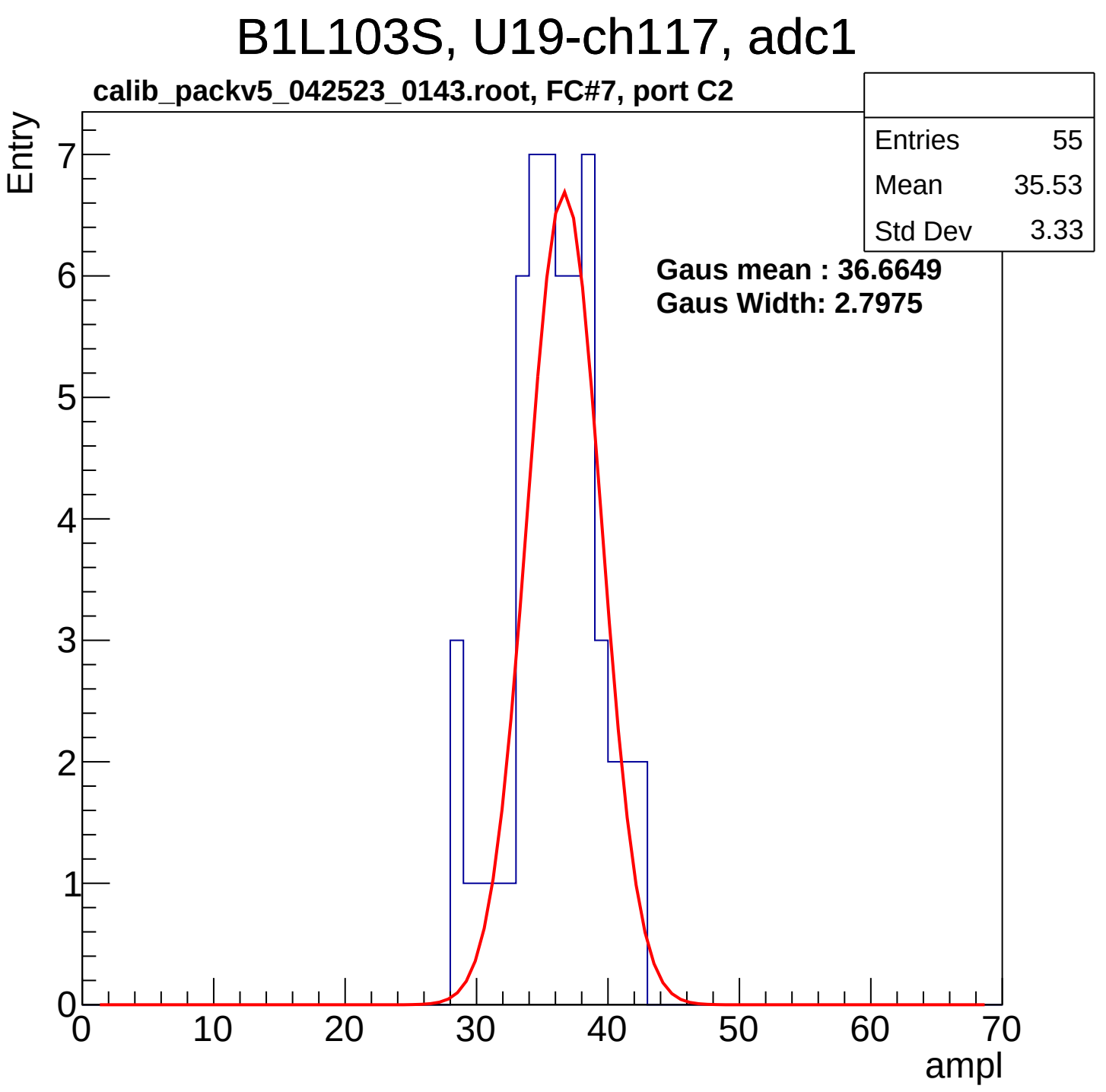
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch116, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch117, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 27.75
Std Dev 5.669
Gaus mean : 28.9991
Gaus Width: 3.7804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch117, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 35.53
Std Dev 3.33
Gaus mean : 36.6649
Gaus Width: 2.7975
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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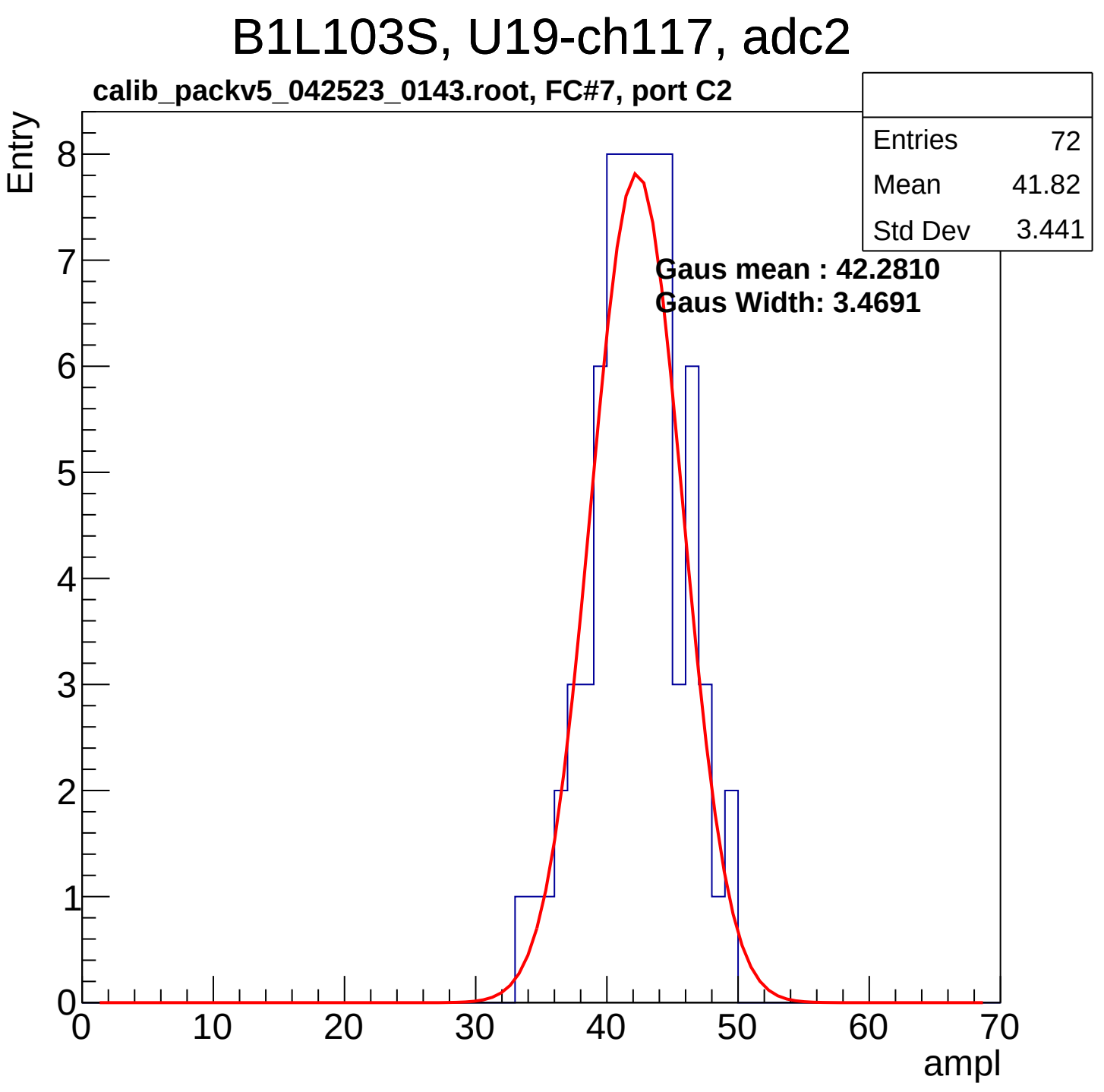
B1L103S, U19-ch117, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 41.82
Std Dev 3.441
Gaus mean : 42.2810
Gaus Width: 3.4691
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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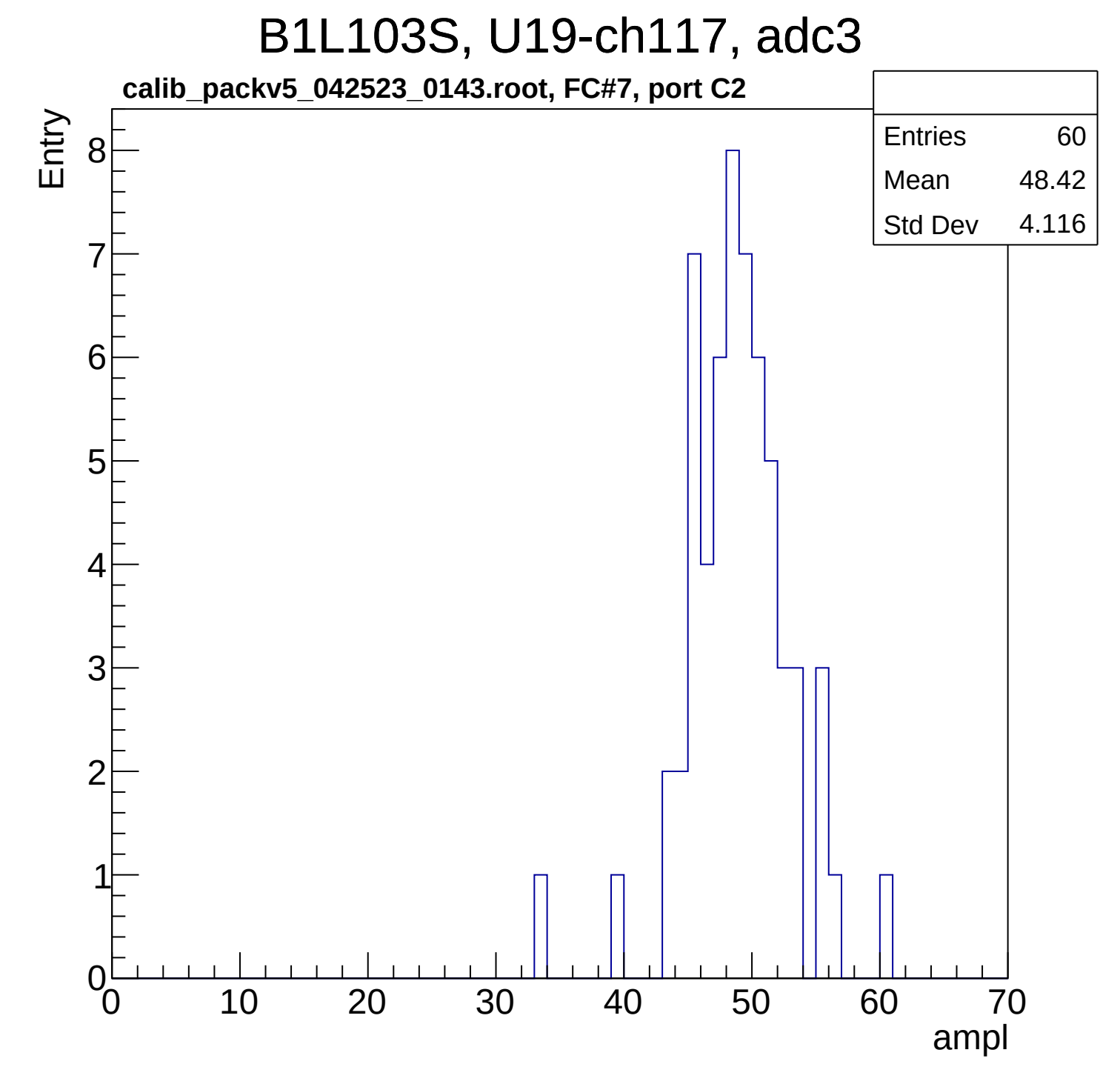
B1L103S, U19-ch117, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.42
Std Dev 4.116
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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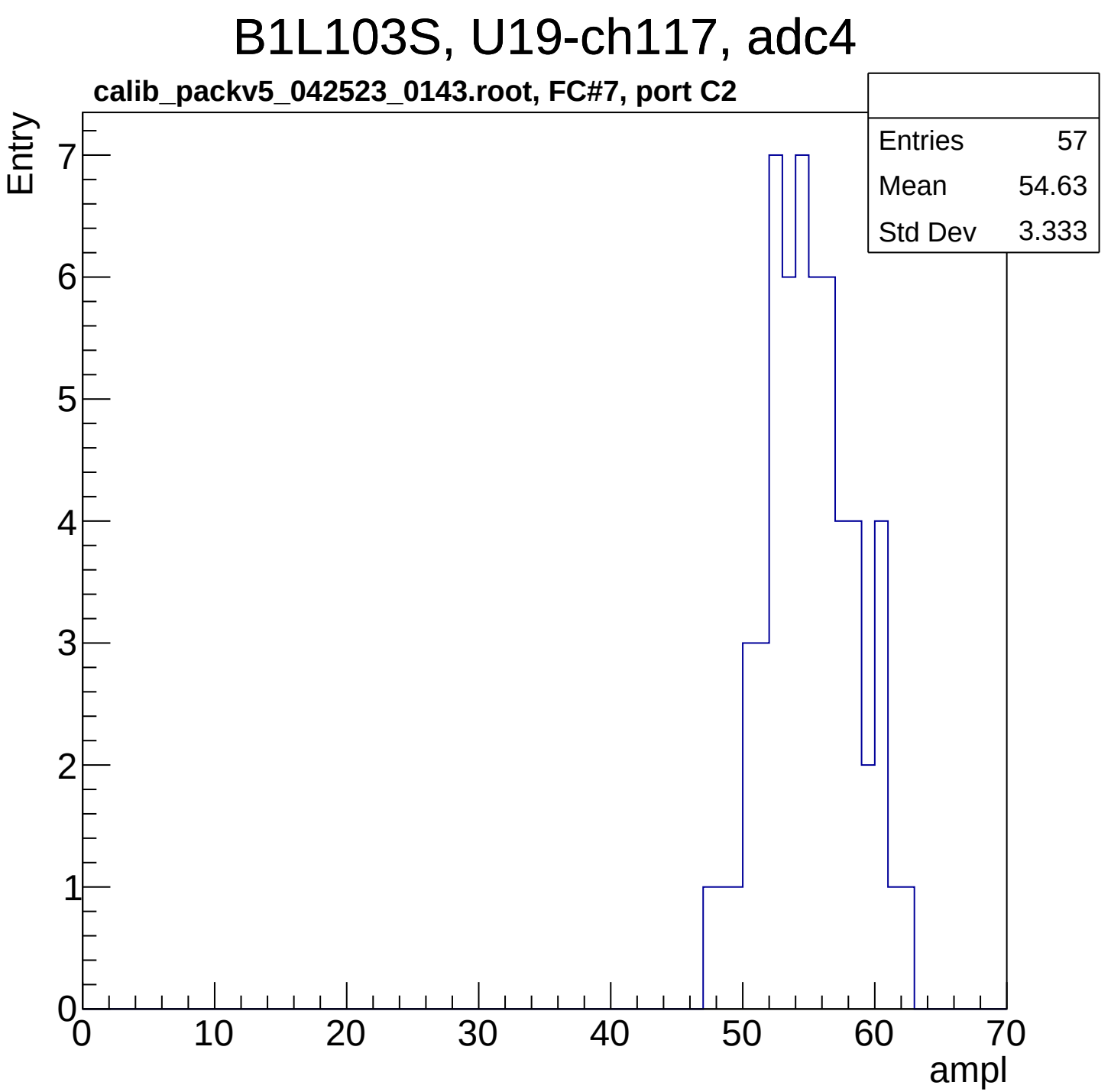
B1L103S, U19-ch117, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 54.63
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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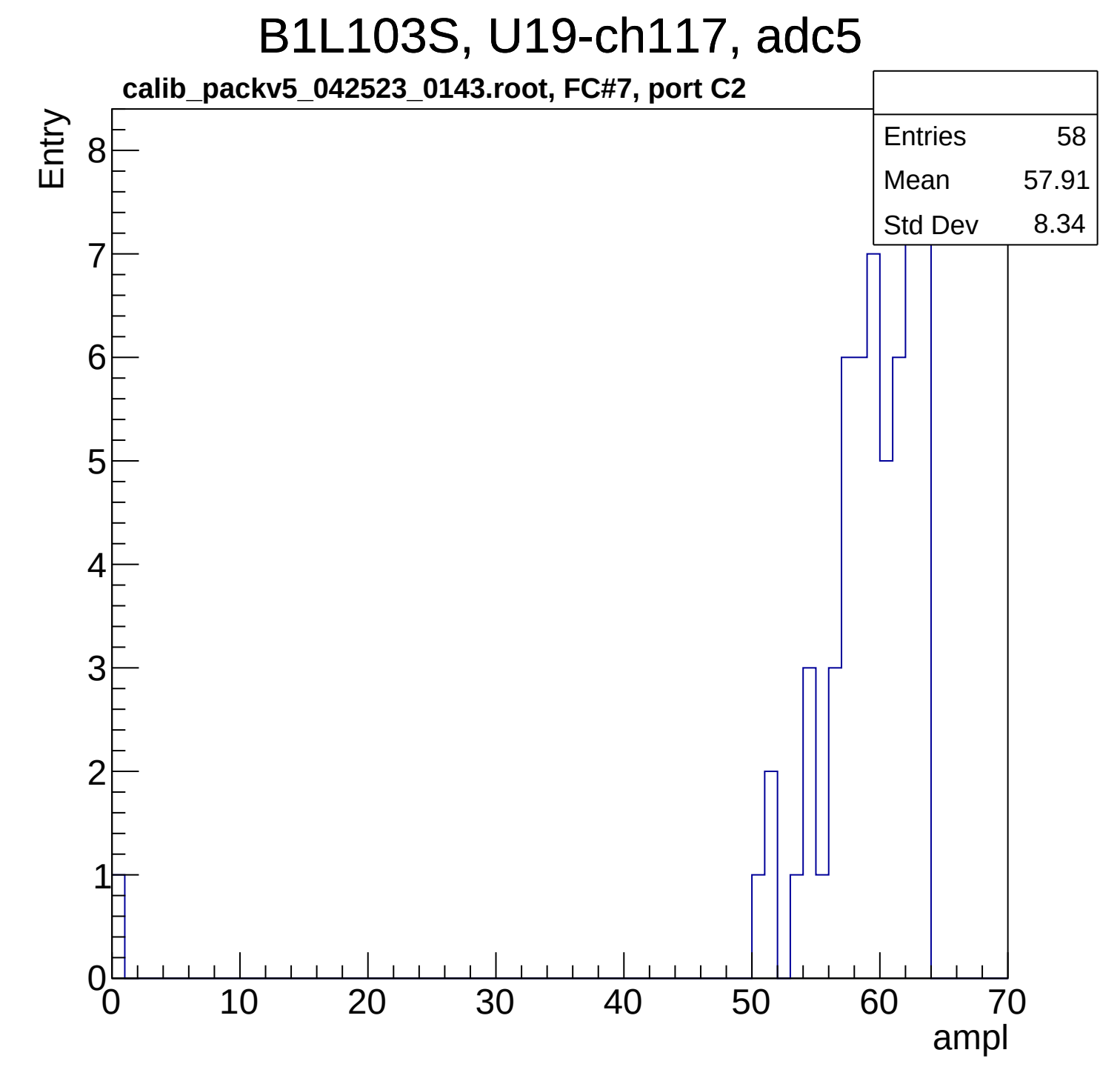
B1L103S, U19-ch117, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.91
Std Dev 8.34
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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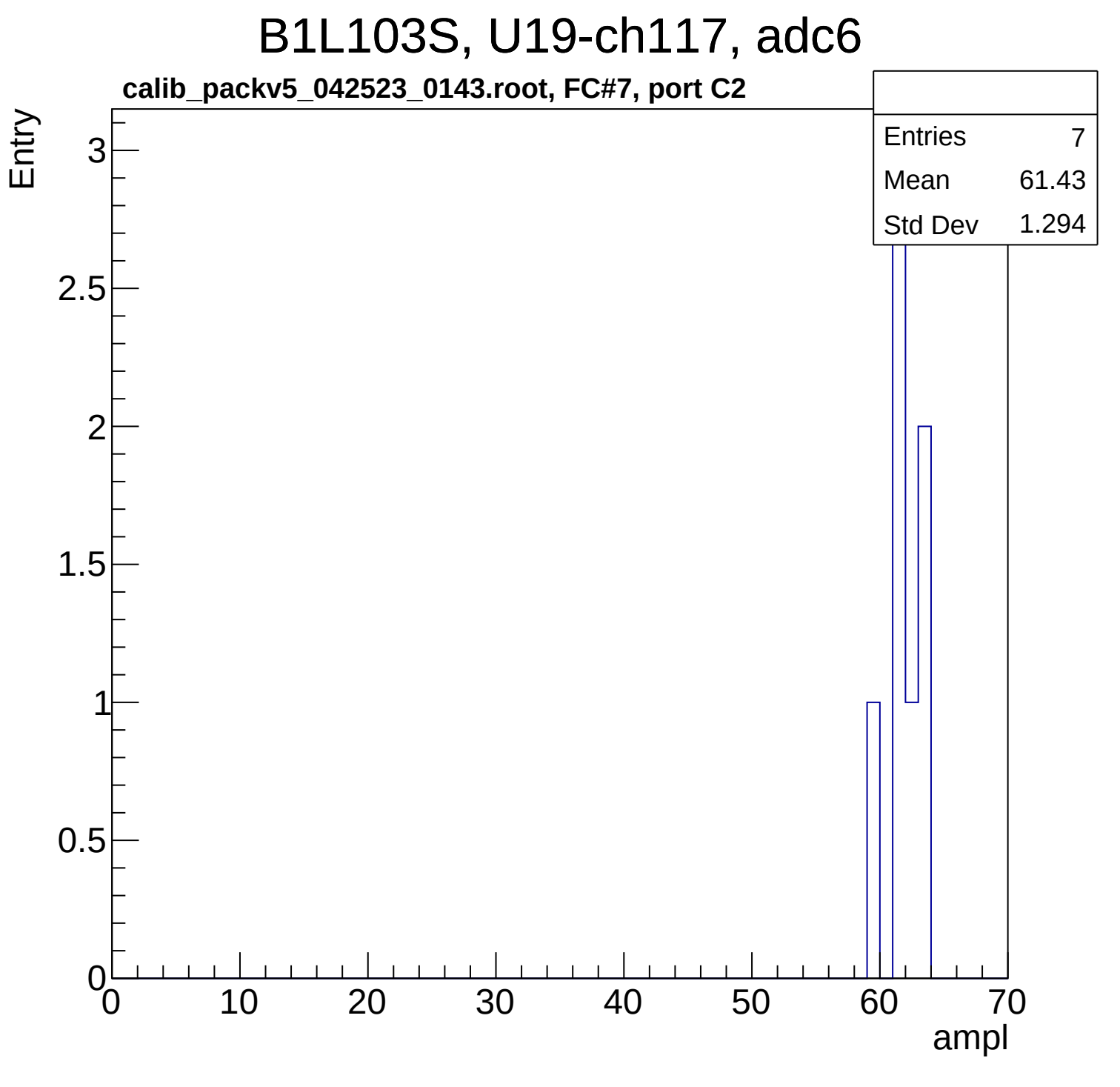
B1L103S, U19-ch117, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.43
Std Dev 1.294
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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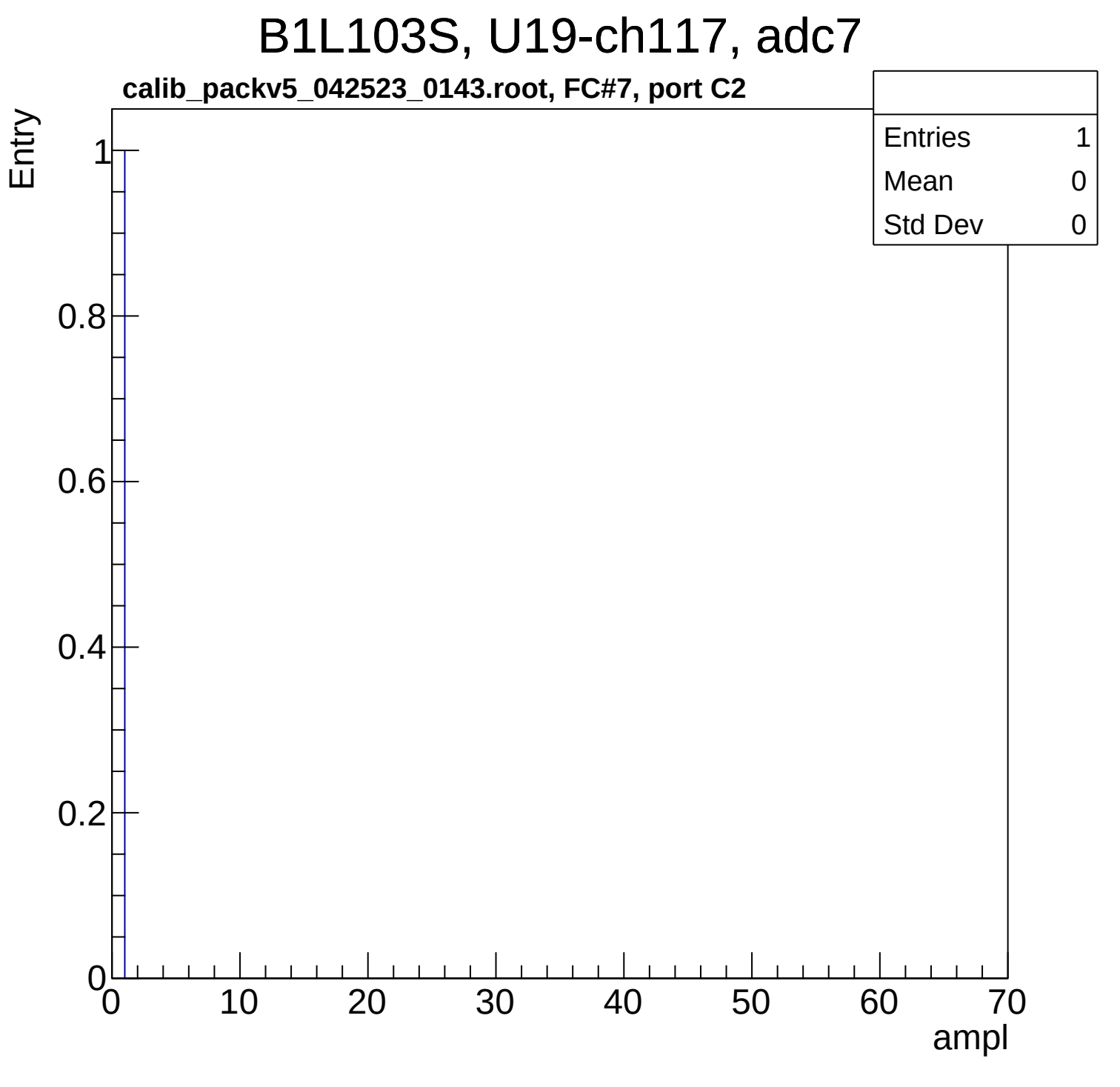
B1L103S, U19-ch117, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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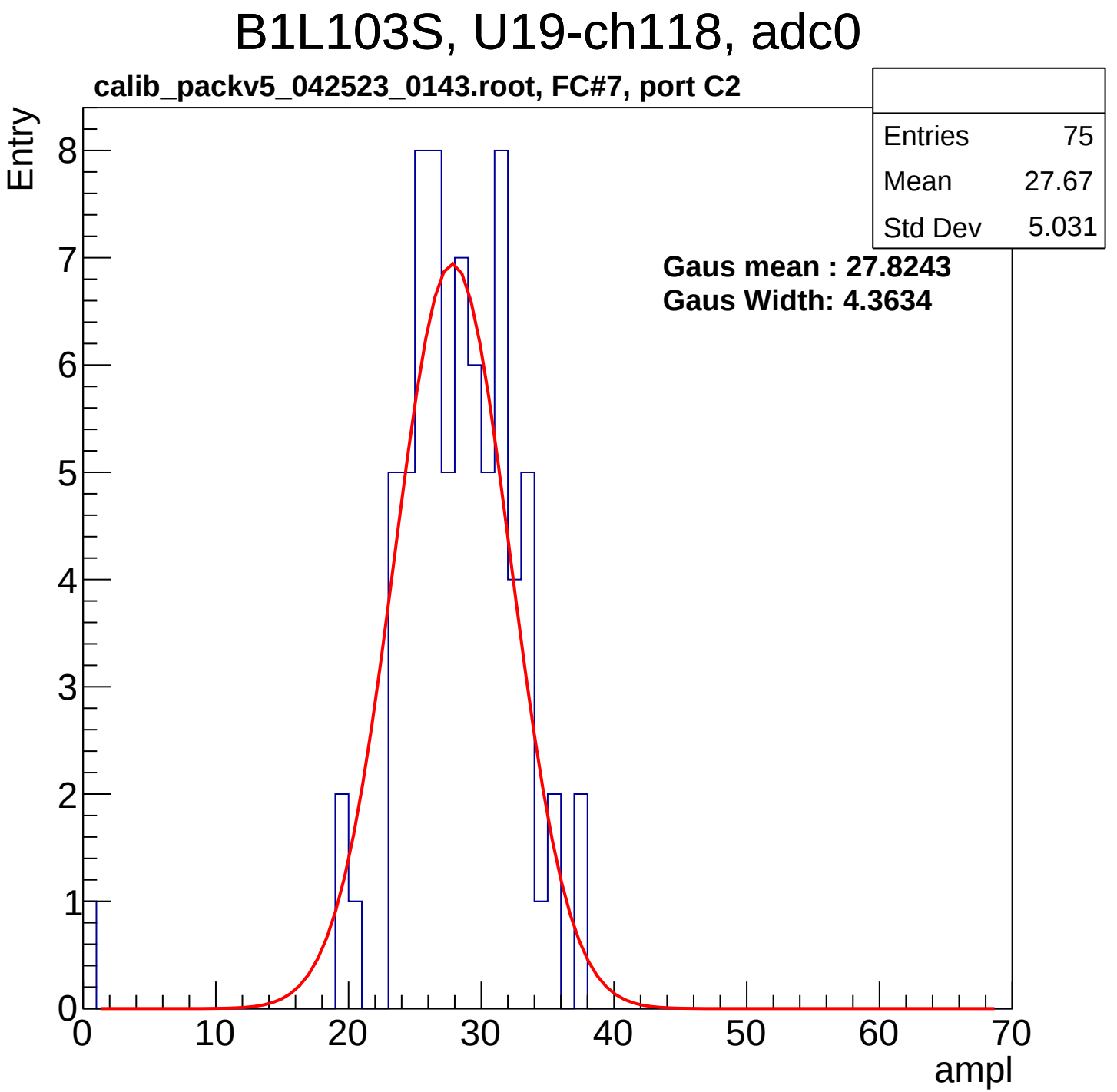
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch118, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 27.67
Std Dev 5.031
Gaus mean : 27.8243
Gaus Width: 4.3634
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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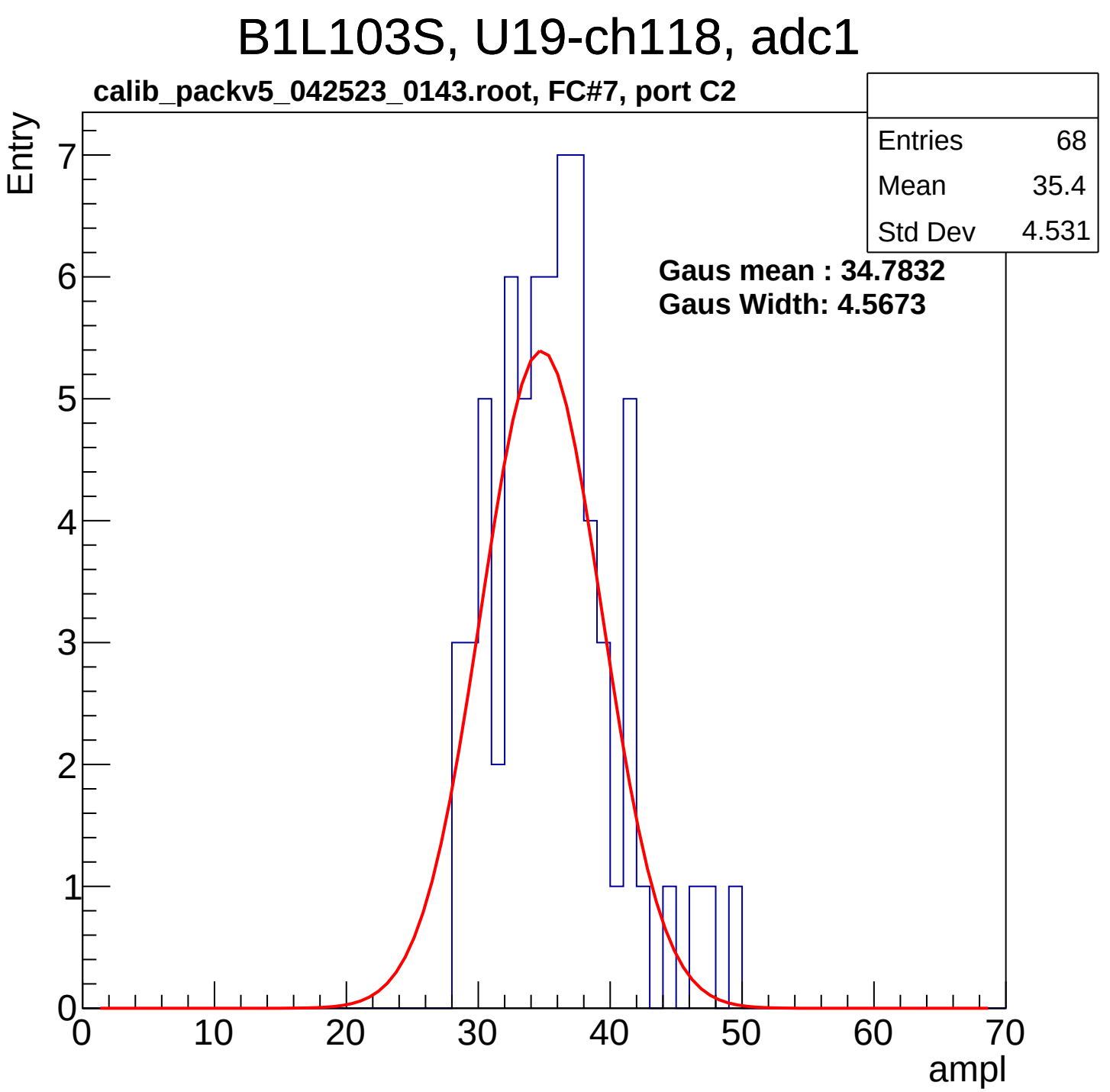
B1L103S, U19-ch118, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 35.4
Std Dev 4.531
Gaus mean : 34.7832
Gaus Width: 4.5673
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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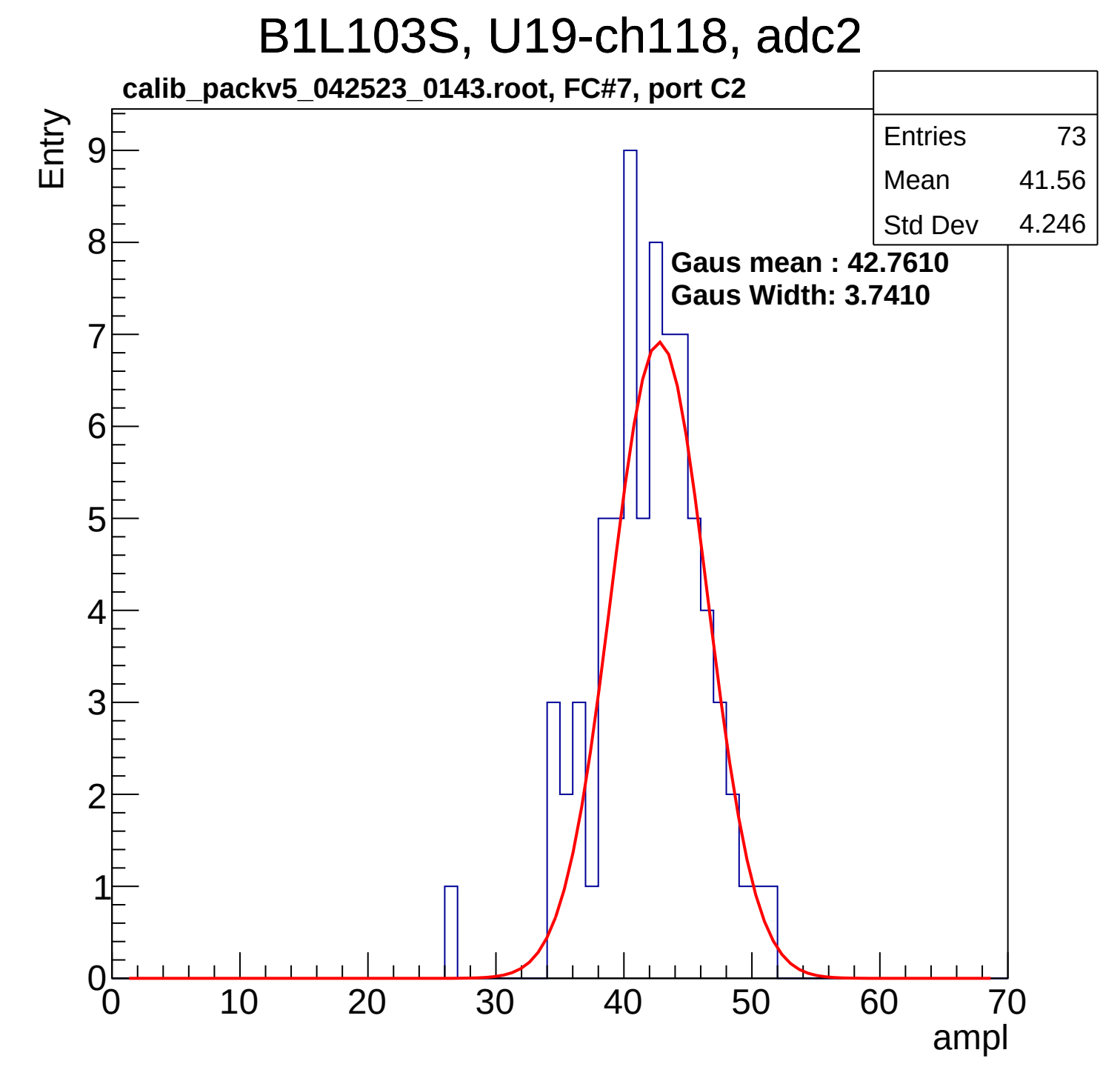
B1L103S, U19-ch118, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 41.56
Std Dev 4.246
Gaus mean : 42.7610
Gaus Width: 3.7410
Entry
ampl
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2
3
4
5
6
7
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0
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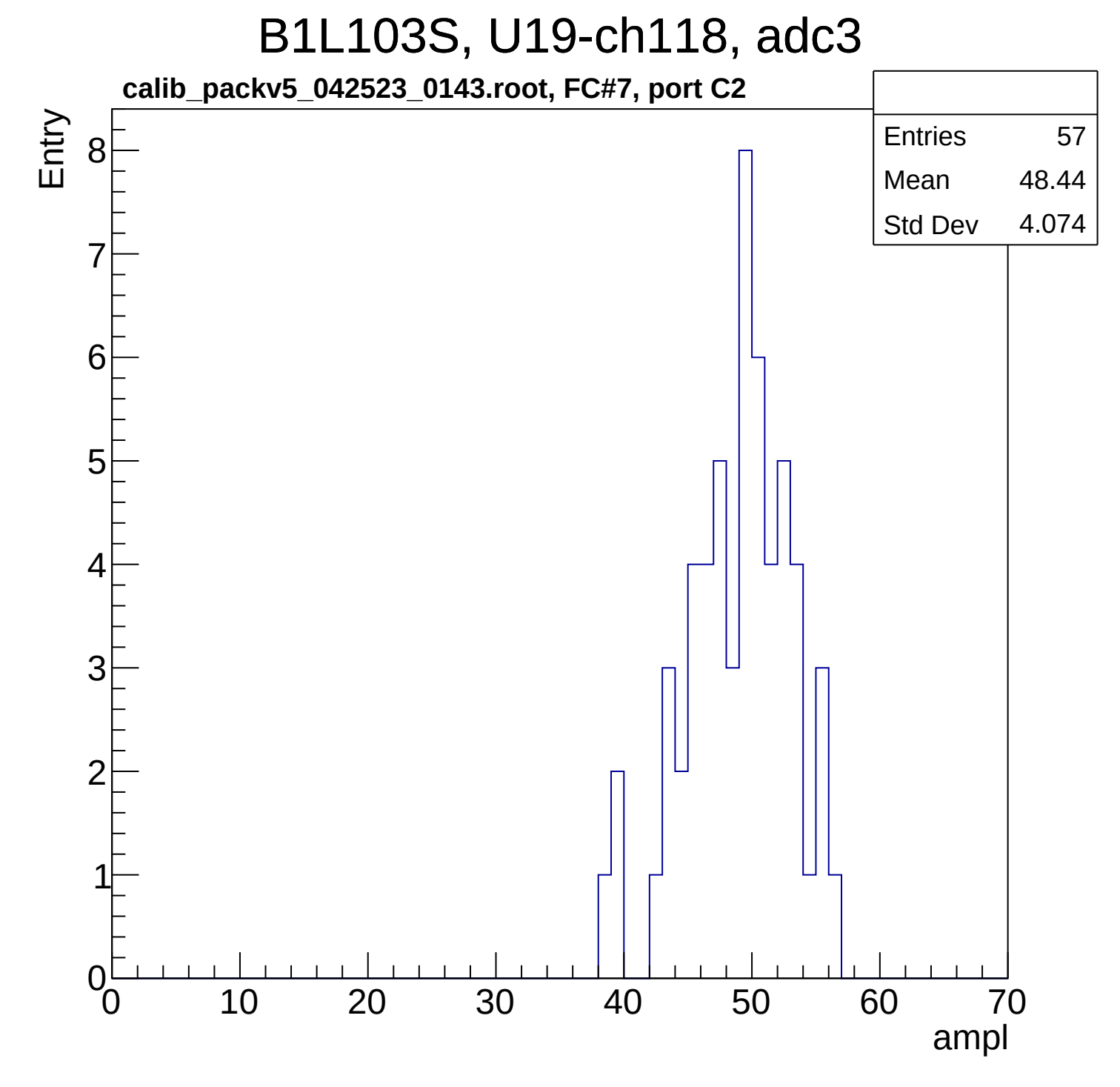
B1L103S, U19-ch118, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 48.44
Std Dev 4.074
Entry
ampl
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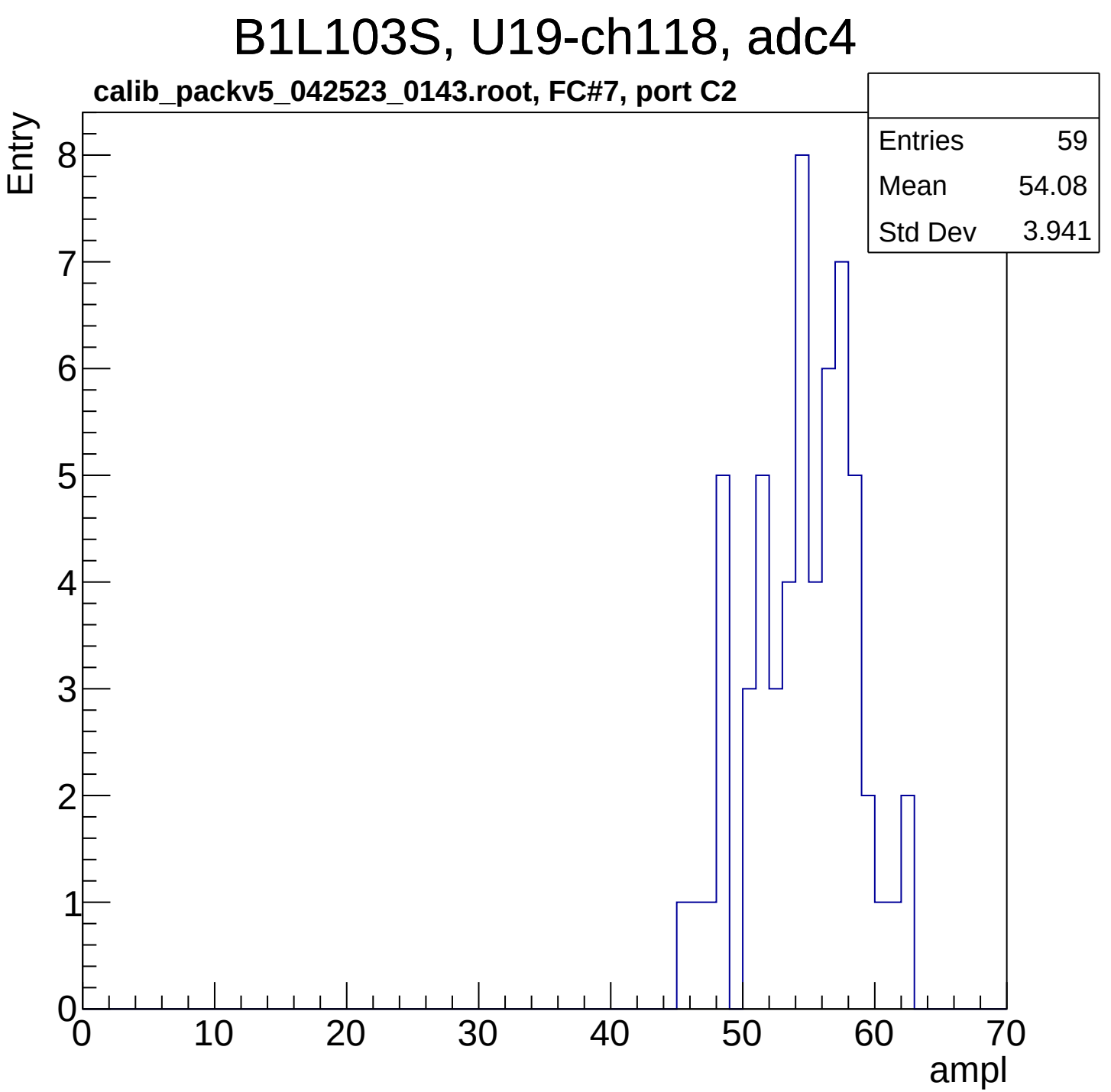
B1L103S, U19-ch118, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.08
Std Dev 3.941
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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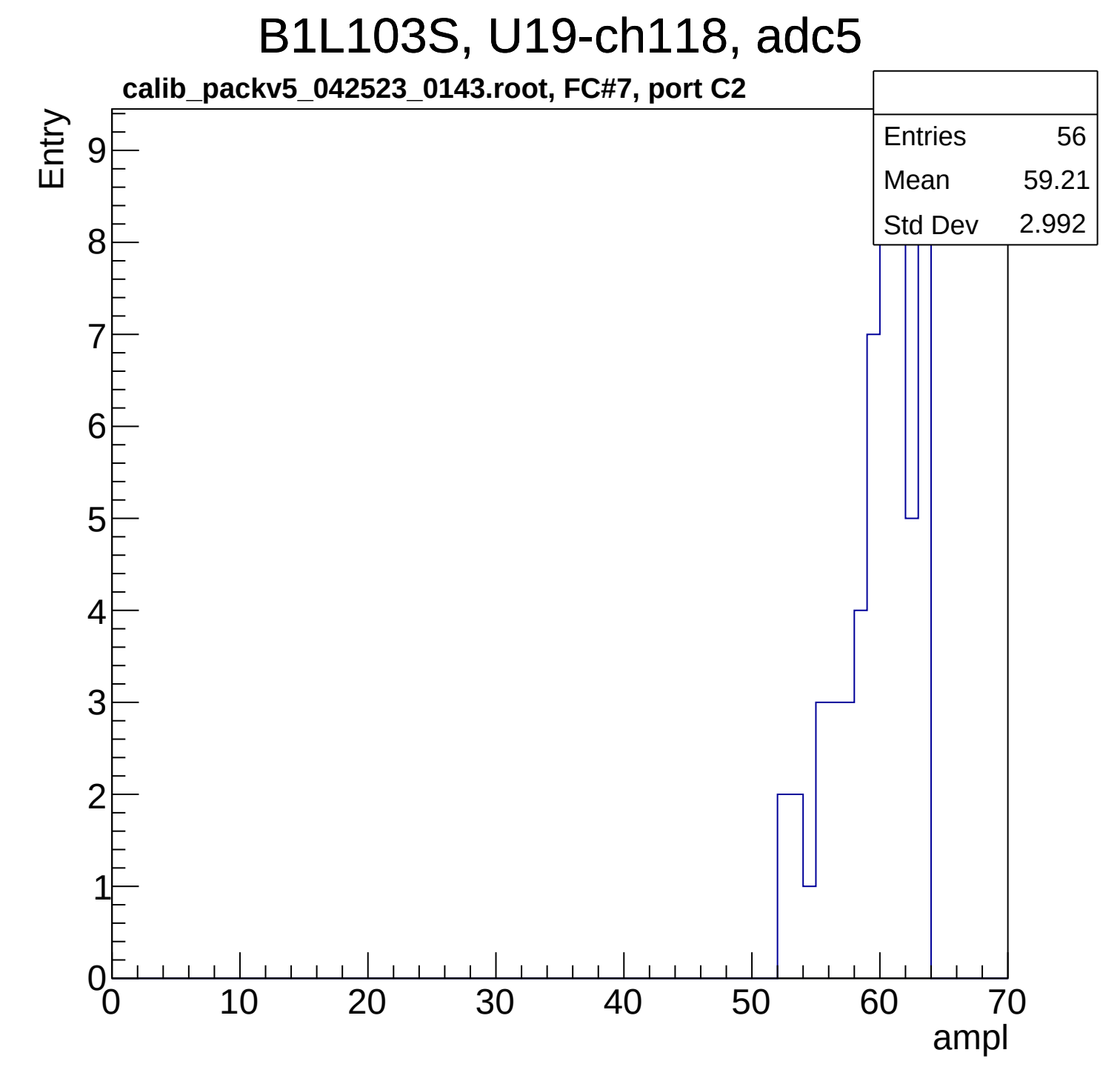
B1L103S, U19-ch118, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 59.21
Std Dev 2.992
Entry
ampl
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5
6
7
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0
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60
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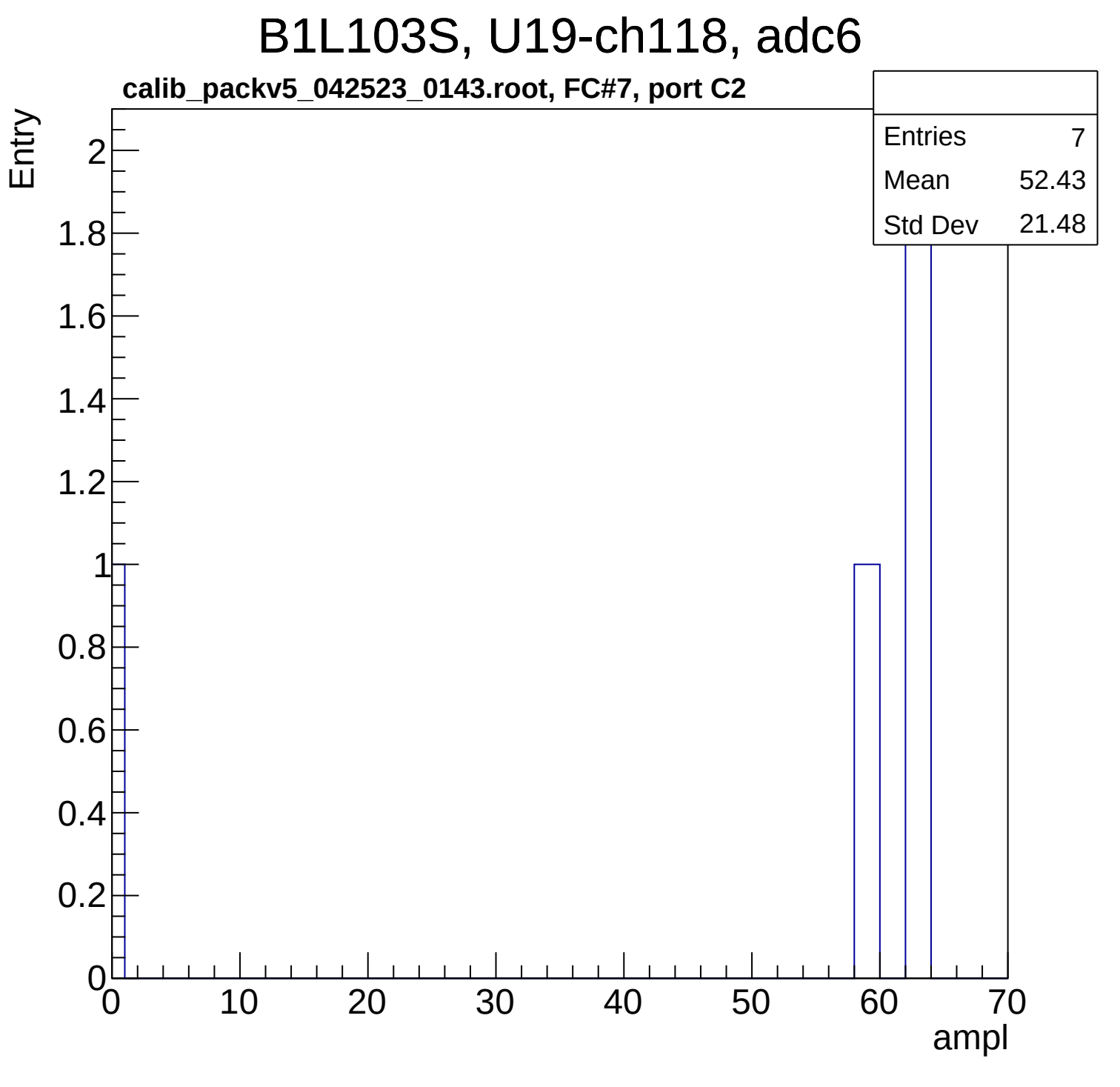
B1L103S, U19-ch118, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 52.43
Std Dev 21.48
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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0
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40
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60
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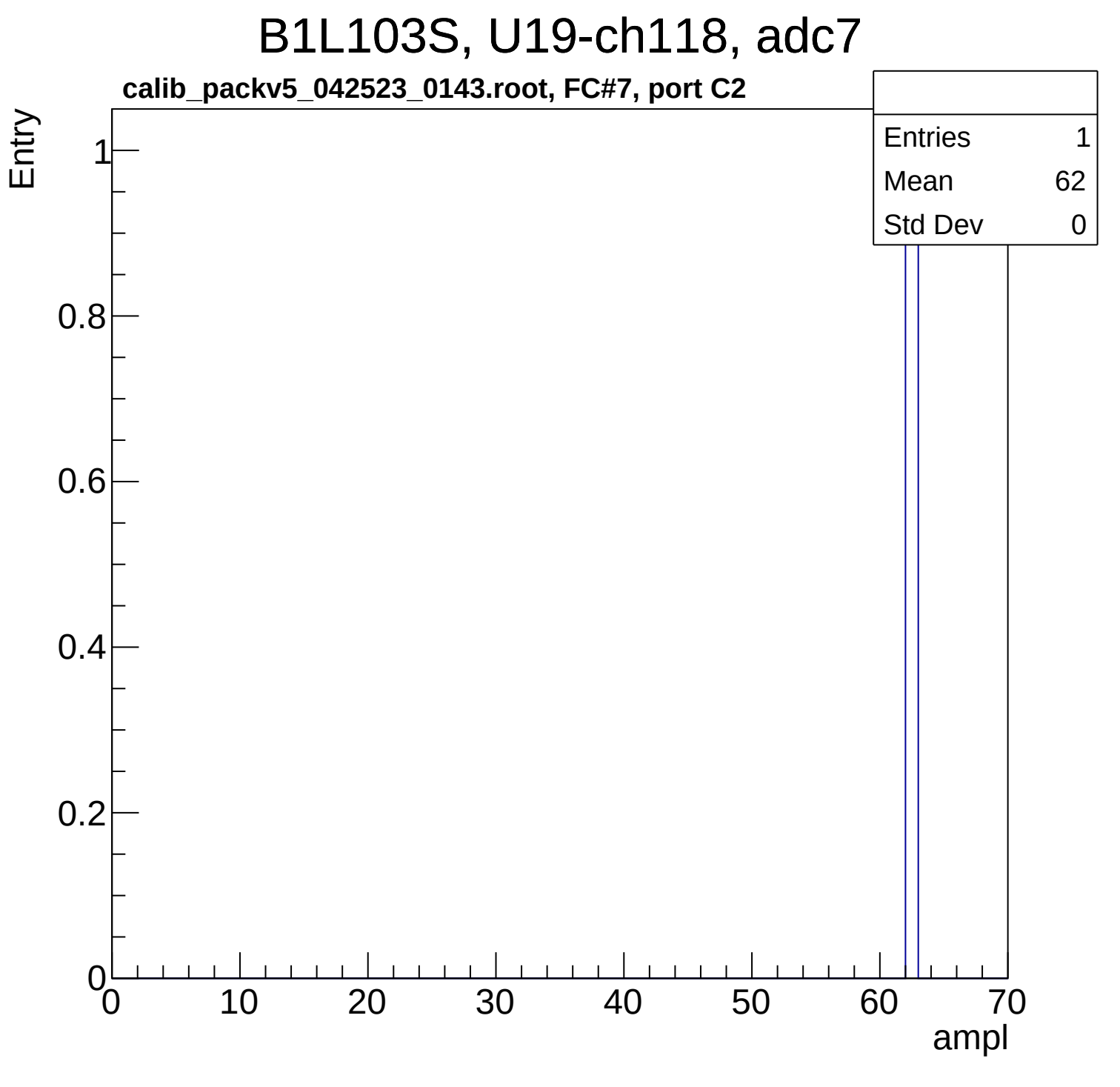
B1L103S, U19-ch118, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
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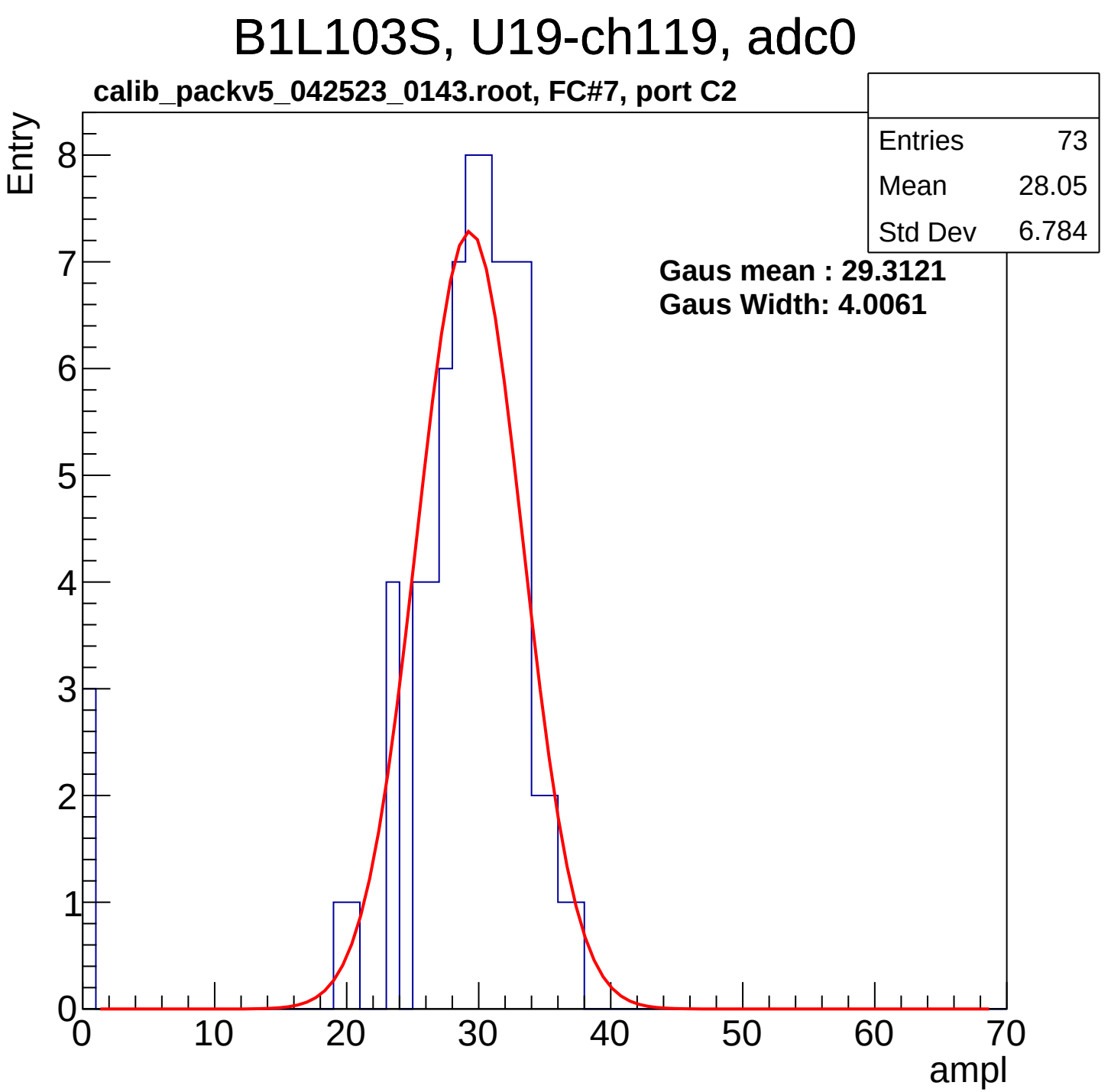
50
60
70
ampl


B1L103S, U19-ch119, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 28.05
Std Dev 6.784
Gaus mean : 29.3121
Gaus Width: 4.0061
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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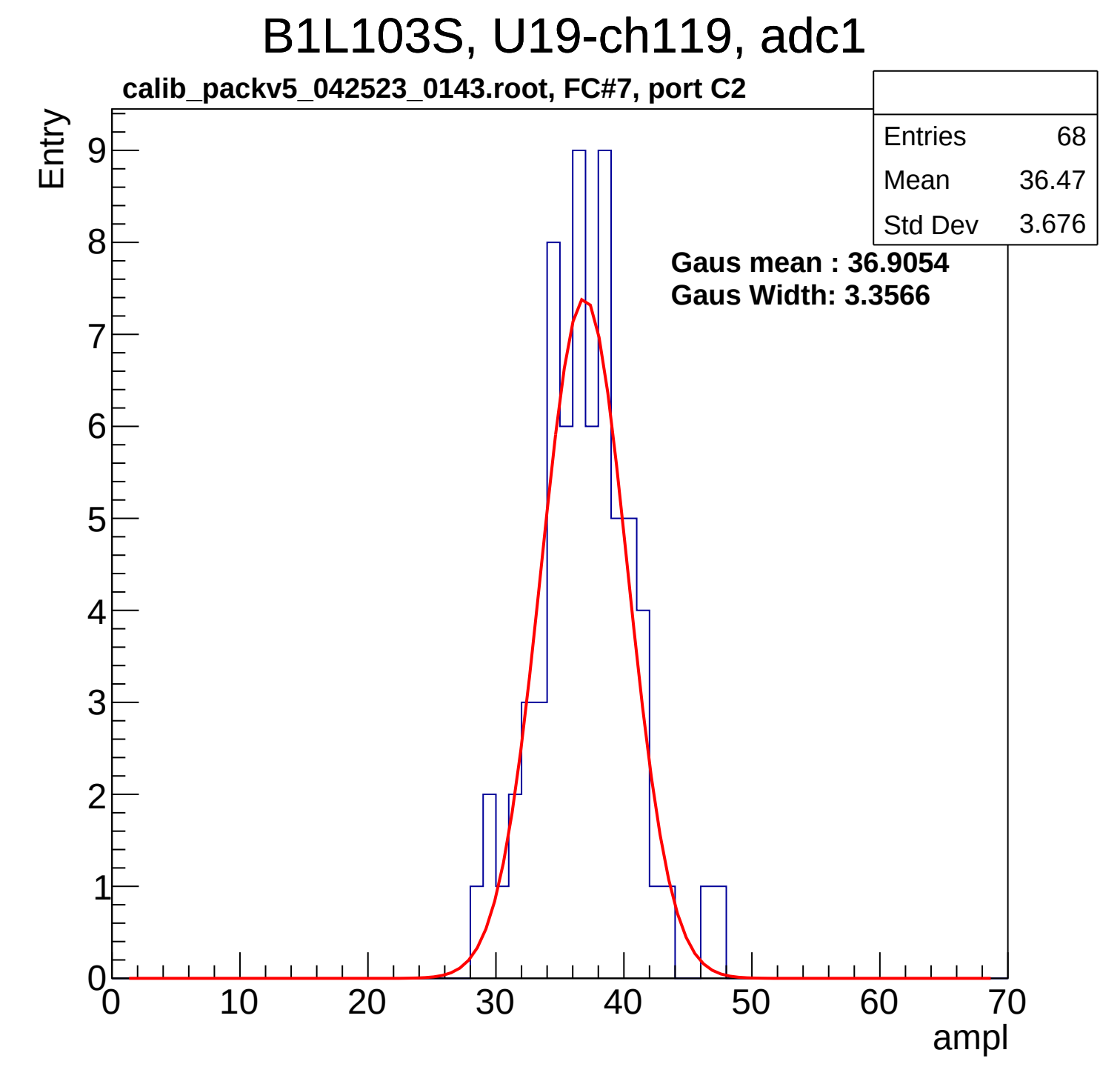
B1L103S, U19-ch119, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.47
Std Dev 3.676
Gaus mean : 36.9054
Gaus Width: 3.3566
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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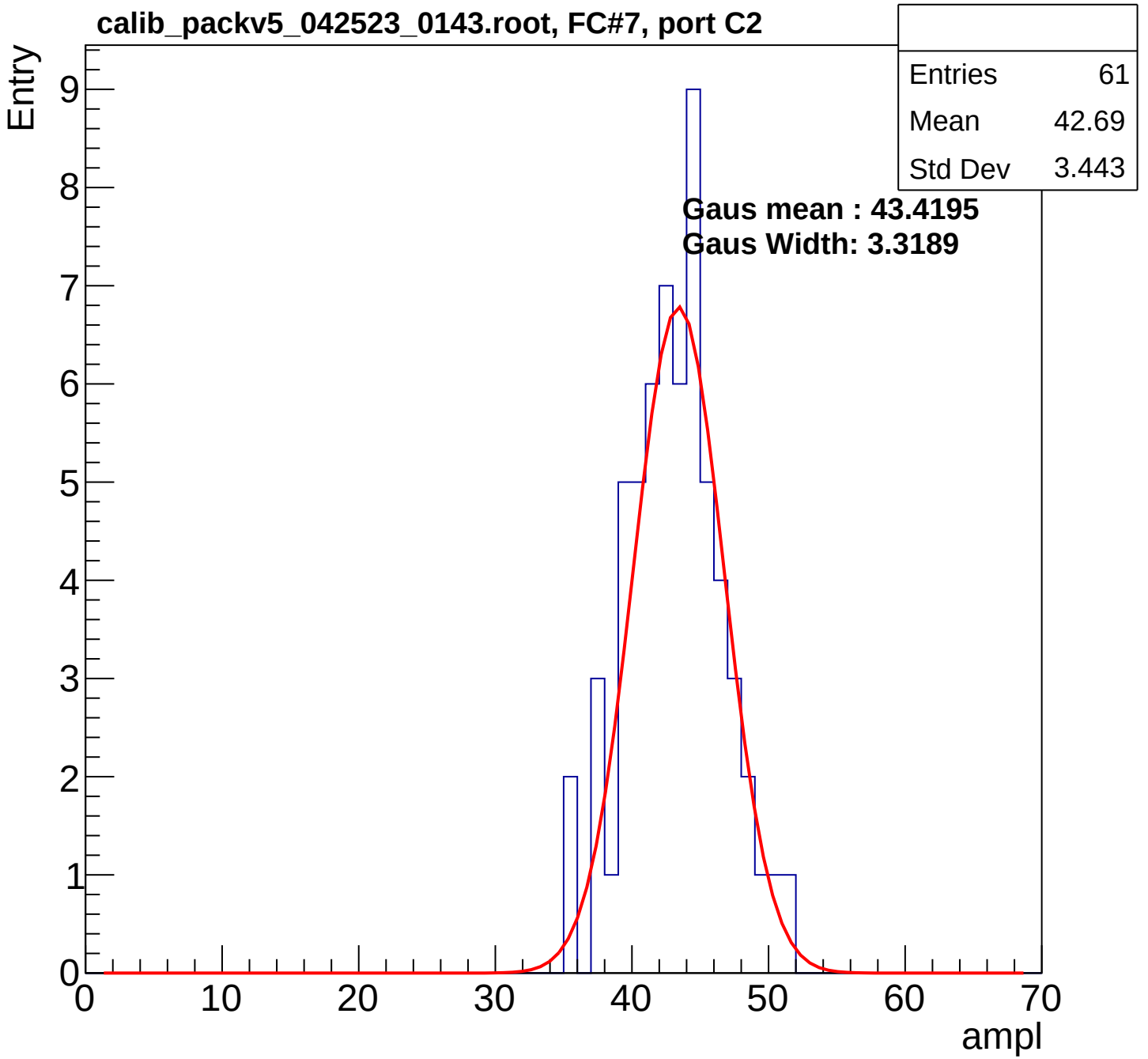
B1L103S, U19-ch119, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.69
Std Dev 3.443
Gaus mean : 43.4195
Gaus Width: 3.3189
Entry
ampl
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2
3
4
5
6
7
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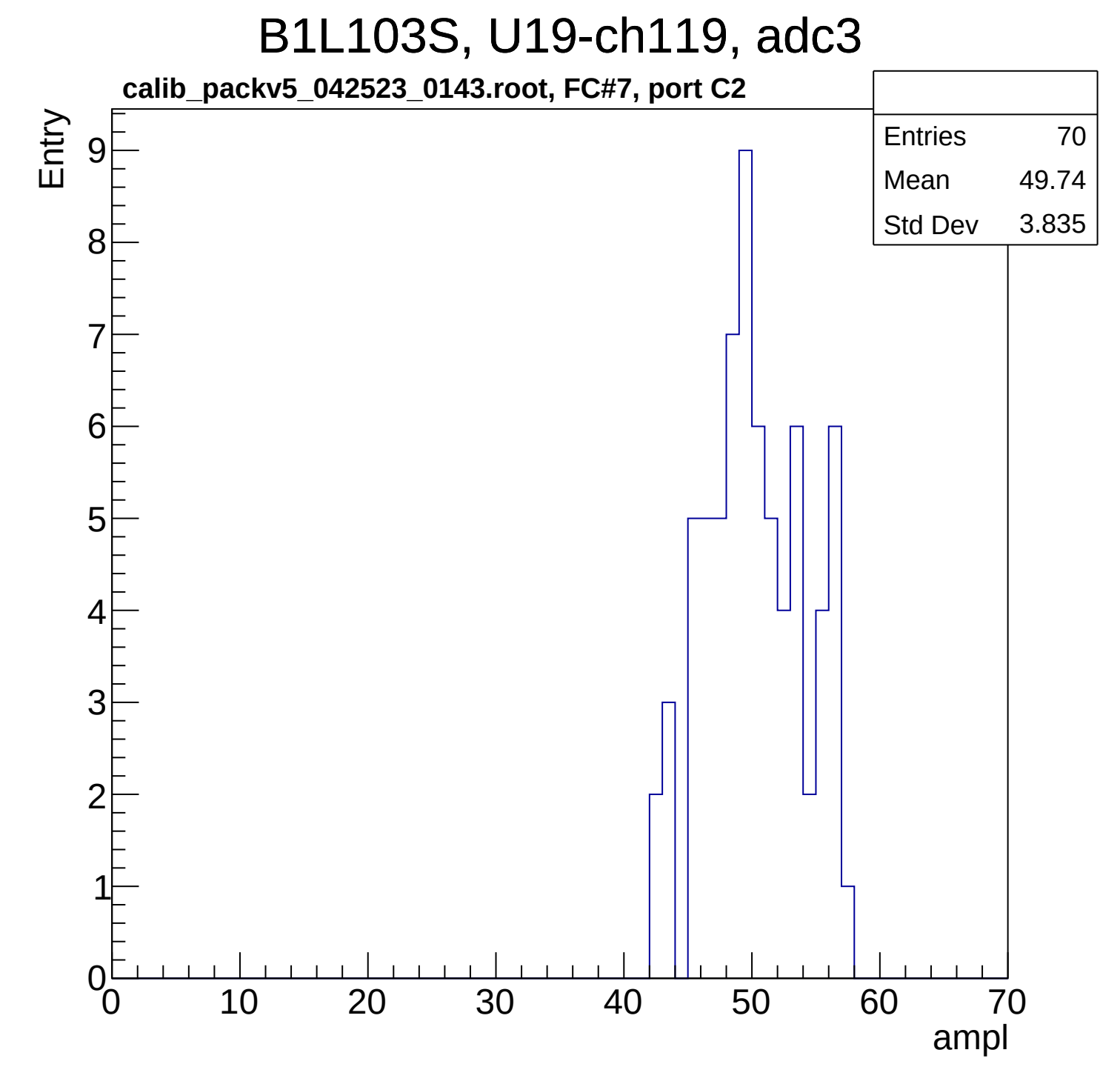
B1L103S, U19-ch119, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.74
Std Dev 3.835
Entry
ampl
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3
4
5
6
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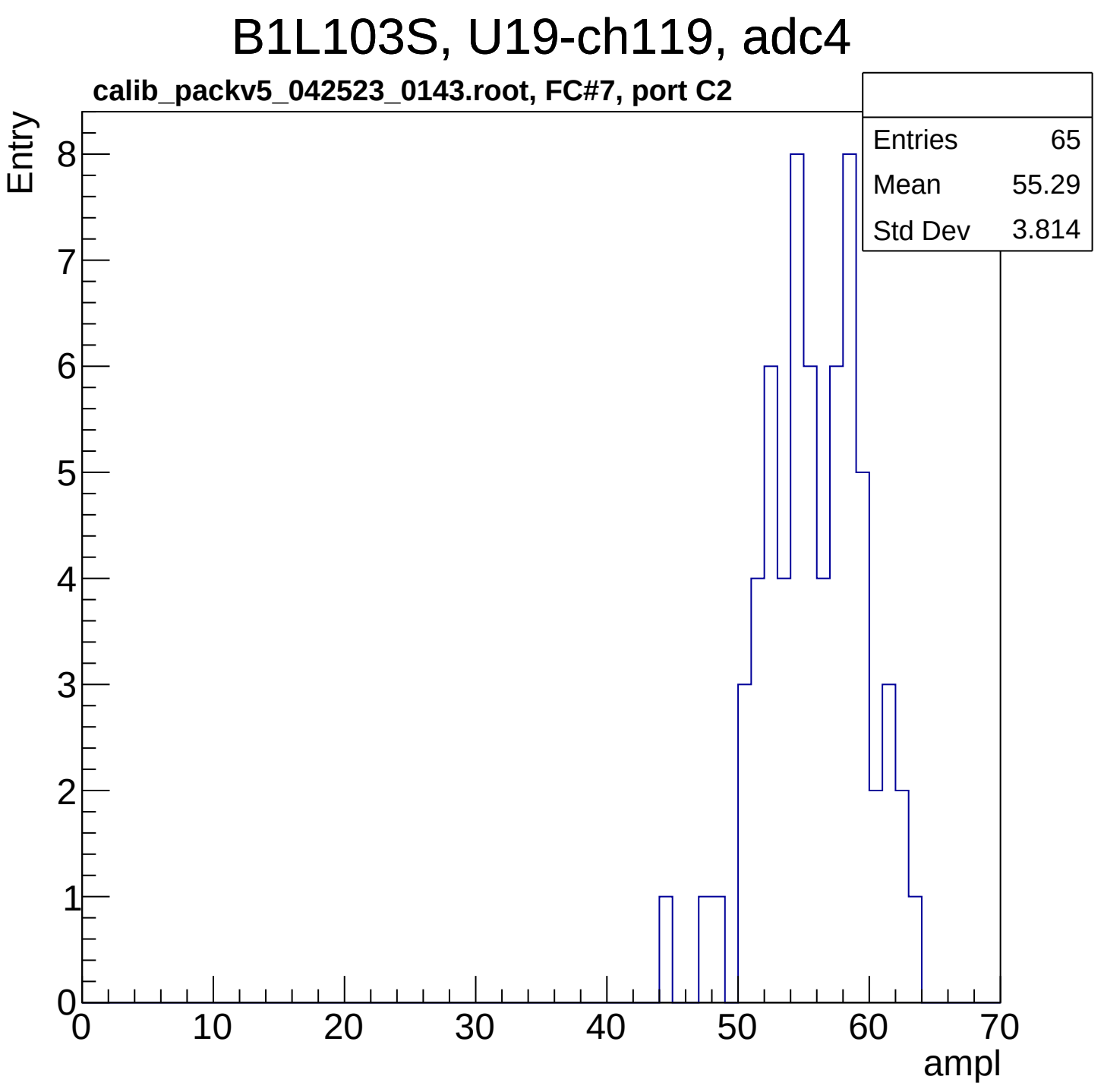
B1L103S, U19-ch119, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 55.29
Std Dev 3.814
Entry
ampl
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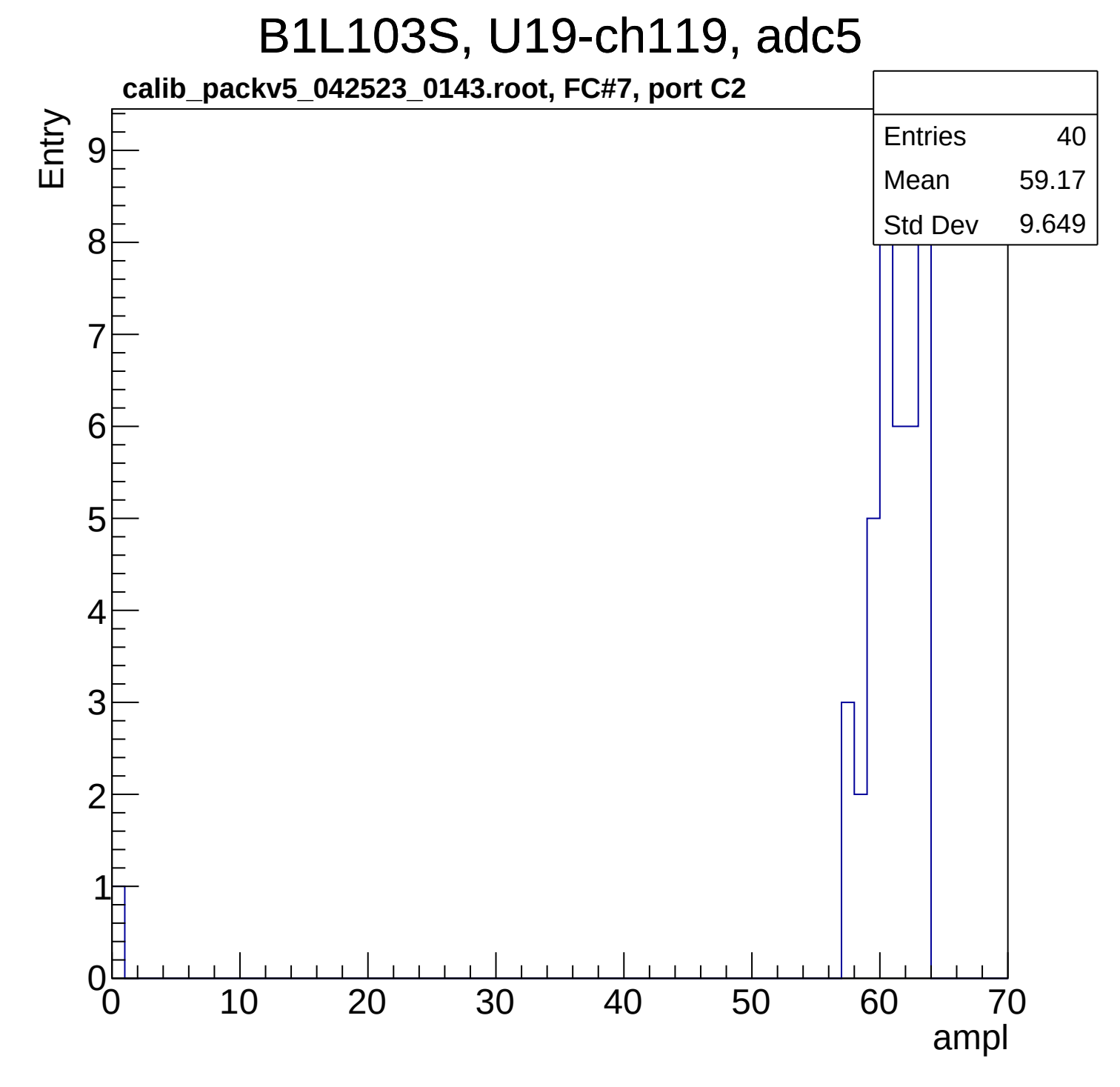
B1L103S, U19-ch119, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 59.17
Std Dev 9.649
Entry
ampl
0
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2
3
4
5
6
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0
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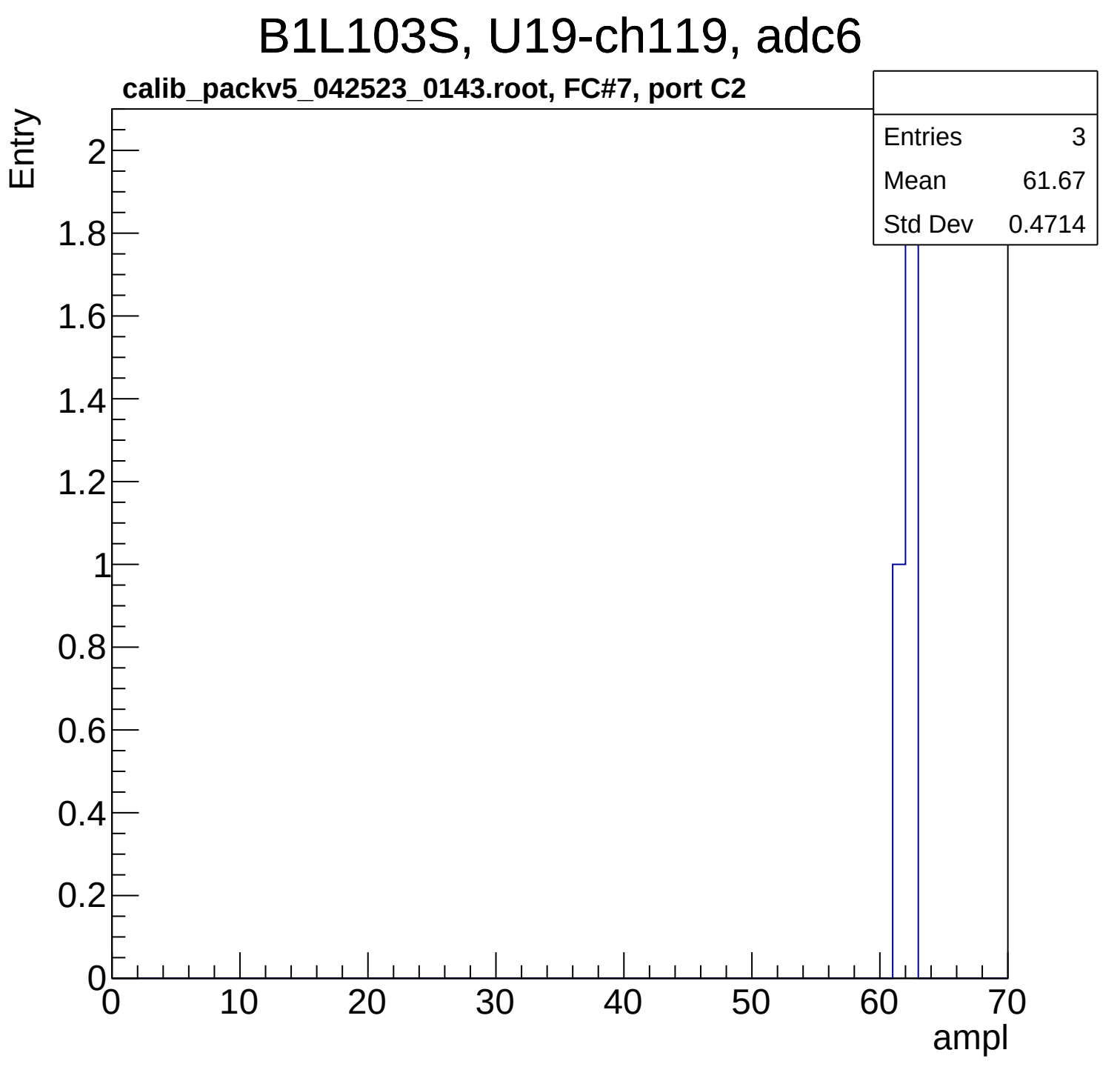
B1L103S, U19-ch119, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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60
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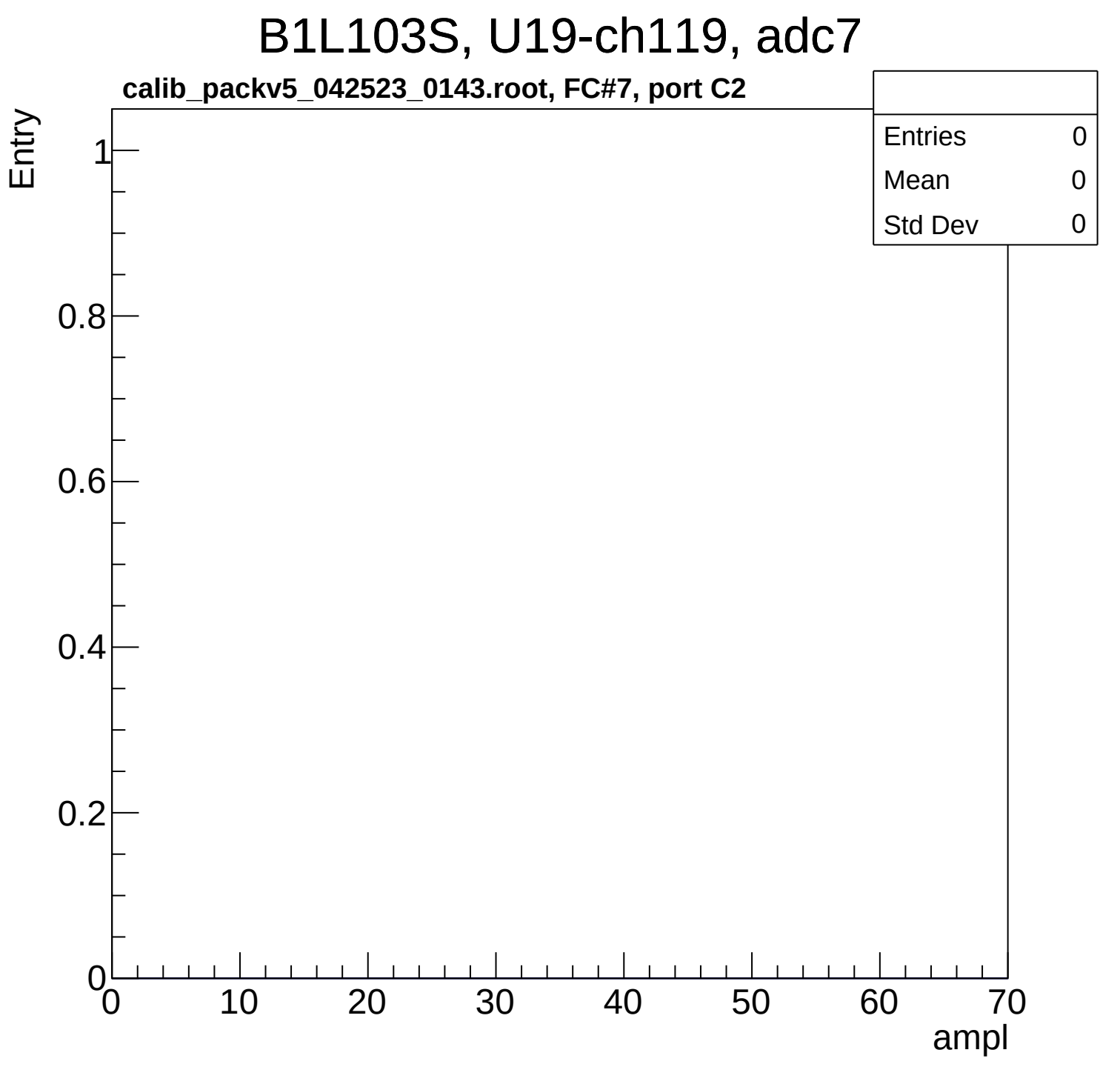
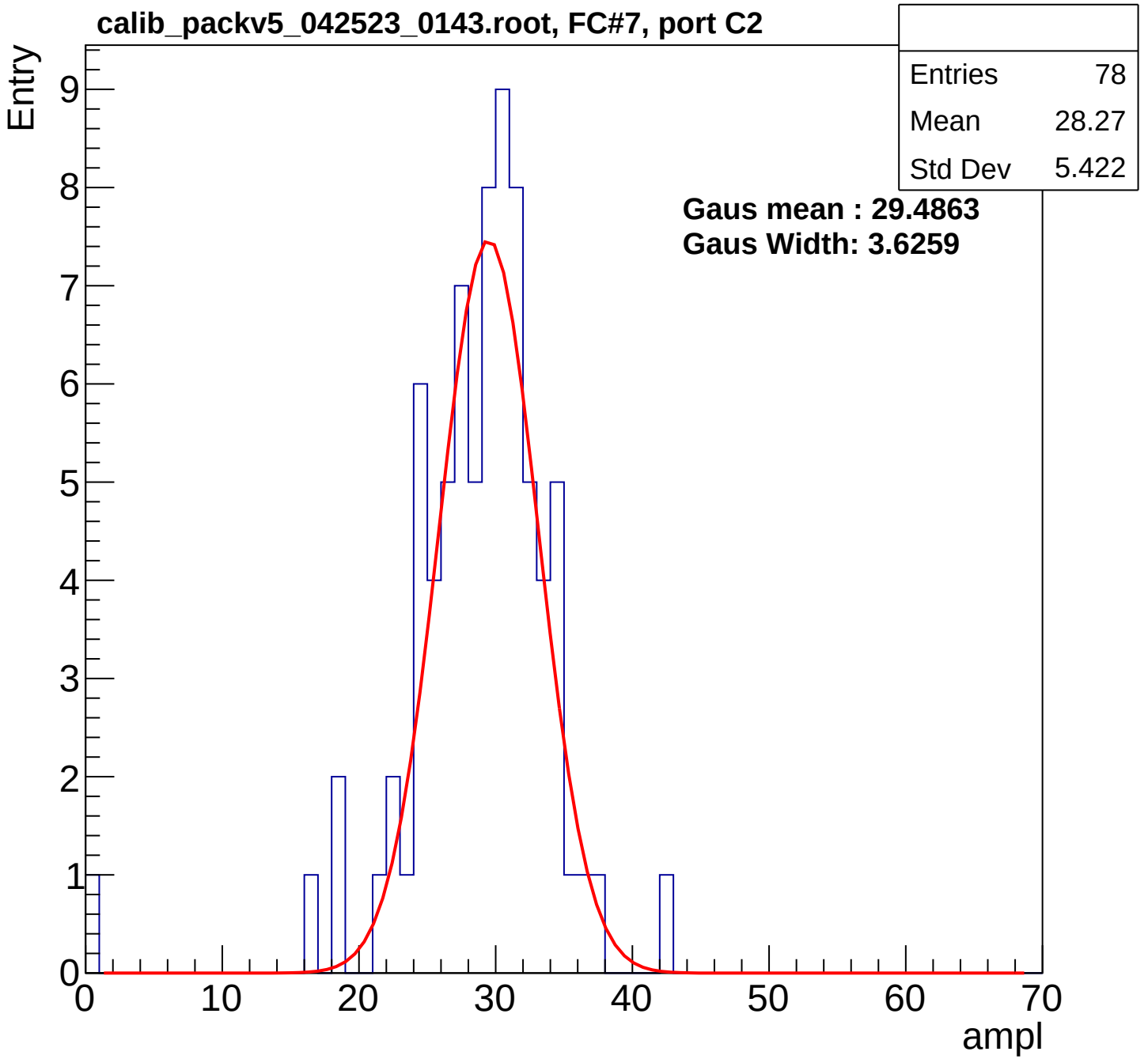

B1L103S, U19-ch119, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch120, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 28.27
Std Dev 5.422
Gaus mean : 29.4863
Gaus Width: 3.6259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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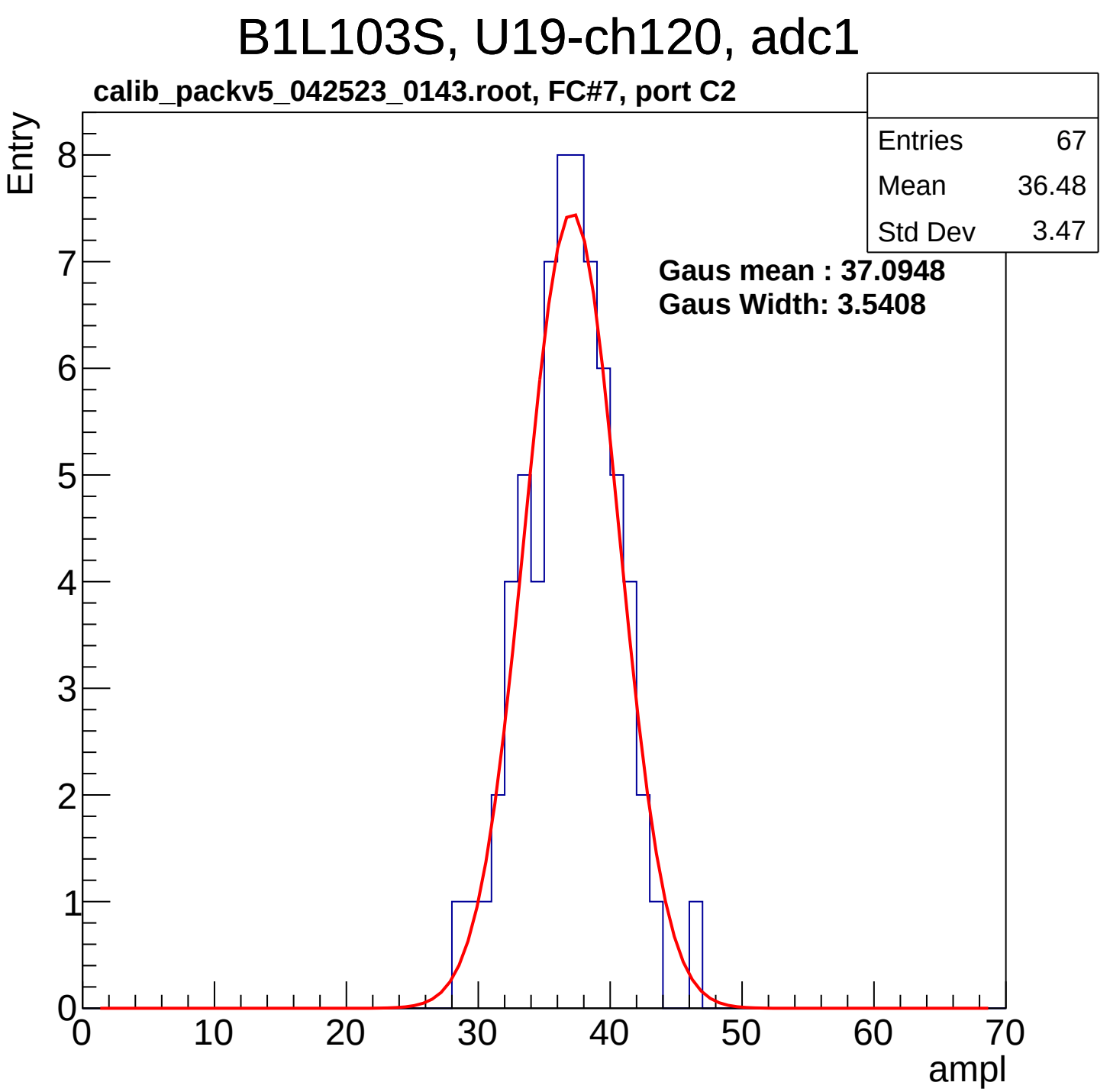
B1L103S, U19-ch120, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.48
Std Dev 3.47
Gaus mean : 37.0948
Gaus Width: 3.5408
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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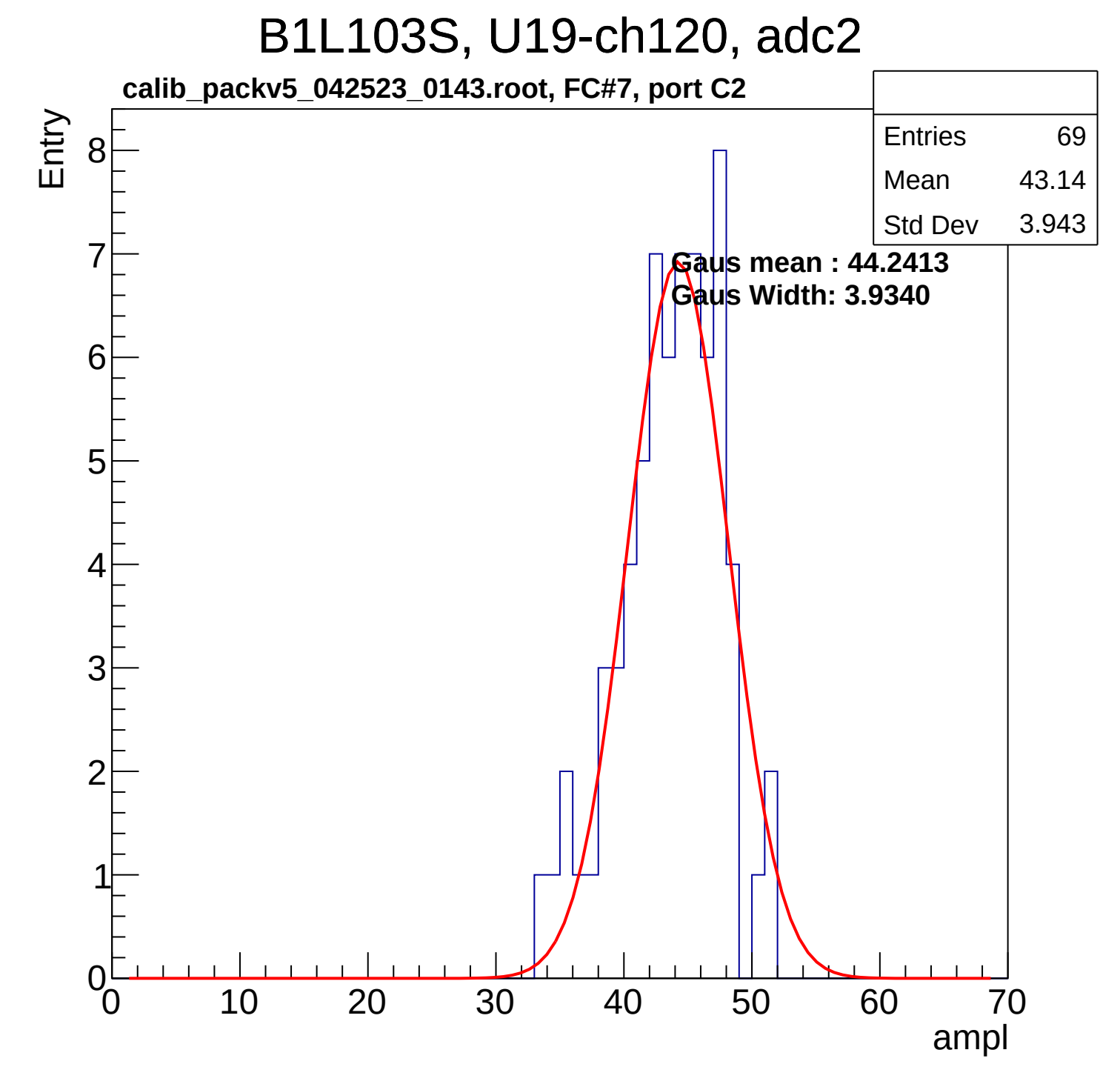
B1L103S, U19-ch120, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 43.14
Std Dev 3.943
Gaus mean : 44.2413
Gaus Width: 3.9340
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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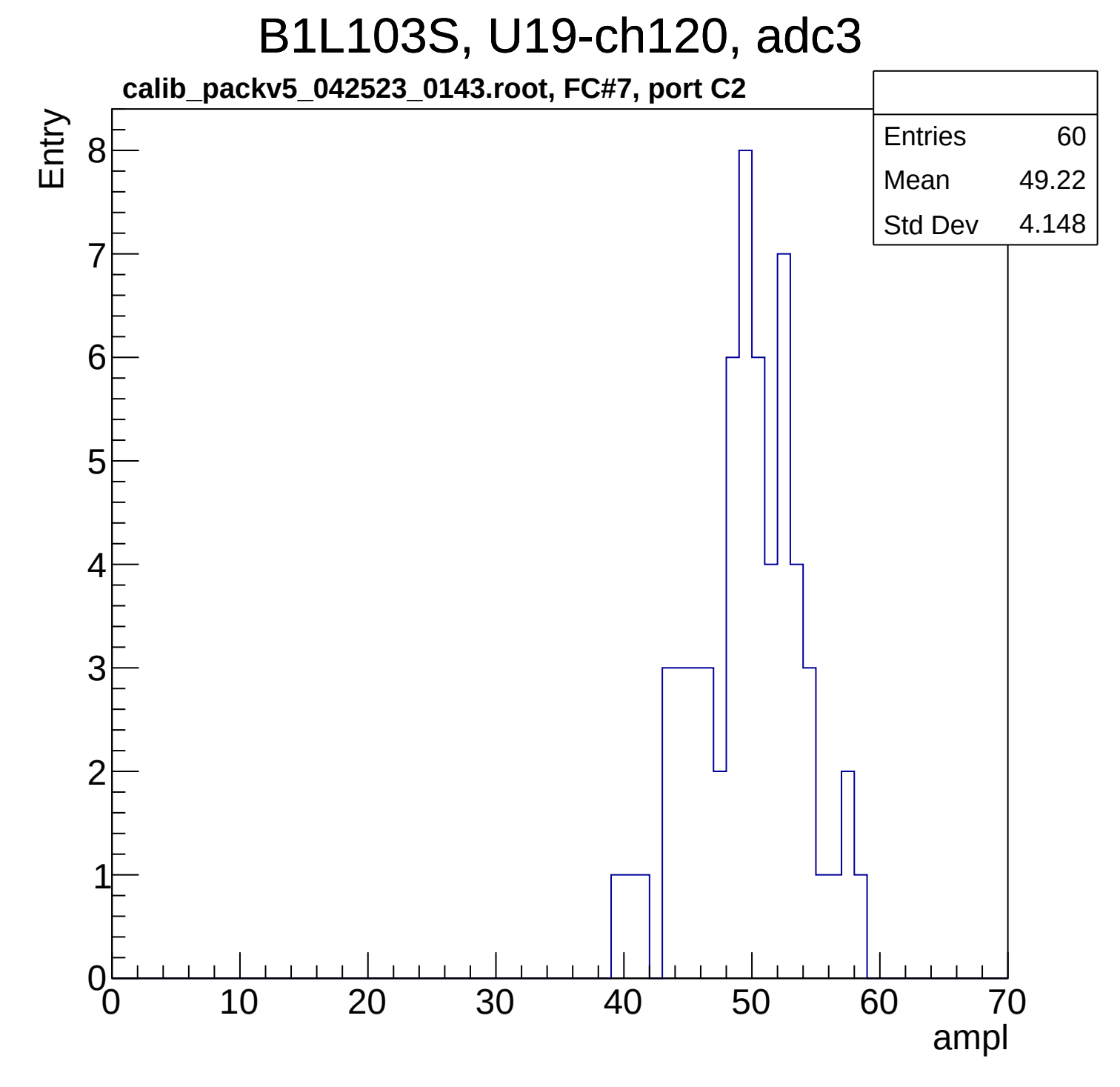
B1L103S, U19-ch120, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.22
Std Dev 4.148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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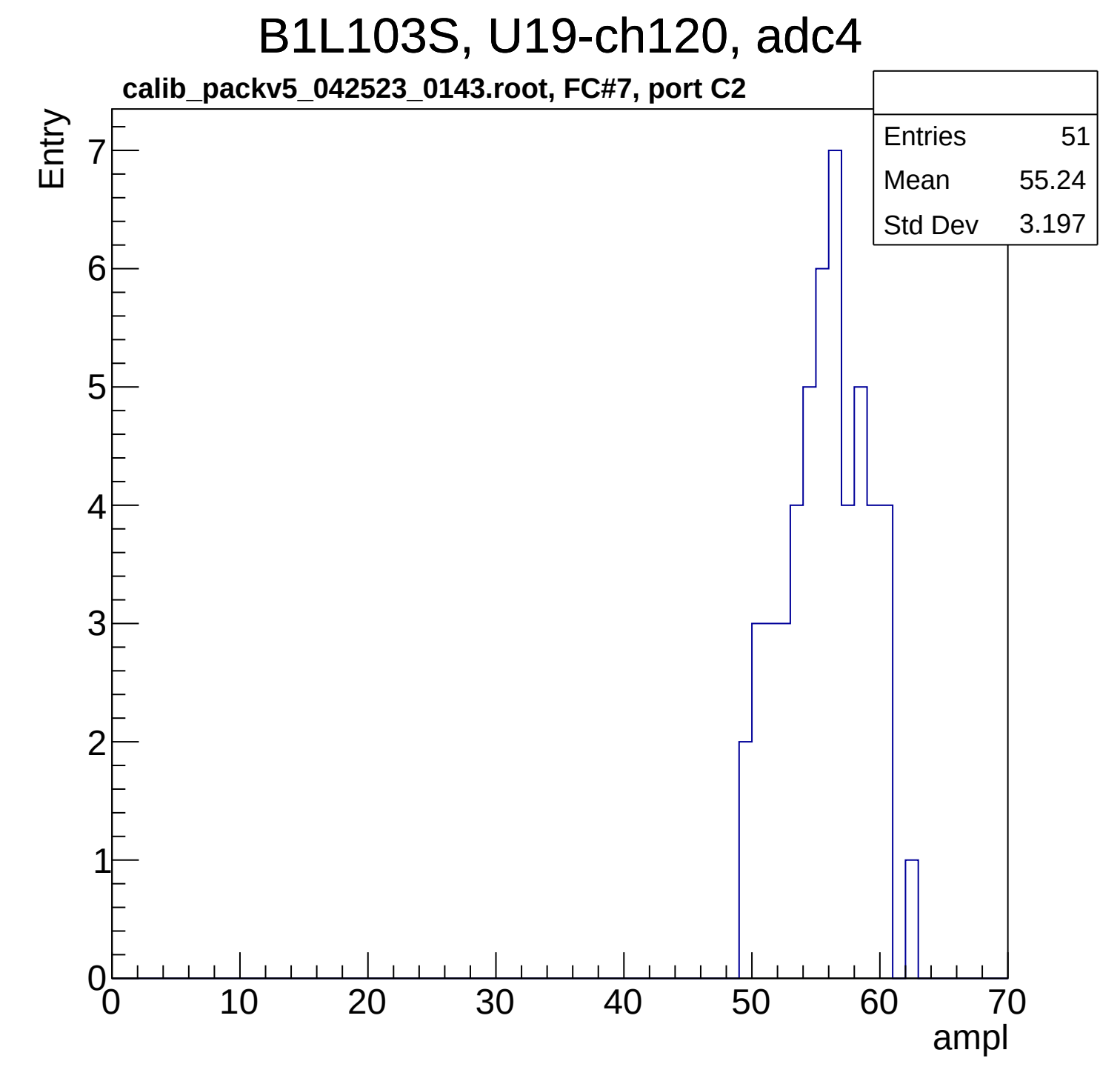
B1L103S, U19-ch120, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.24
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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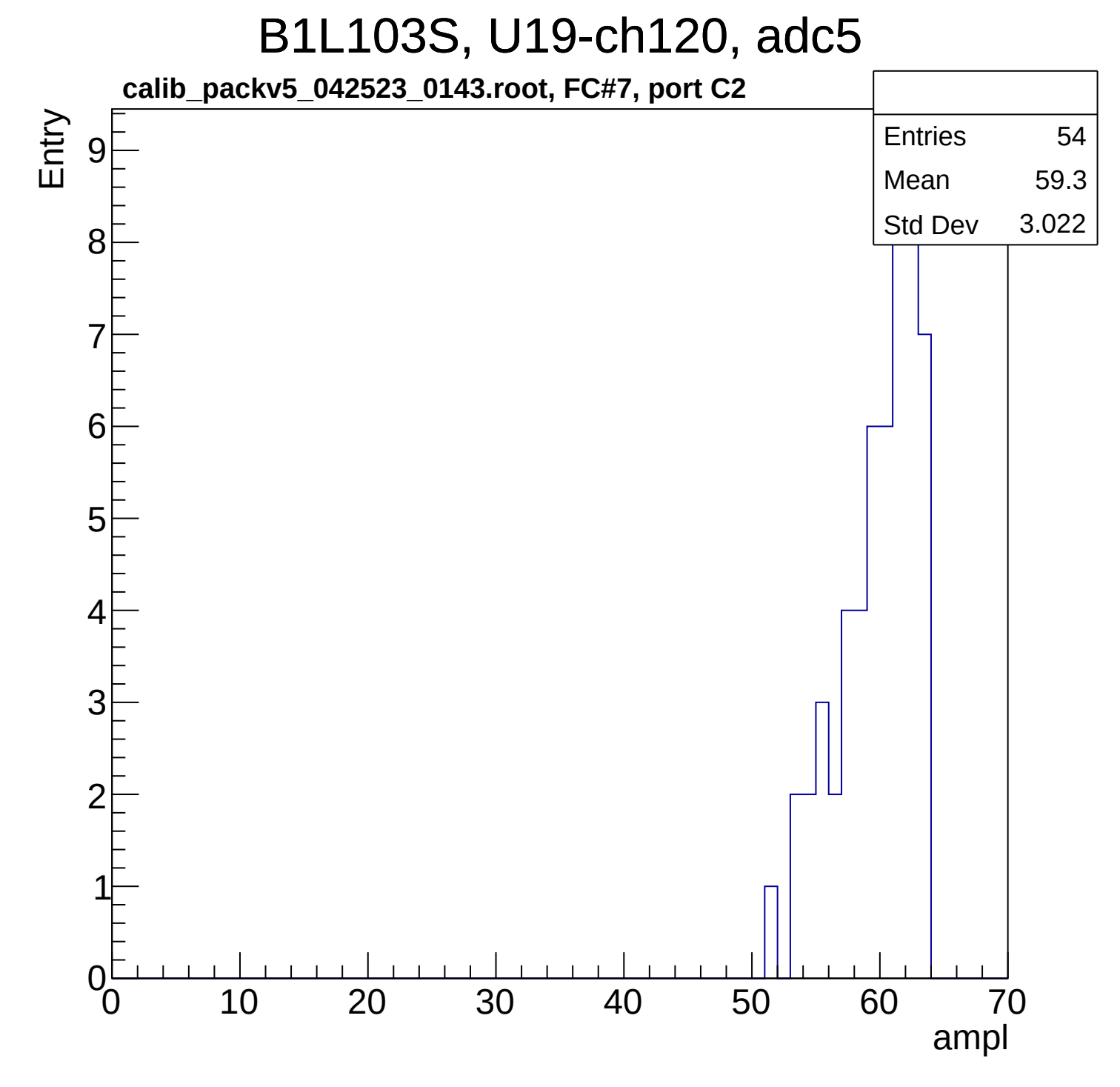
B1L103S, U19-ch120, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 59.3
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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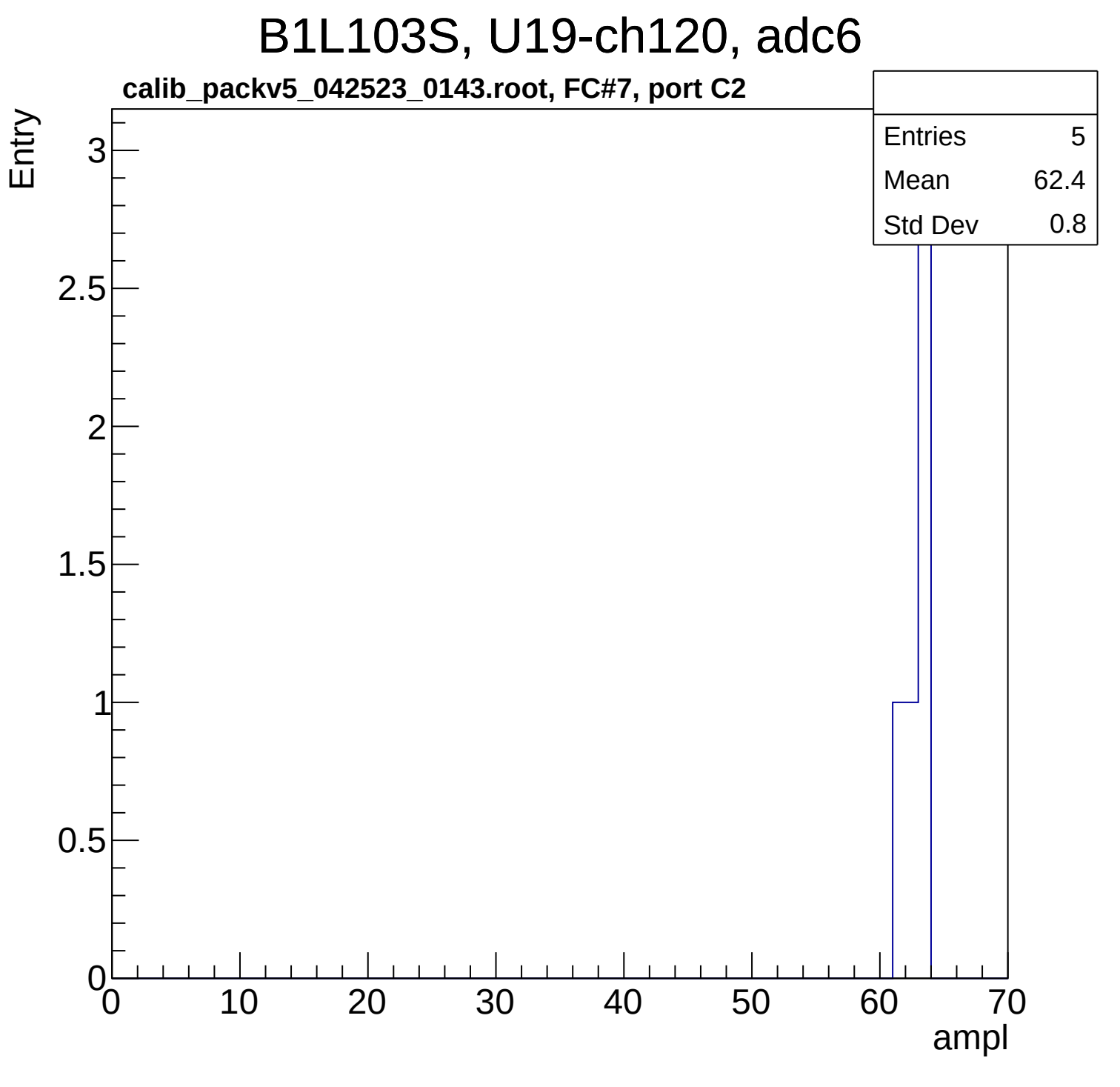
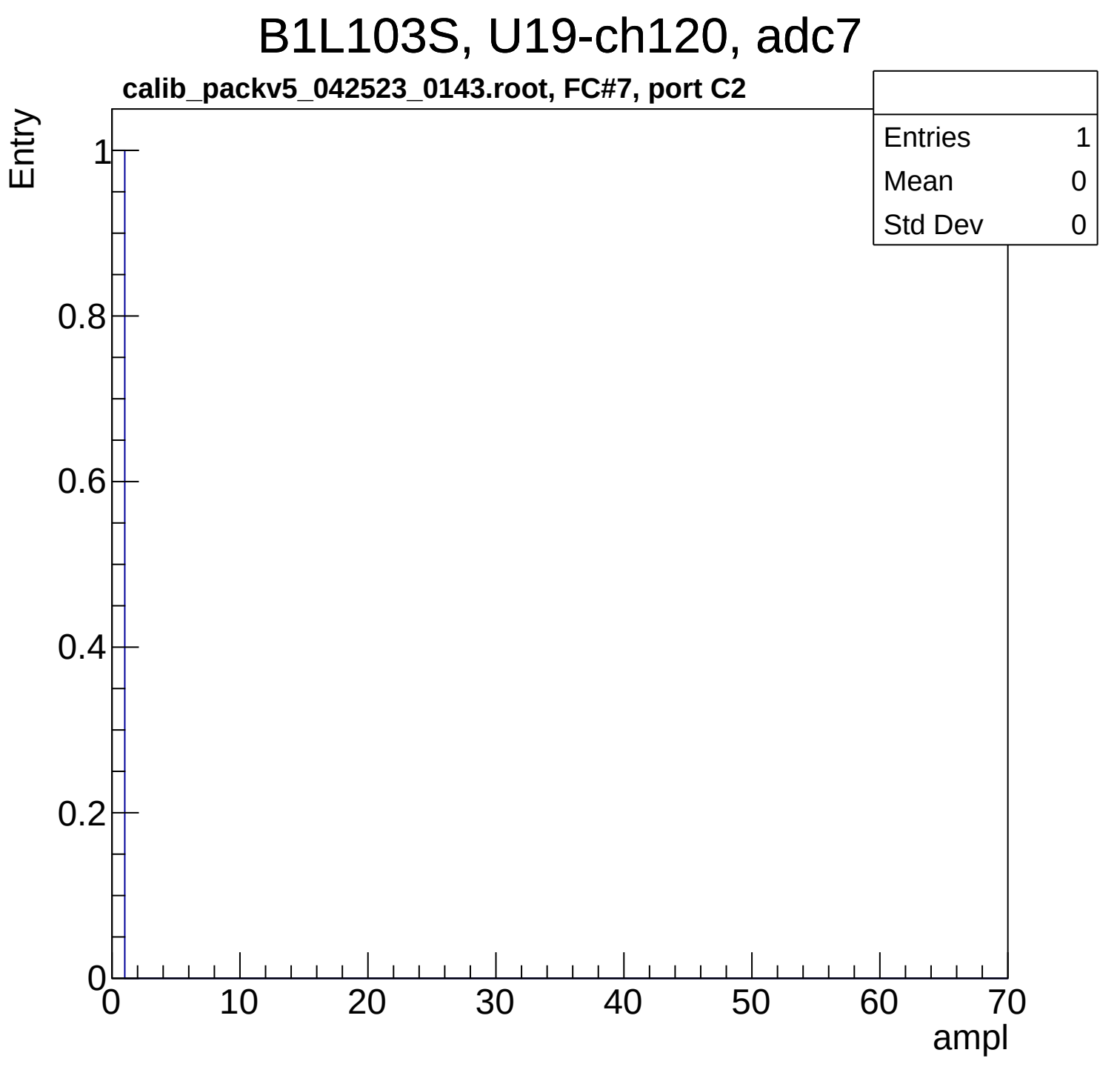
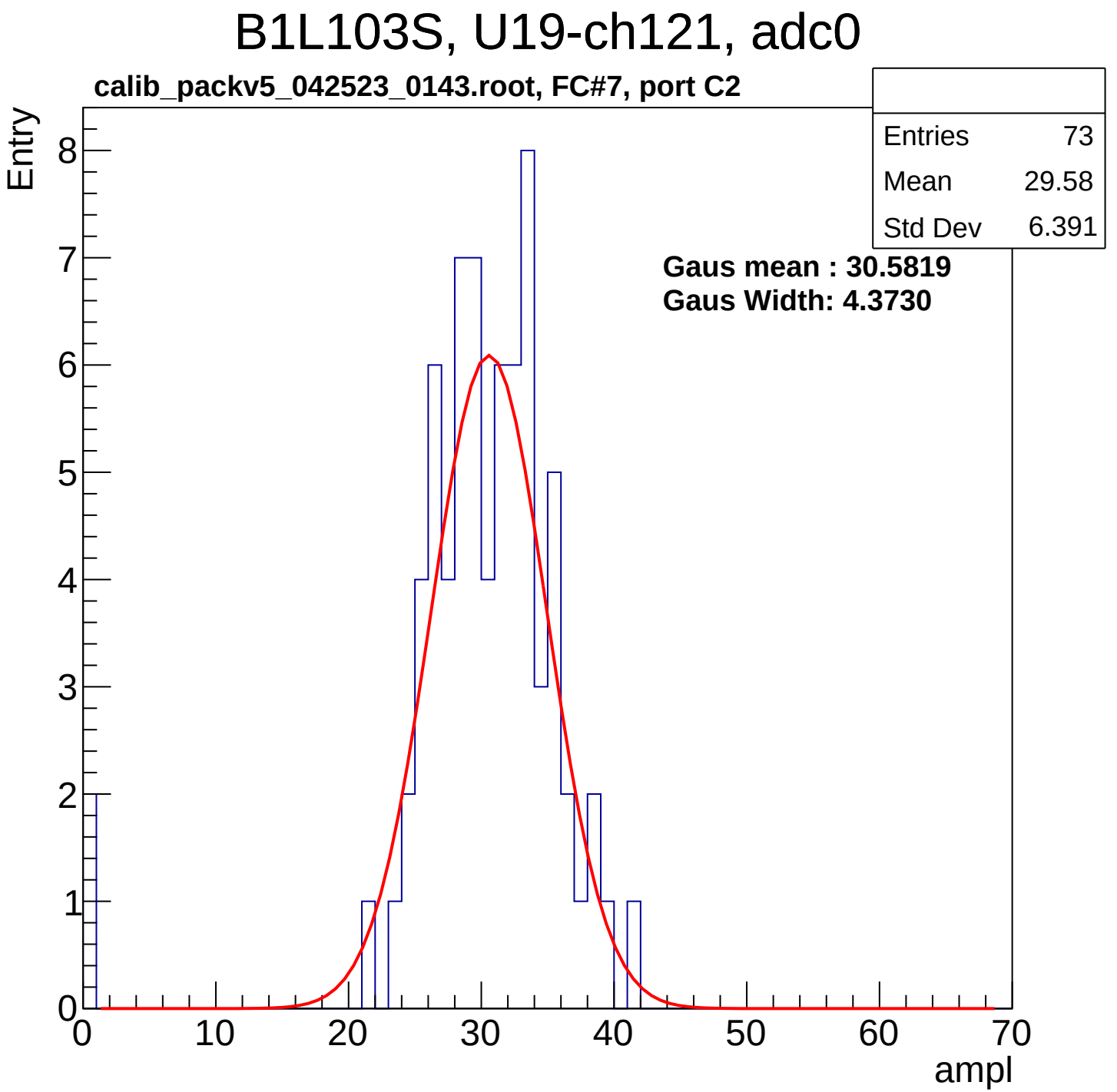
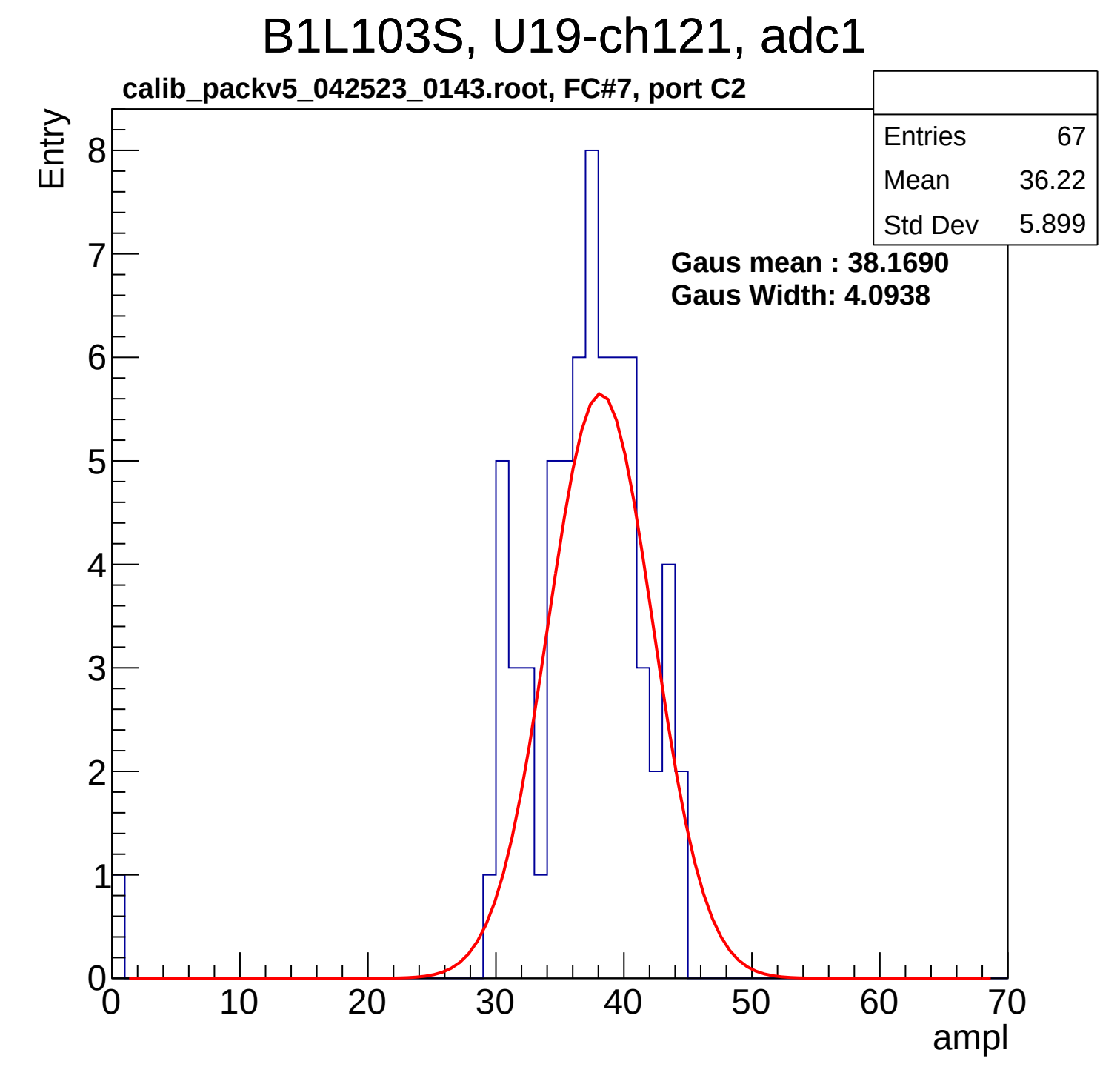
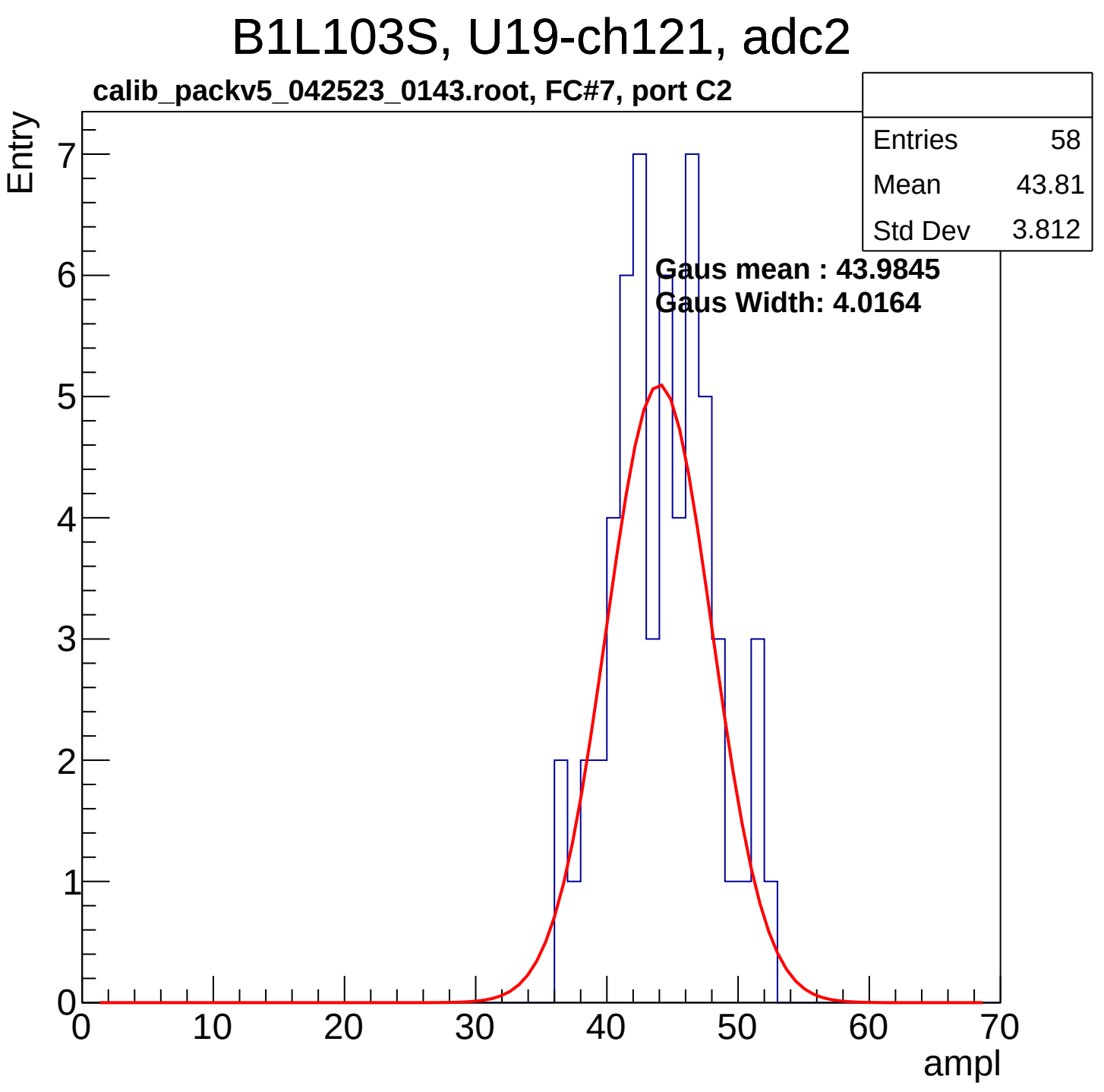

B1L103S, U19-ch120, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch120, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch121, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.58
Std Dev 6.391
Gaus mean : 30.5819
Gaus Width: 4.3730
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch121, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.22
Std Dev 5.899
Gaus mean : 38.1690
Gaus Width: 4.0938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch121, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 43.81
Std Dev 3.812
Gaus mean : 43.9845
Gaus Width: 4.0164
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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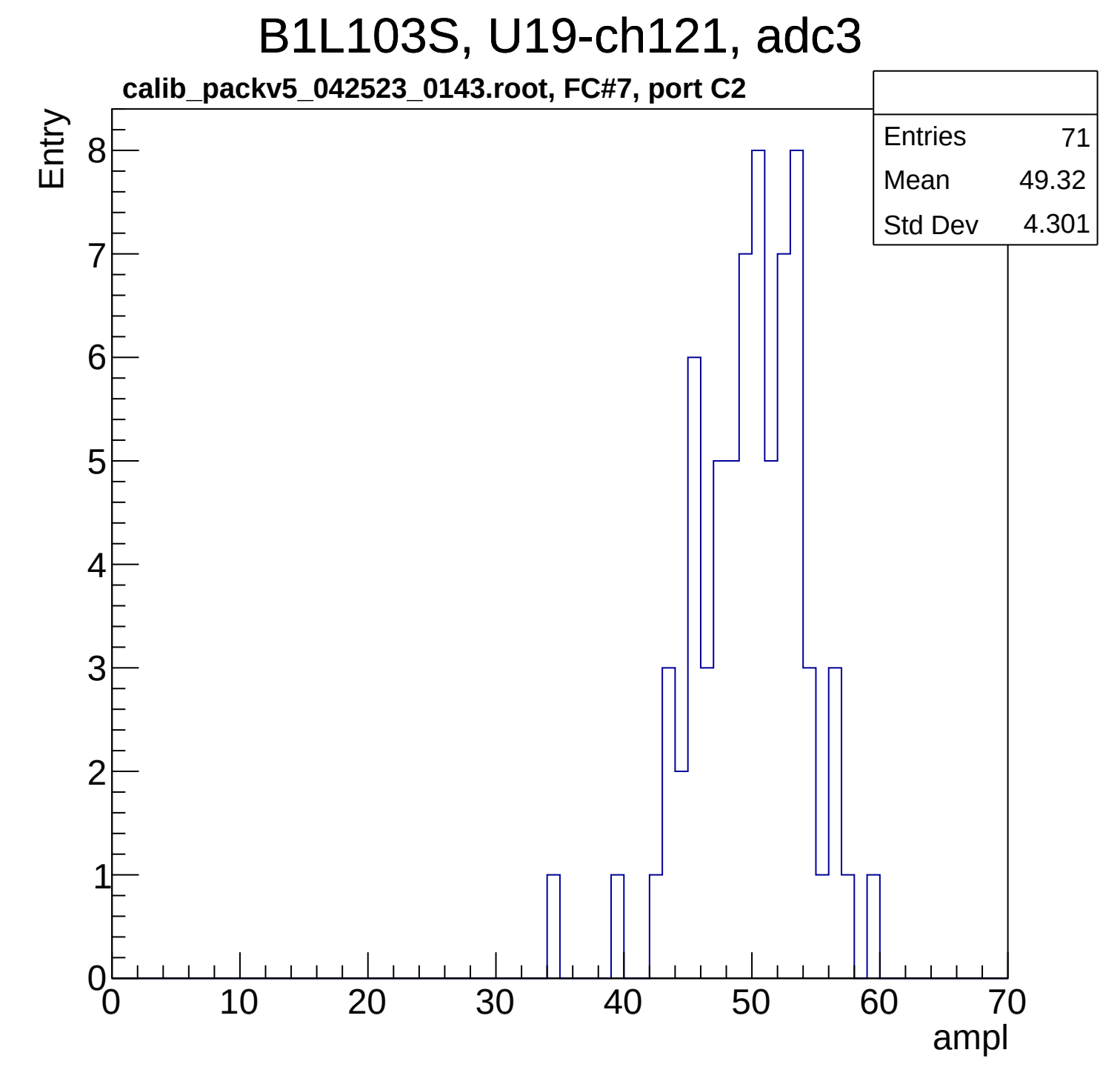
B1L103S, U19-ch121, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.32
Std Dev 4.301
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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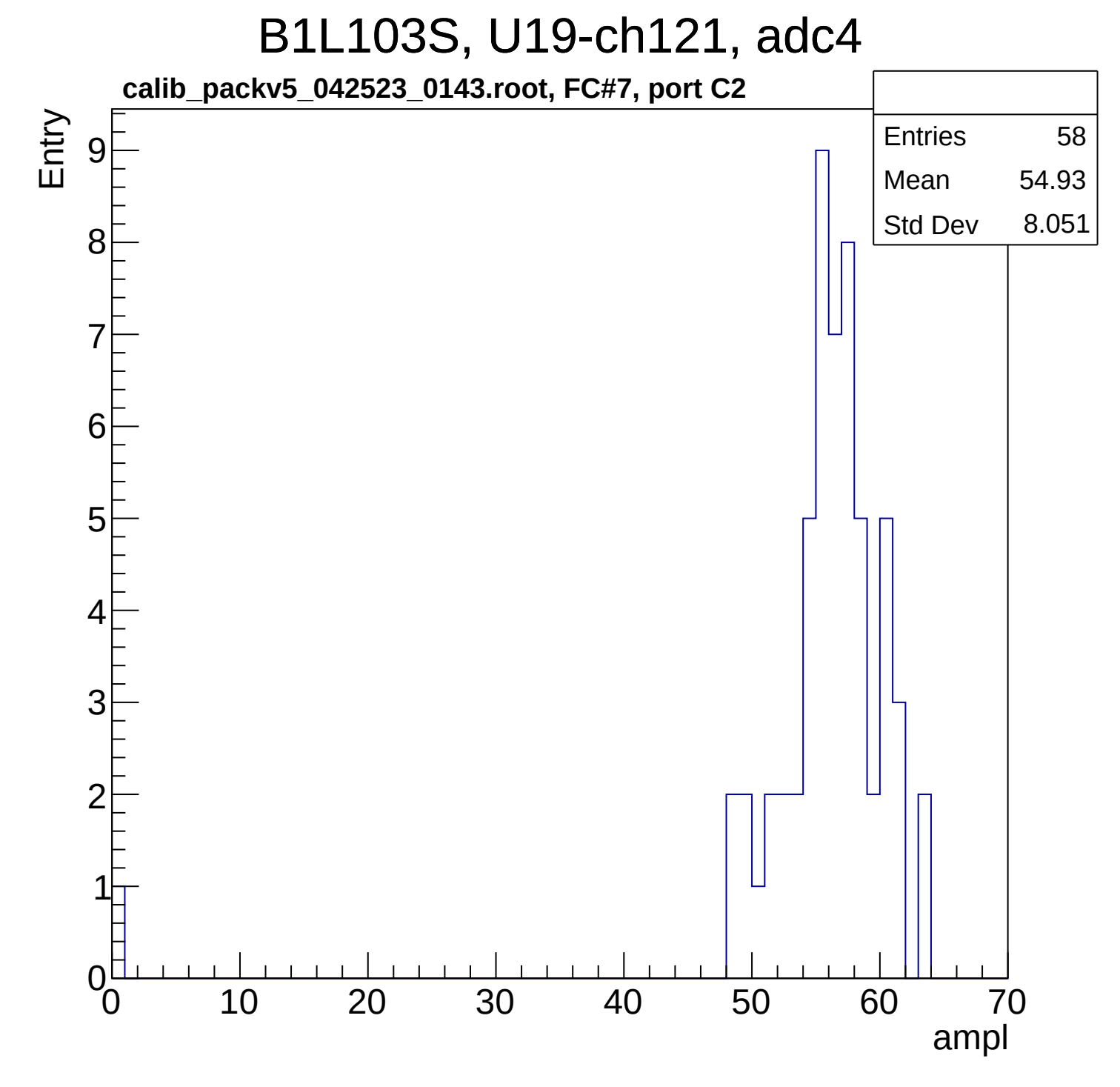
B1L103S, U19-ch121, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 54.93
Std Dev 8.051
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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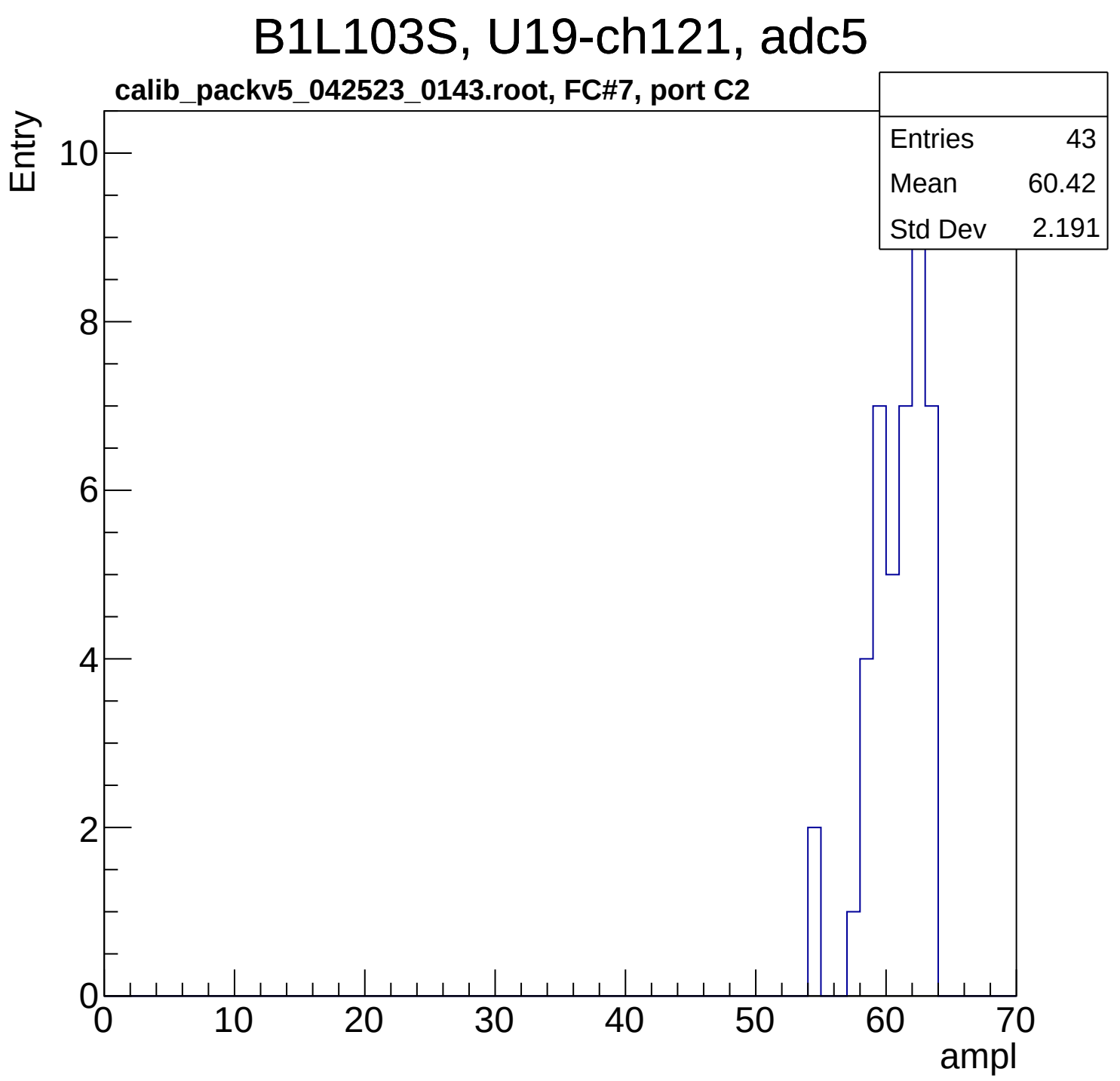
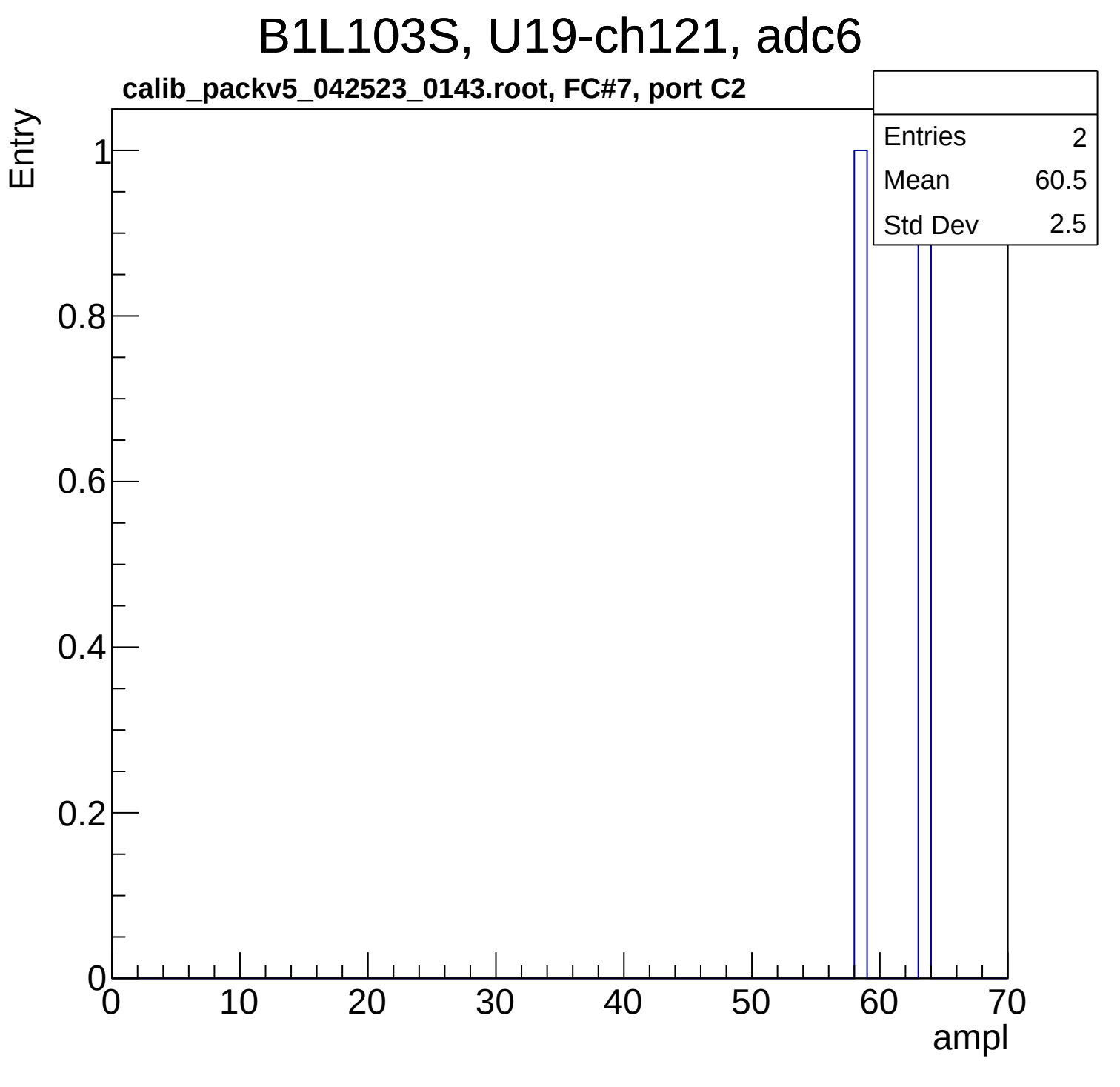
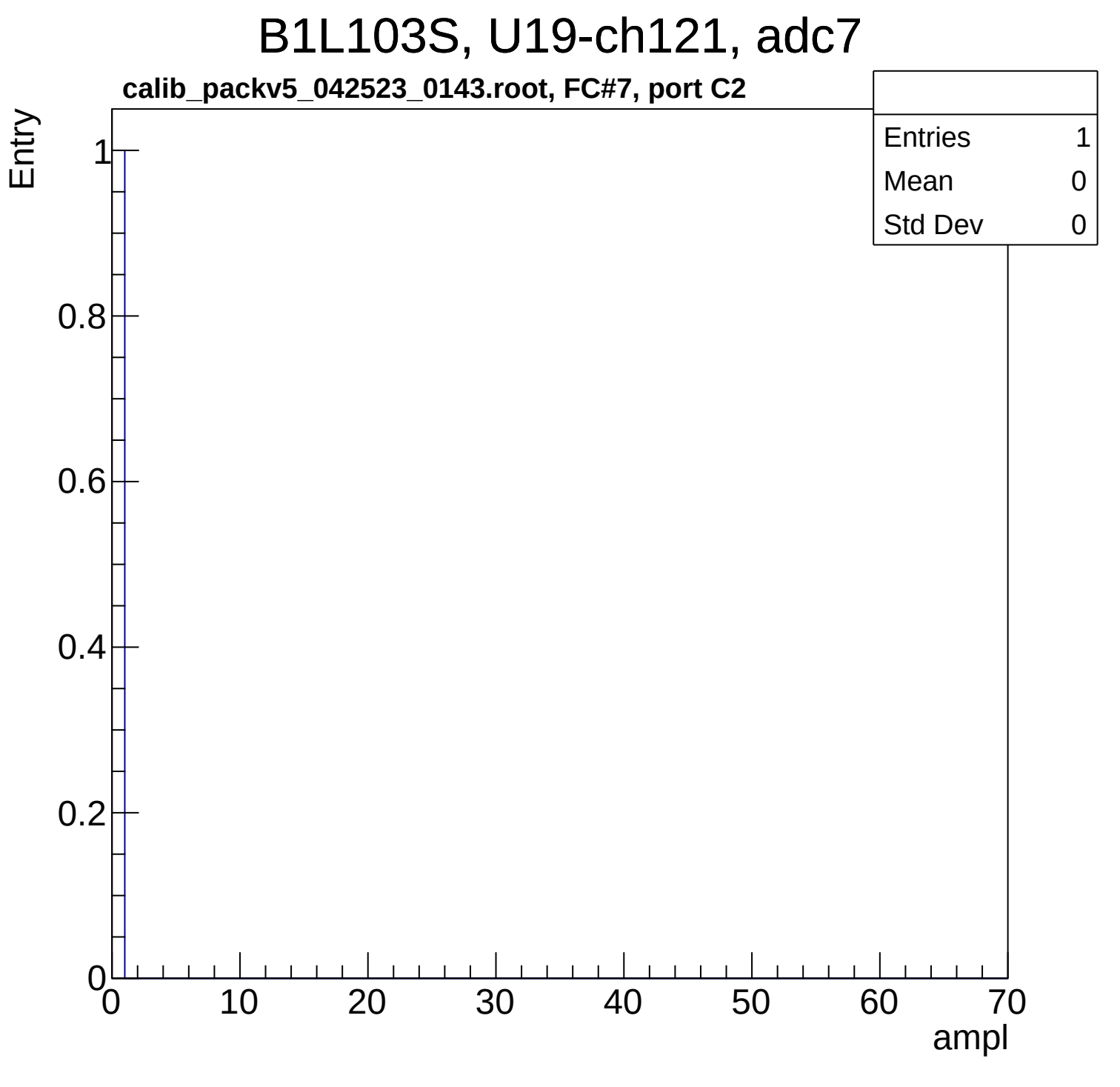

B1L103S, U19-ch121, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 60.42
Std Dev 2.191
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch121, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch121, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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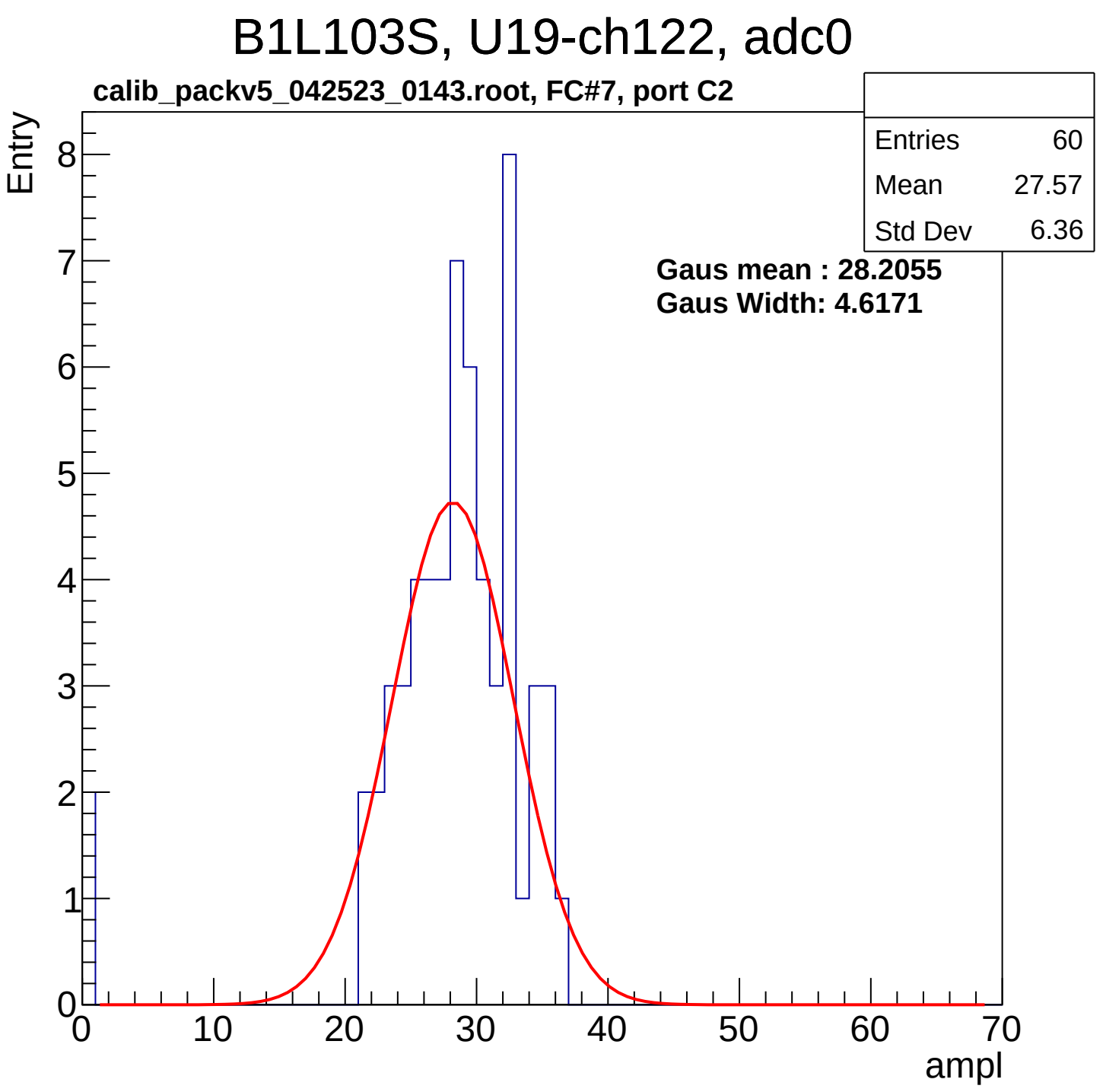
30
40
50
60
70
ampl


B1L103S, U19-ch122, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 27.57
Std Dev 6.36
Gaus mean : 28.2055
Gaus Width: 4.6171
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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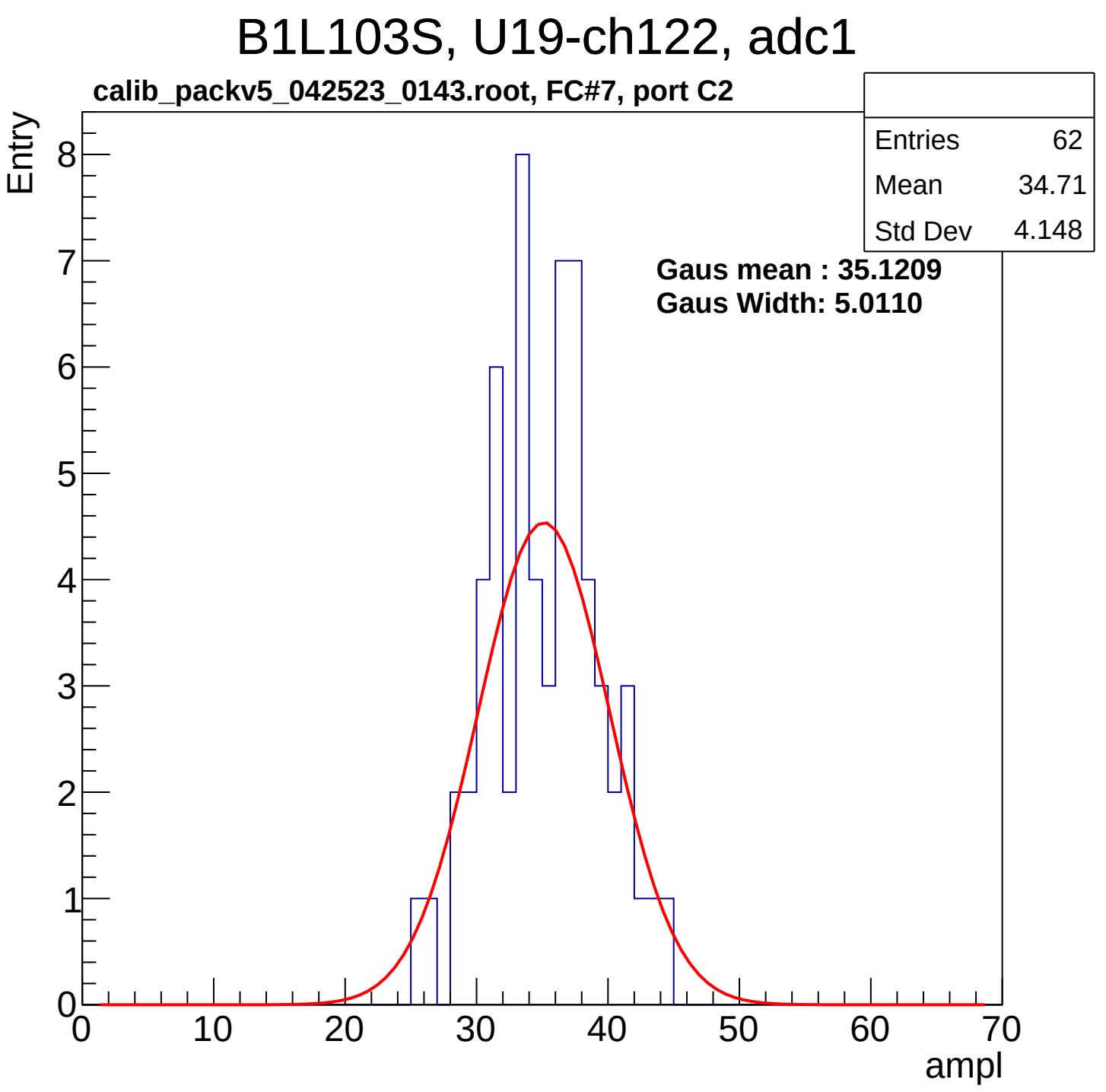
B1L103S, U19-ch122, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 34.71
Std Dev 4.148
Gaus mean : 35.1209
Gaus Width: 5.0110
Entry
ampl
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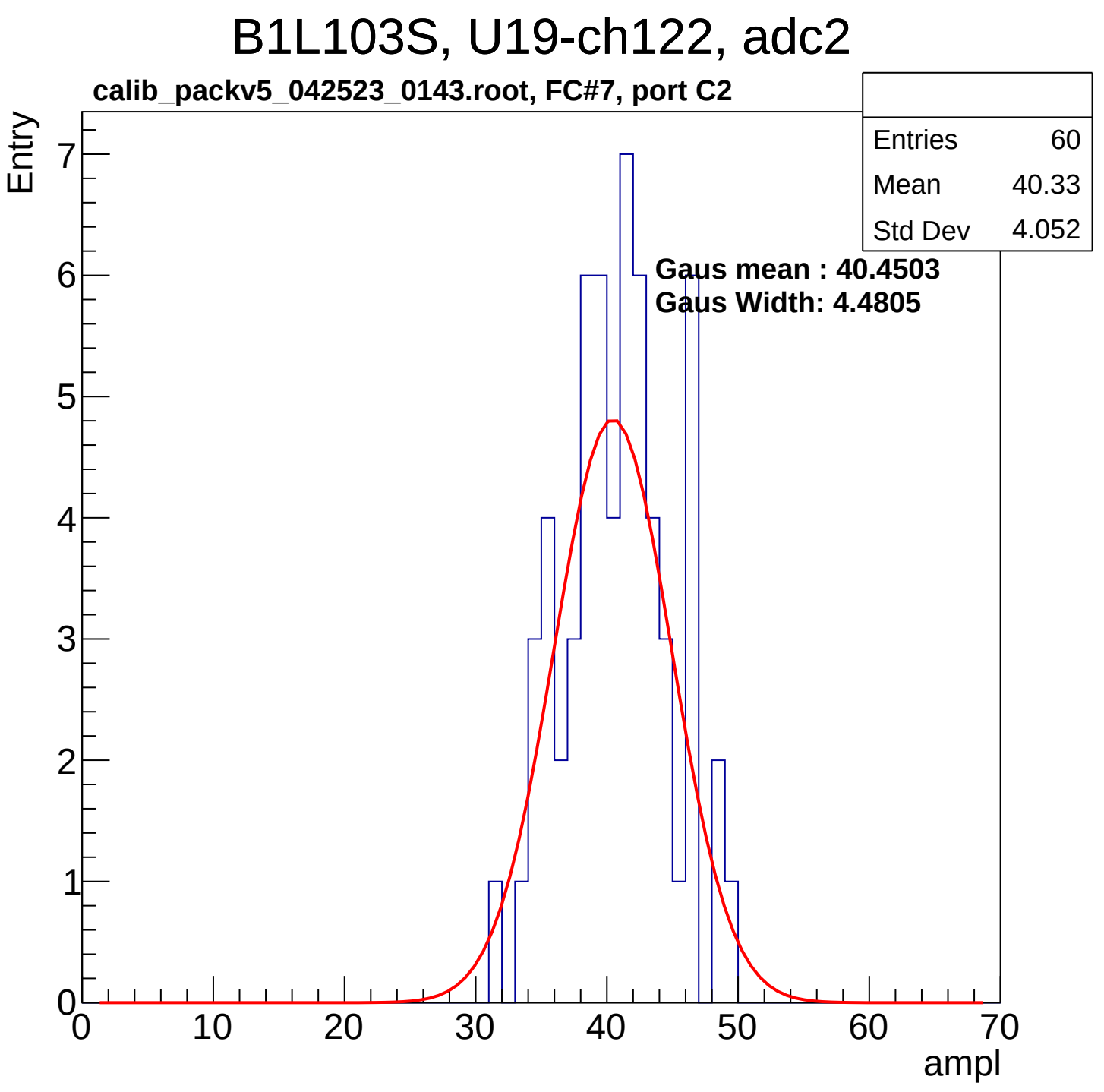
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch122, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 40.33
Std Dev 4.052
Gaus mean : 40.4503
Gaus Width: 4.4805
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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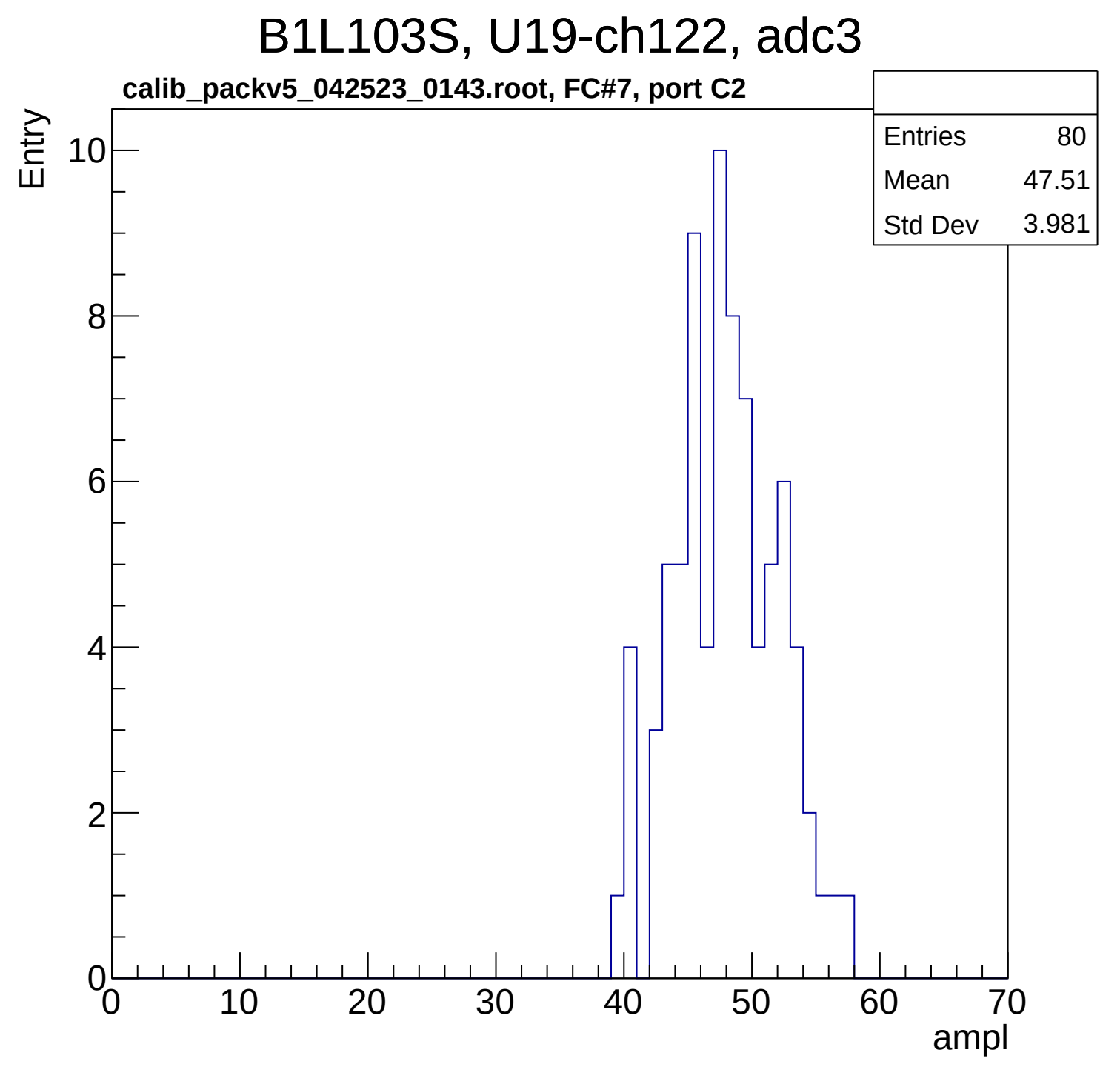
B1L103S, U19-ch122, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 47.51
Std Dev 3.981
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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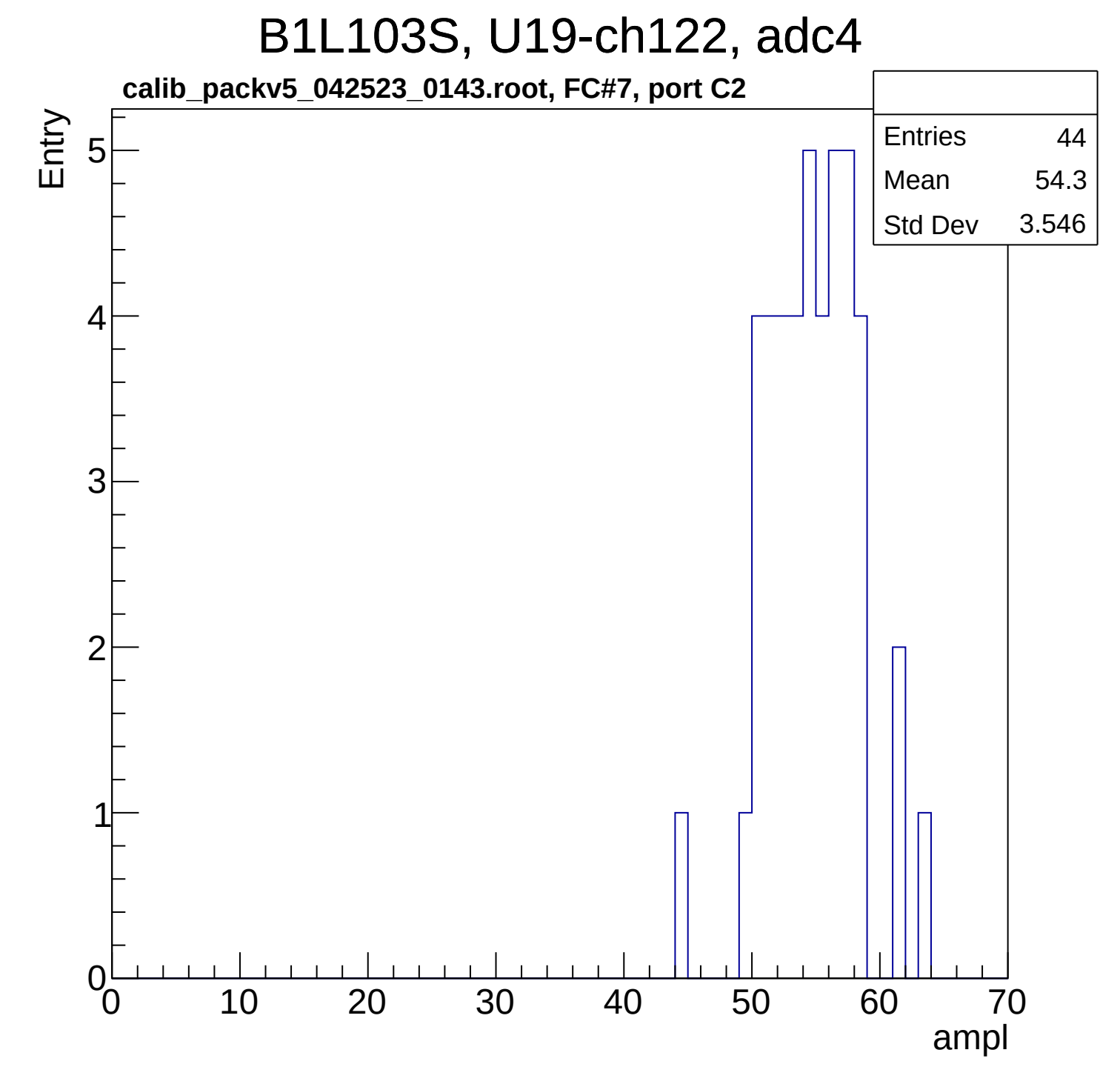
B1L103S, U19-ch122, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 54.3
Std Dev 3.546
Entry
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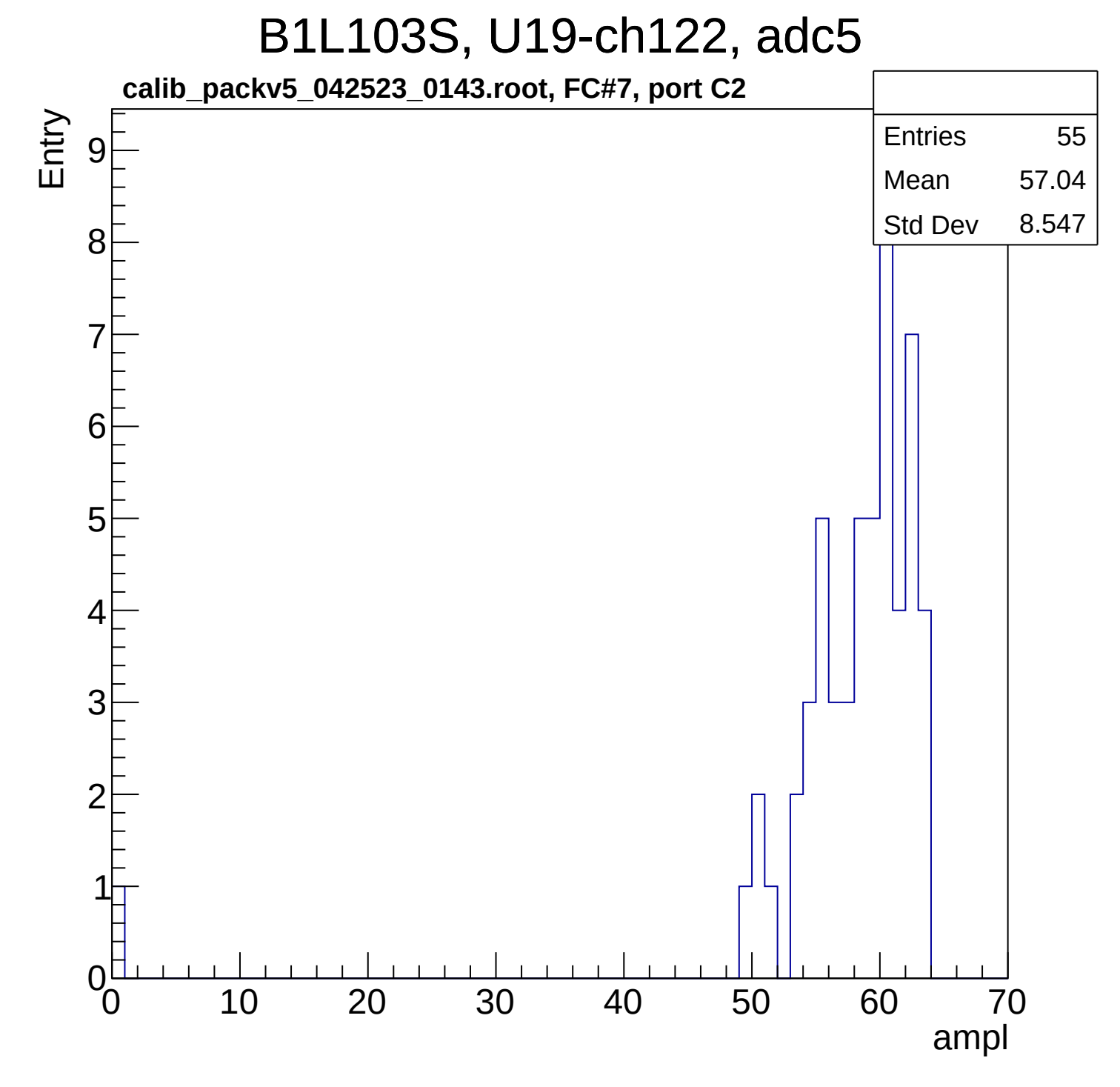
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch122, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 57.04
Std Dev 8.547
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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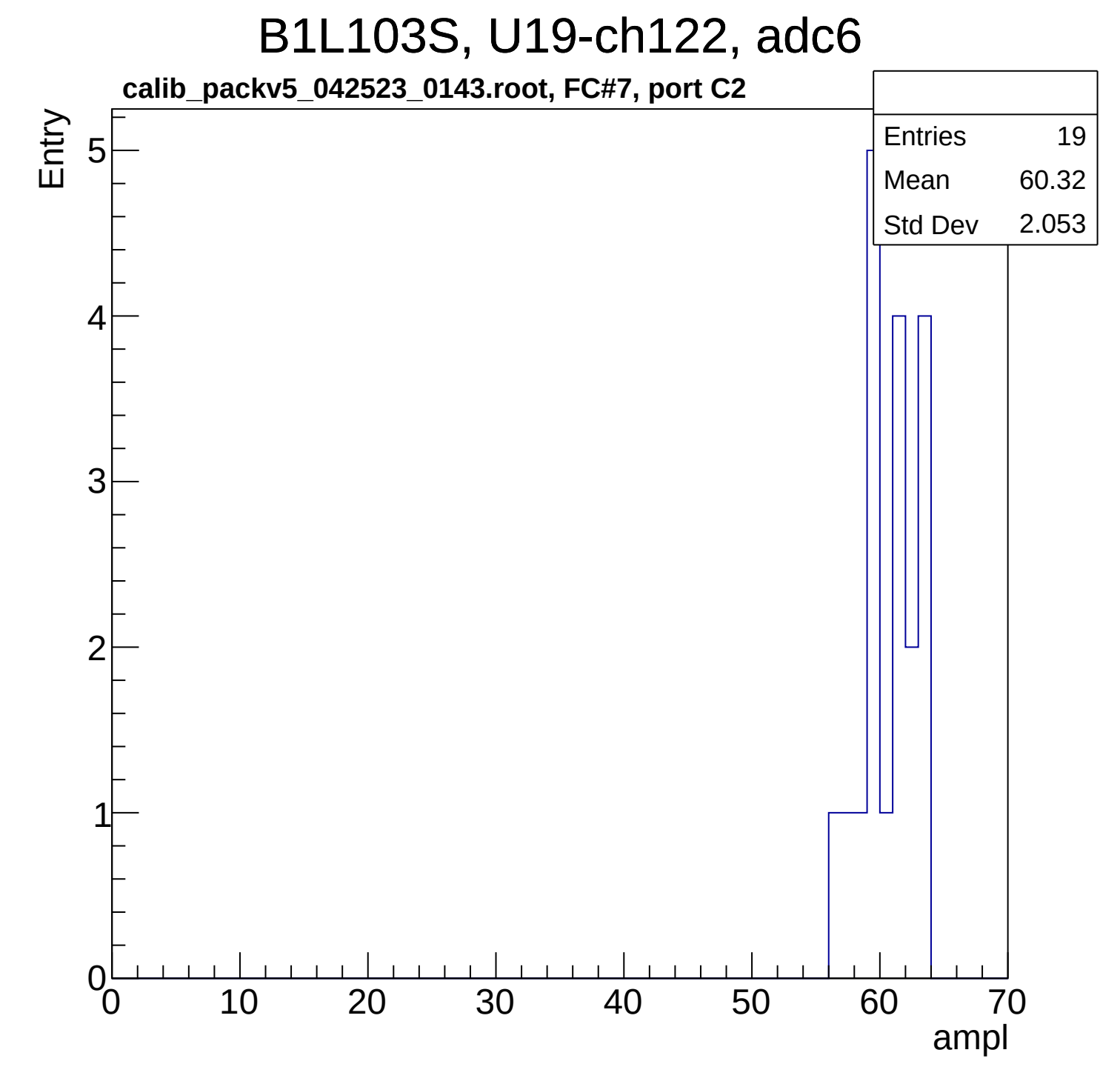
B1L103S, U19-ch122, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 60.32
Std Dev 2.053
Entry
ampl
0
1
2
3
4
5
0
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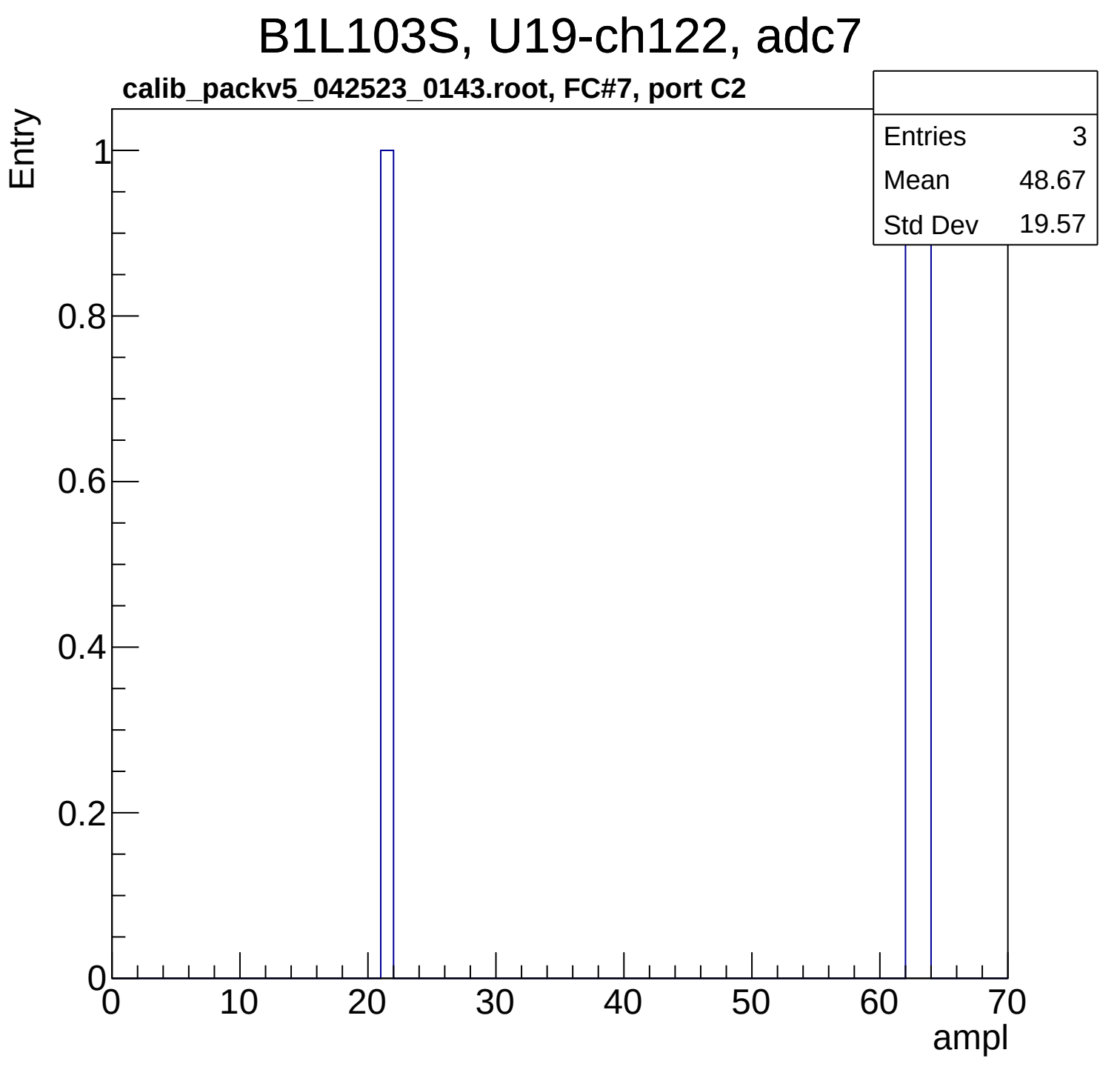
30
40
50
60
70


B1L103S, U19-ch122, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 48.67
Std Dev 19.57
Entry
ampl
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0.4
0.6
0.8
1
0
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40
50
60
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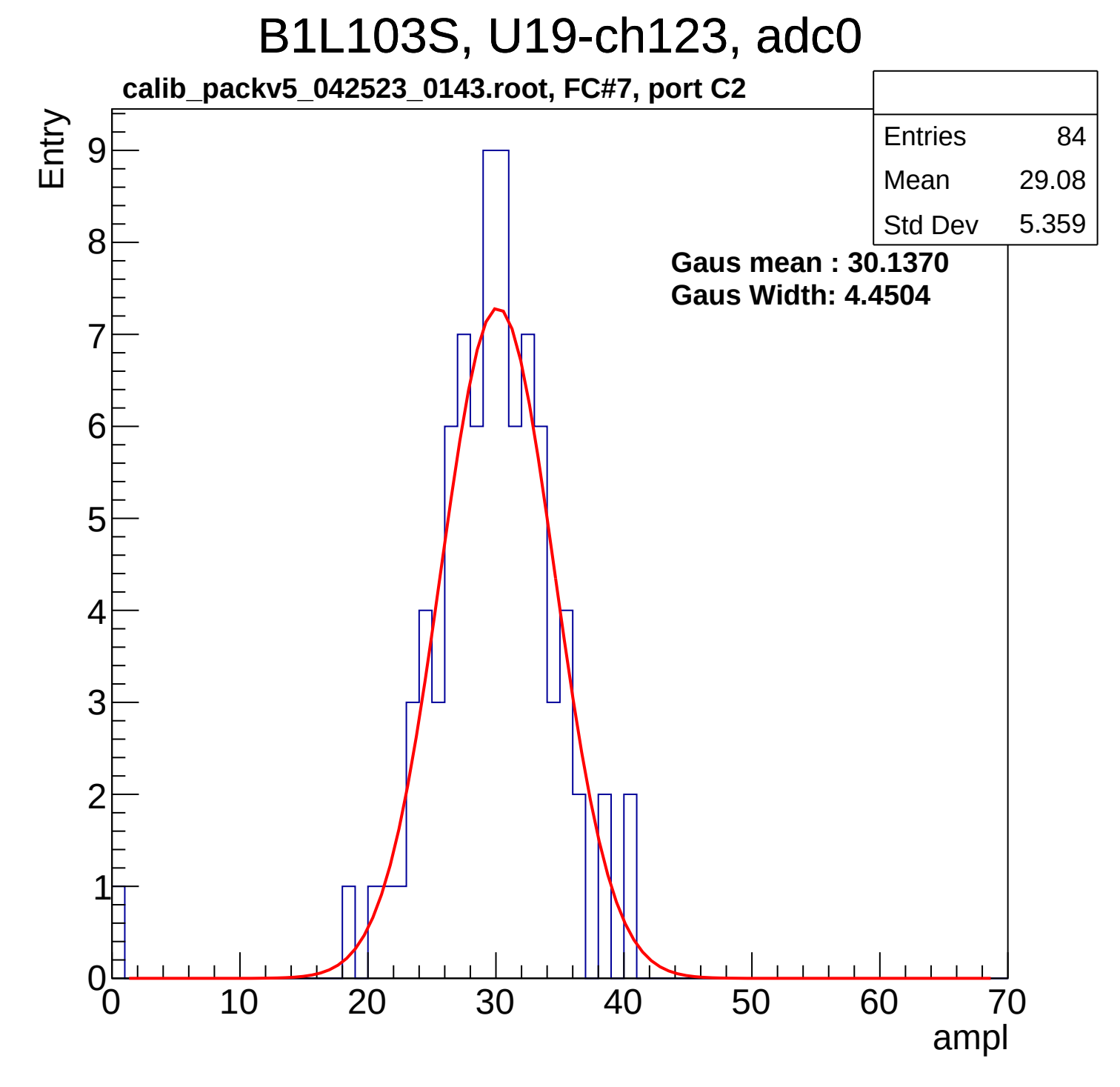
B1L103S, U19-ch123, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 29.08
Std Dev 5.359
Gaus mean : 30.1370
Gaus Width: 4.4504
Entry
ampl
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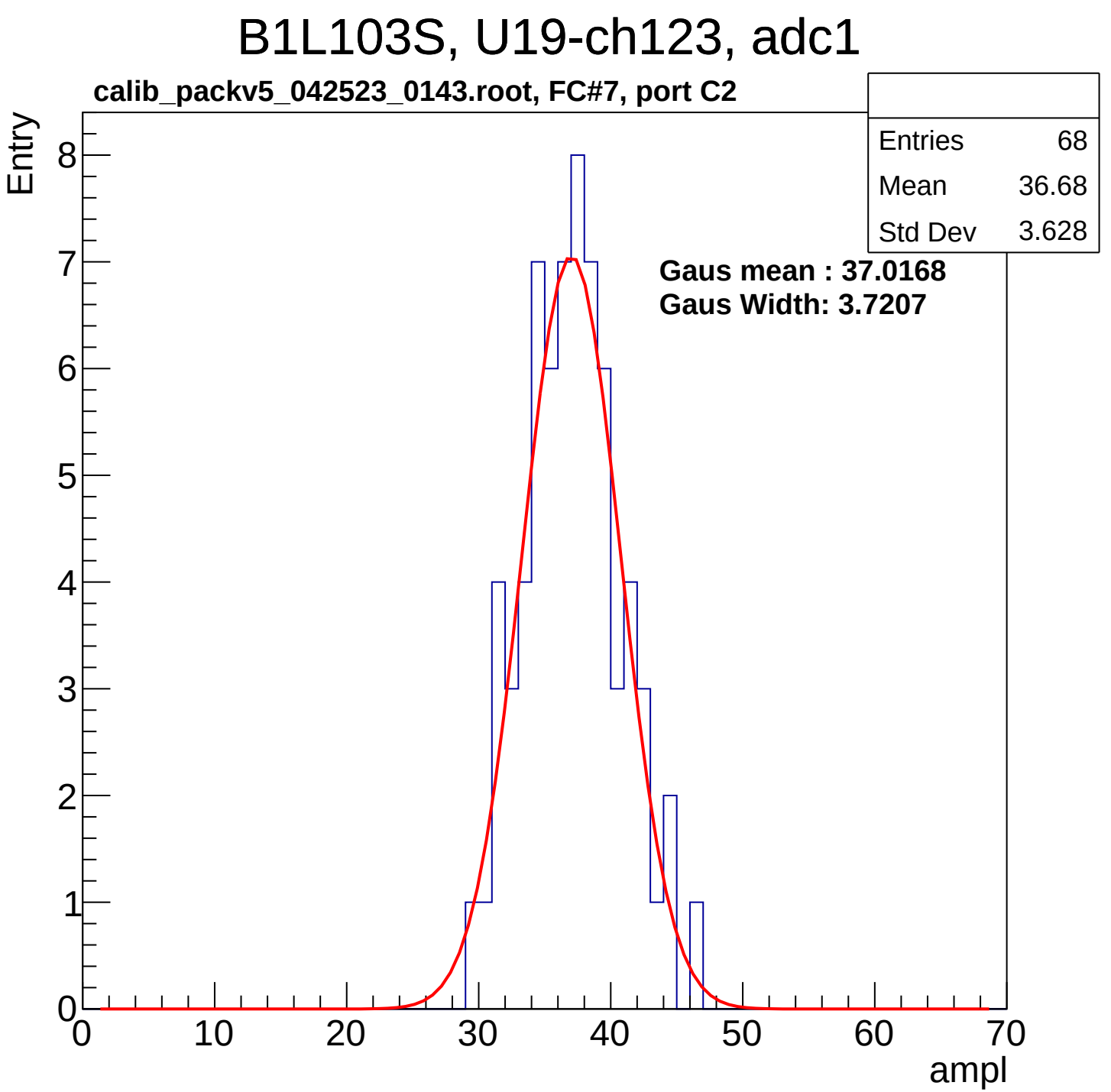
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch123, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.68
Std Dev 3.628
Gaus mean : 37.0168
Gaus Width: 3.7207
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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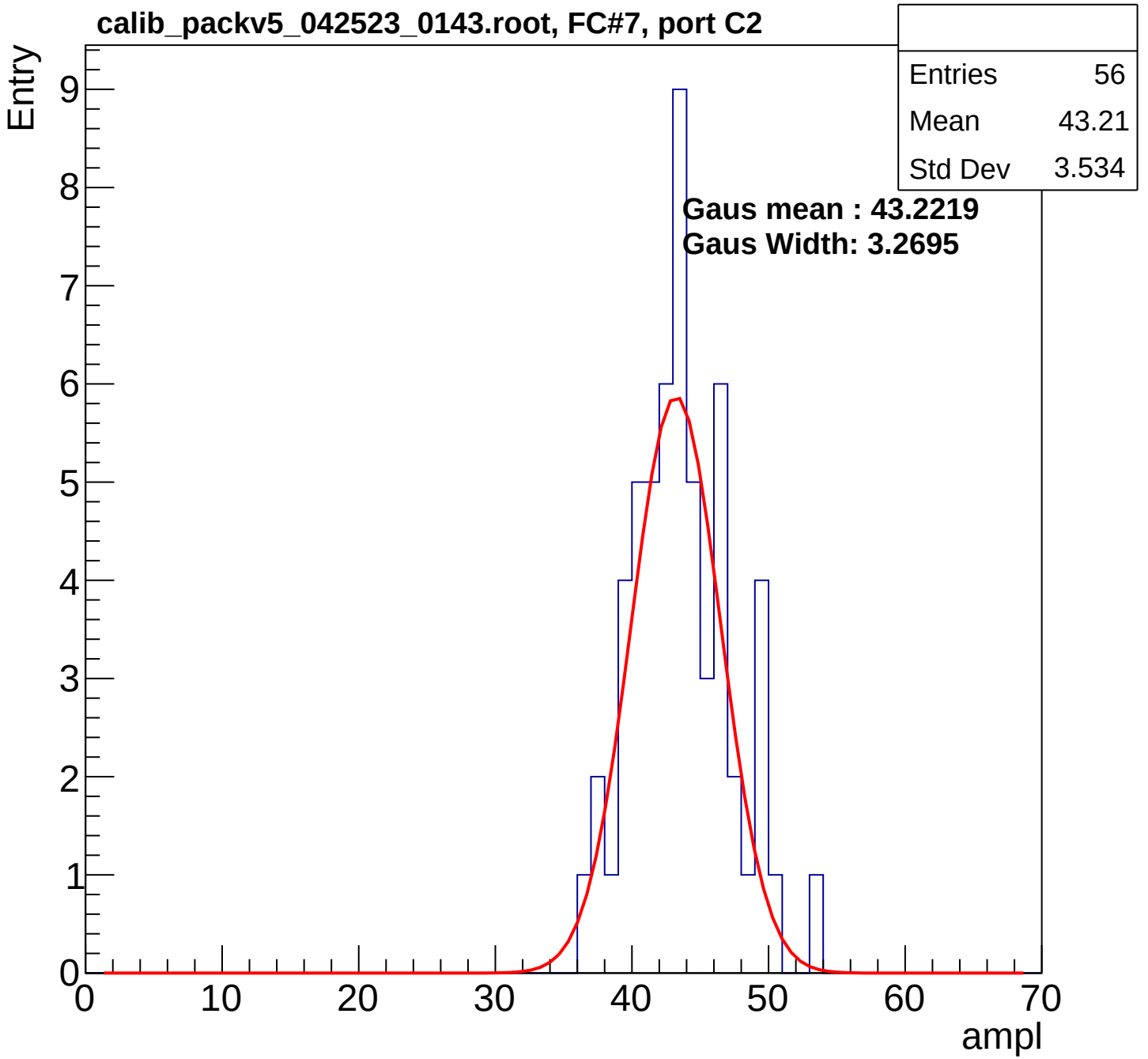
B1L103S, U19-ch123, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.21
Std Dev 3.534
Gaus mean : 43.2219
Gaus Width: 3.2695
Entry
ampl
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2
3
4
5
6
7
8
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0
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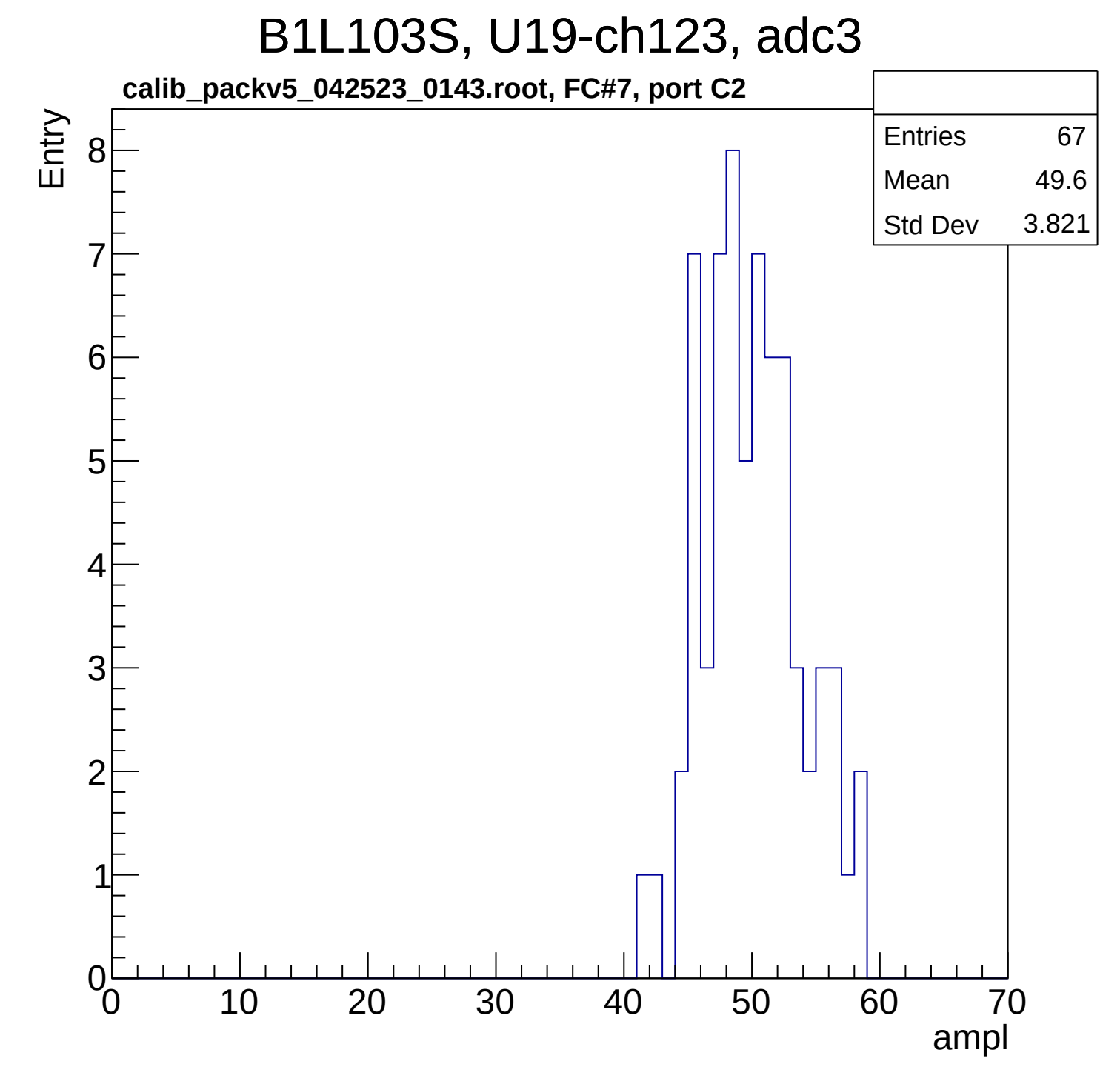
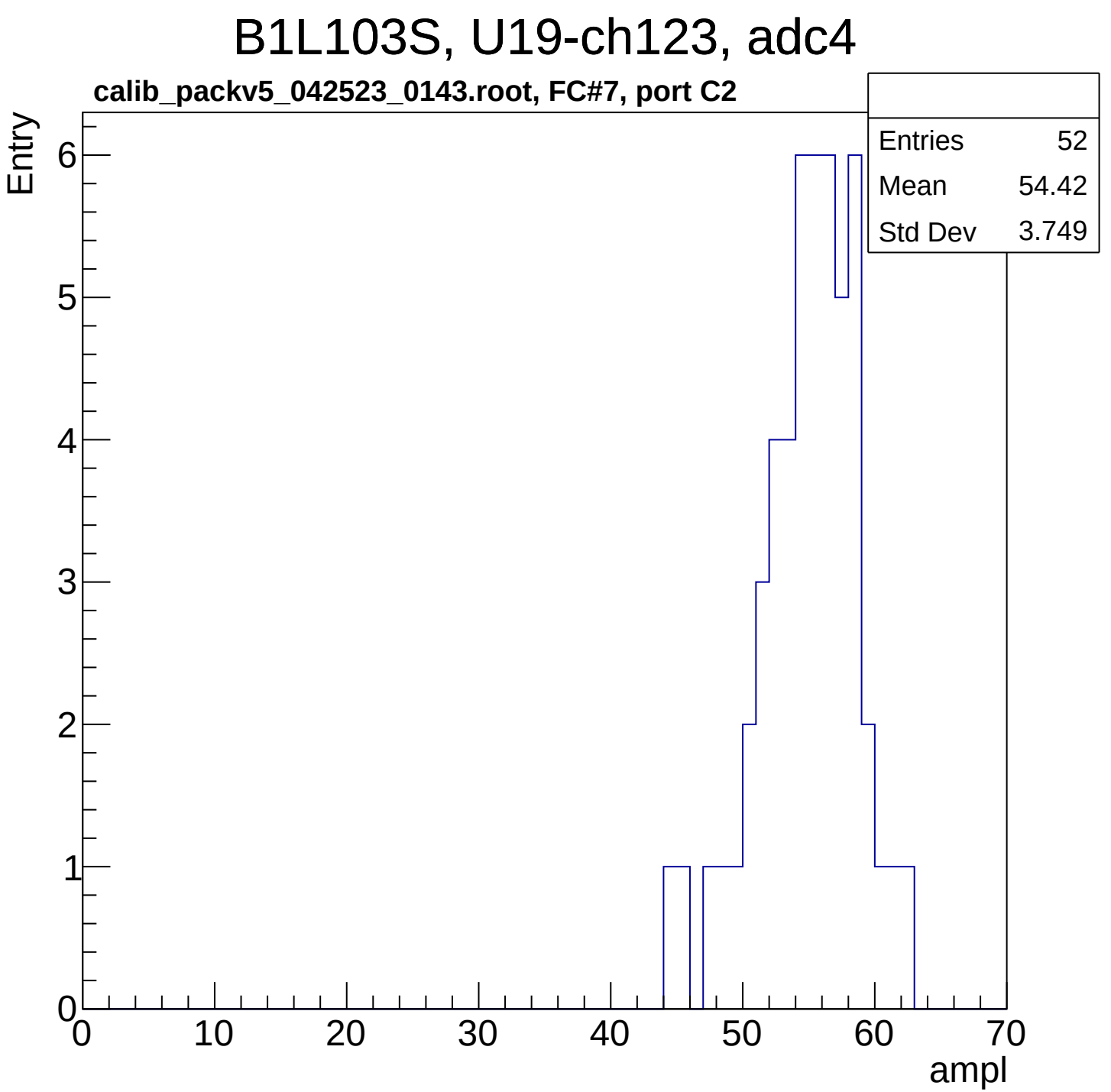

B1L103S, U19-ch123, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 49.6
Std Dev 3.821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch123, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 54.42
Std Dev 3.749
Entry
0
1
2
3
4
5
6
0
10
20
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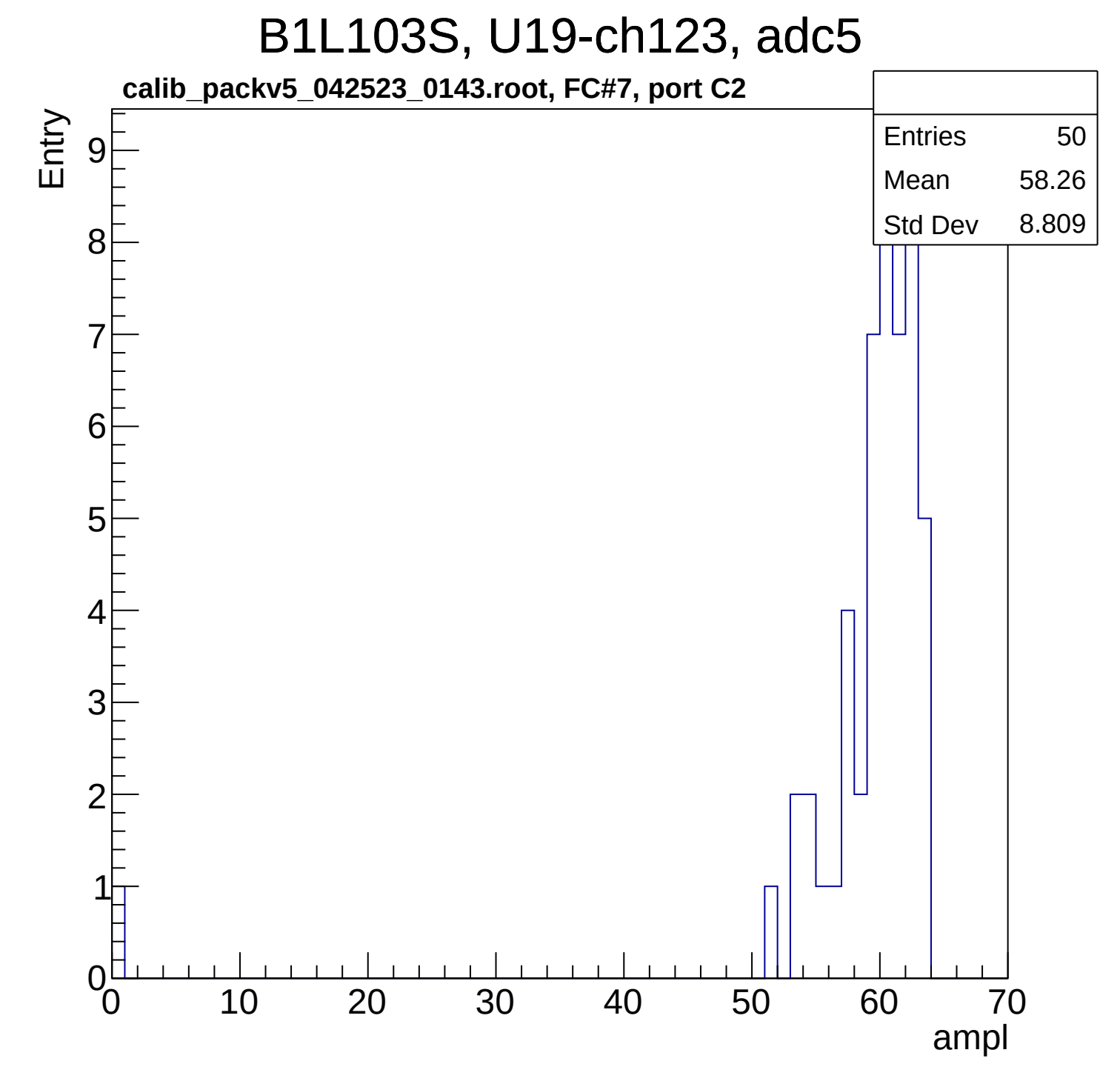
40
50
60
70
ampl


B1L103S, U19-ch123, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.26
Std Dev 8.809
Entry
ampl
0
1
2
3
4
5
6
7
8
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60
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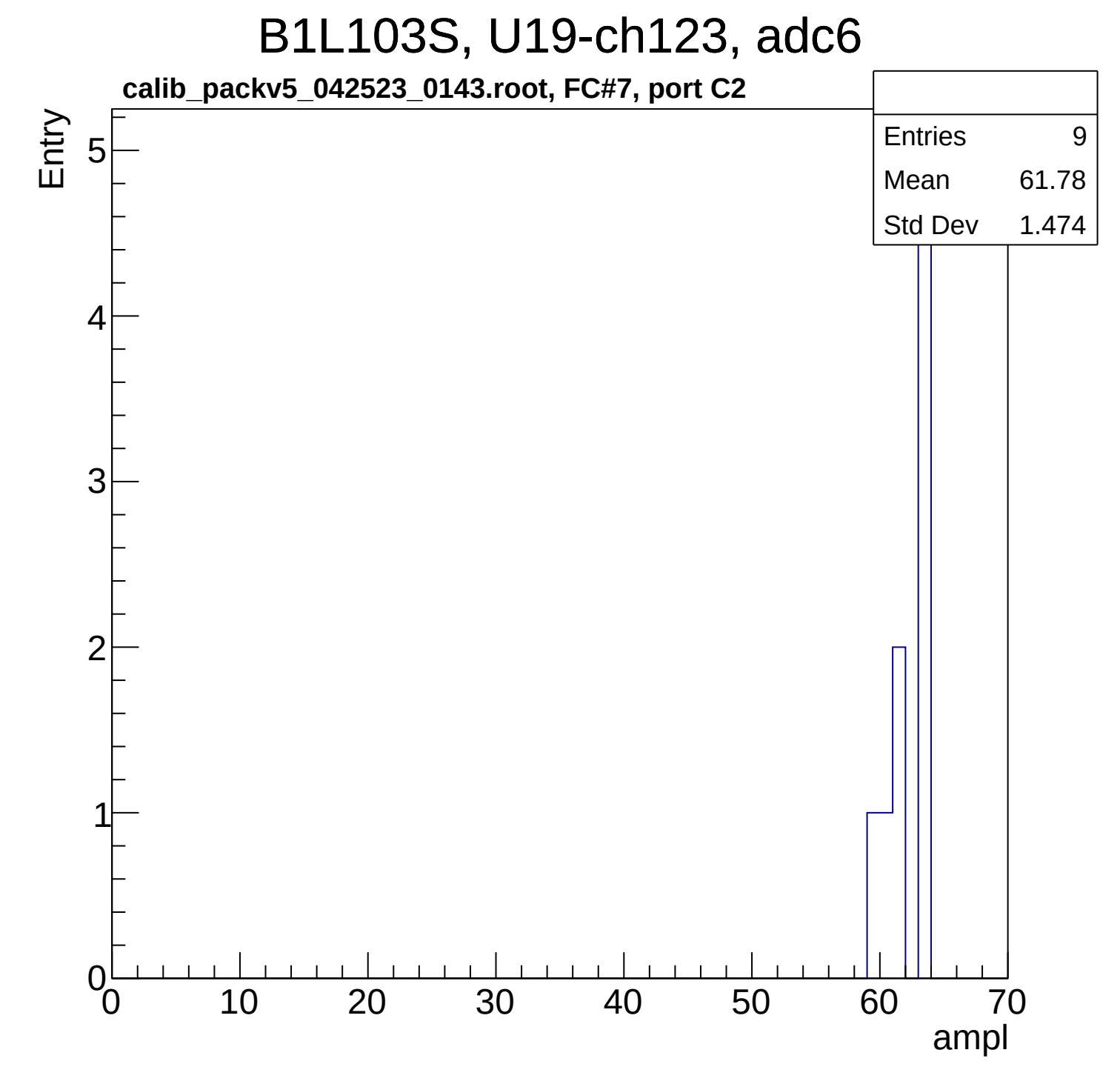
B1L103S, U19-ch123, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.78
Std Dev 1.474
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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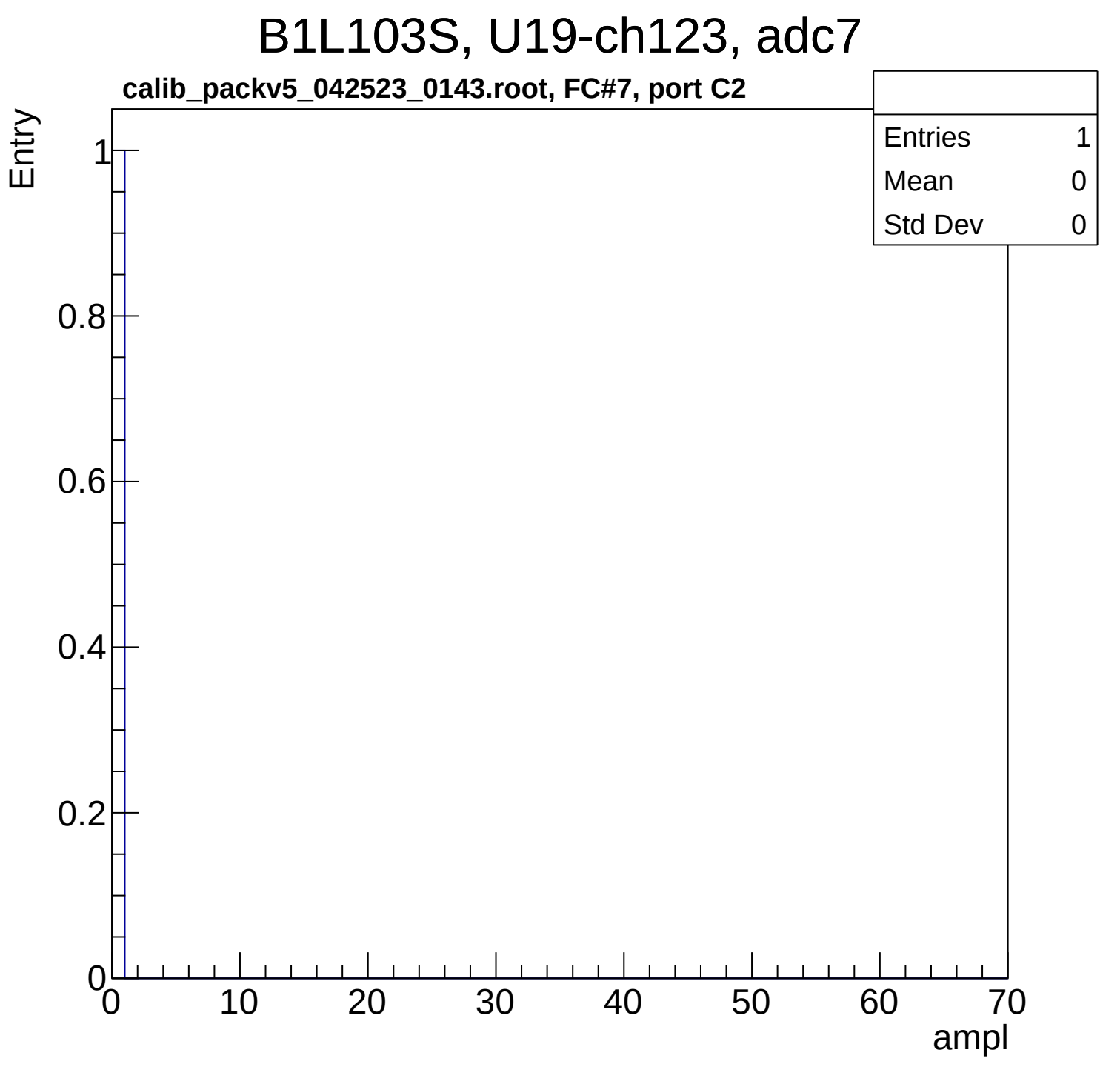
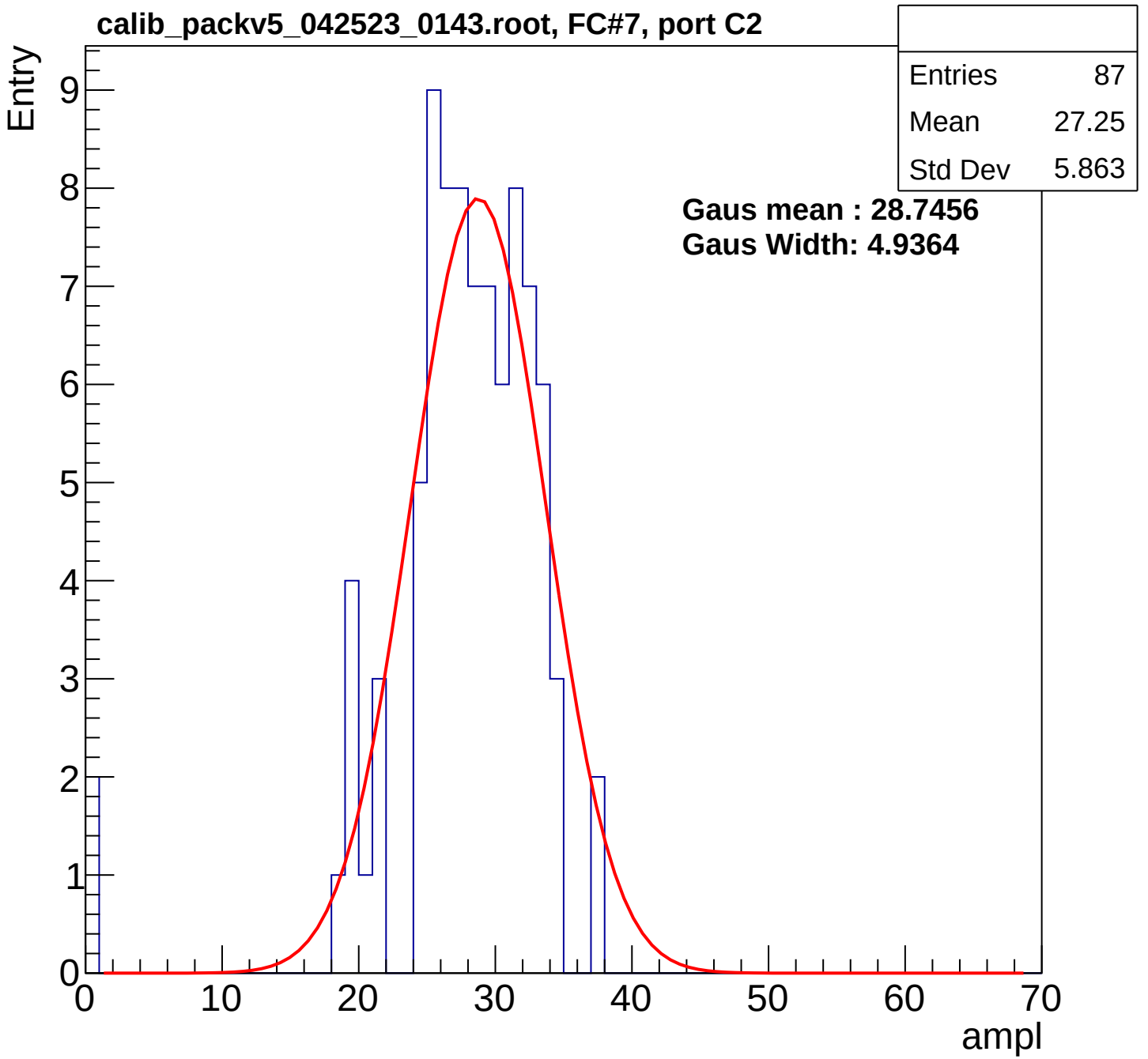
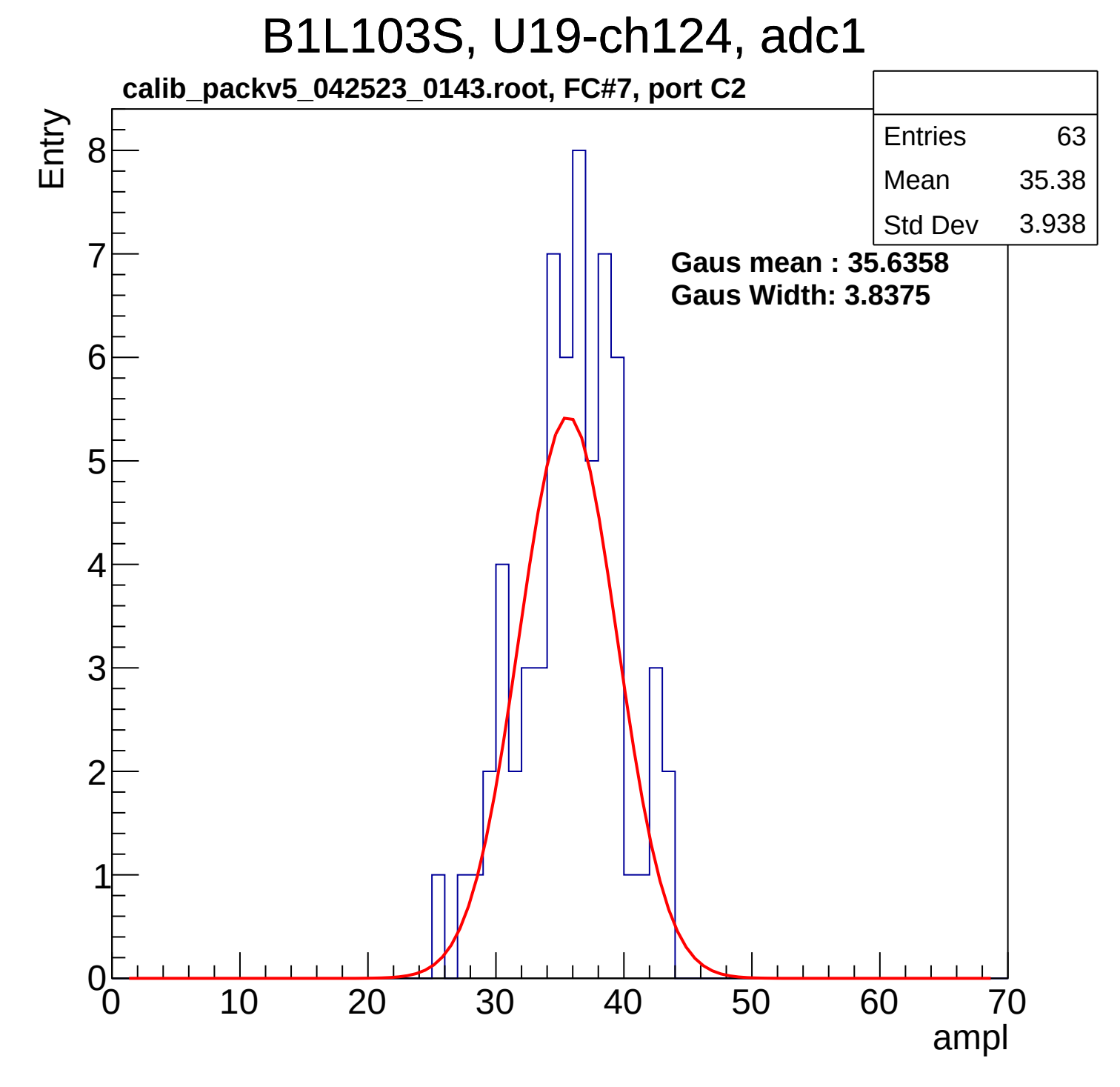

B1L103S, U19-ch123, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch124, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 27.25
Std Dev 5.863
Gaus mean : 28.7456
Gaus Width: 4.9364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch124, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.38
Std Dev 3.938
Gaus mean : 35.6358
Gaus Width: 3.8375
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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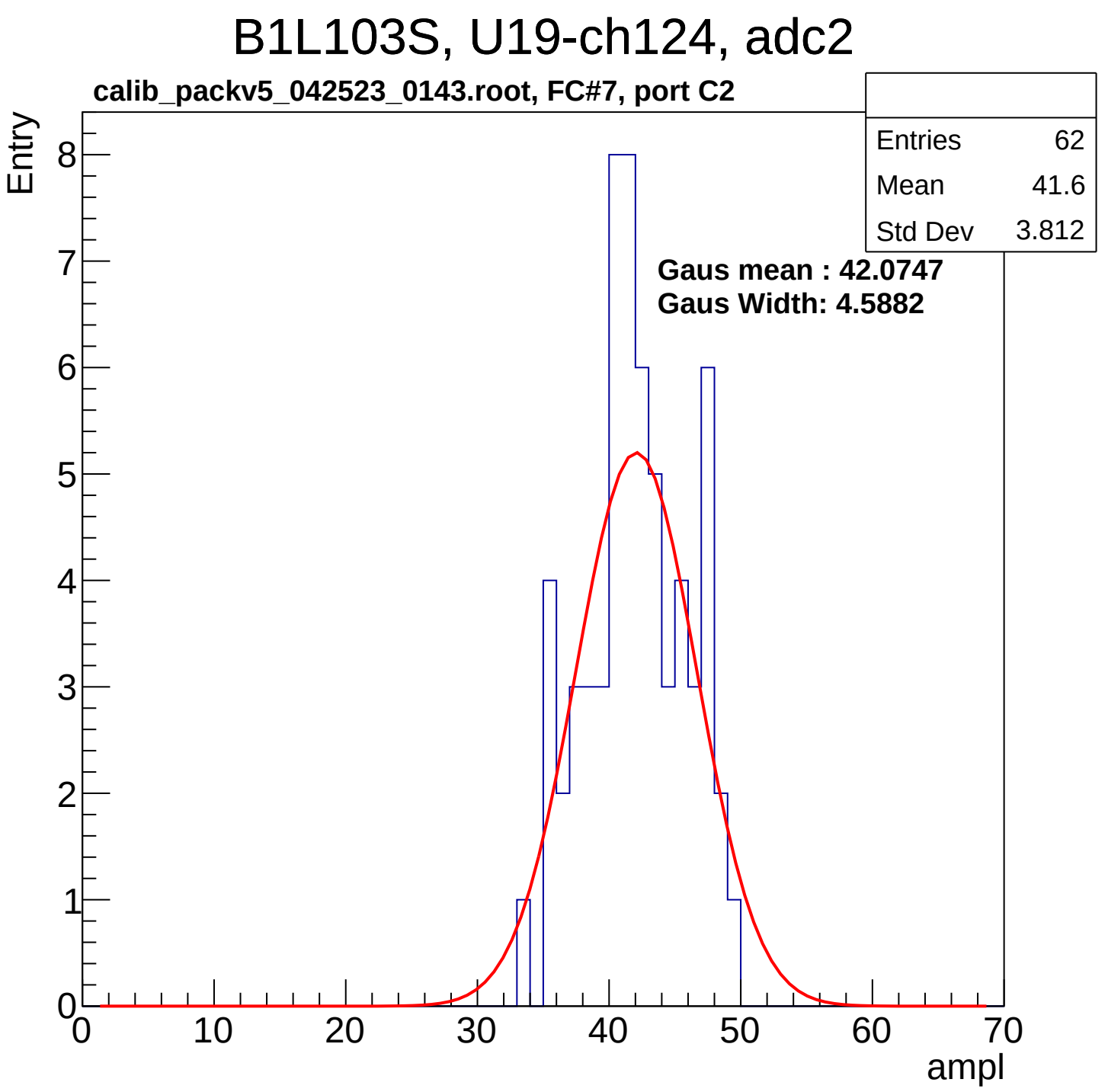
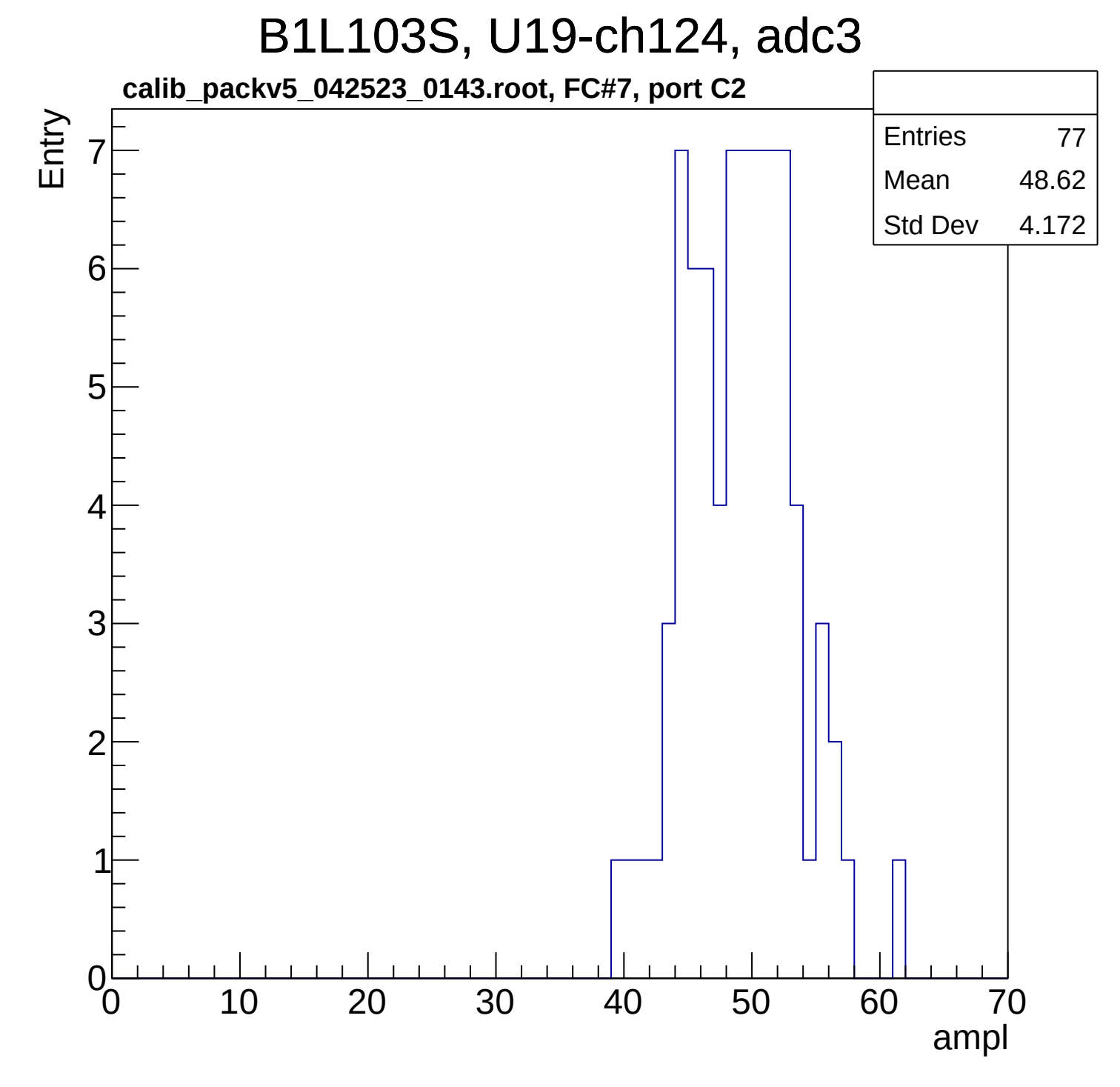
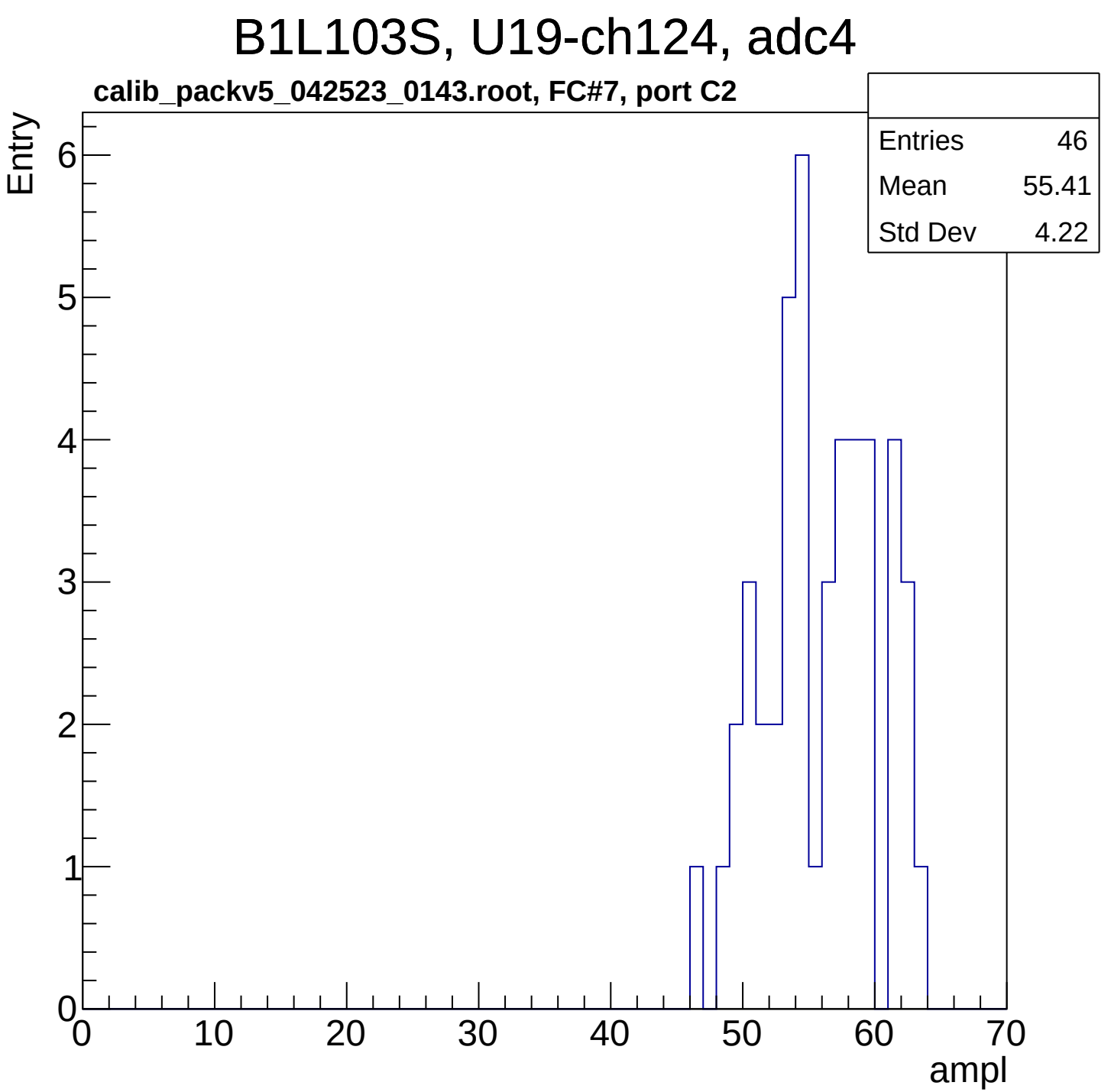
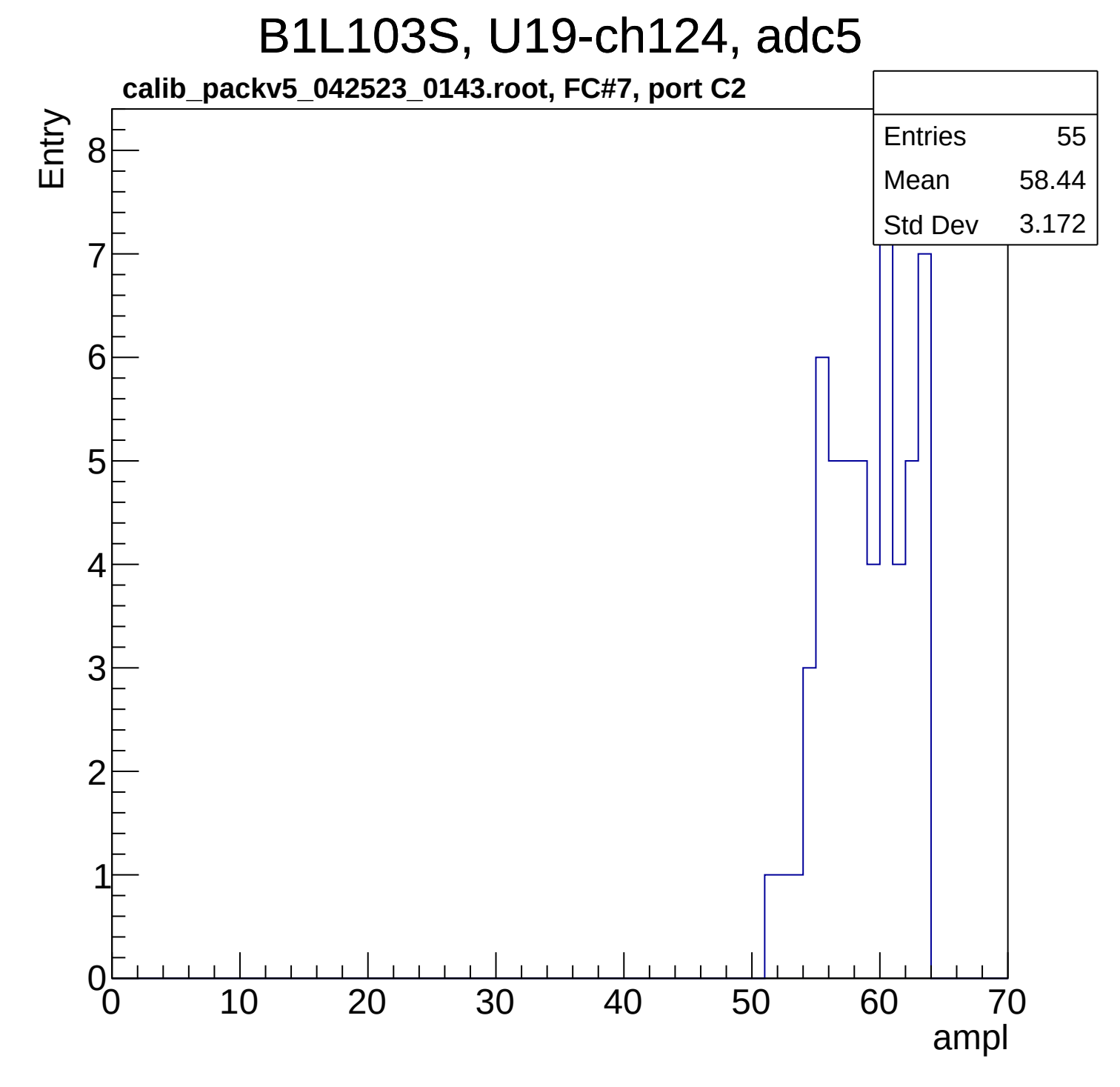
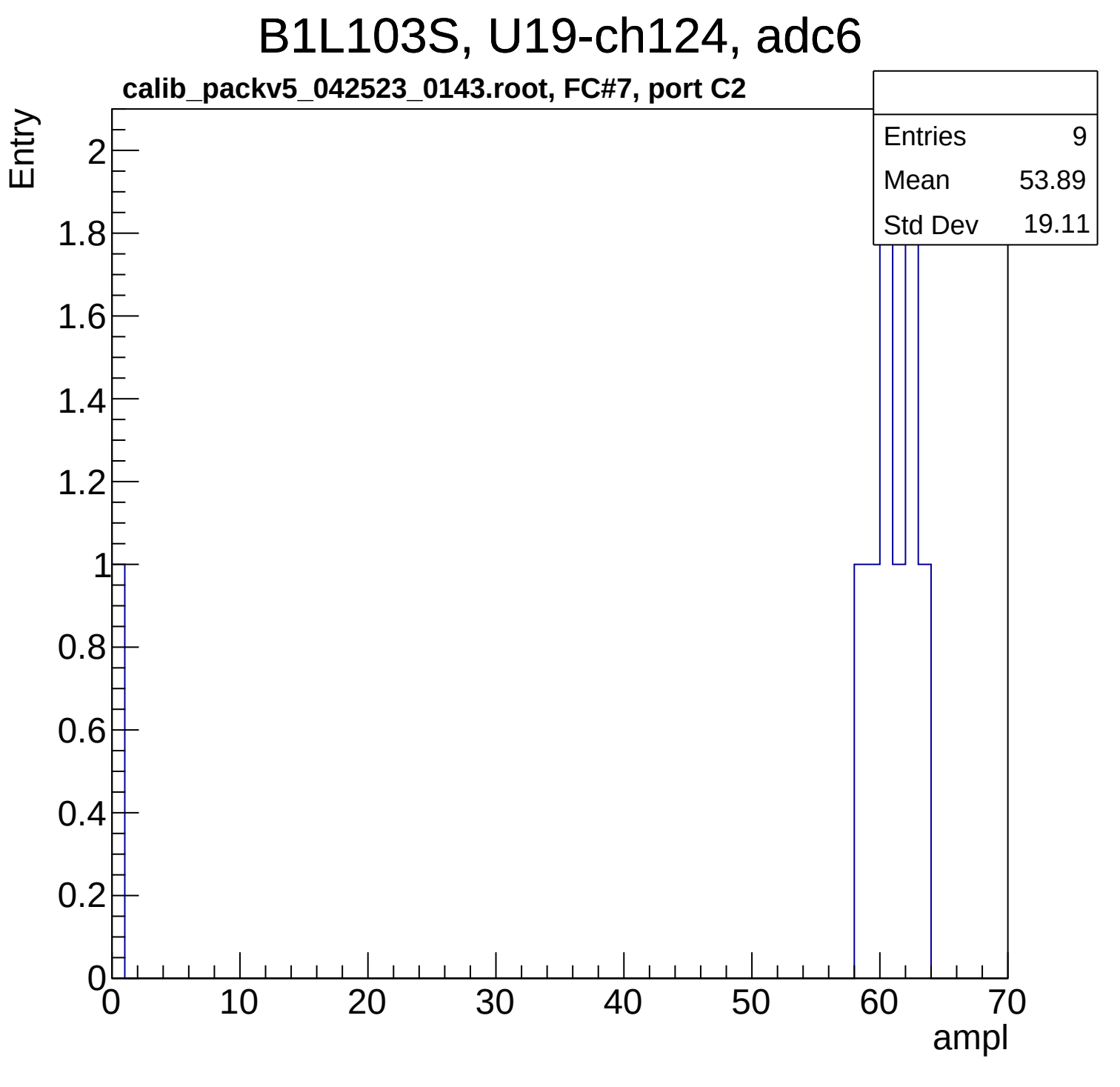
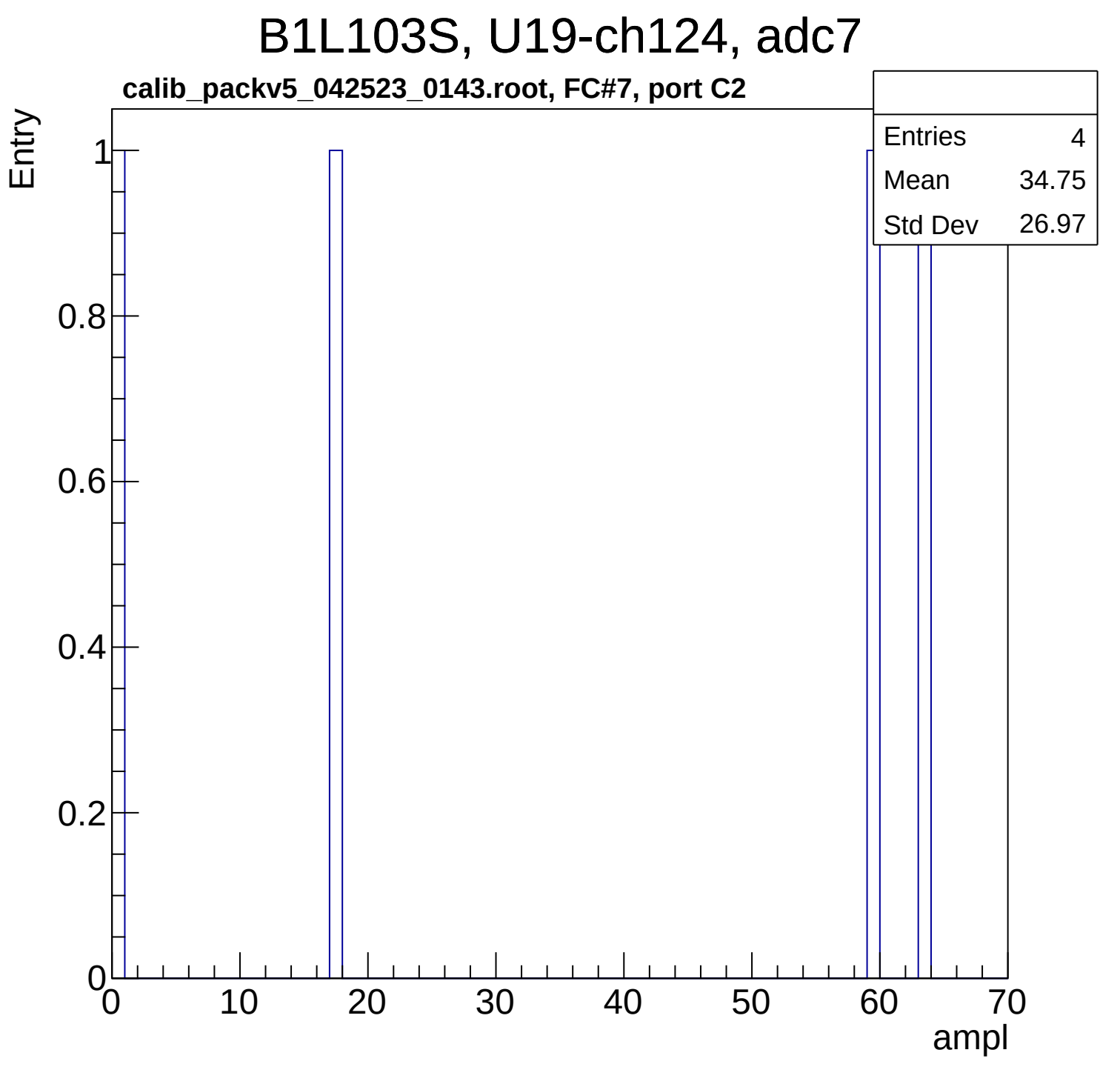

B1L103S, U19-ch124, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 41.6
Std Dev 3.812
Gaus mean : 42.0747
Gaus Width: 4.5882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch124, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 48.62
Std Dev 4.172
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch124, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 55.41
Std Dev 4.22
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch124, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 58.44
Std Dev 3.172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch124, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 53.89
Std Dev 19.11
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U19-ch124, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 34.75
Std Dev 26.97
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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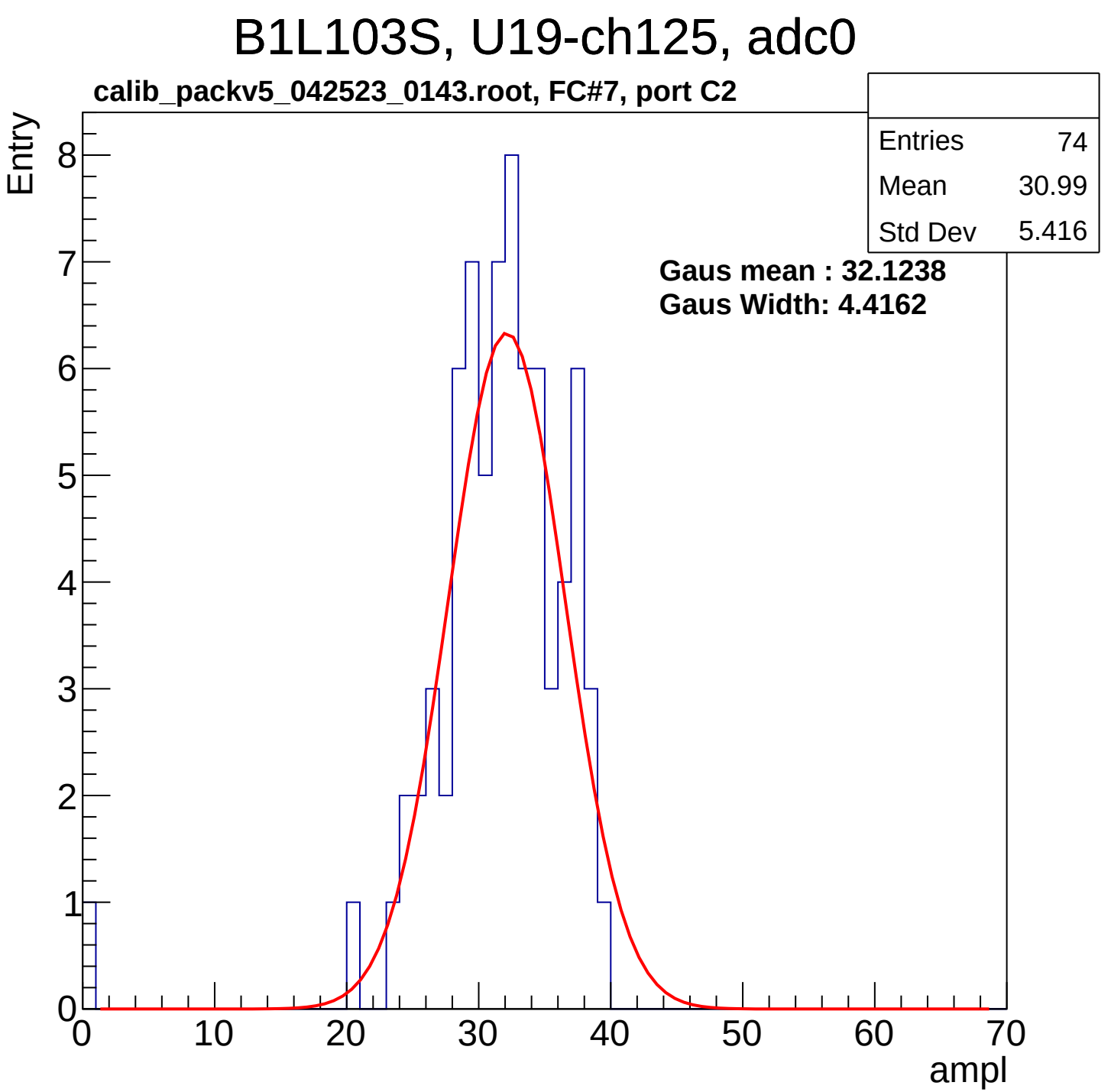
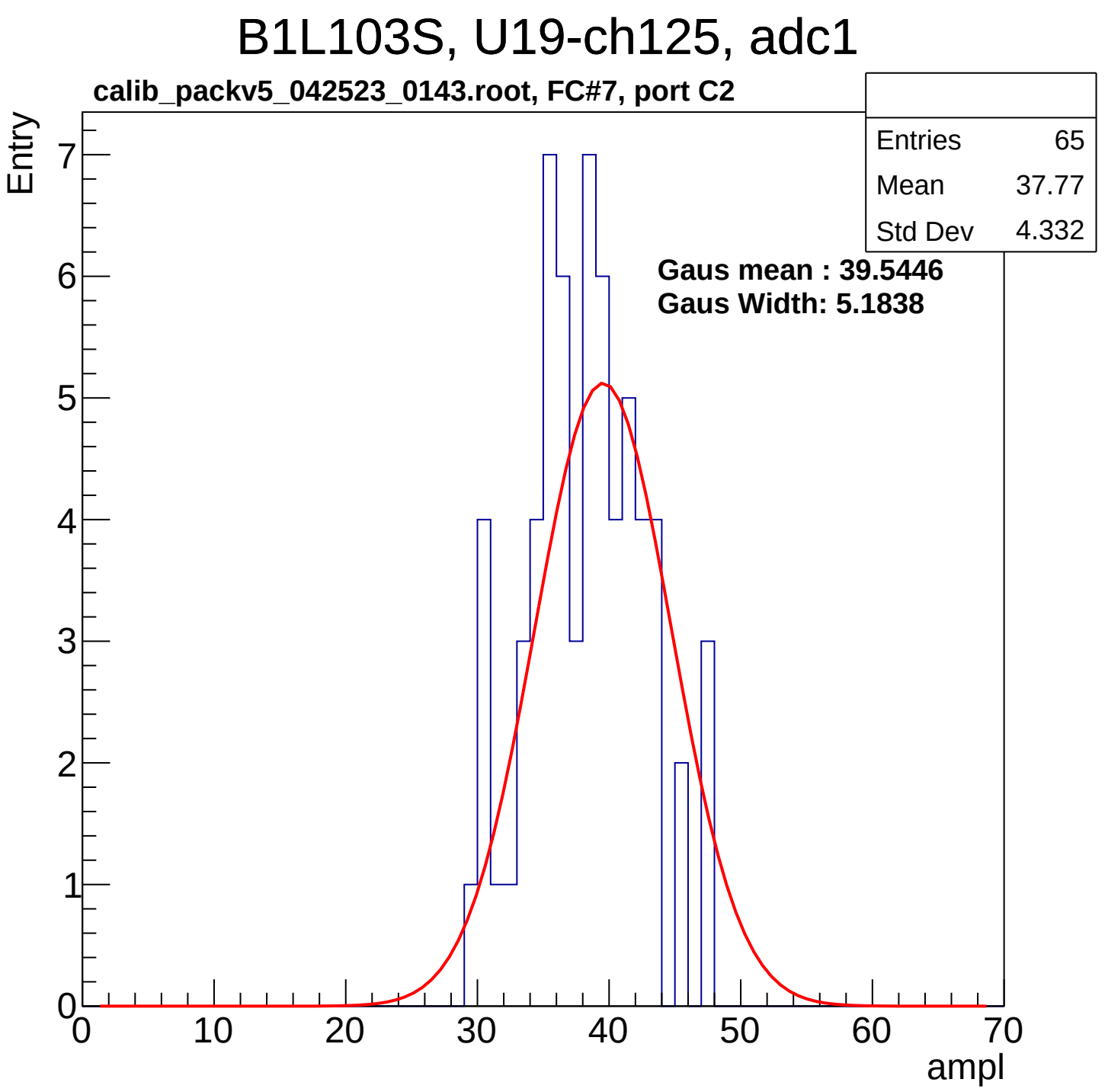
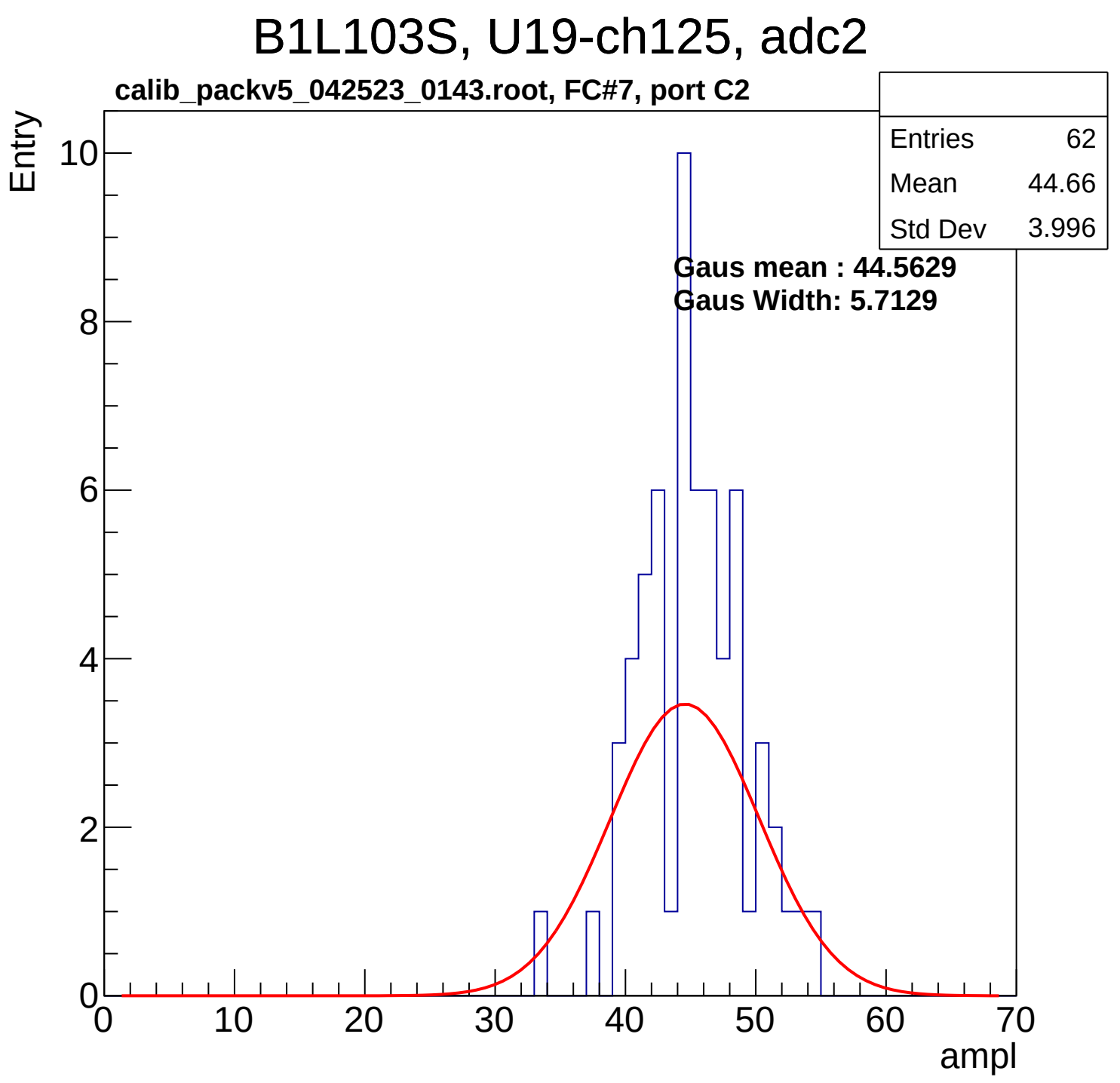

B1L103S, U19-ch125, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 30.99
Std Dev 5.416
Gaus mean : 32.1238
Gaus Width: 4.4162
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch125, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 37.77
Std Dev 4.332
Gaus mean : 39.5446
Gaus Width: 5.1838
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch125, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 44.66
Std Dev 3.996
Gaus mean : 44.5629
Gaus Width: 5.7129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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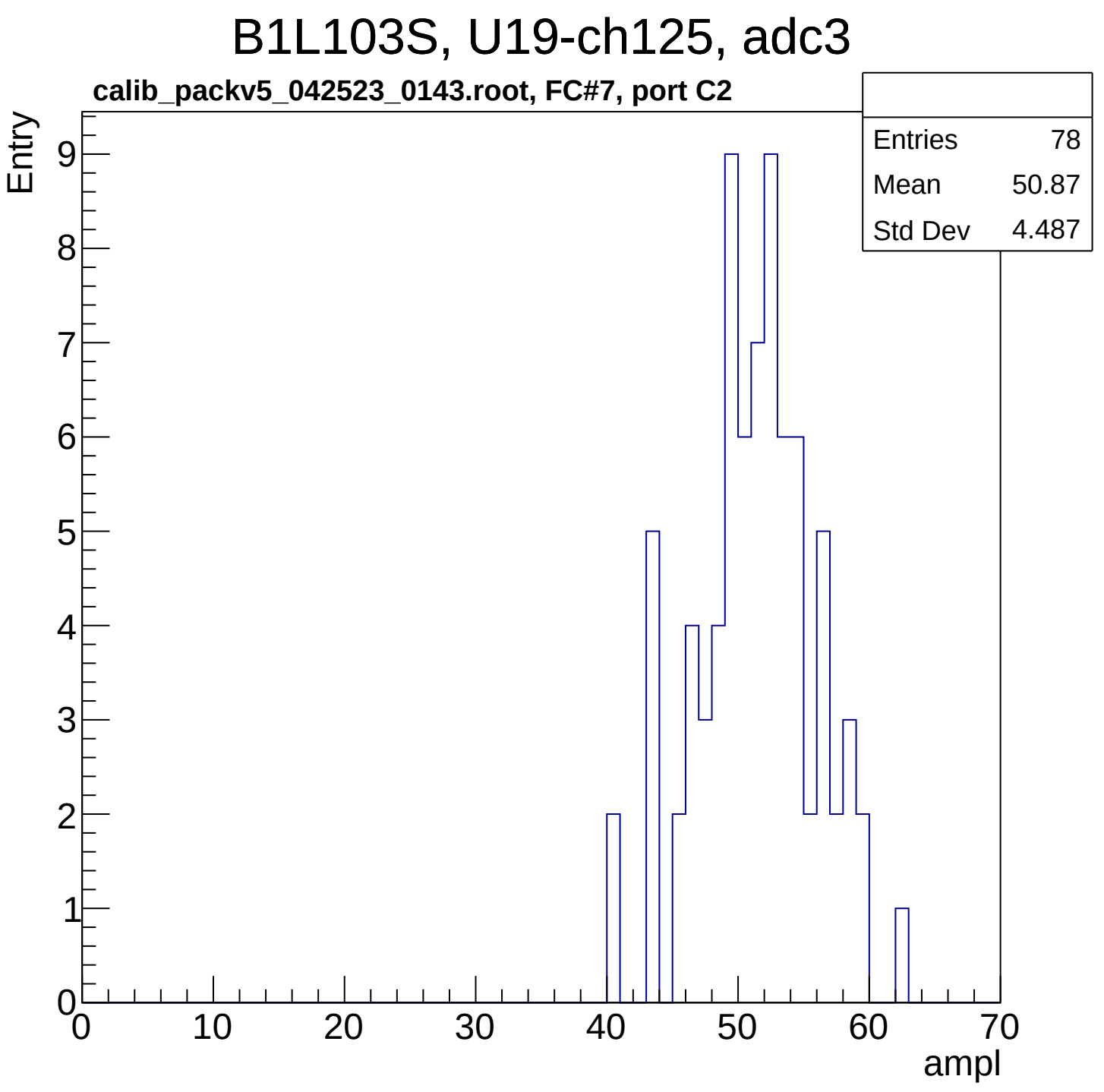
B1L103S, U19-ch125, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 50.87
Std Dev 4.487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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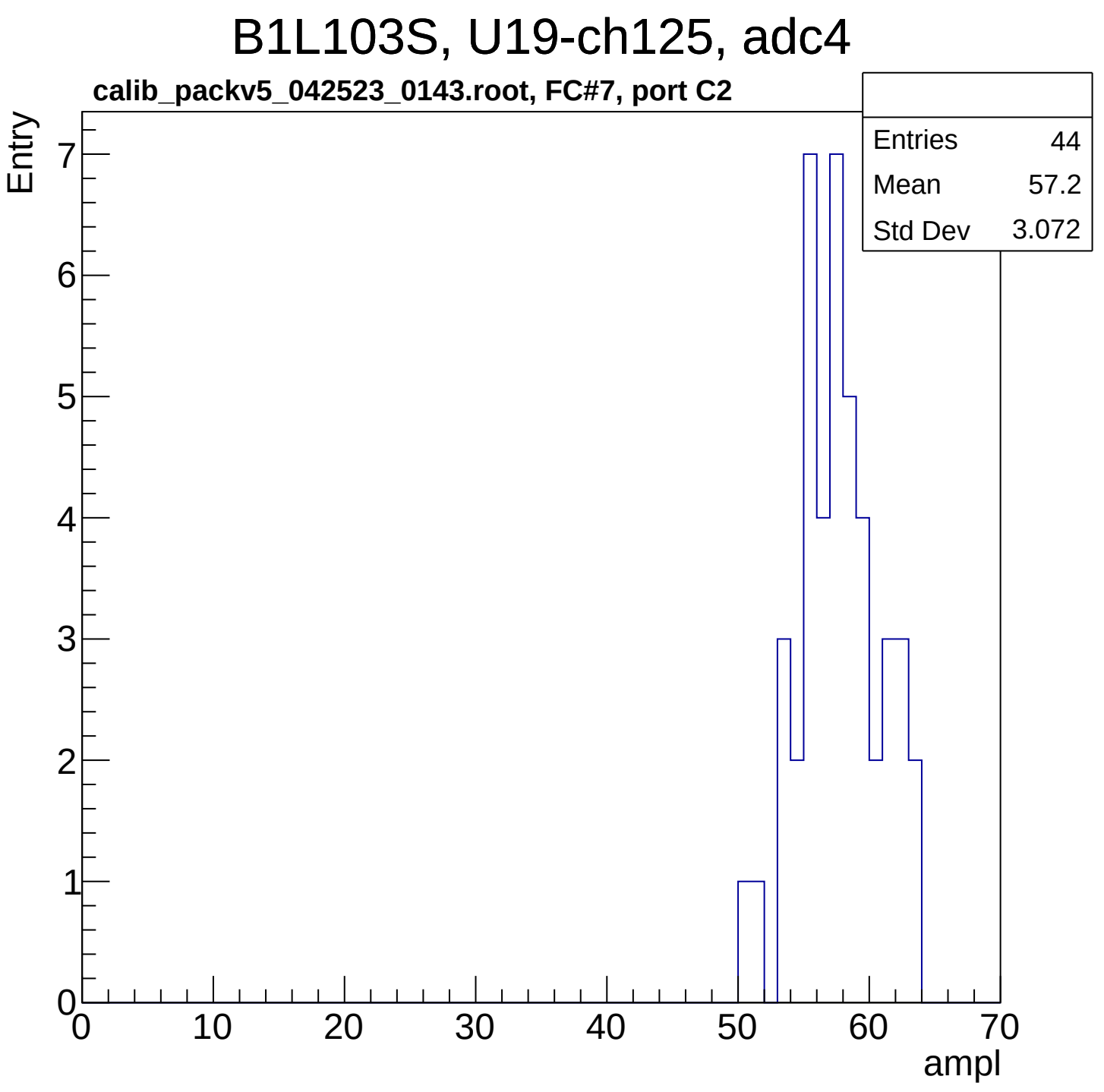
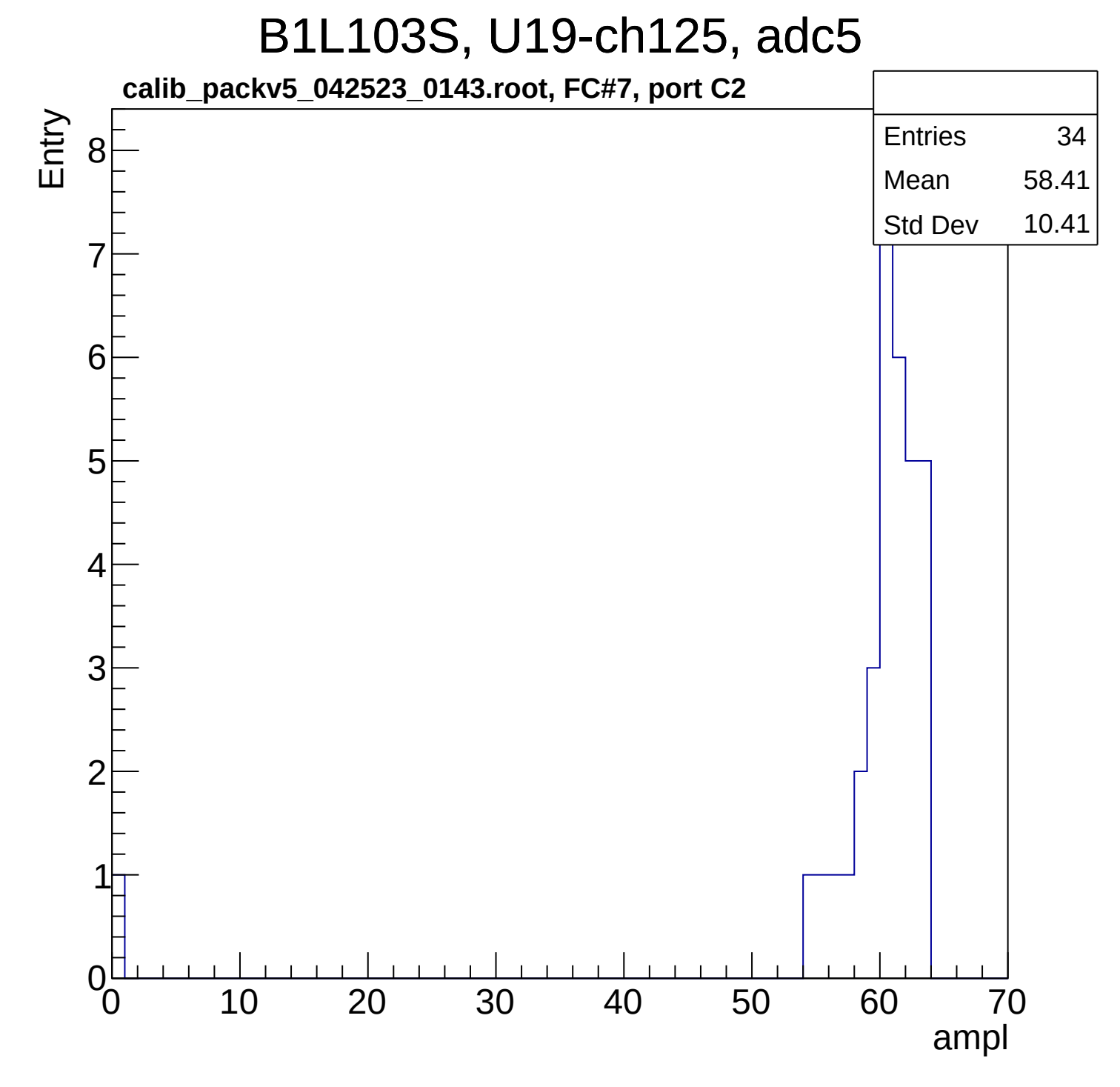

B1L103S, U19-ch125, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 57.2
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch125, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.41
Std Dev 10.41
Entry
ampl
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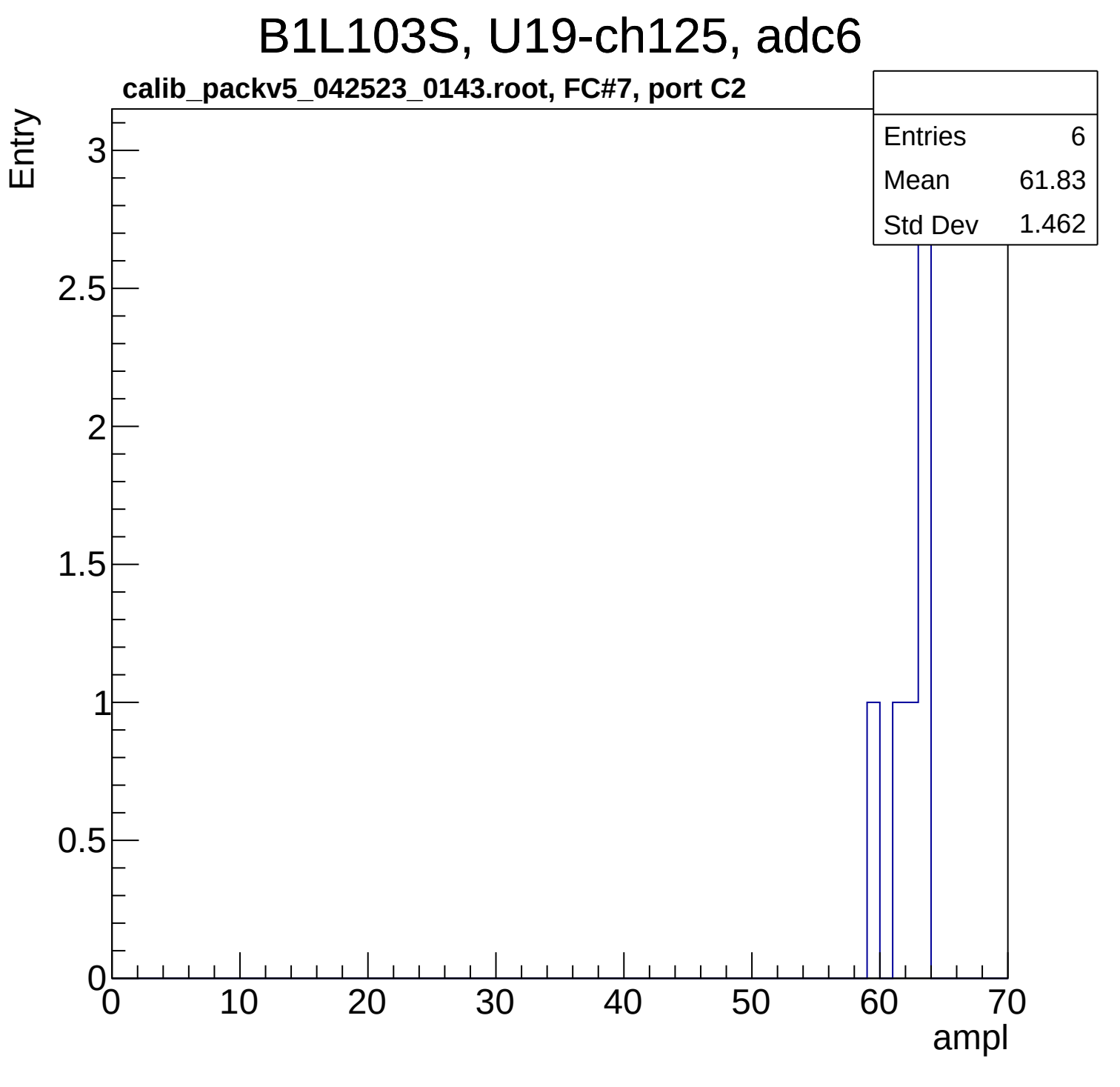
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch125, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
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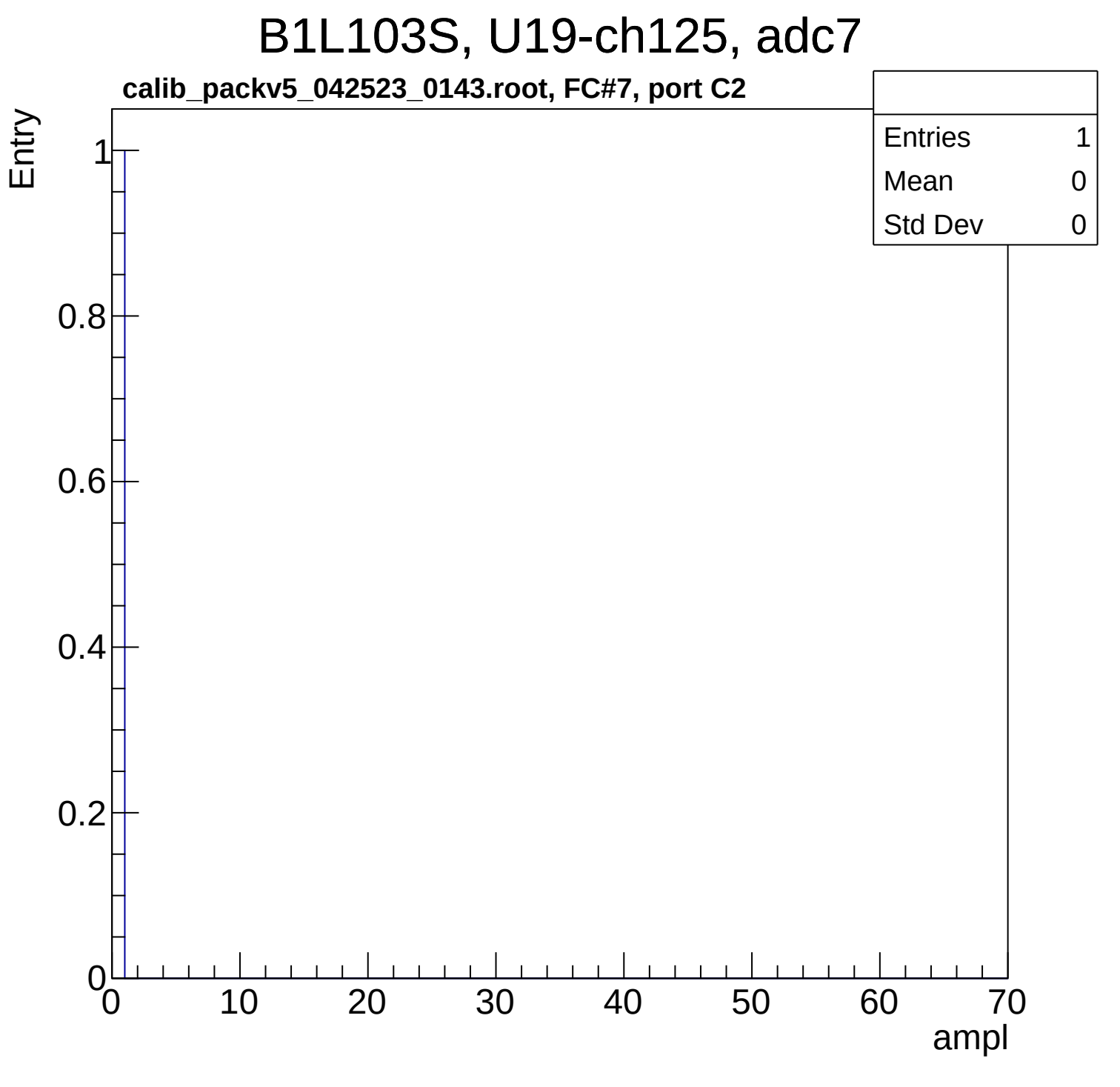
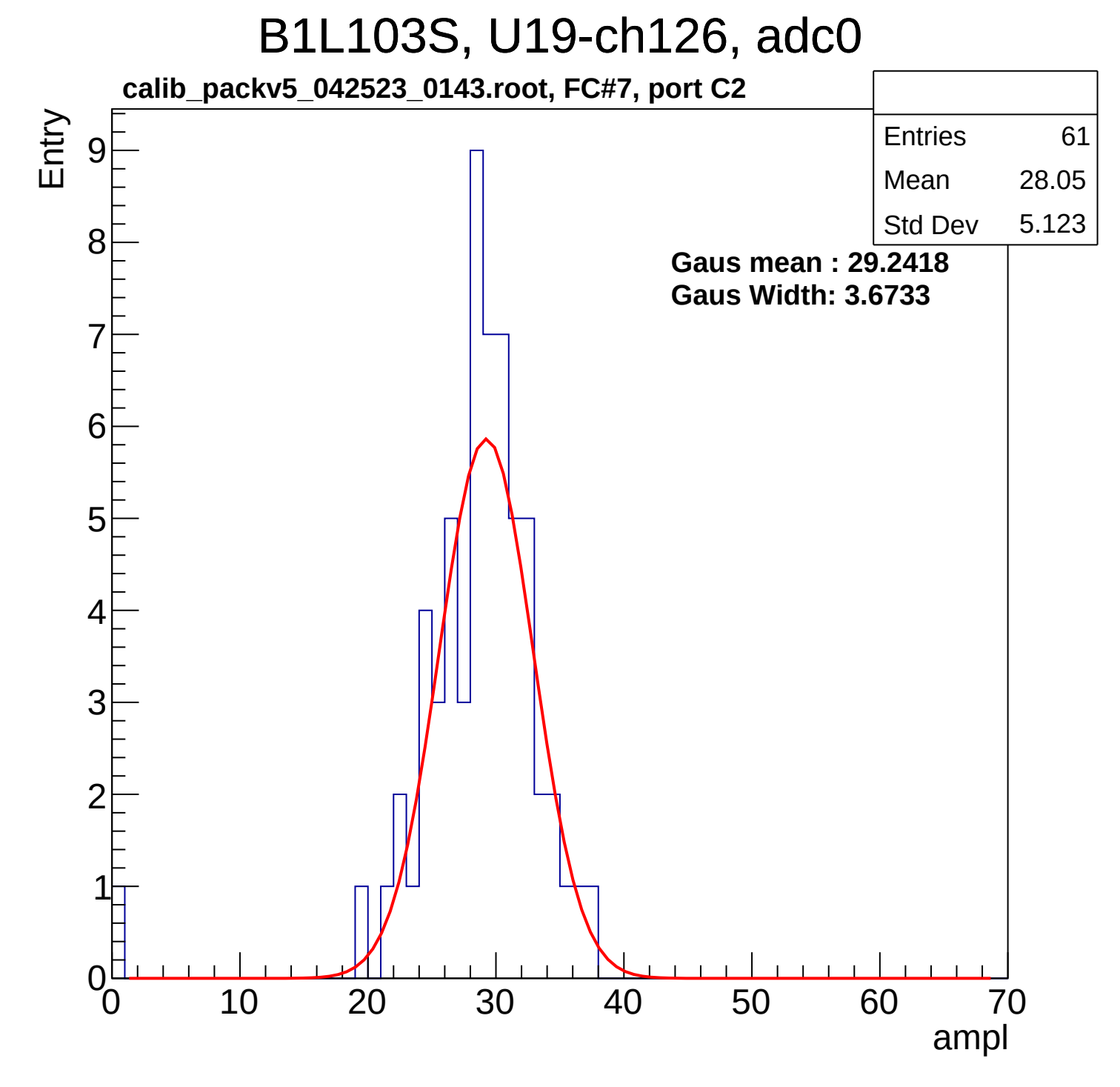
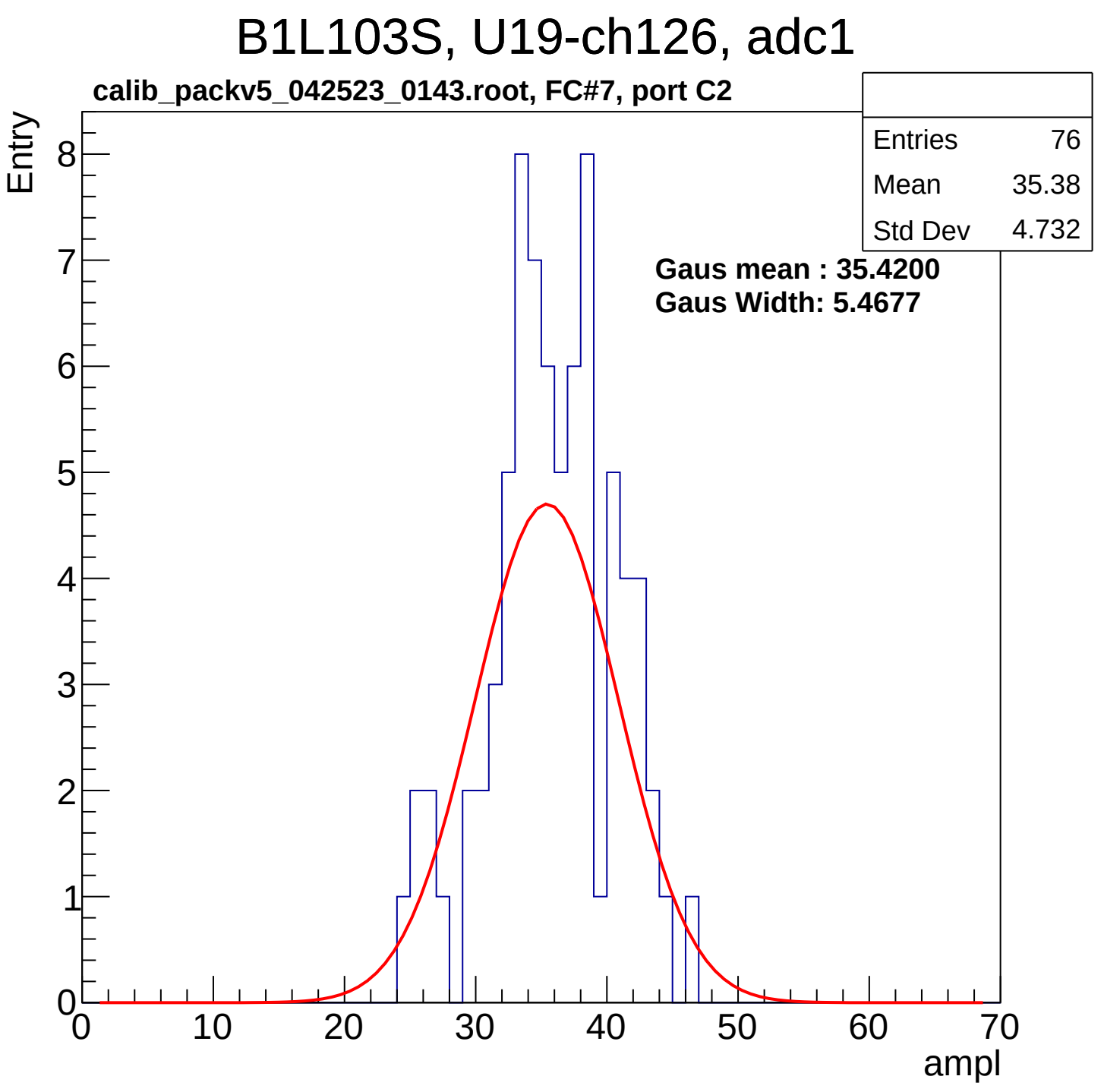
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U19-ch125, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch126, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 28.05
Std Dev 5.123
Gaus mean : 29.2418
Gaus Width: 3.6733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 35.38
Std Dev 4.732
Gaus mean : 35.4200
Gaus Width: 5.4677
Entry
ampl
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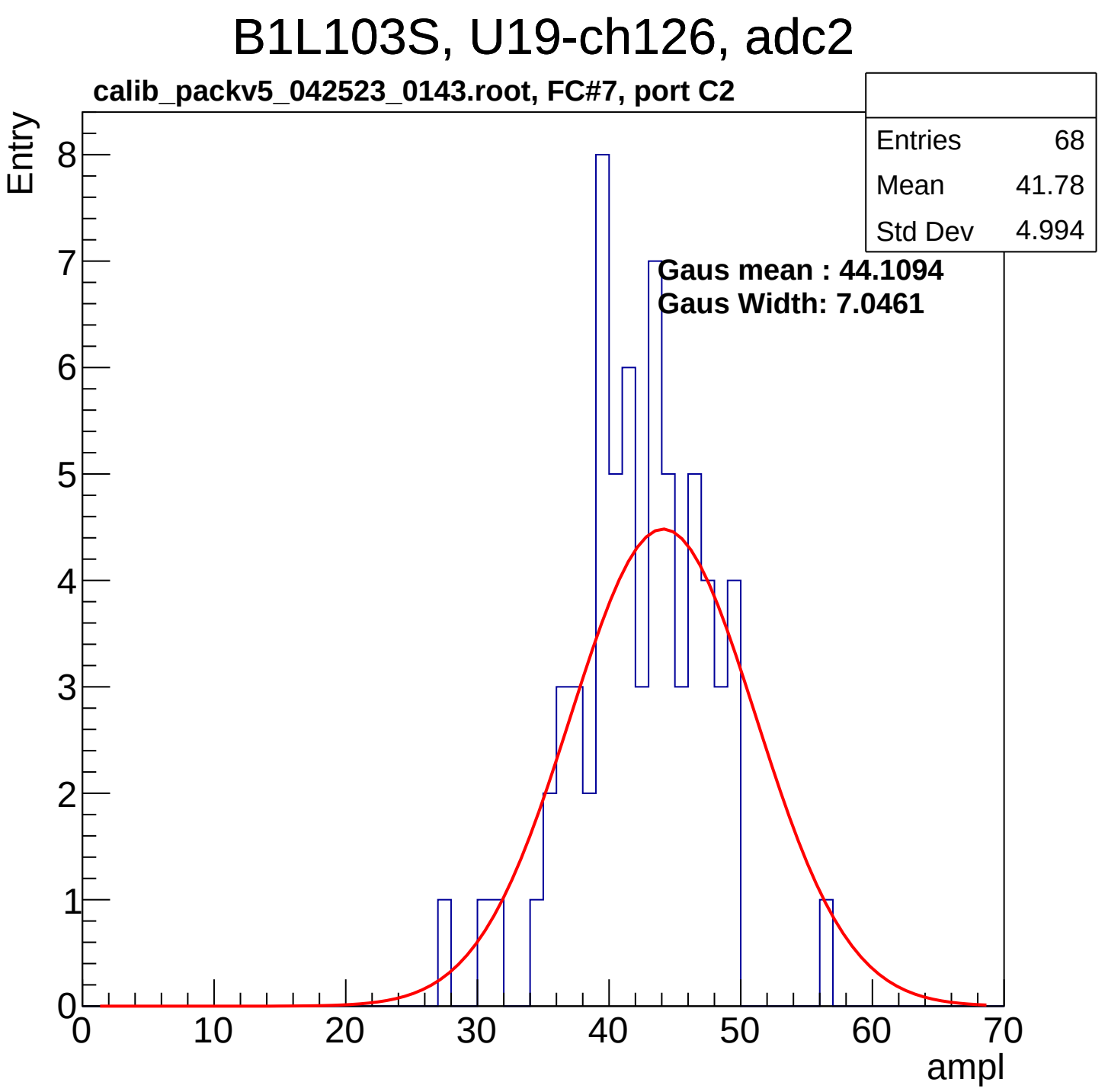
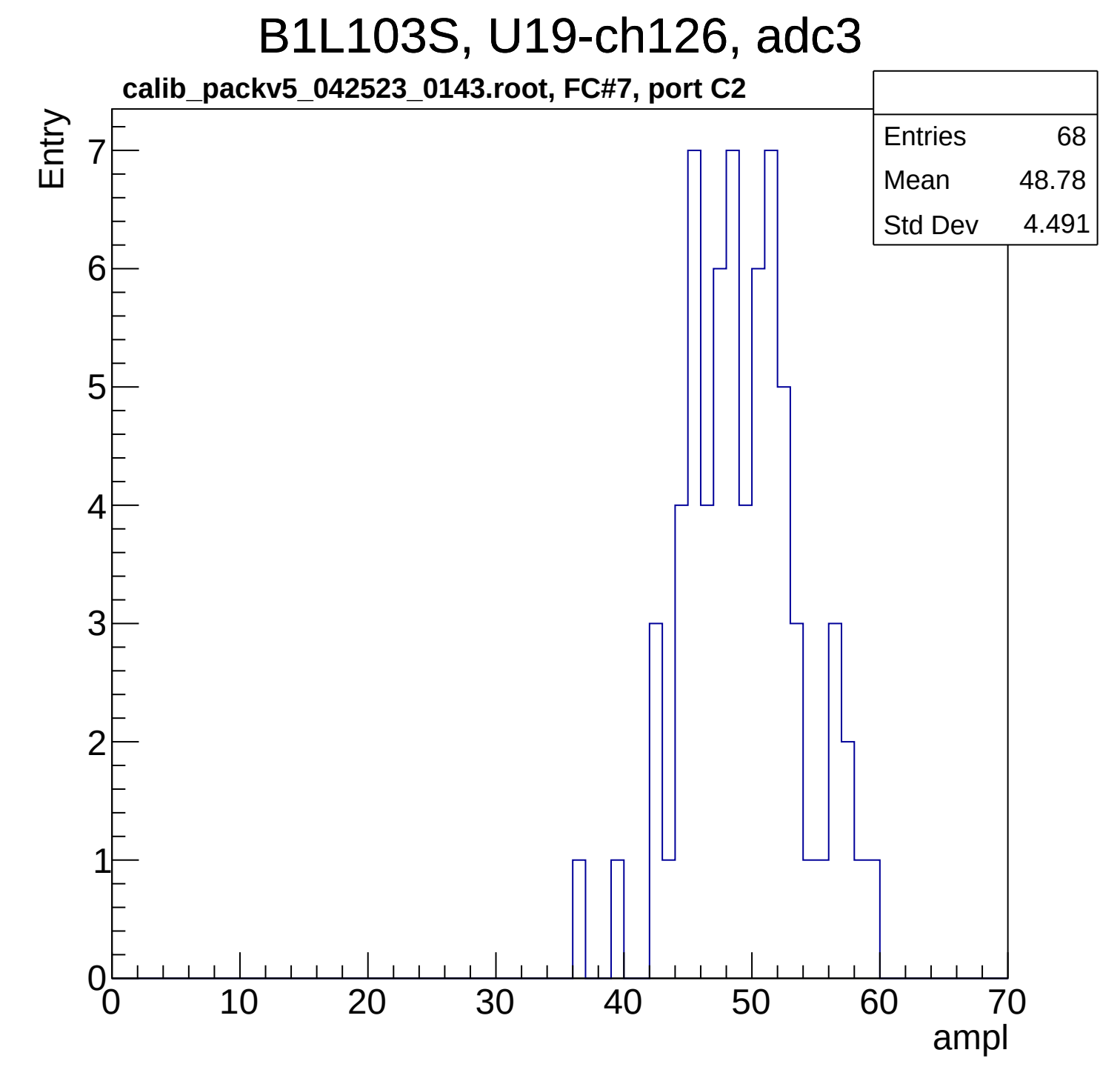
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 41.78
Std Dev 4.994
Gaus mean : 44.1094
Gaus Width: 7.0461
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 48.78
Std Dev 4.491
Entry
ampl
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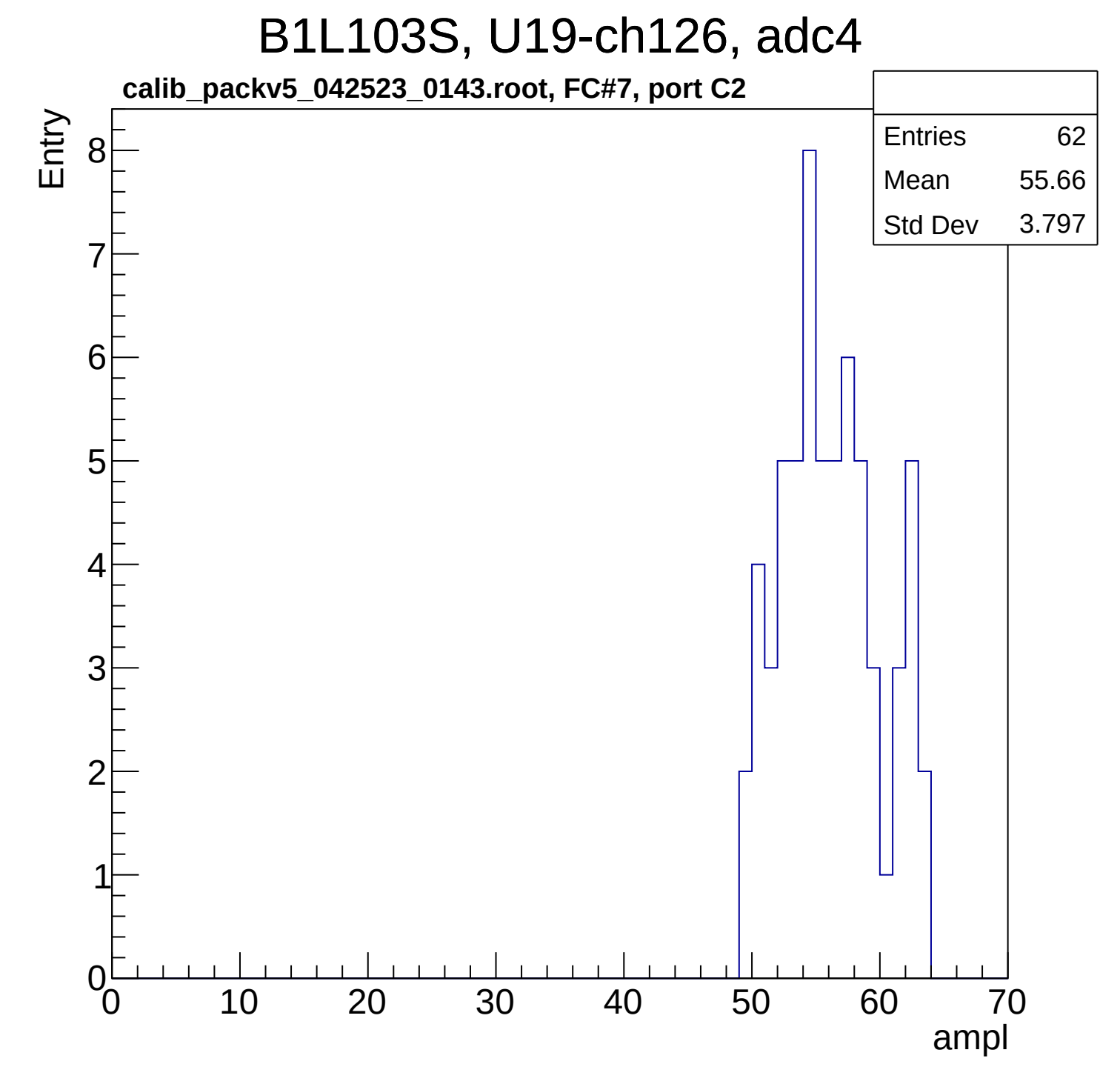
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.66
Std Dev 3.797
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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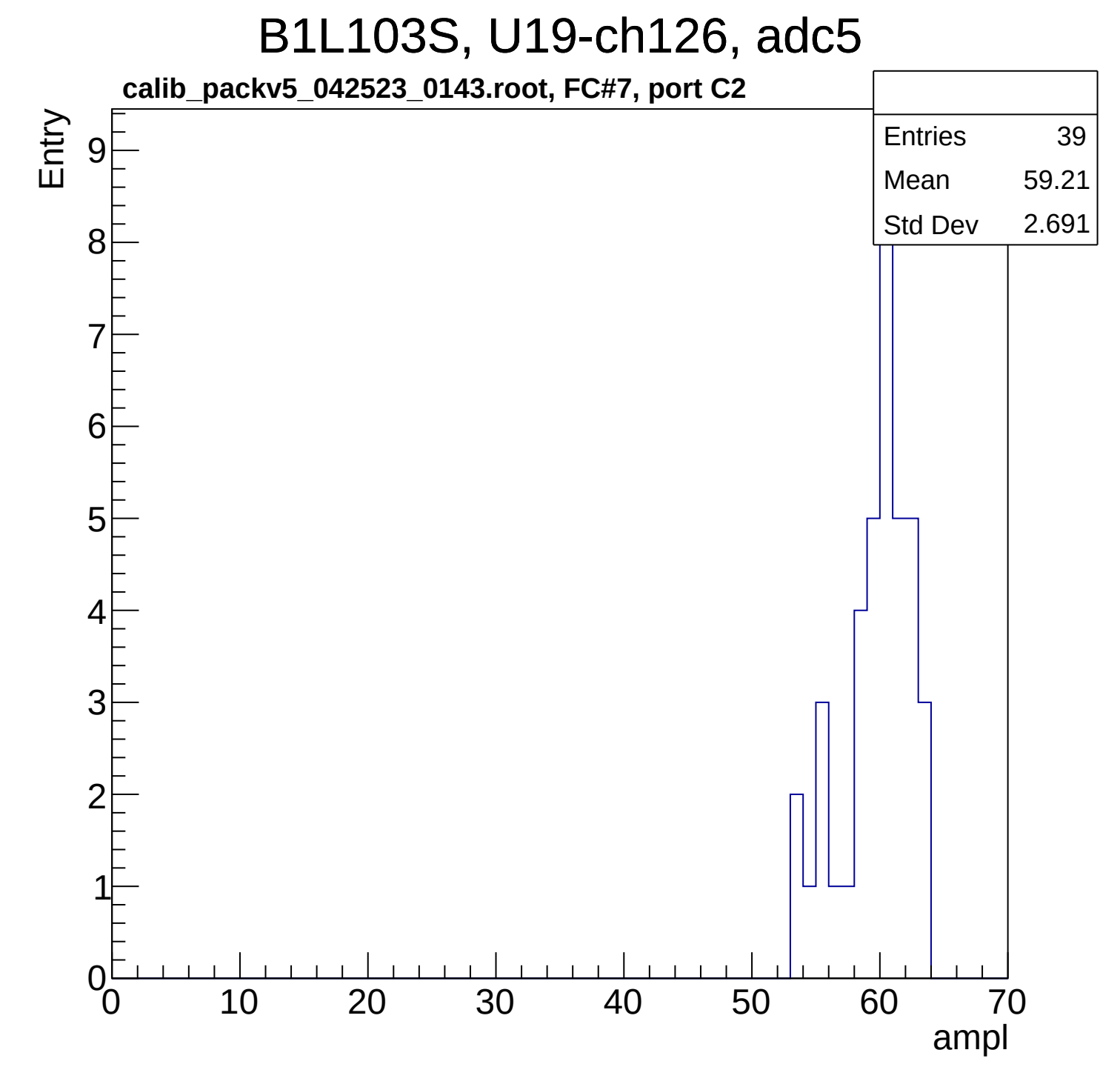
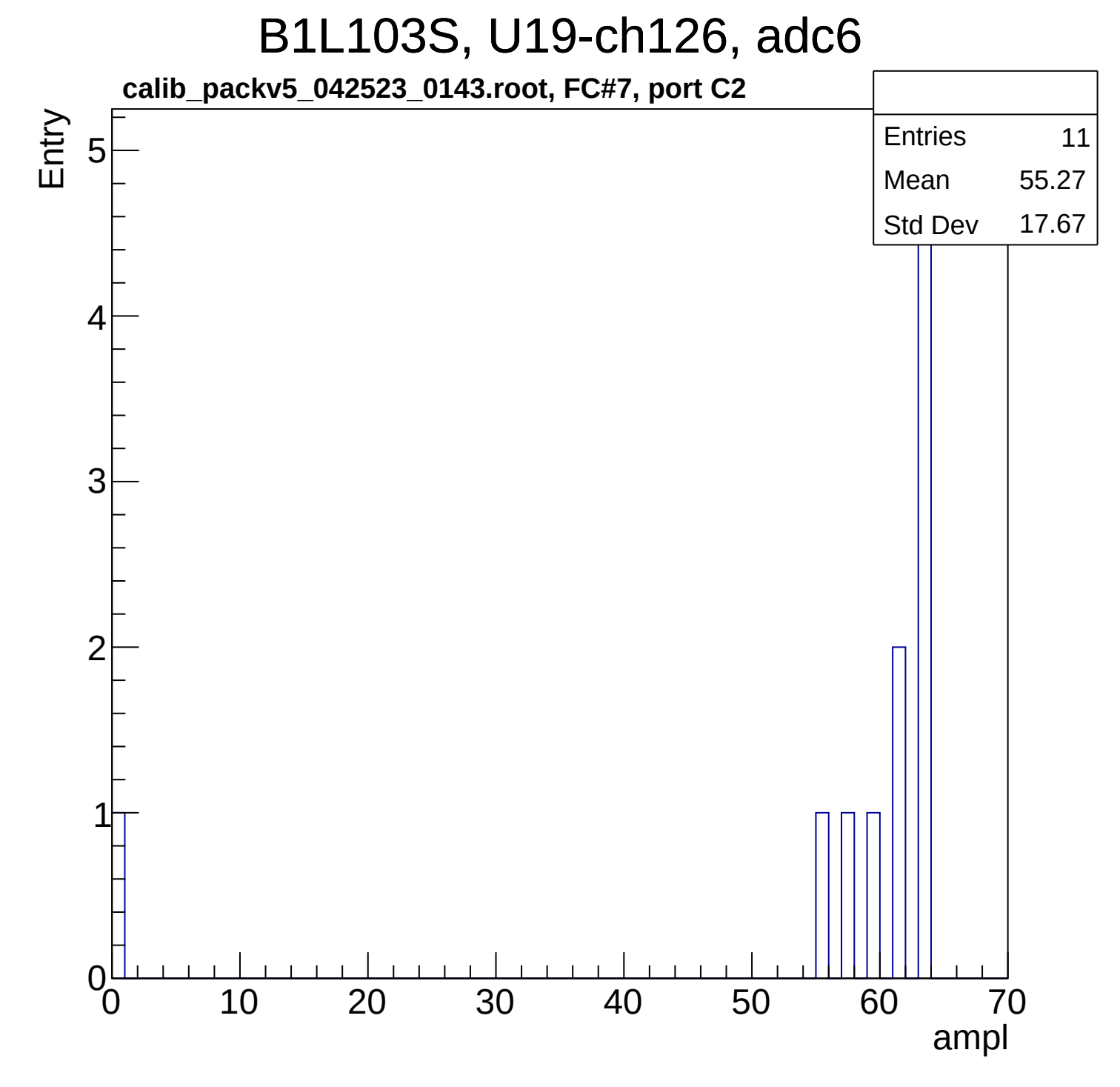
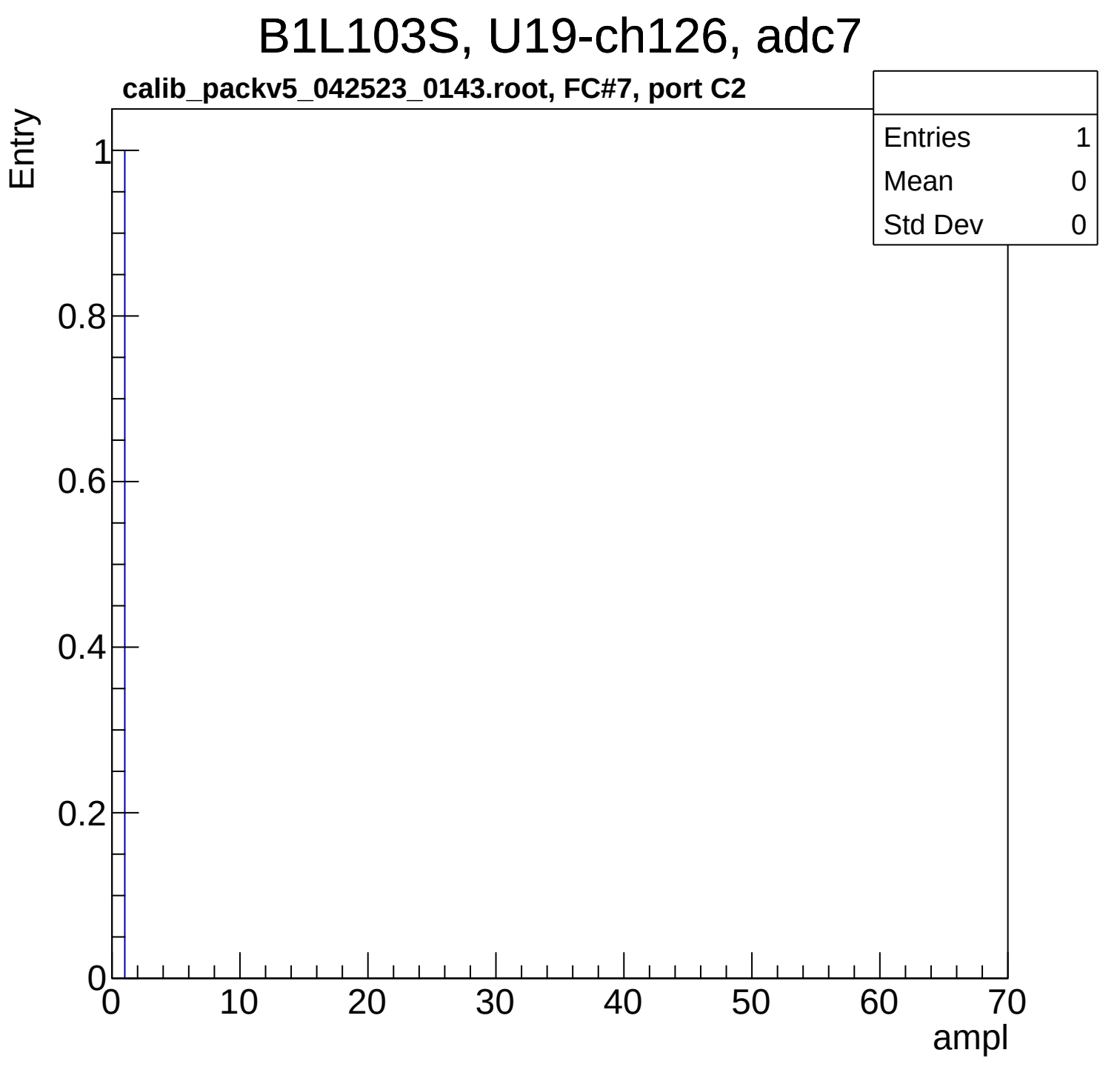

B1L103S, U19-ch126, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 59.21
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 55.27
Std Dev 17.67
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U19-ch126, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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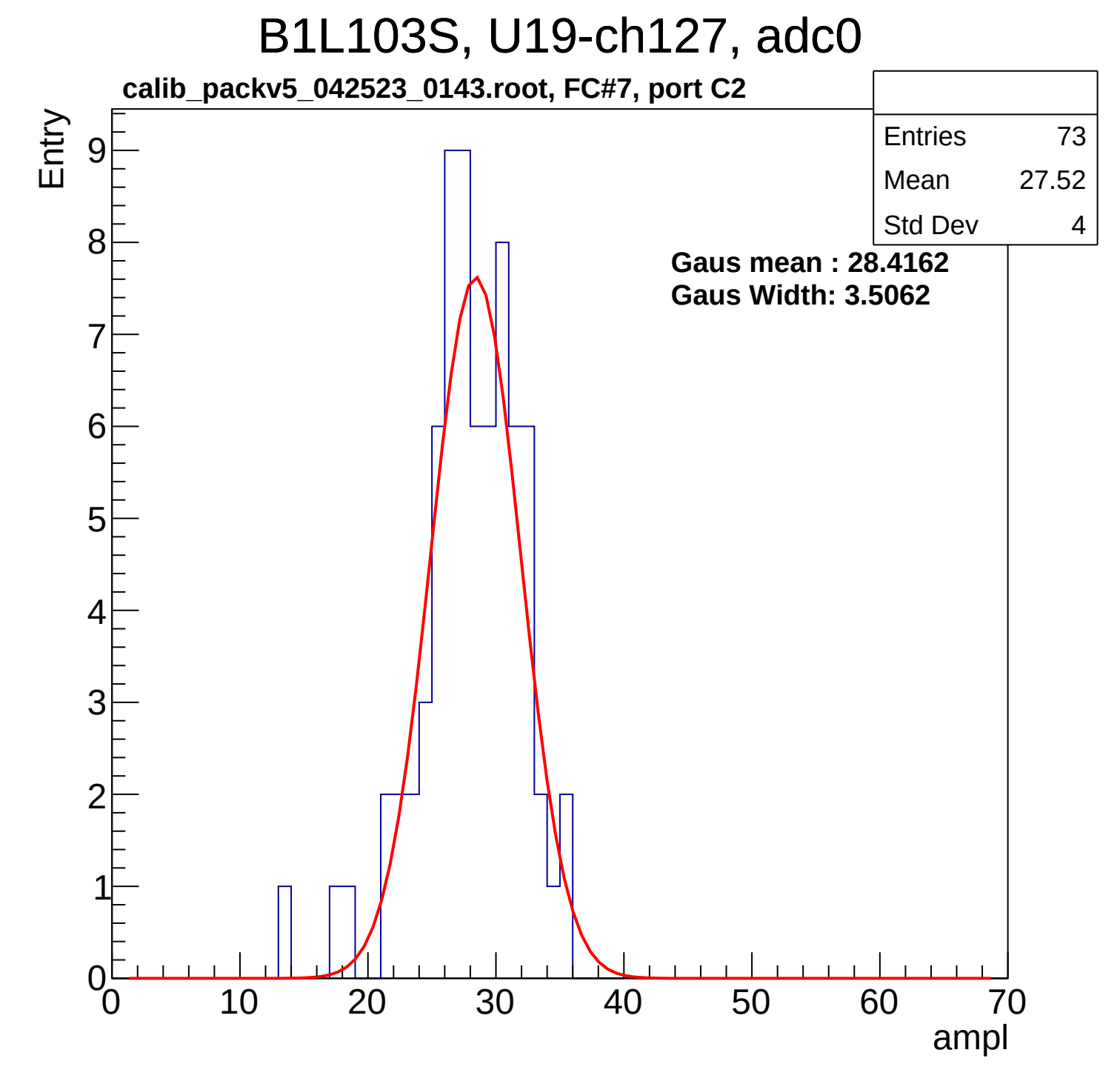
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch127, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 27.52
Std Dev 4
Gaus mean : 28.4162
Gaus Width: 3.5062
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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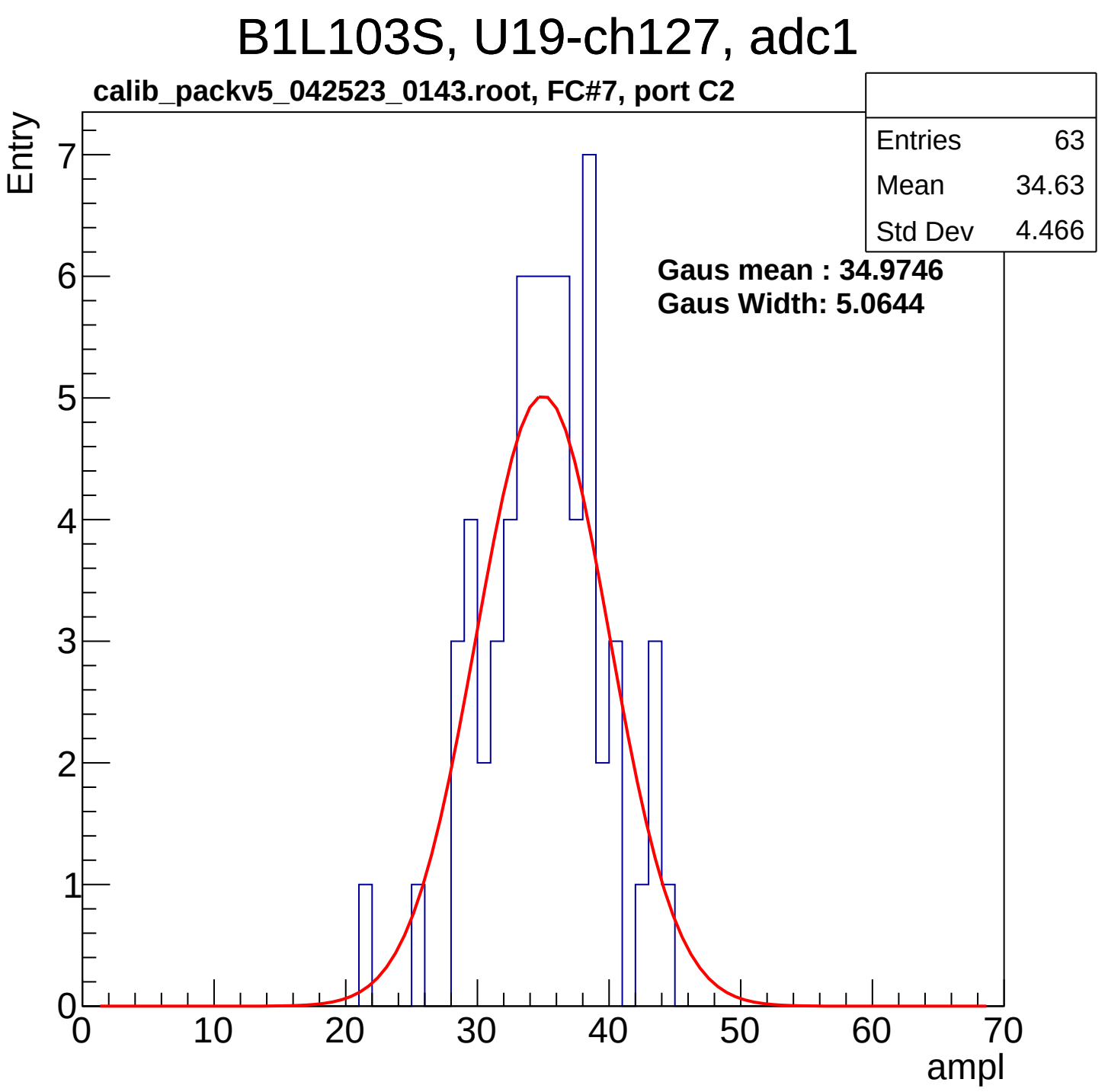
B1L103S, U19-ch127, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 34.63
Std Dev 4.466
Gaus mean : 34.9746
Gaus Width: 5.0644
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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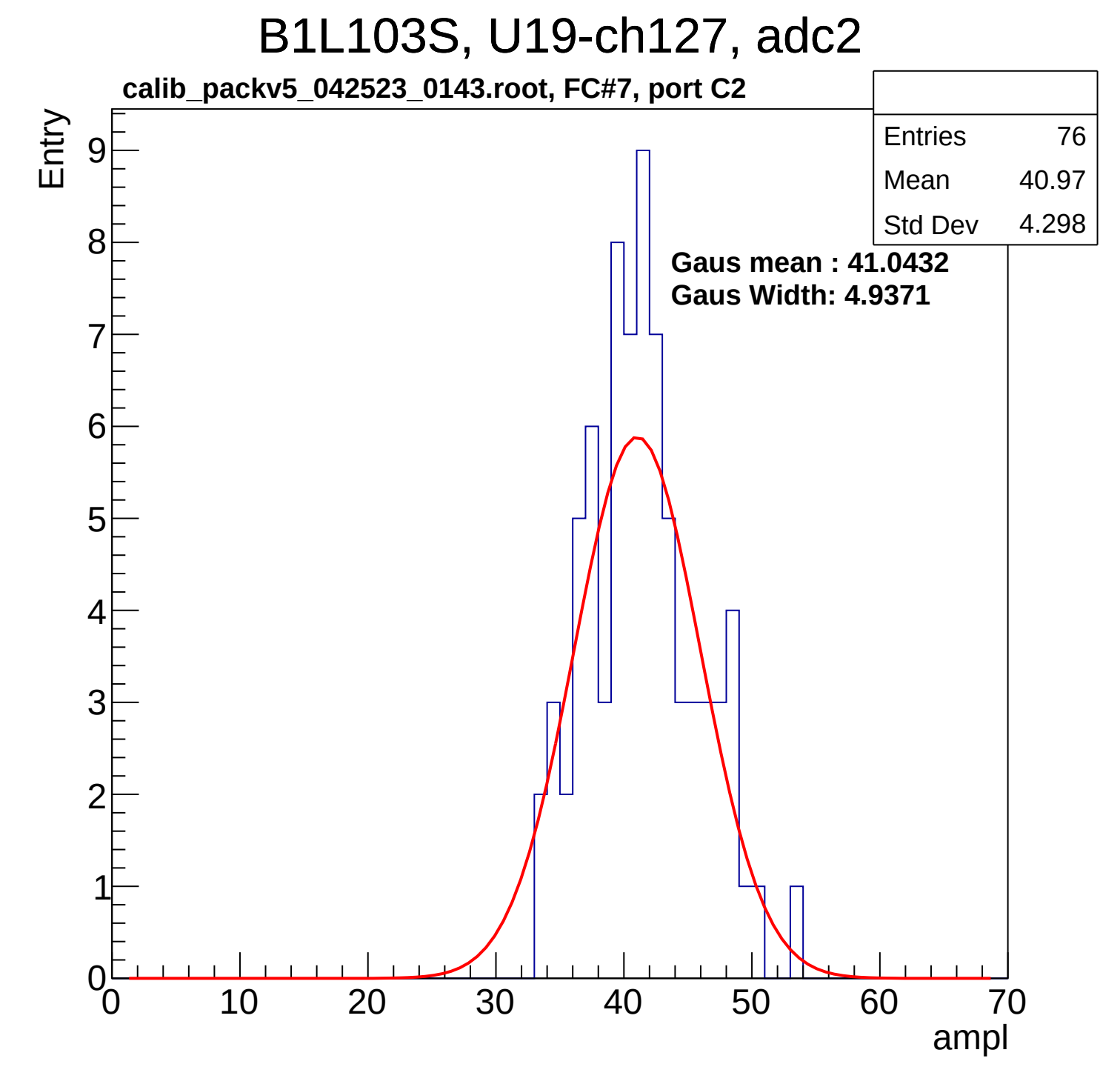
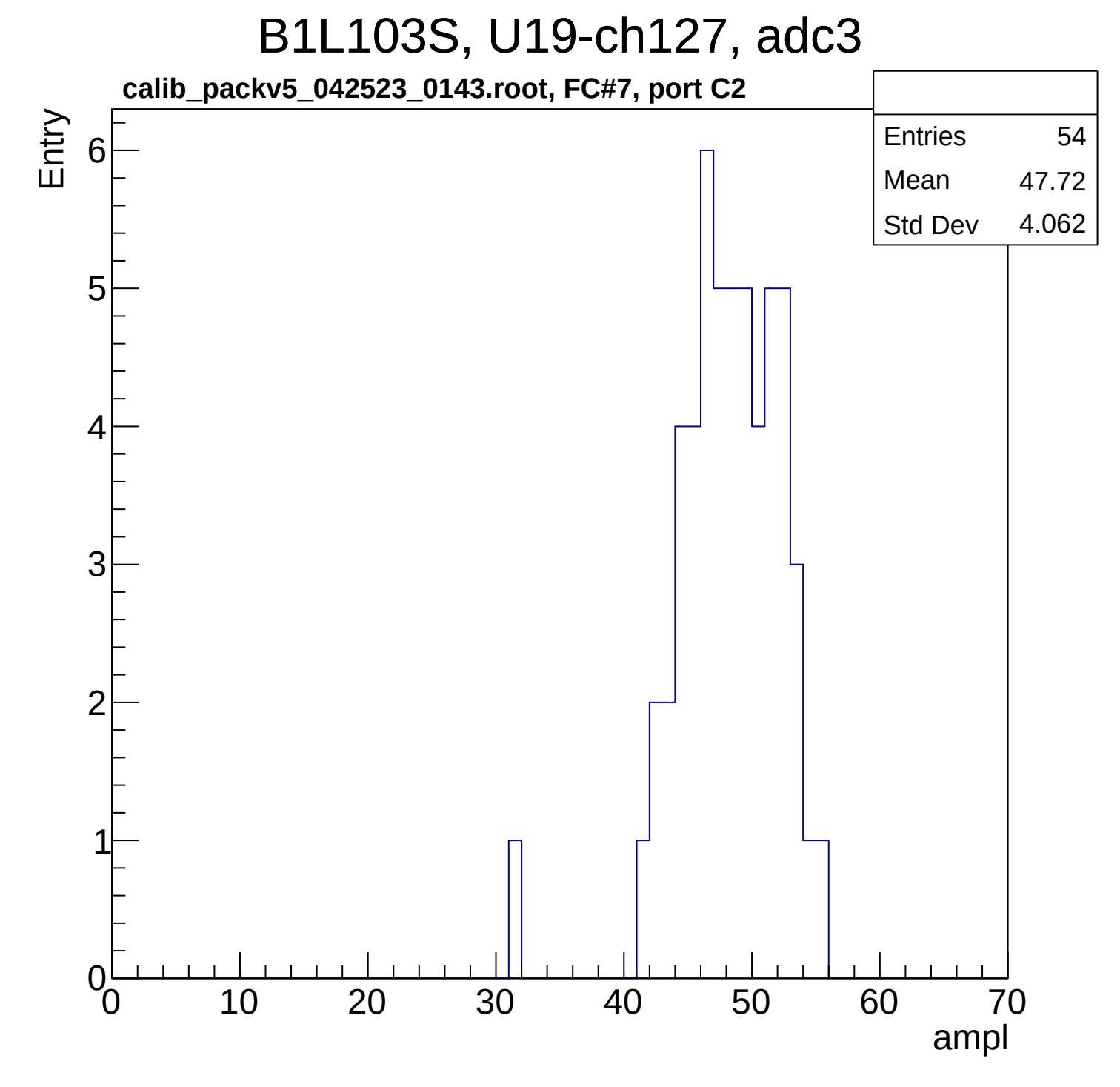
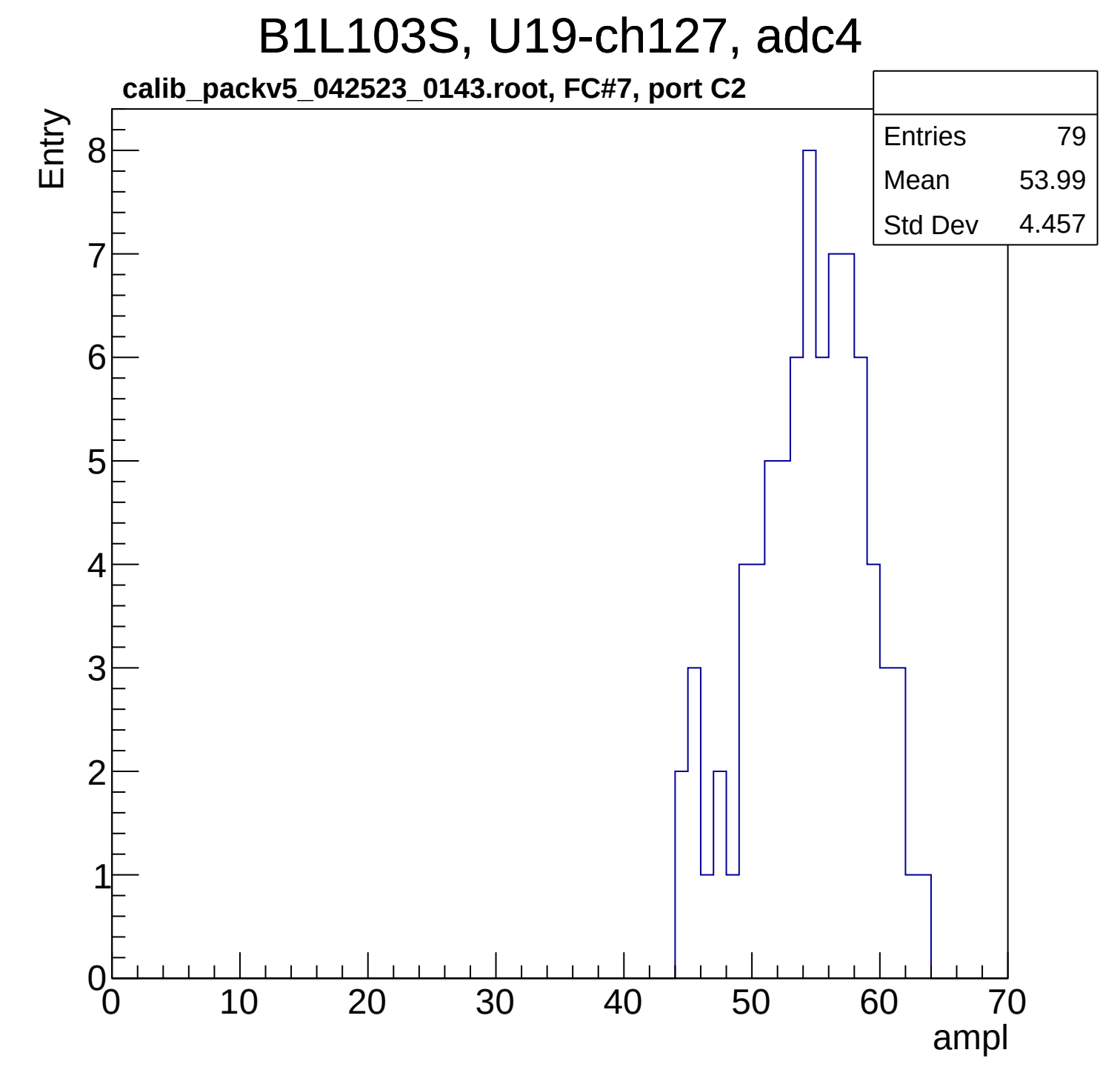
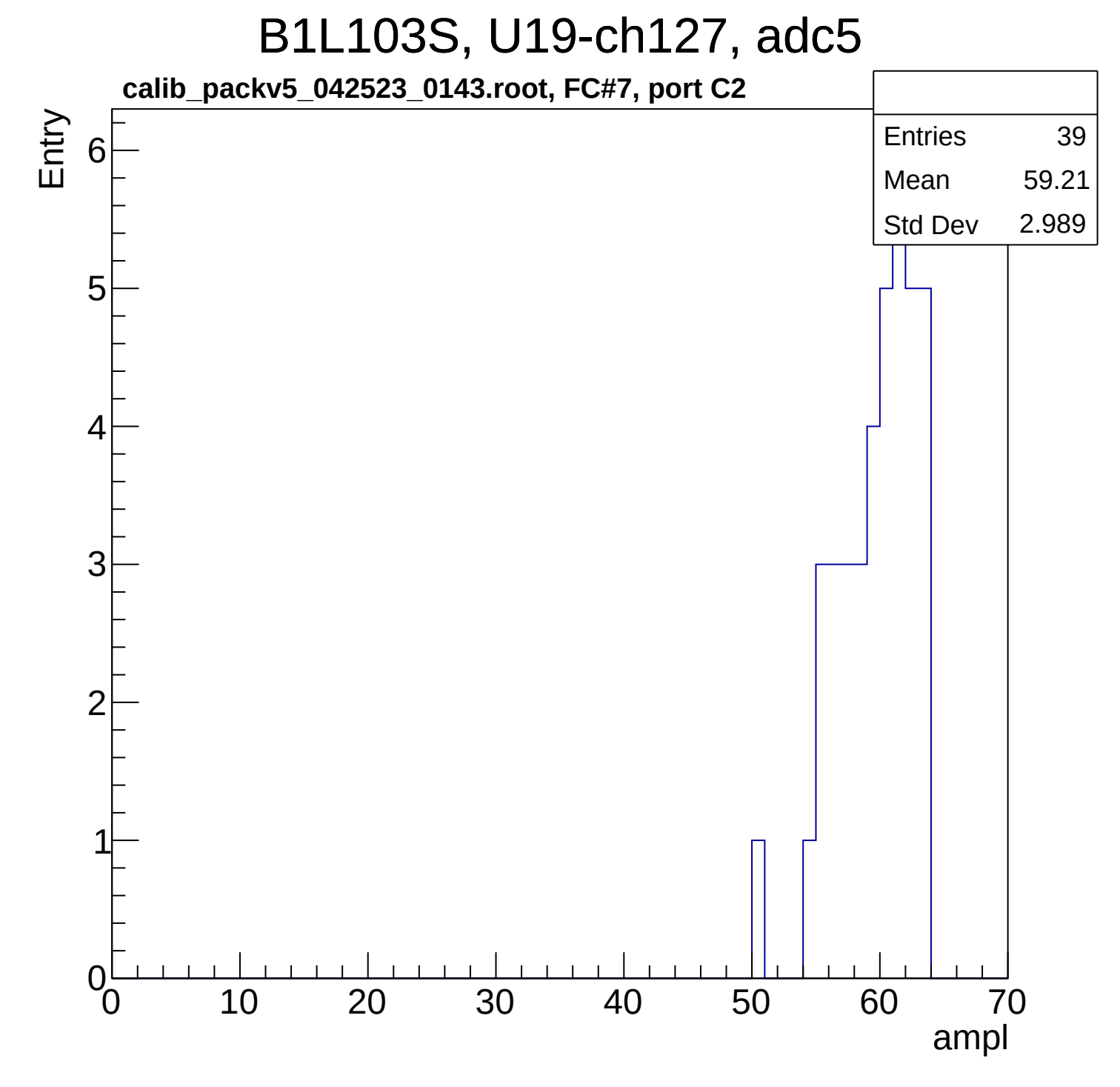
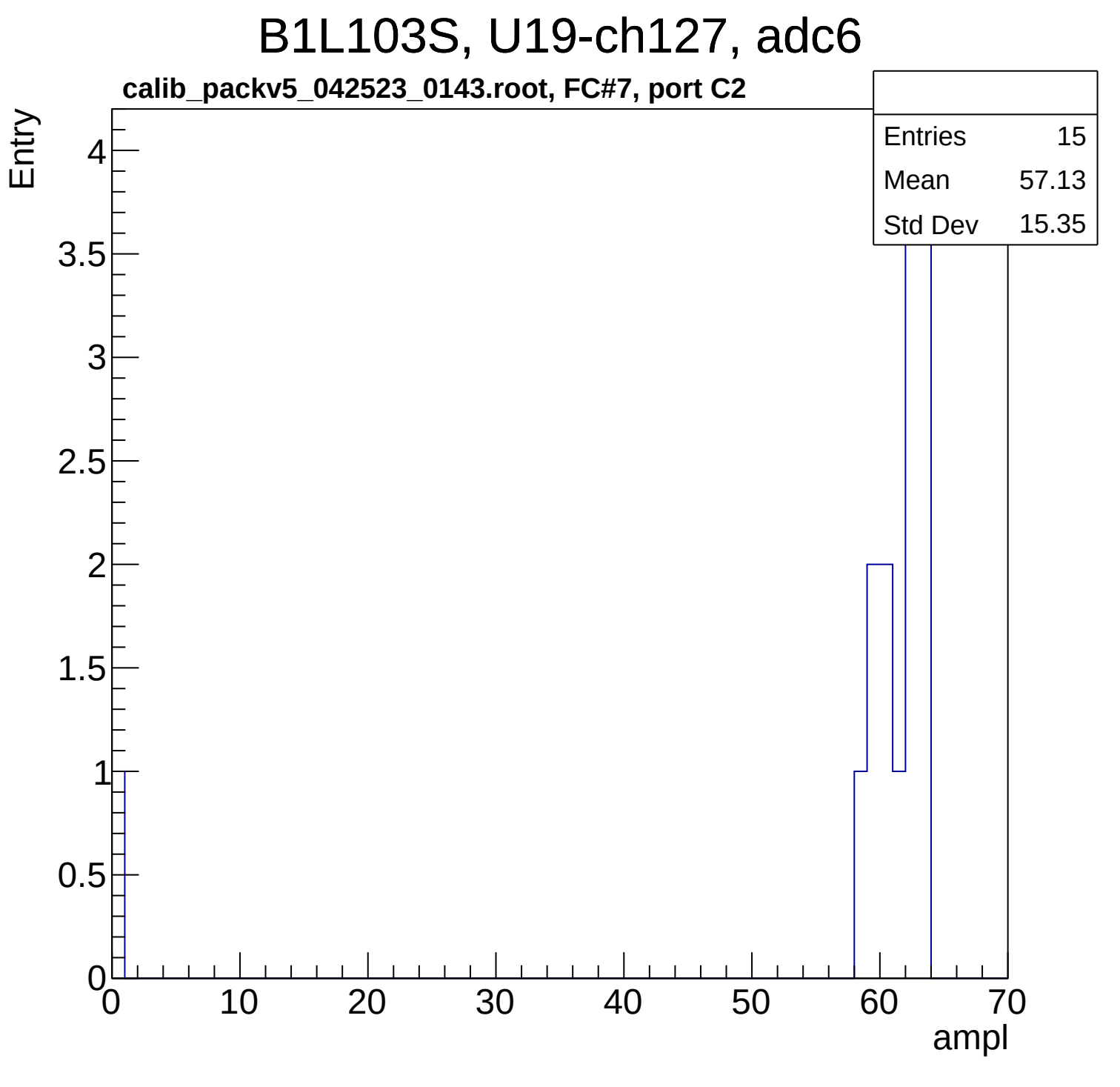
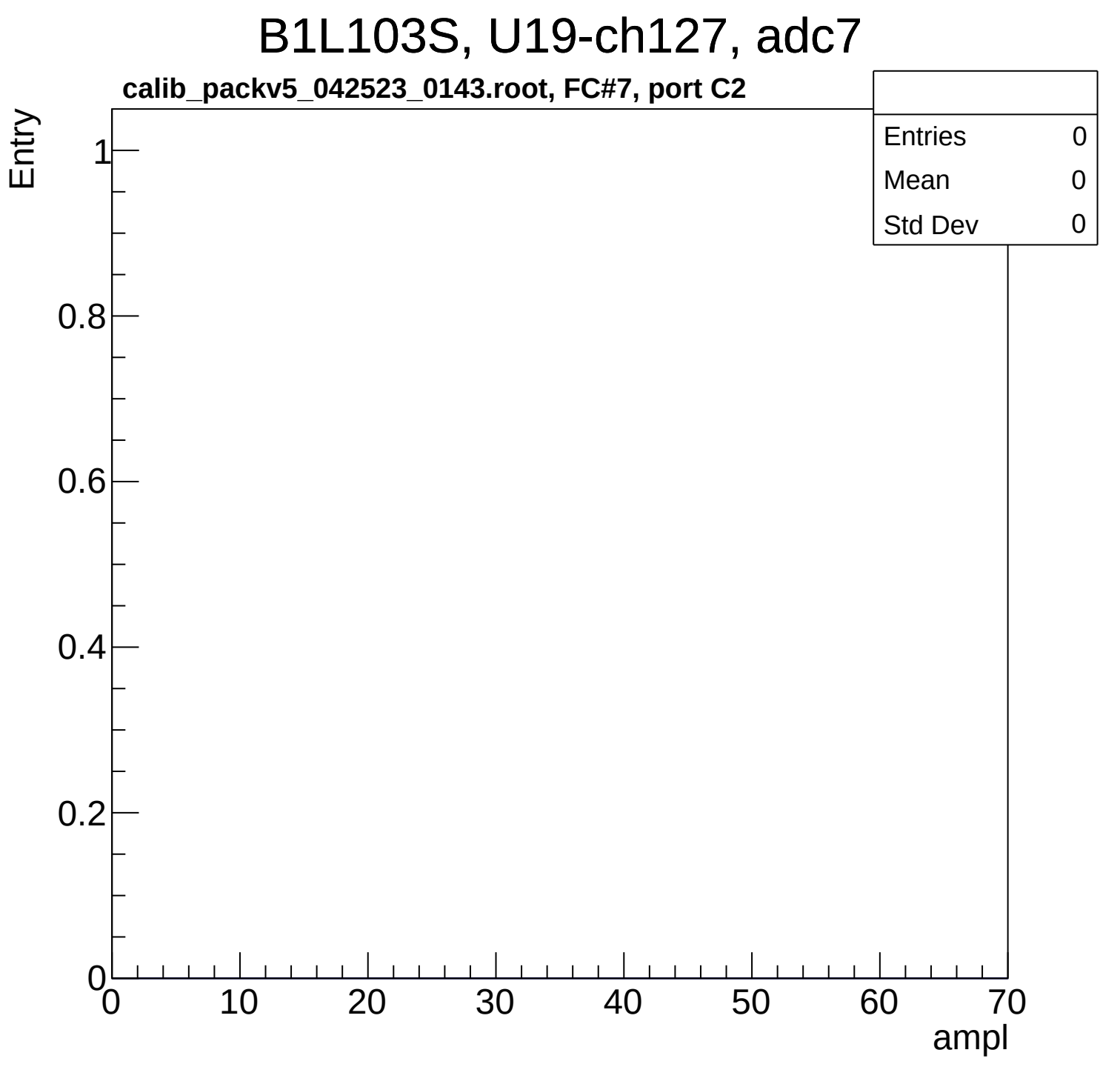

B1L103S, U19-ch127, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 40.97
Std Dev 4.298
Gaus mean : 41.0432
Gaus Width: 4.9371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

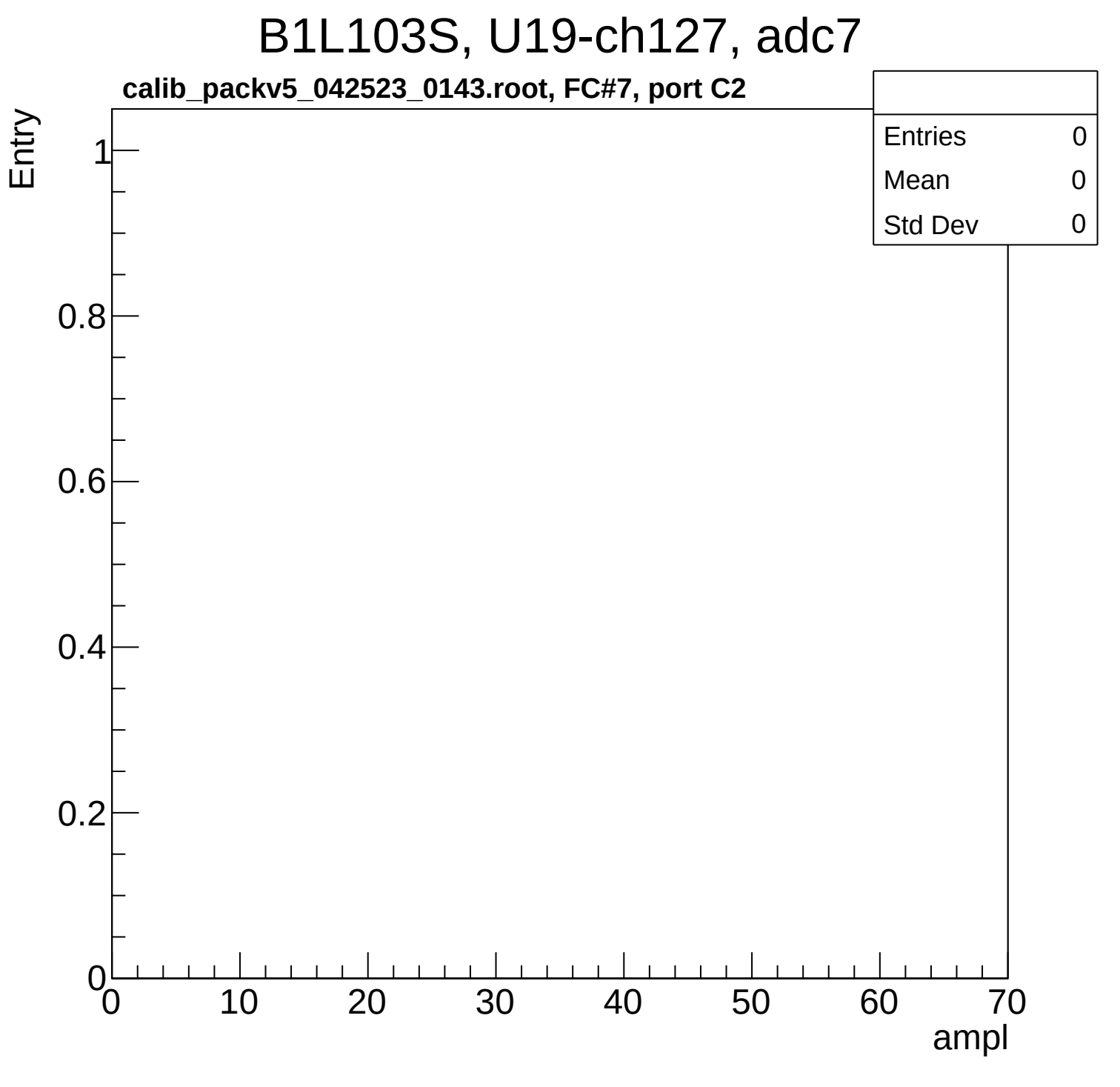

B1L103S, U19-ch127, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 47.72
Std Dev 4.062
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch127, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 53.99
Std Dev 4.457
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U19-ch127, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 59.21
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U19-ch127, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 57.13
Std Dev 15.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U19-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U19-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl