



# B1L002S, U17-ch0, adc0

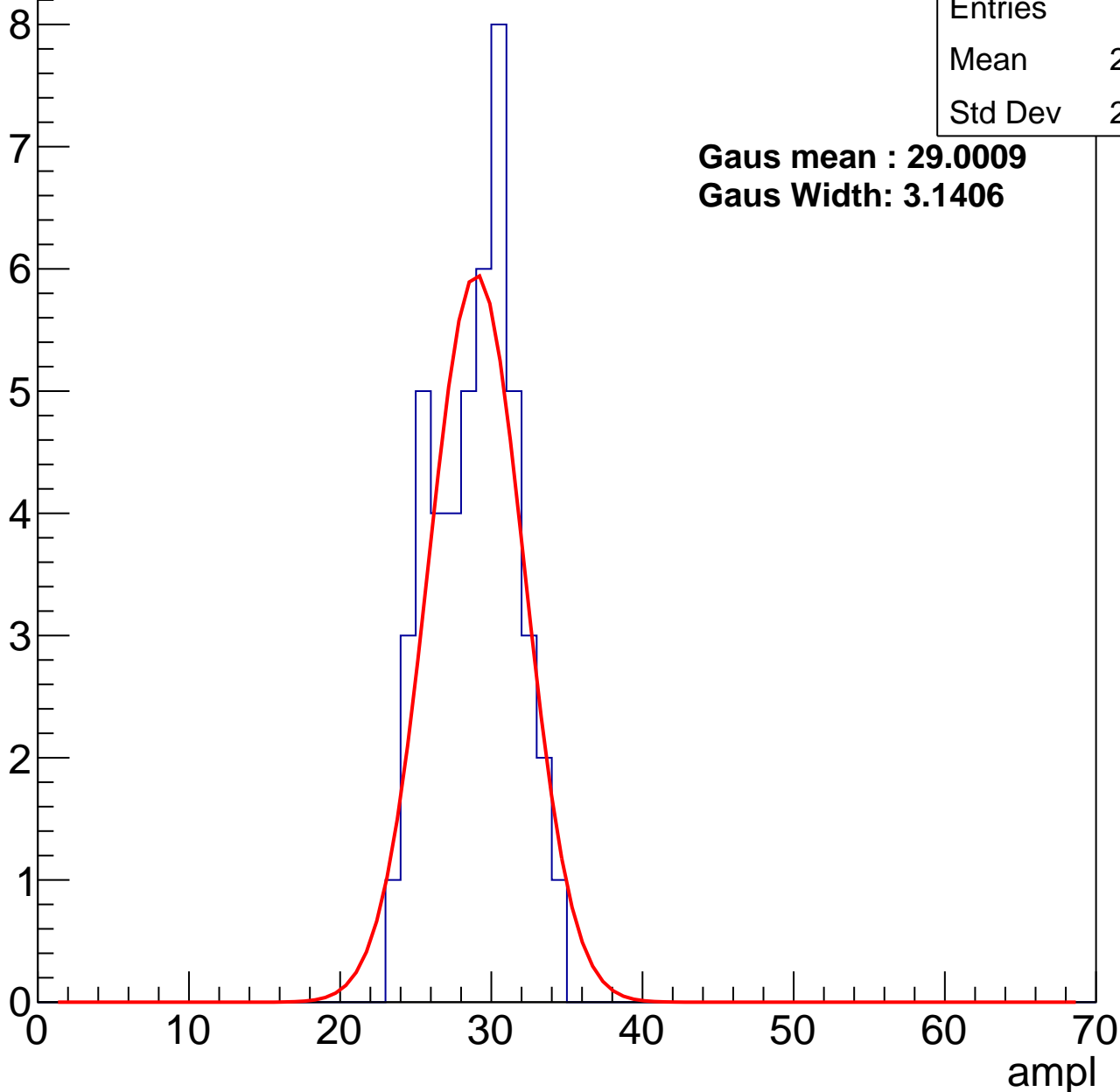
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	28.45
Std Dev	2.712

**Gaus mean : 29.0009**

**Gaus Width: 3.1406**



# B1L002S, U17-ch0, adc1

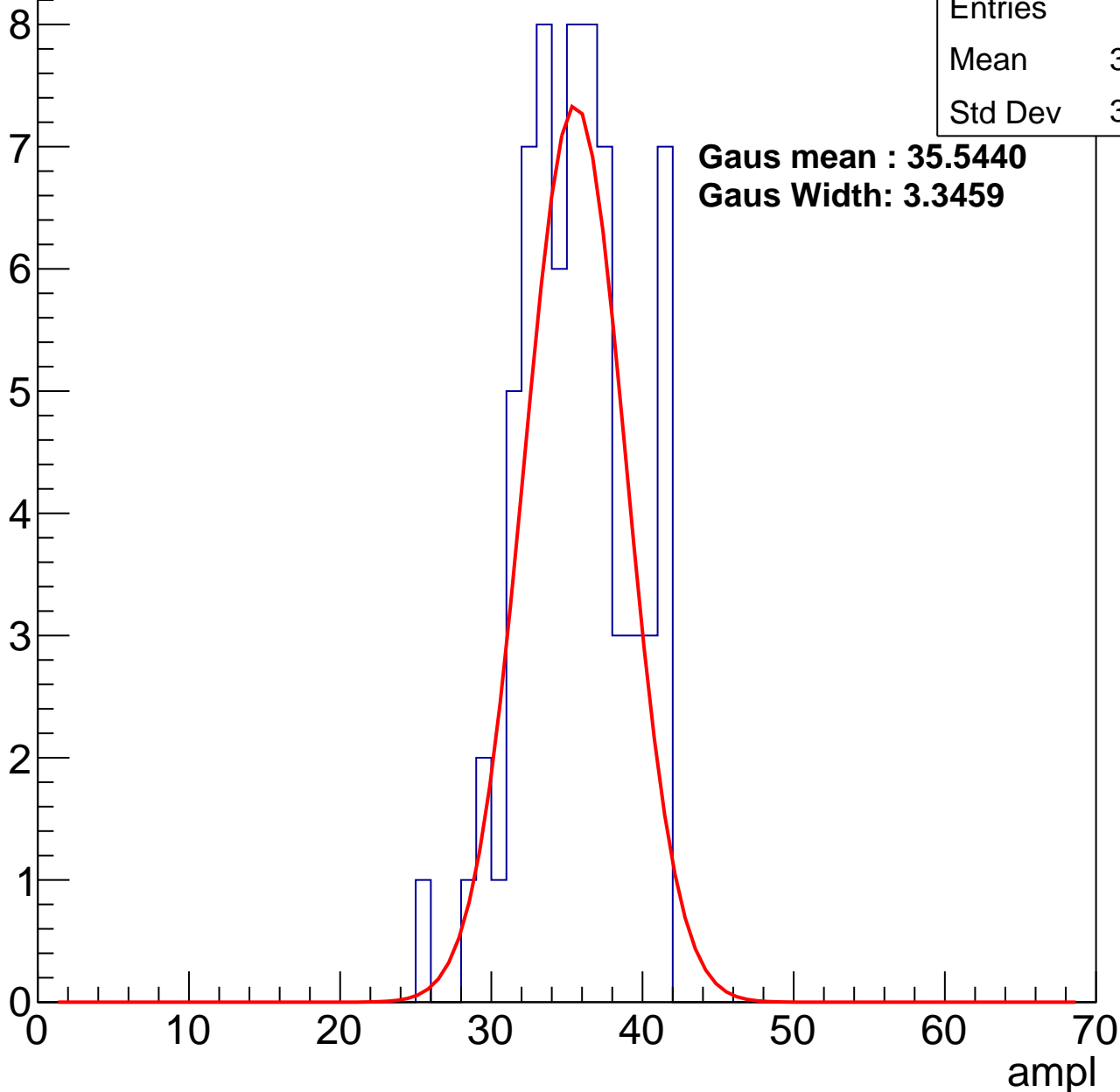
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	35.04
Std Dev	3.515

**Gaus mean : 35.5440**

**Gaus Width: 3.3459**



# B1L002S, U17-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	40.64
Std Dev	3.397

**Gaus mean : 40.6142**

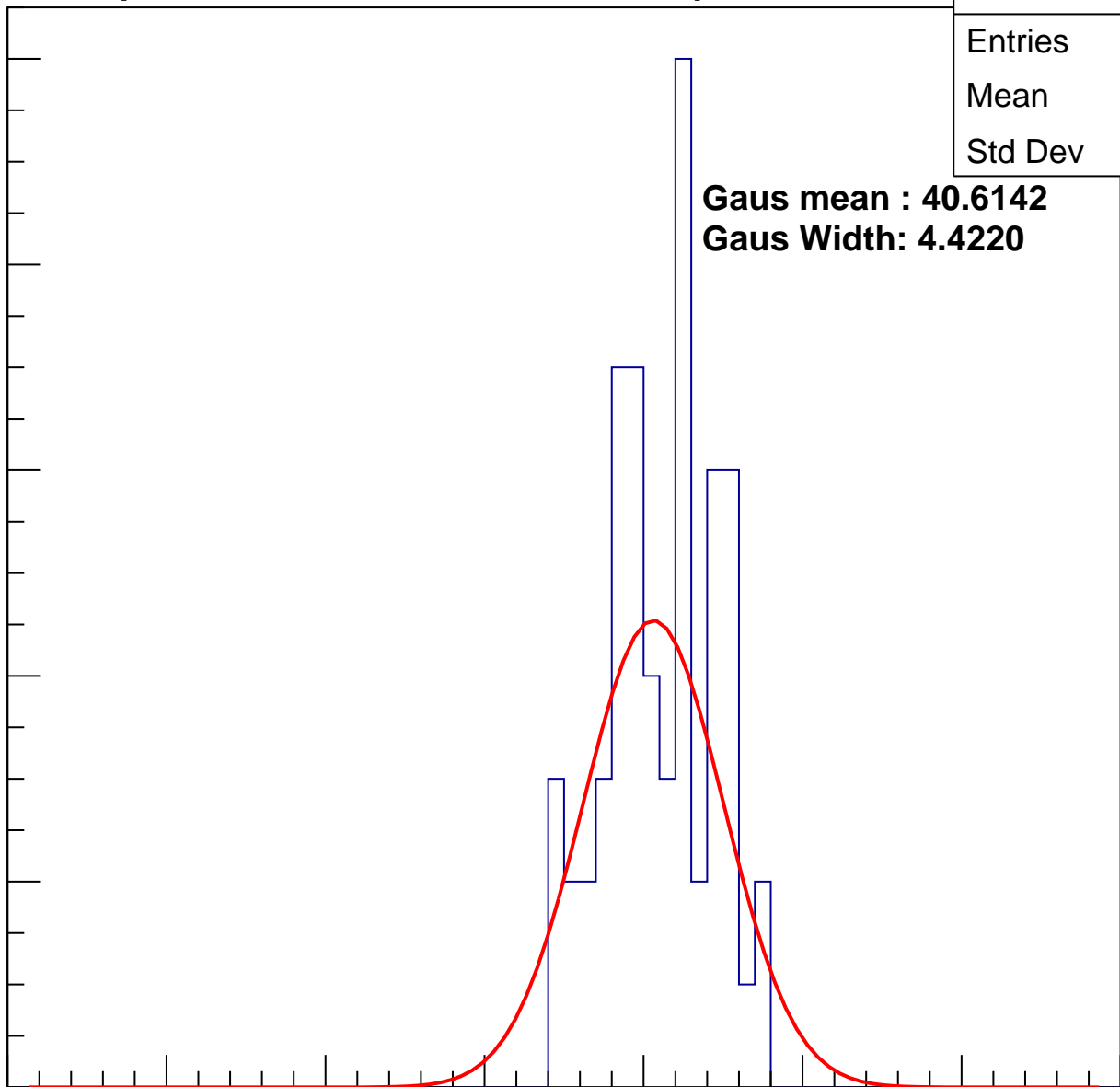
**Gaus Width: 4.4220**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

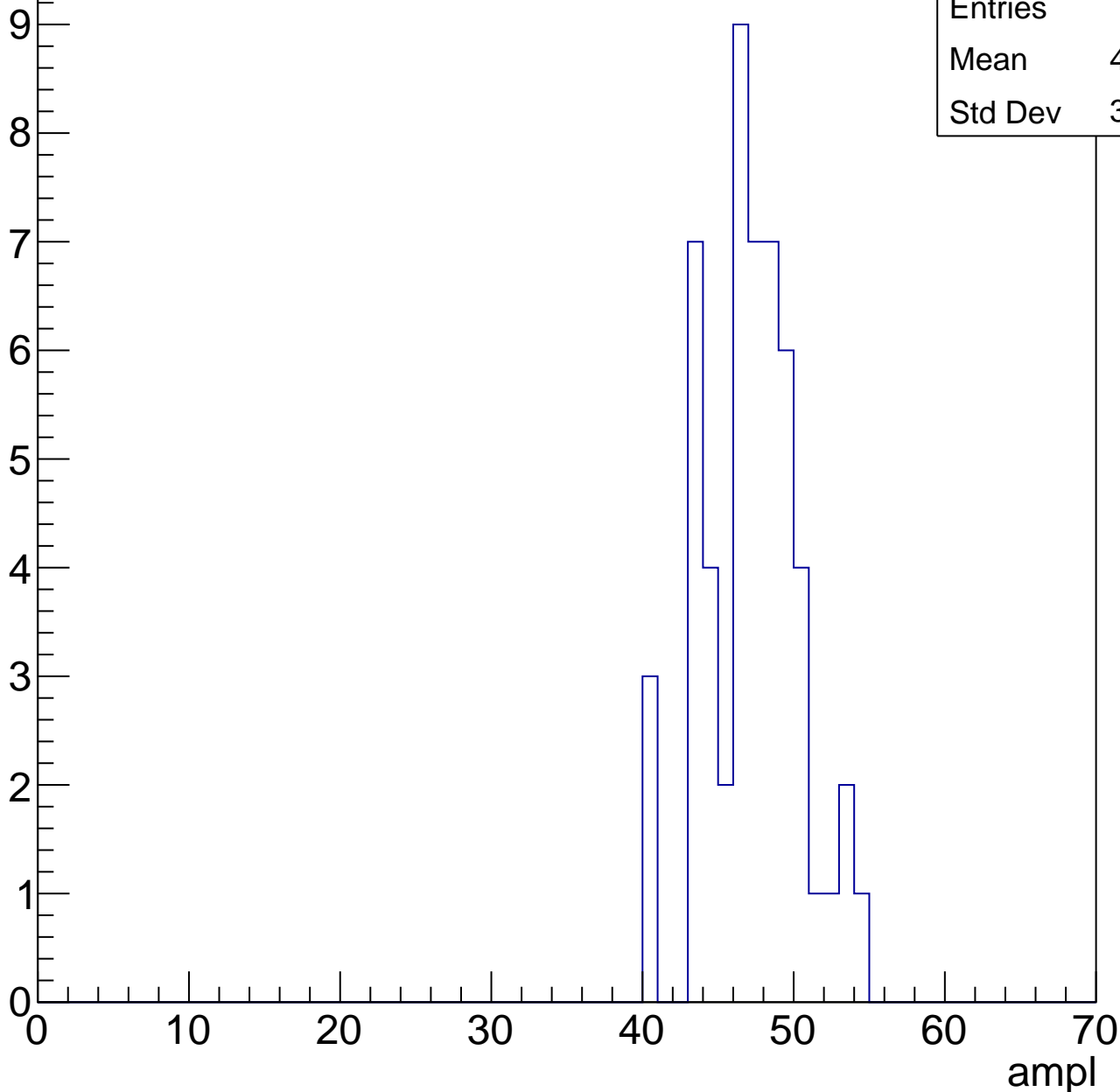


# B1L002S, U17-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	46.72
Std Dev	3.153

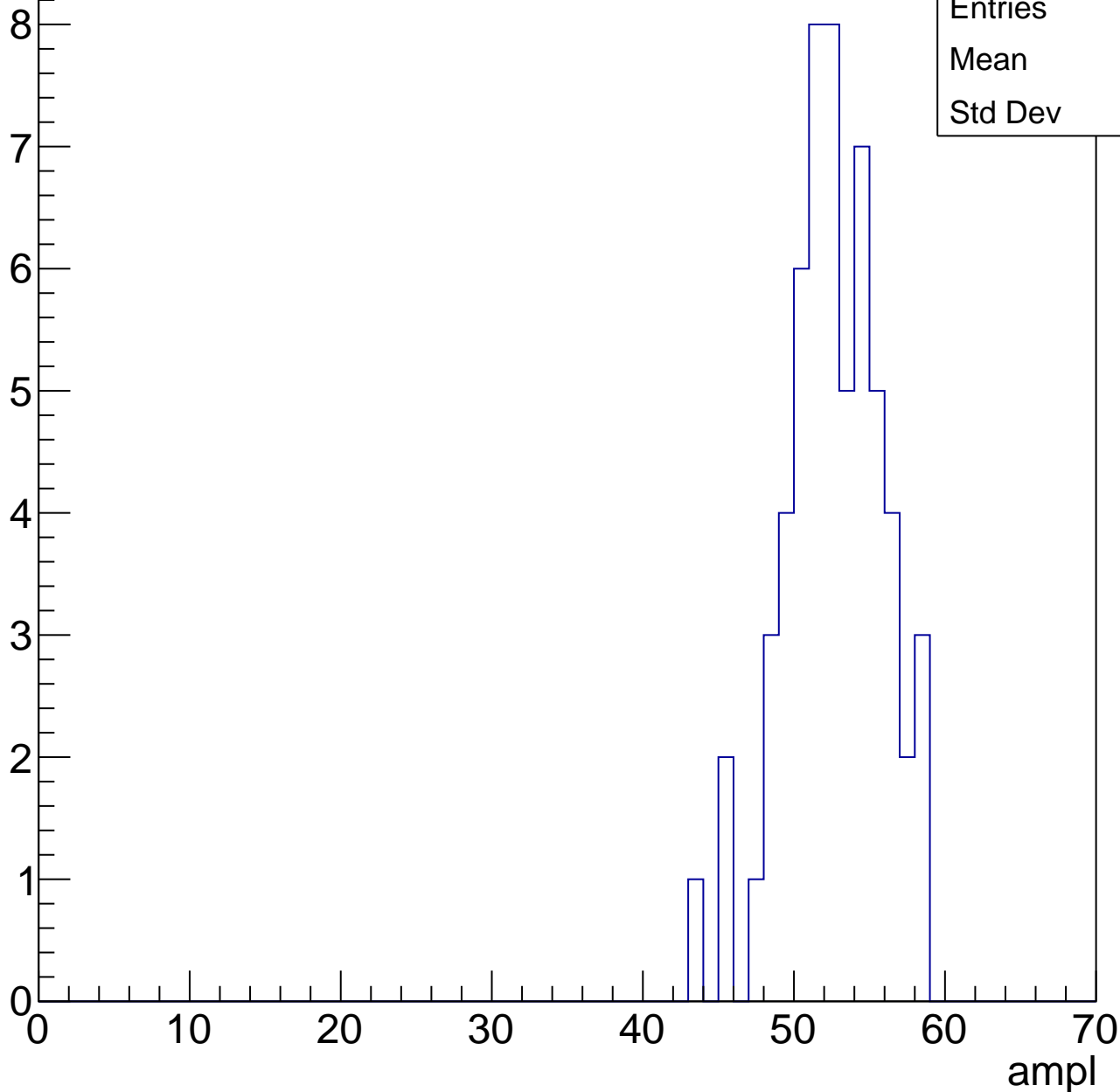


# B1L002S, U17-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	52.1
Std Dev	3.24

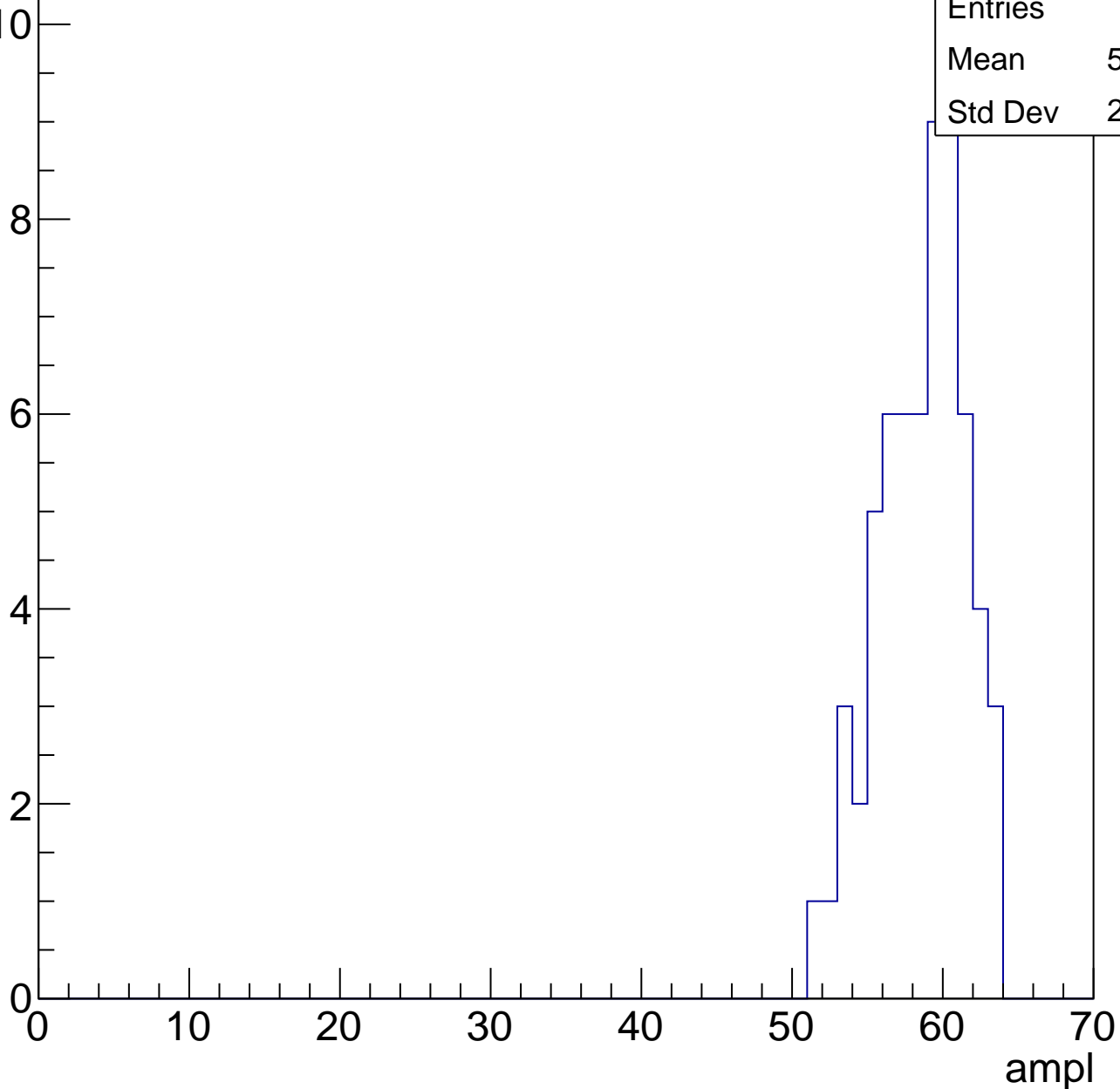


# B1L002S, U17-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.15
Std Dev	2.862

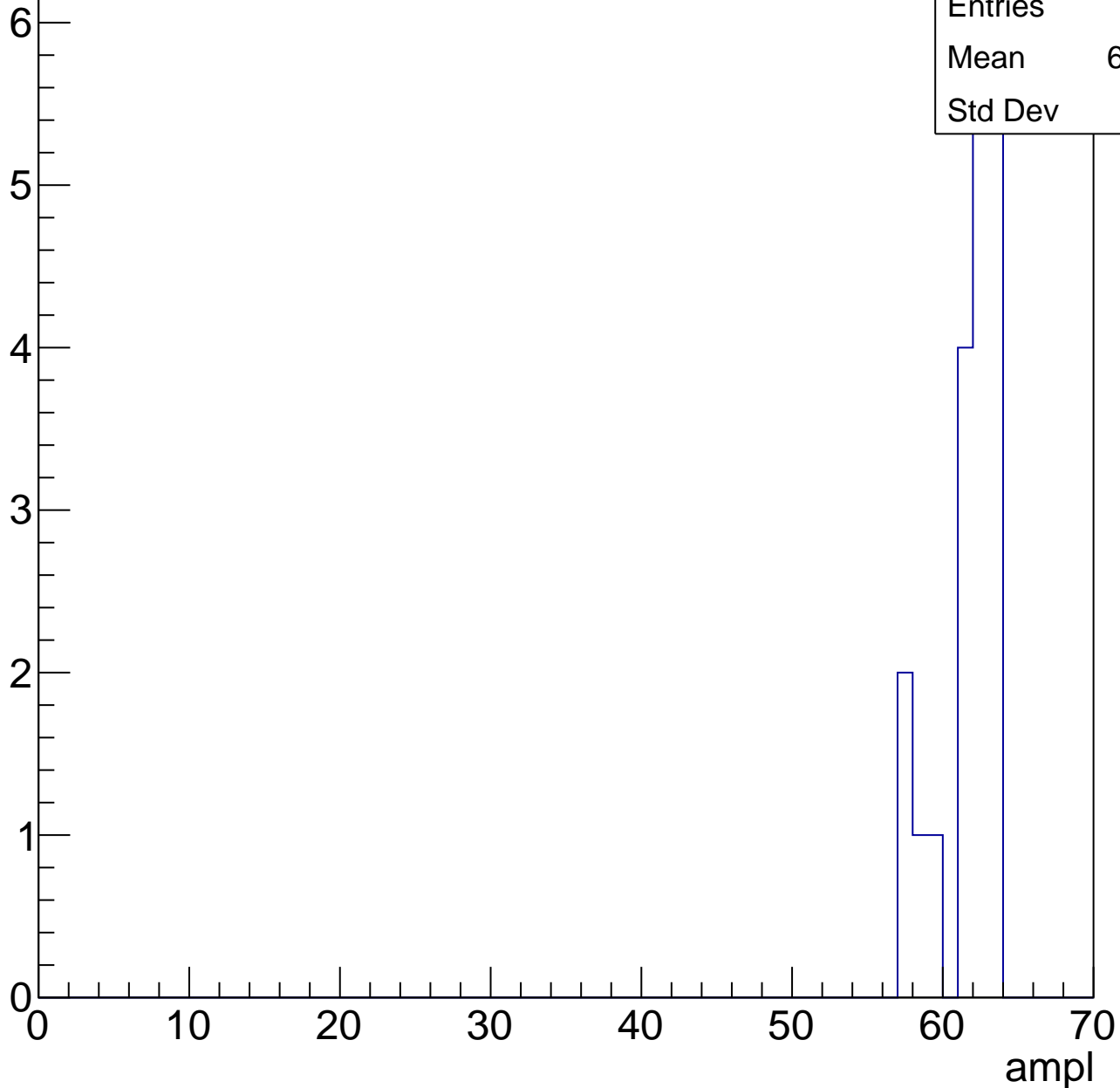


# B1L002S, U17-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.25
Std Dev	1.92

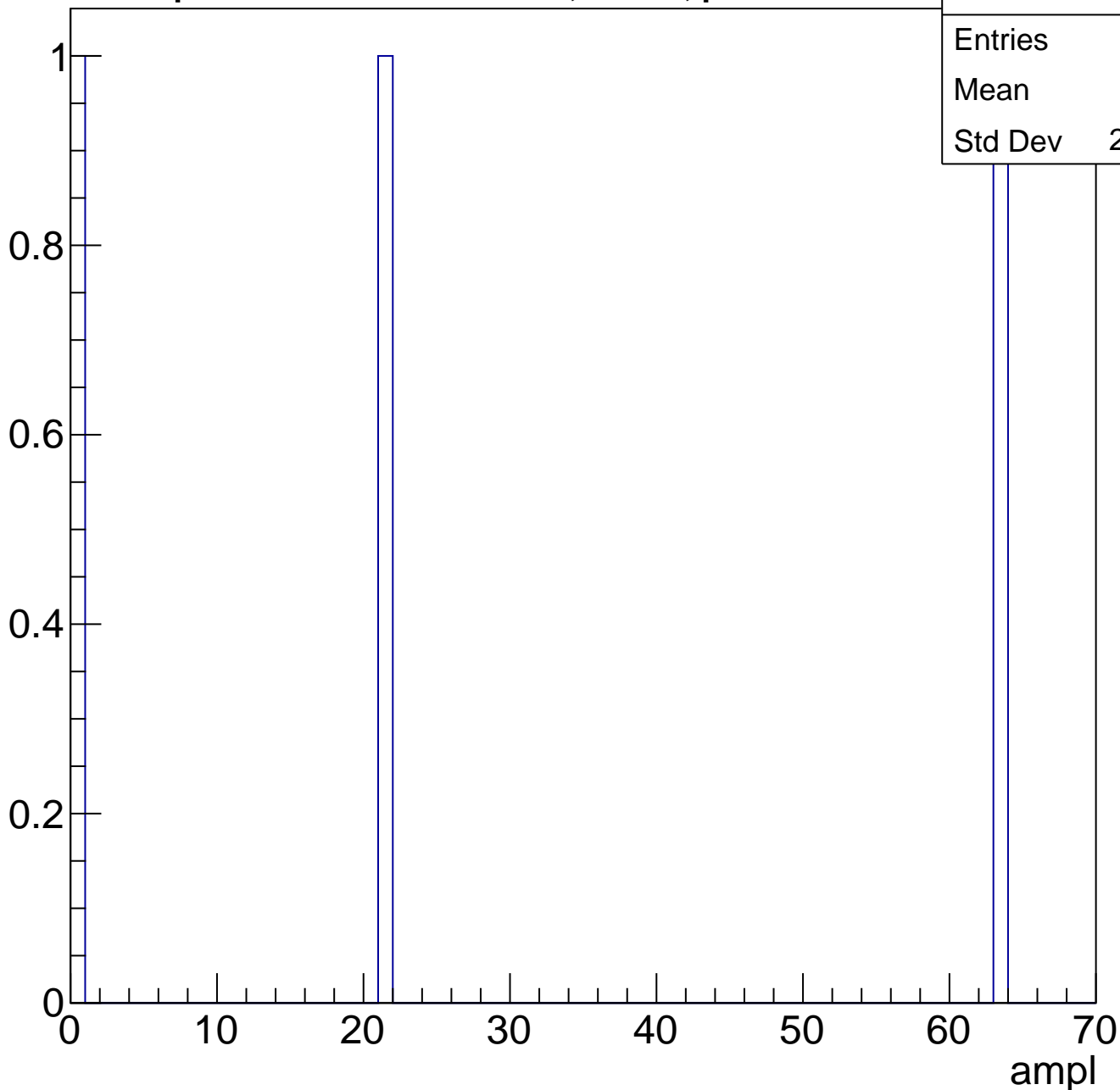




# B1L002S, U17-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L002S, U17-ch1, adc0

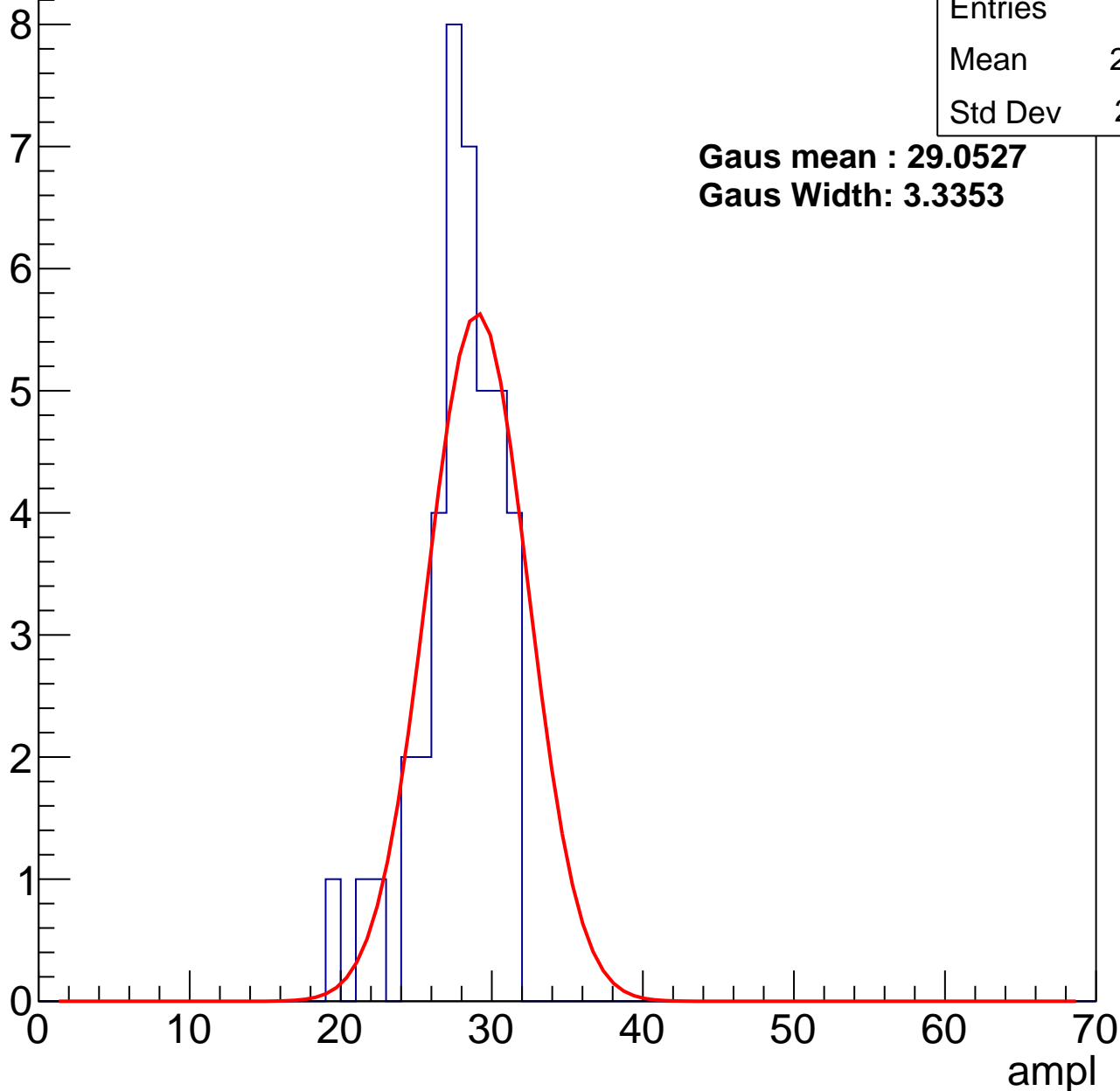
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	27.38
Std Dev	2.671

**Gaus mean : 29.0527**

**Gaus Width: 3.3353**



# B1L002S, U17-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	33.76
Std Dev	3.32

**Gaus mean : 34.0062**

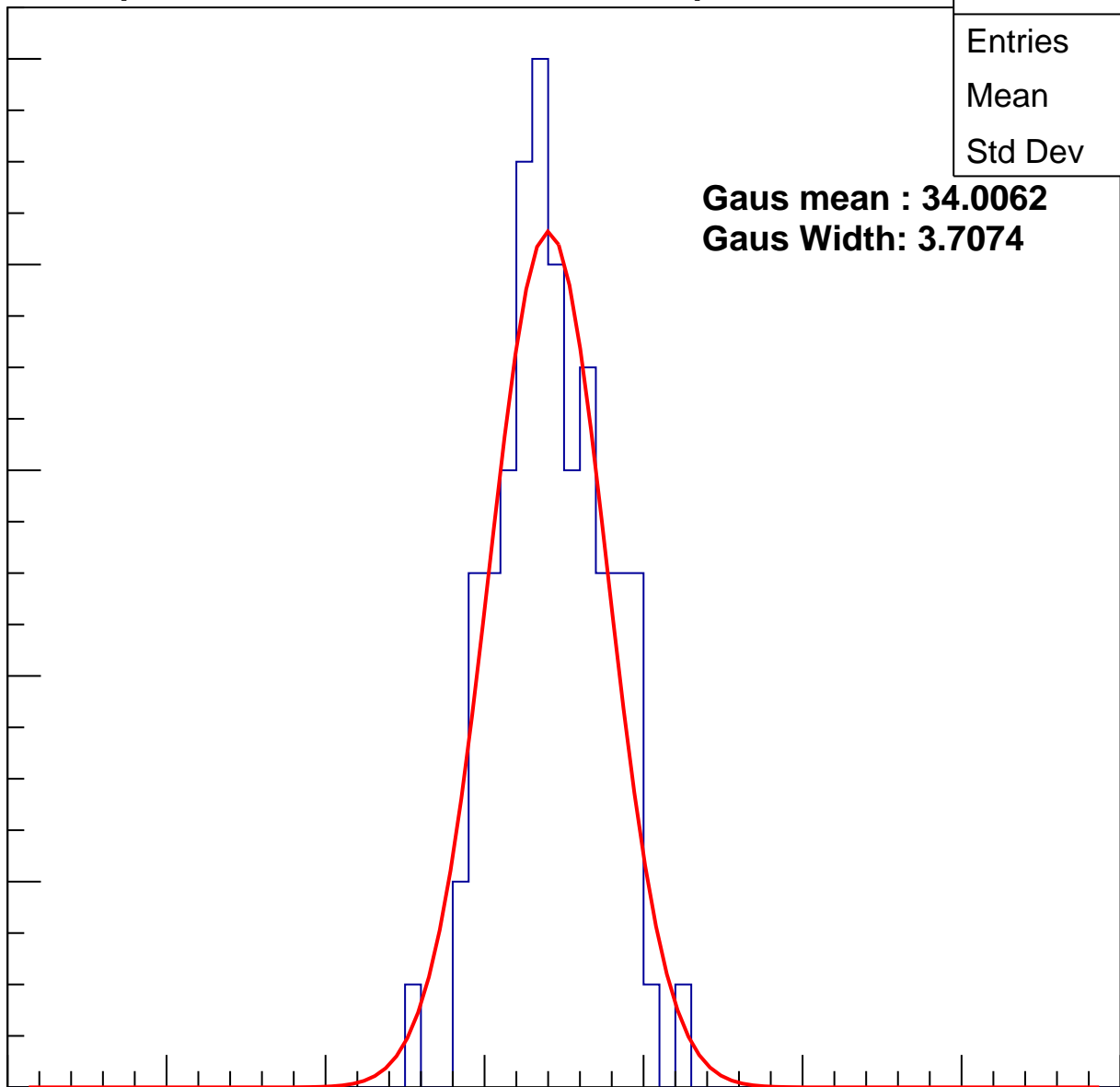
**Gaus Width: 3.7074**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch1, adc2

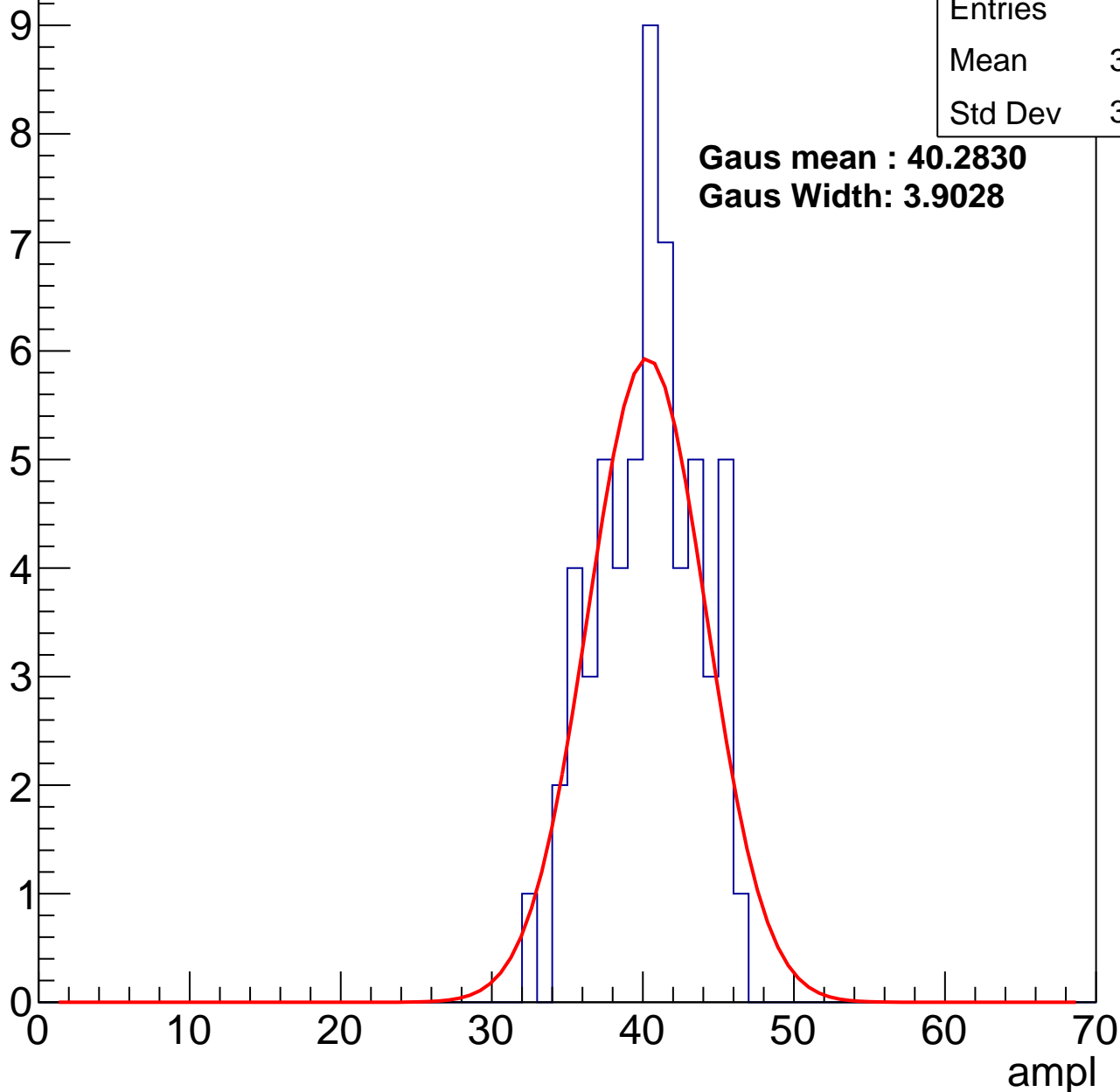
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.88
Std Dev	3.286

**Gaus mean : 40.2830**

**Gaus Width: 3.9028**

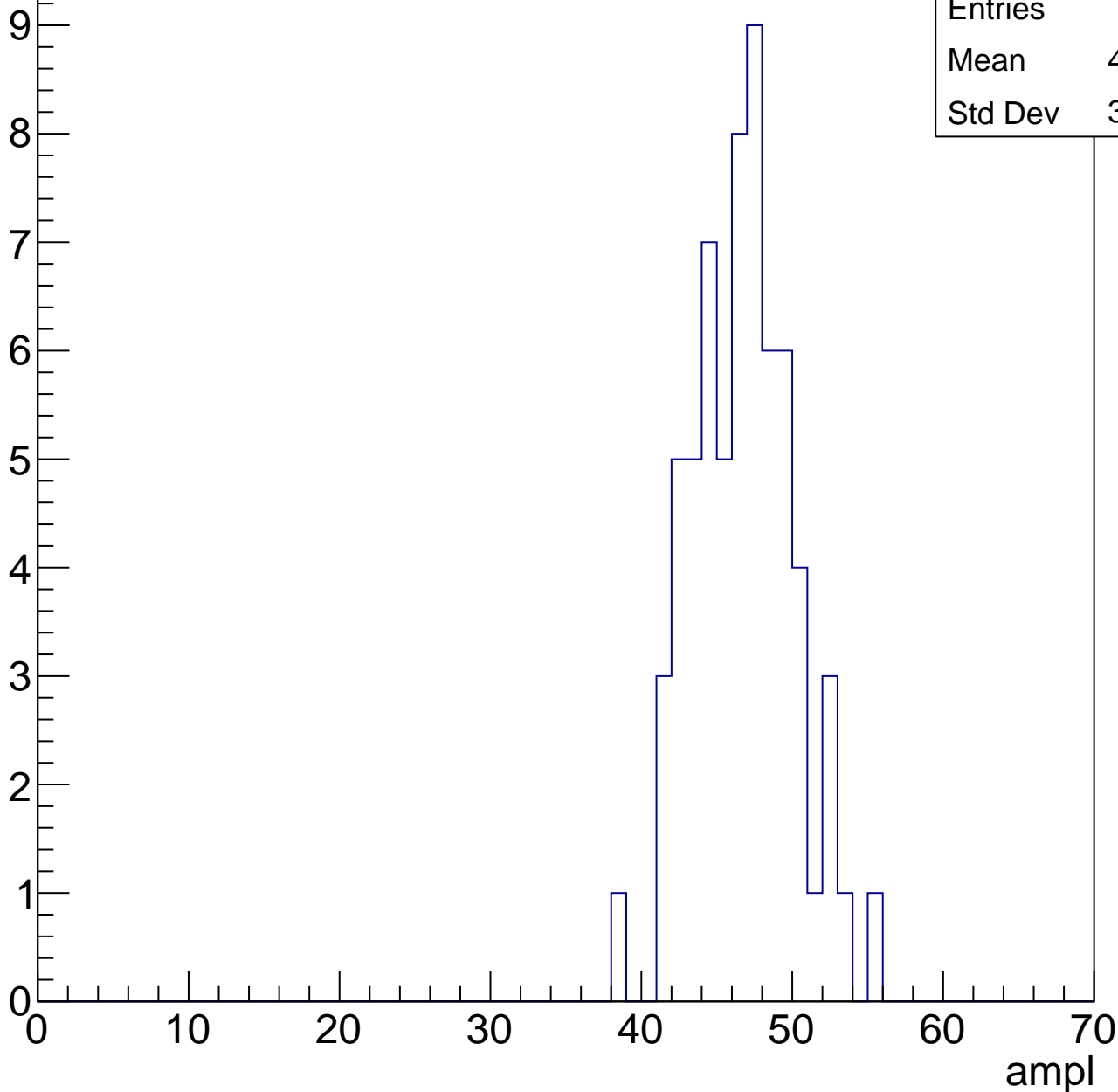


# B1L002S, U17-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.26
Std Dev	3.297

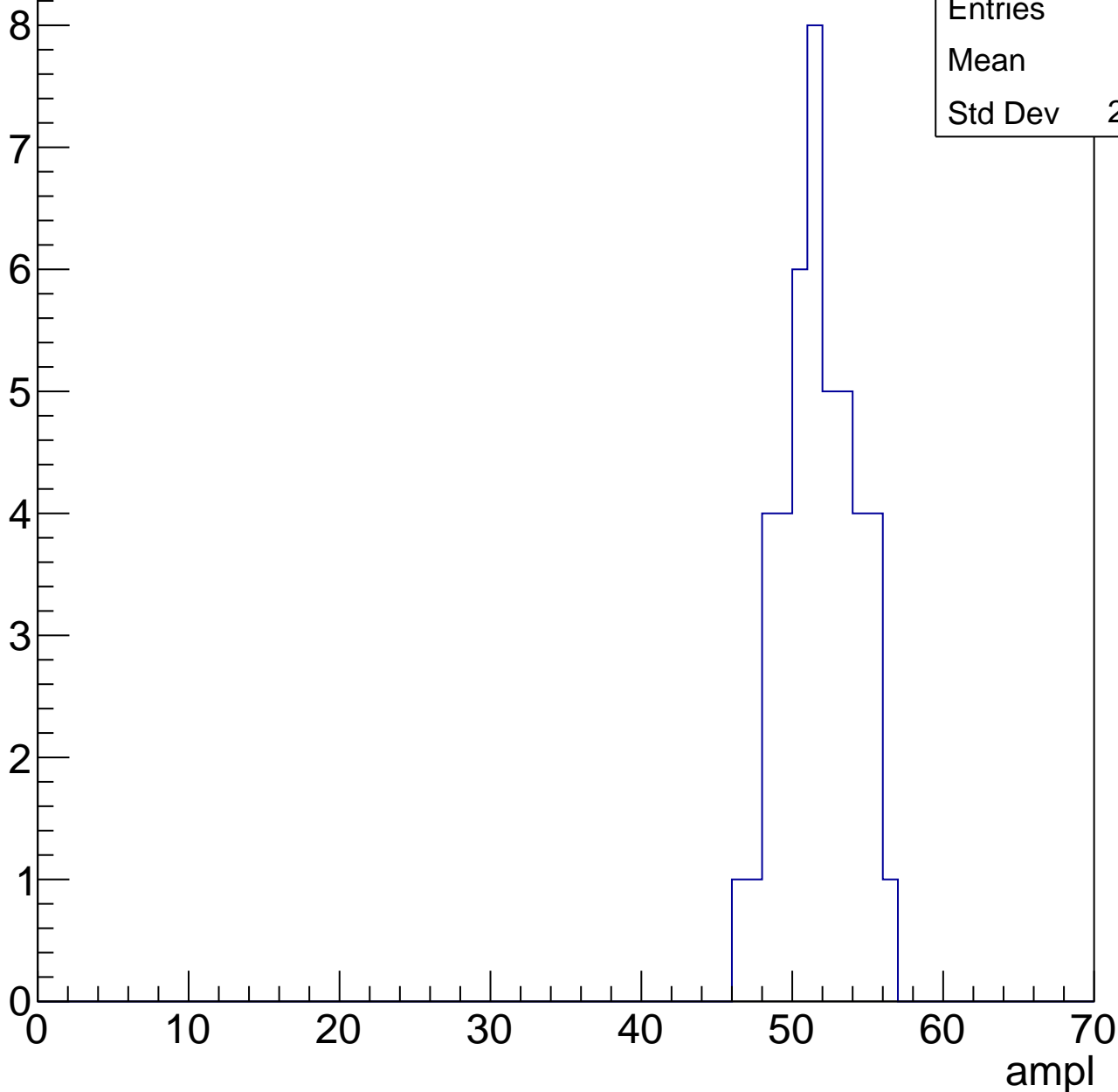


# B1L002S, U17-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

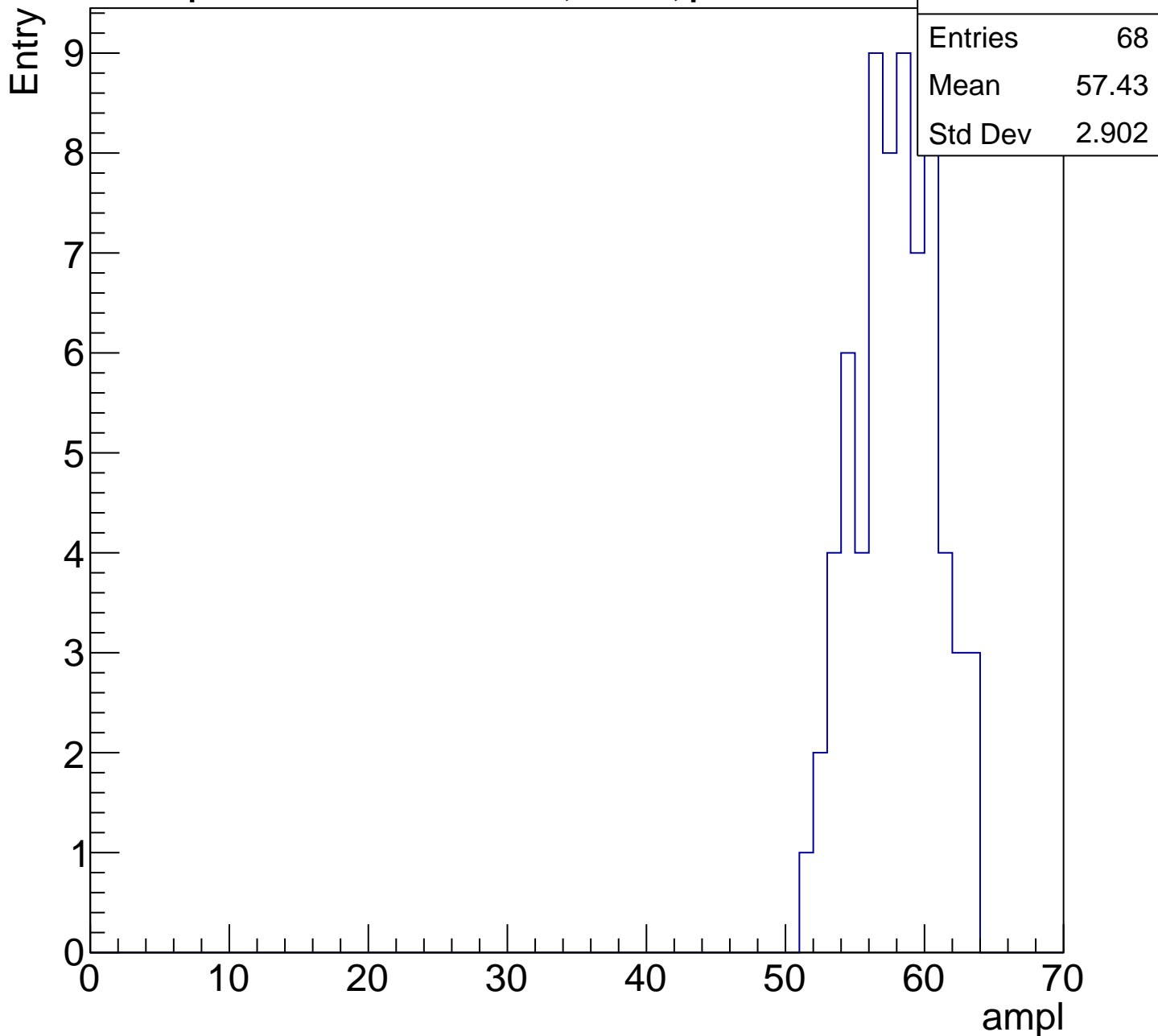
Entry

Entries	43
Mean	51.3
Std Dev	2.387



# B1L002S, U17-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

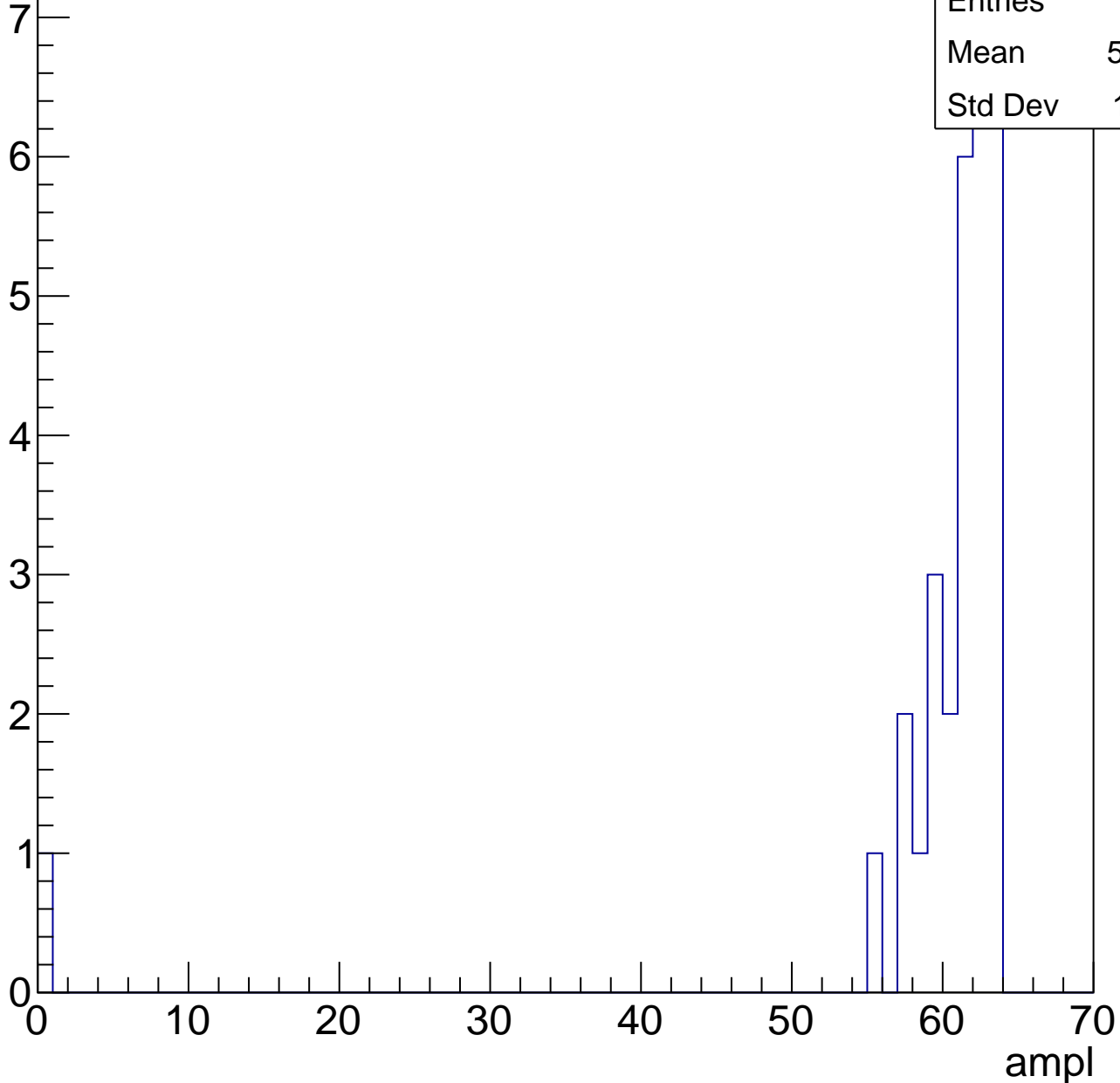


# B1L002S, U17-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.83
Std Dev	11.11





# B1L002S, U17-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U17-ch2, adc0

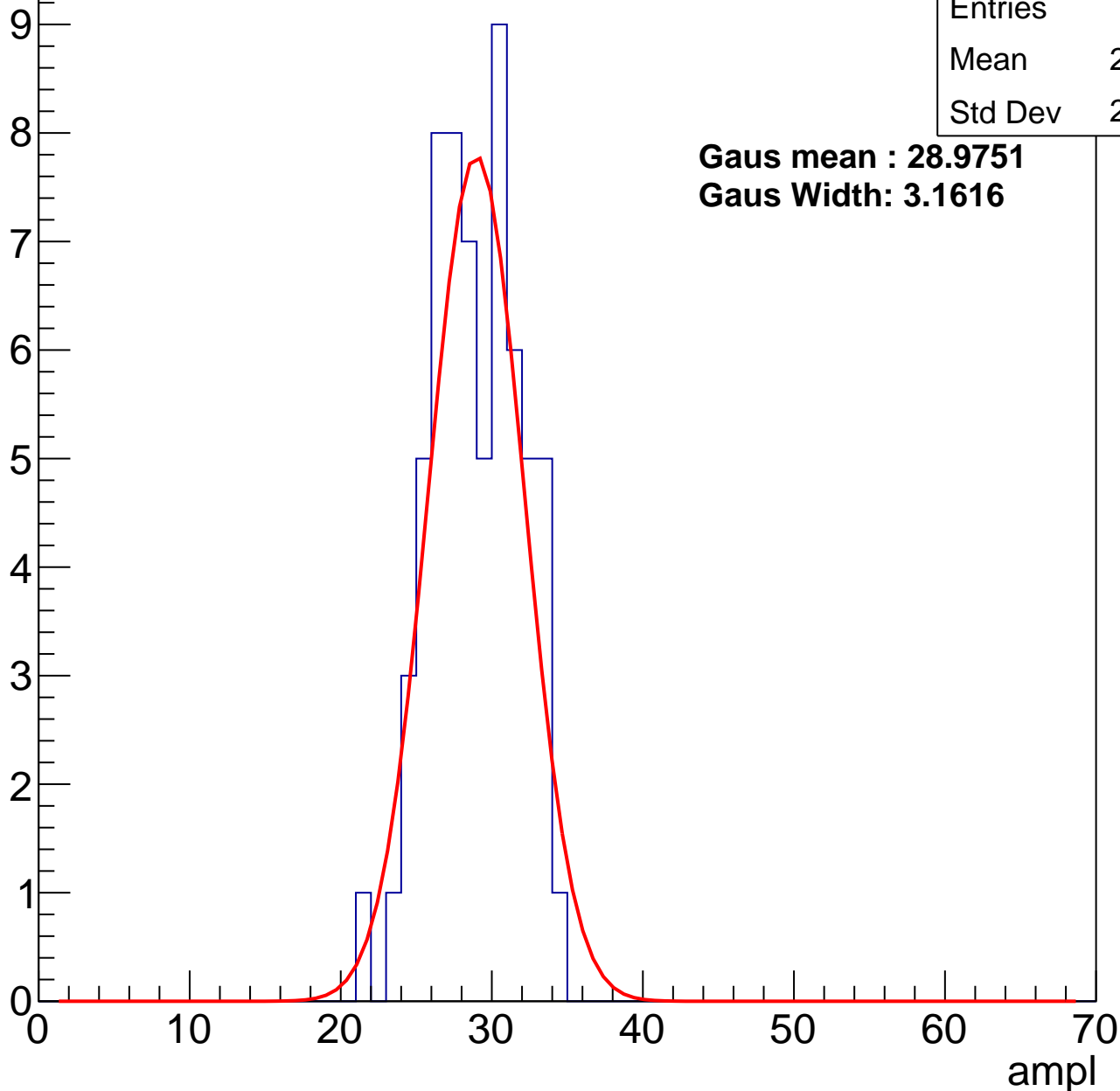
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.45
Std Dev	2.877

**Gaus mean : 28.9751**

**Gaus Width: 3.1616**



# B1L002S, U17-ch2, adc1

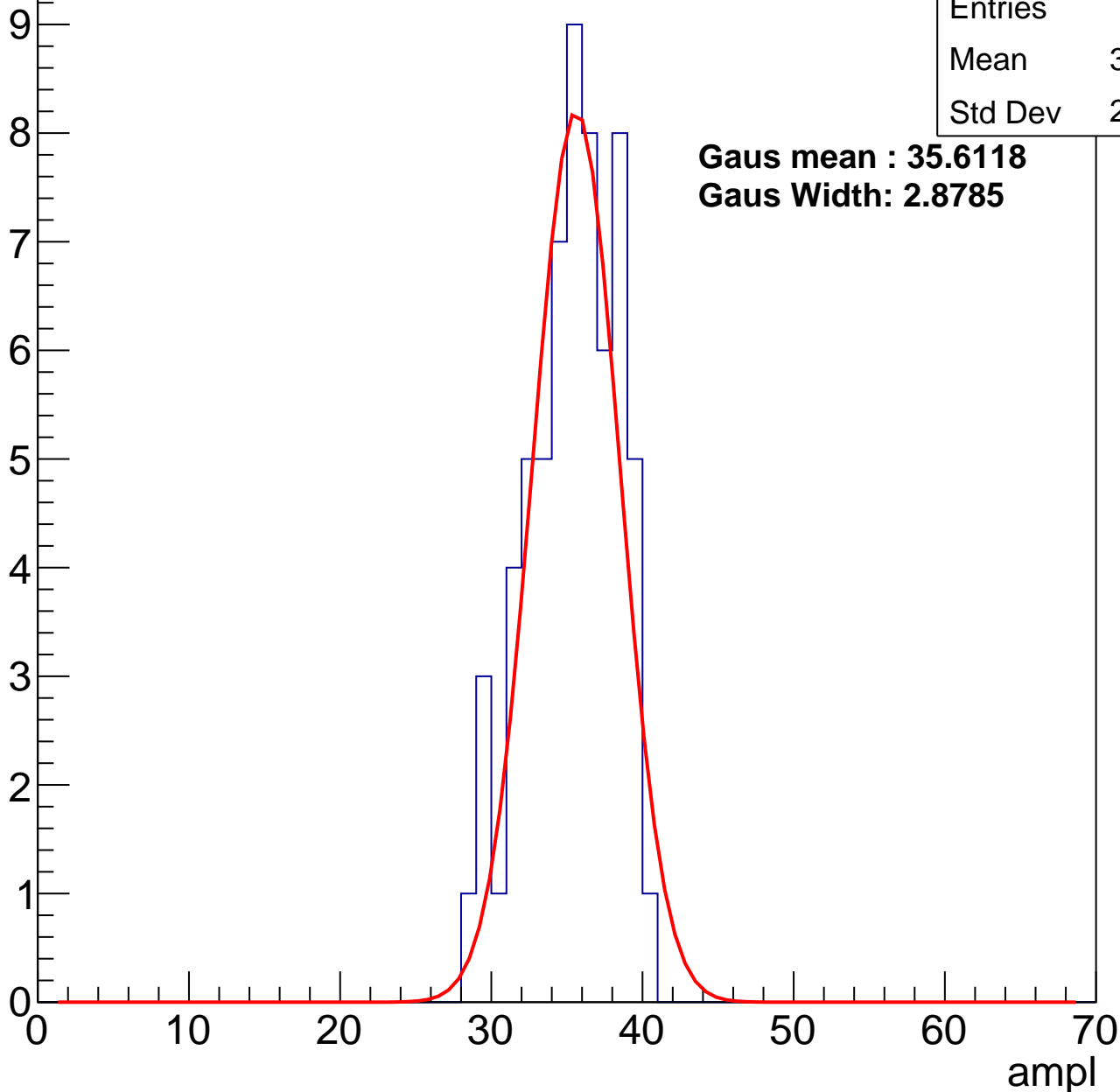
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.86
Std Dev	2.889

**Gaus mean : 35.6118**

**Gaus Width: 2.8785**



# B1L002S, U17-ch2, adc2

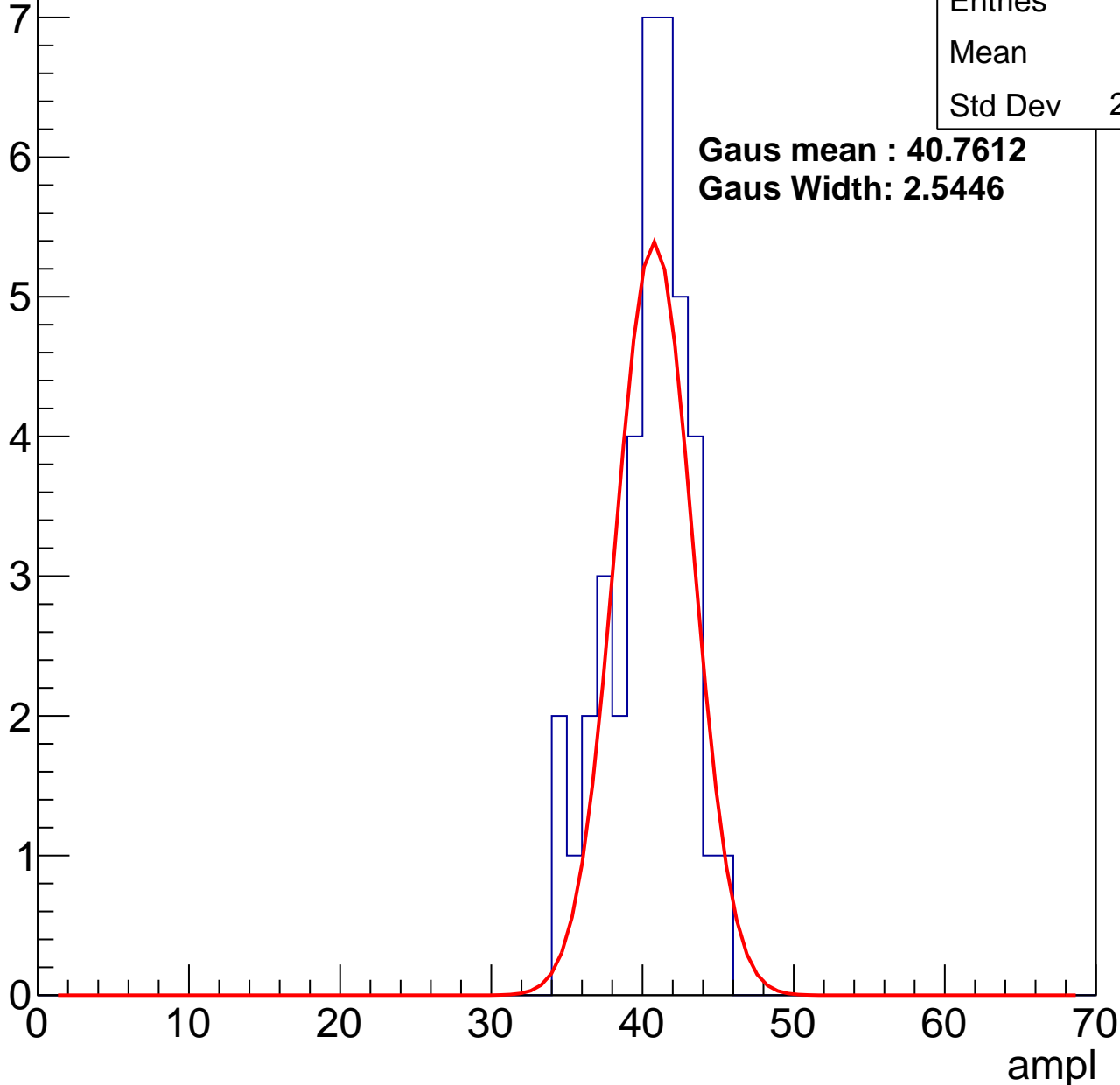
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	39.9
Std Dev	2.639

**Gaus mean : 40.7612**

**Gaus Width: 2.5446**



# B1L002S, U17-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

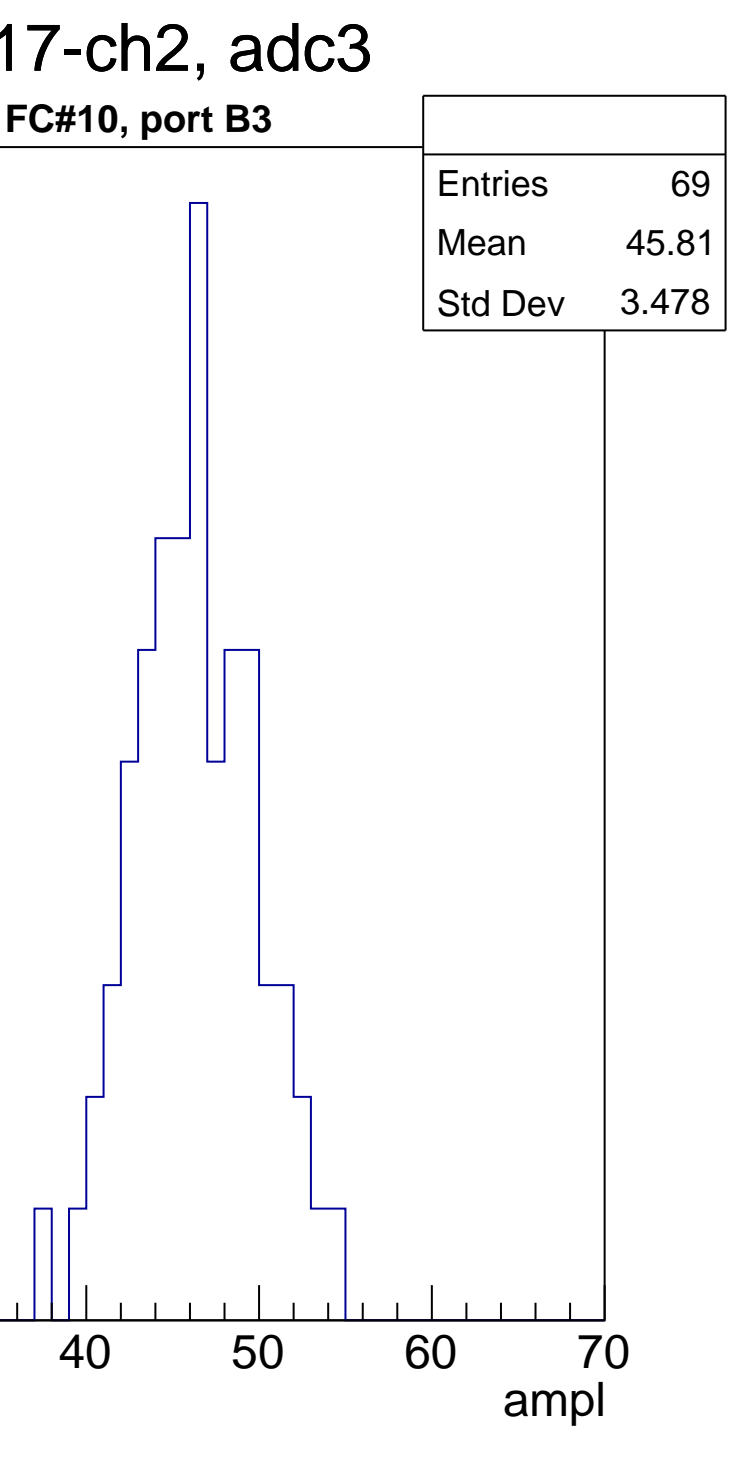
Entries	69
Mean	45.81
Std Dev	3.478

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

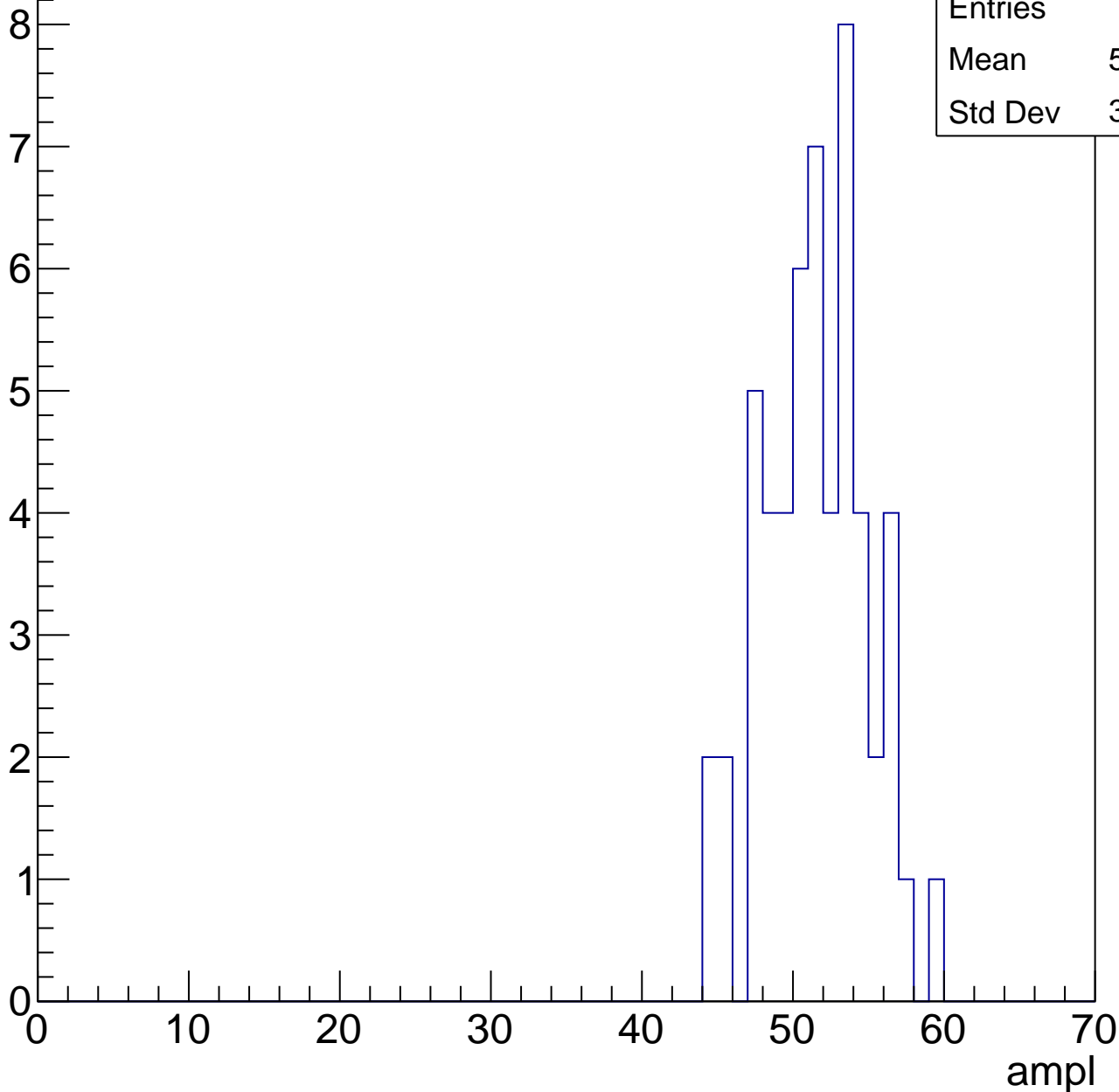


# B1L002S, U17-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.04
Std Dev	3.366

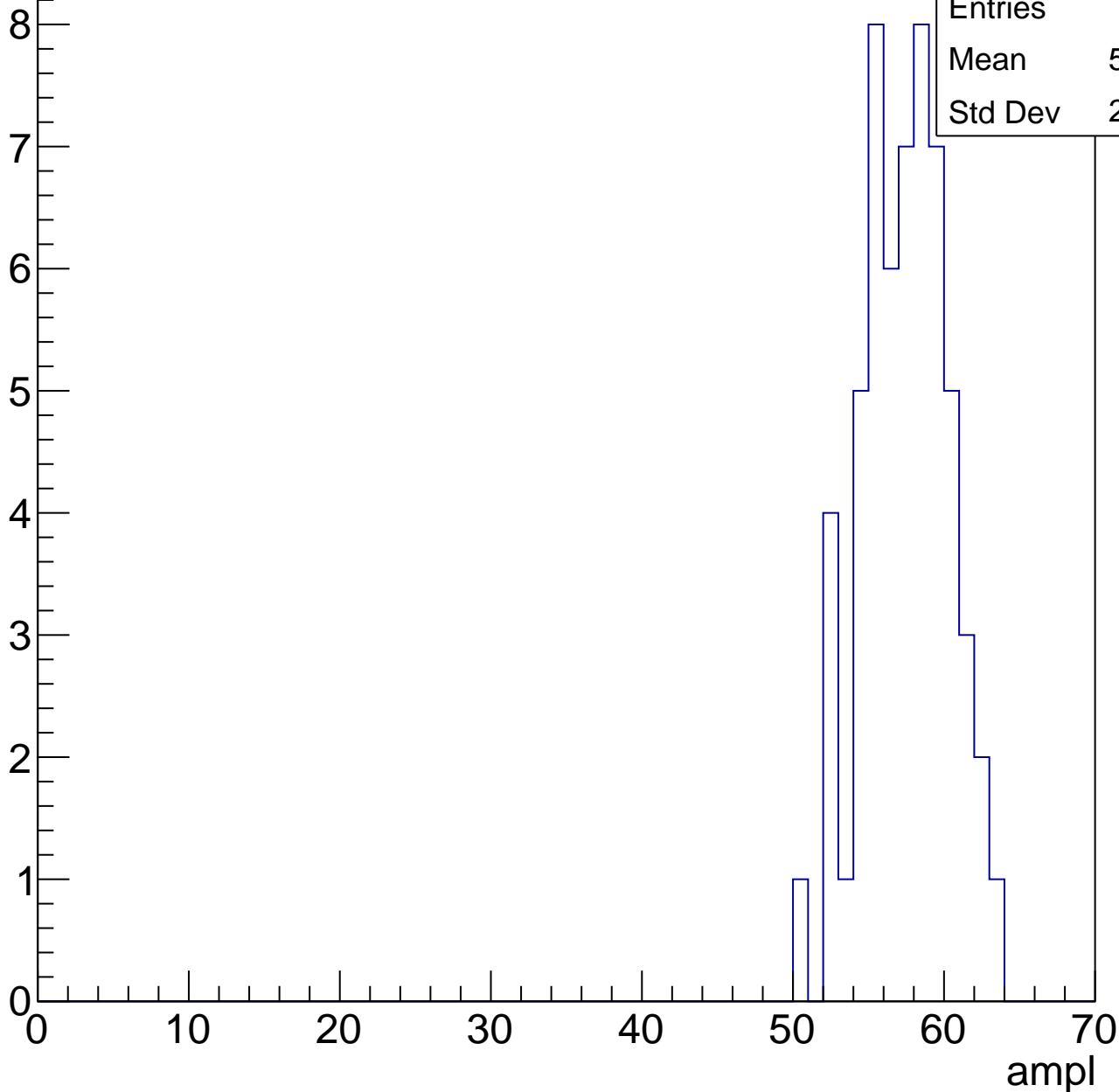


# B1L002S, U17-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.95
Std Dev	2.825

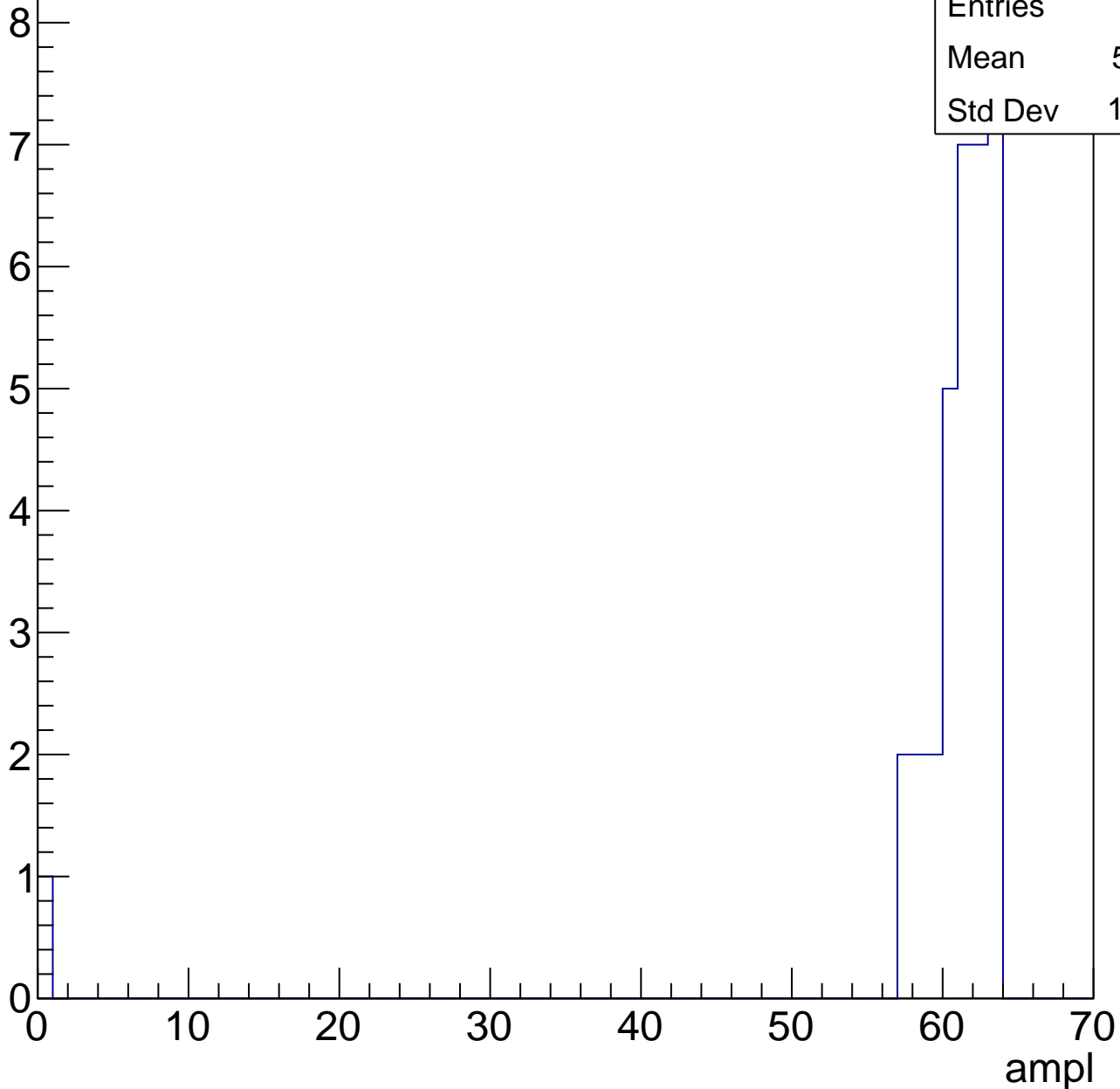


# B1L002S, U17-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	59.21
Std Dev	10.45





# B1L002S, U17-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch3, adc0

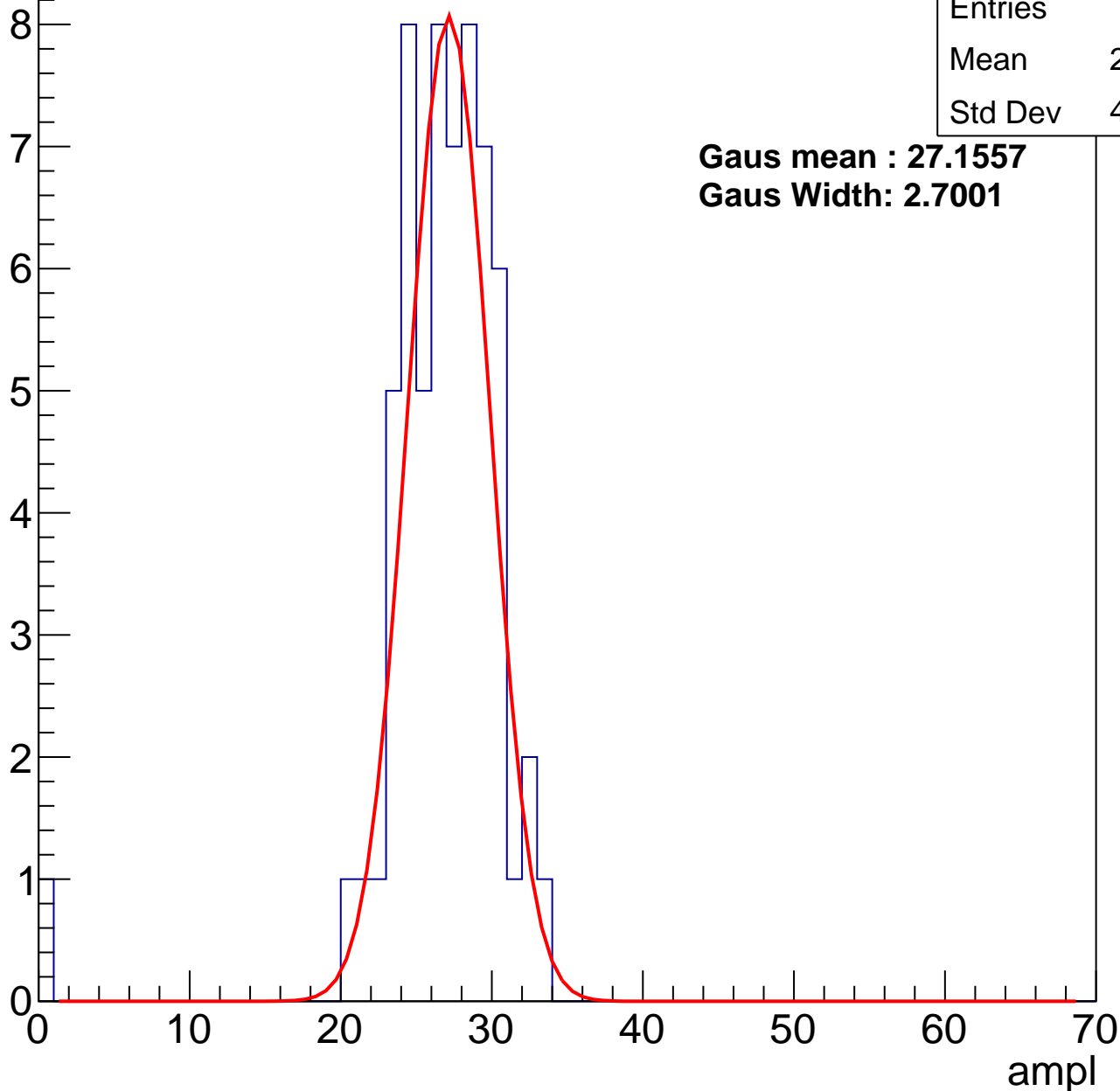
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26.24
Std Dev	4.354

**Gaus mean : 27.1557**

**Gaus Width: 2.7001**



# B1L002S, U17-ch3, adc1

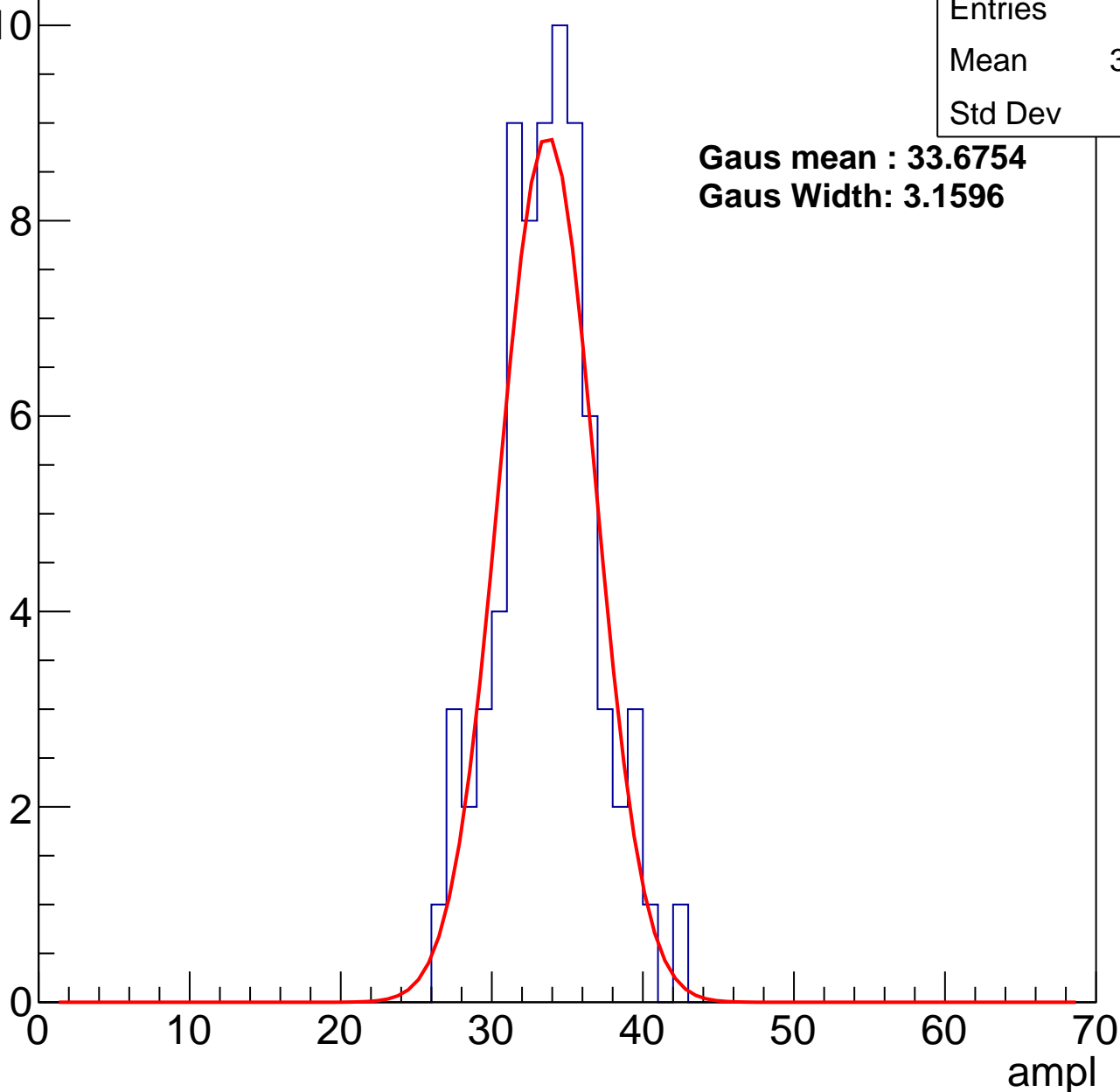
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	33.23
Std Dev	3.22

**Gaus mean : 33.6754**

**Gaus Width: 3.1596**



# B1L002S, U17-ch3, adc2

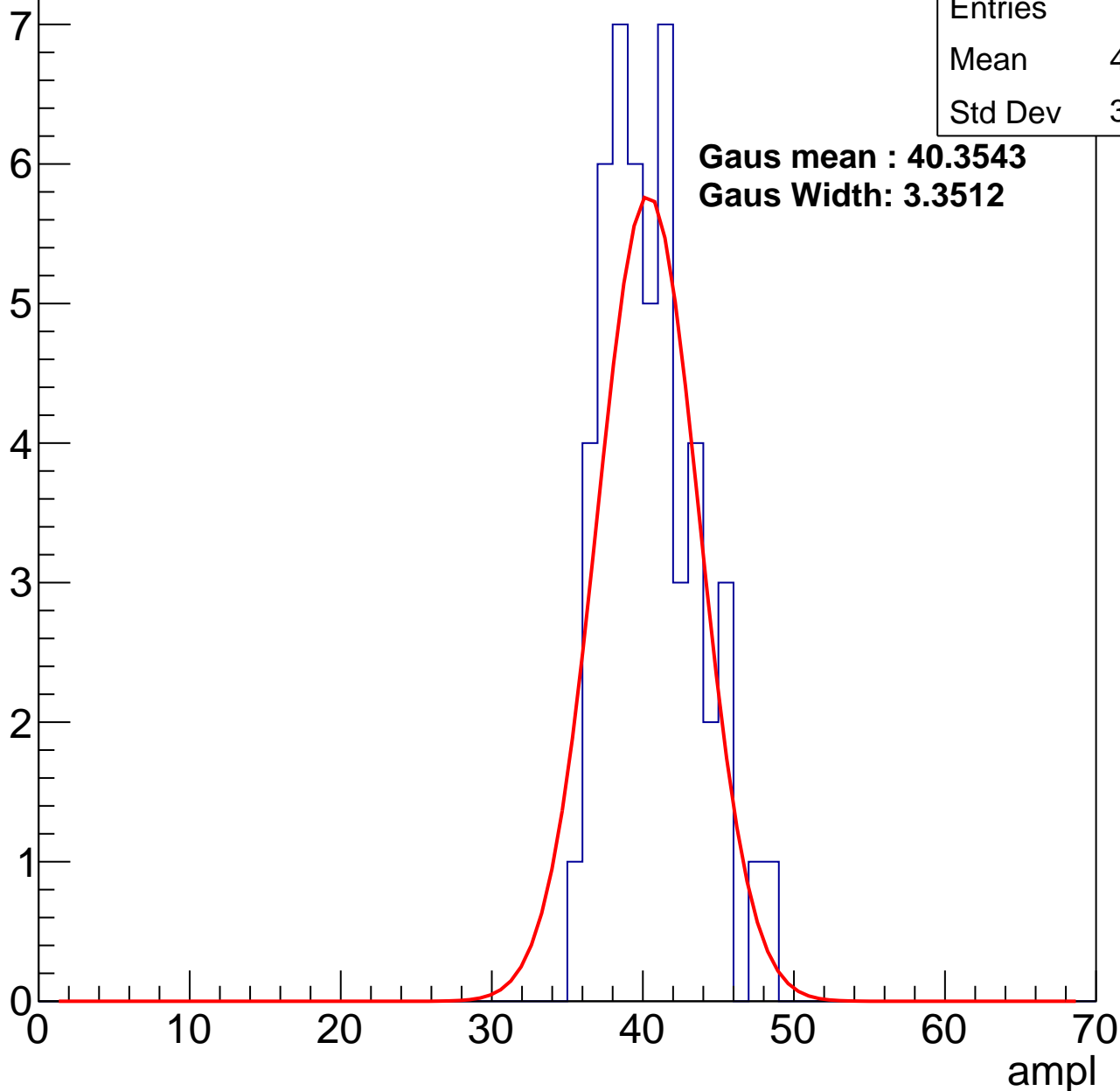
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.08
Std Dev	3.006

**Gaus mean : 40.3543**

**Gaus Width: 3.3512**



# B1L002S, U17-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	44.8
Std Dev	3.188

Entry

10

8

6

4

2

0

0

10

20

30

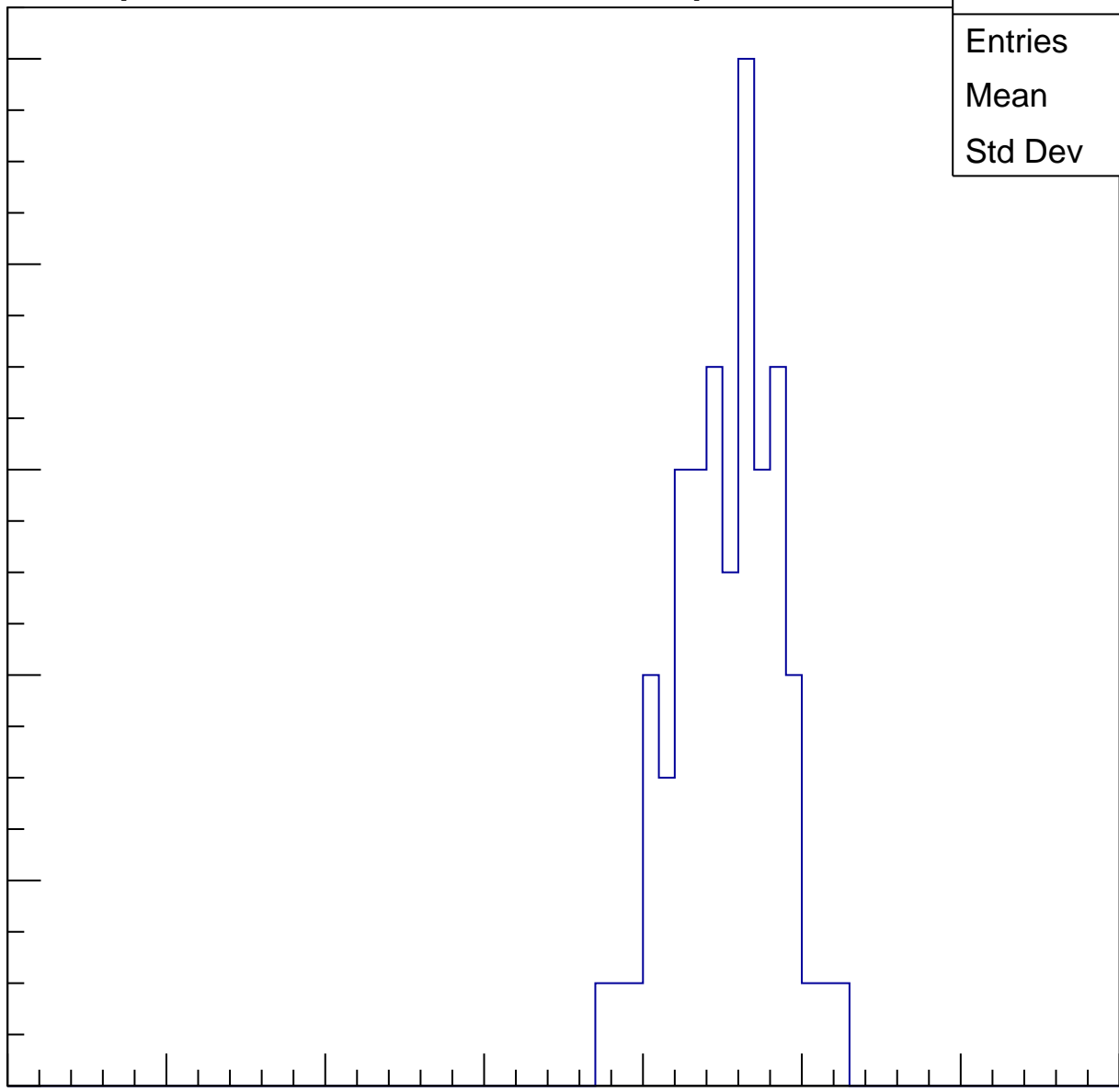
40

50

60

70

ampl

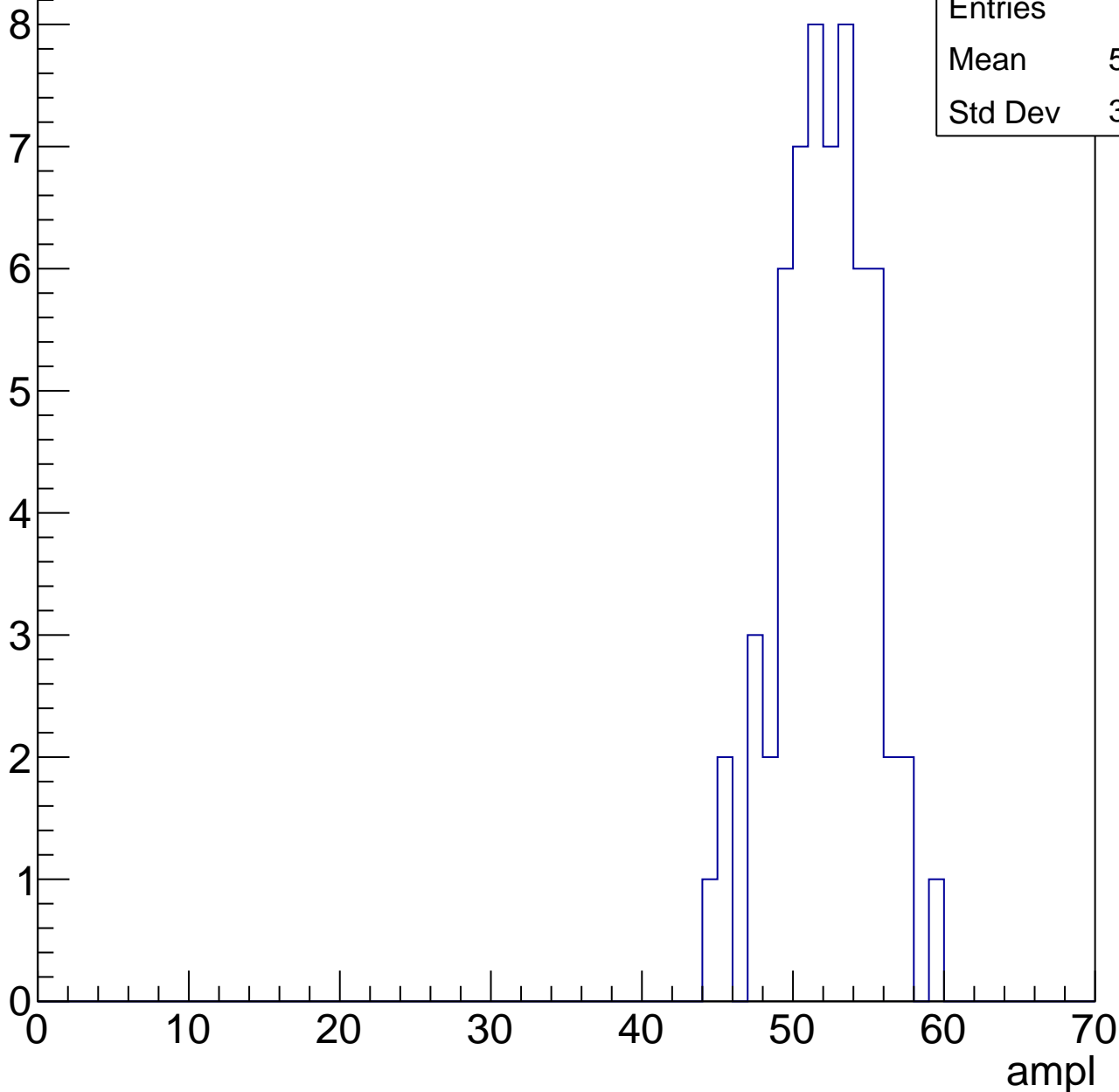


# B1L002S, U17-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	51.64
Std Dev	3.052

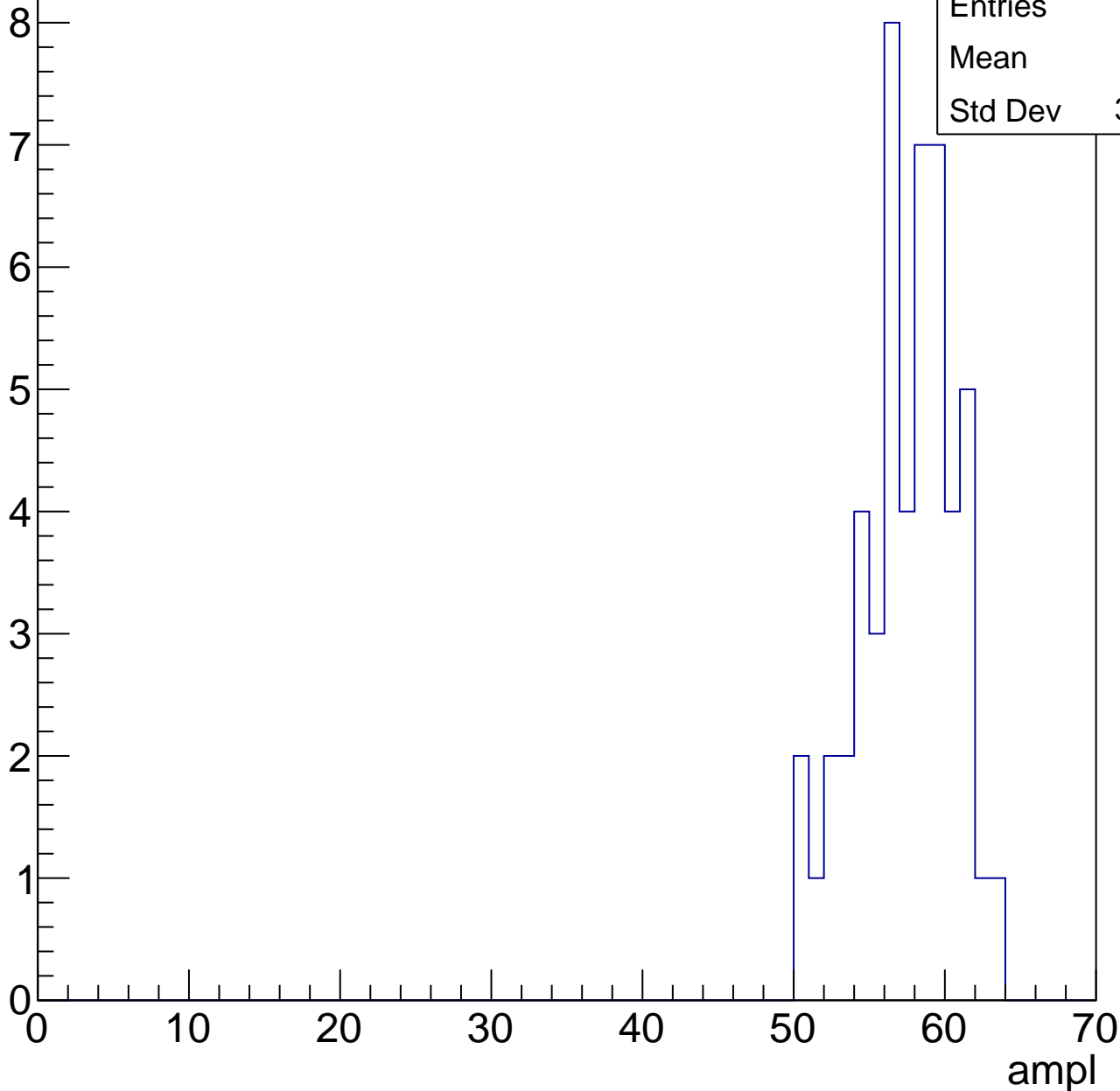


# B1L002S, U17-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57
Std Dev	3.081

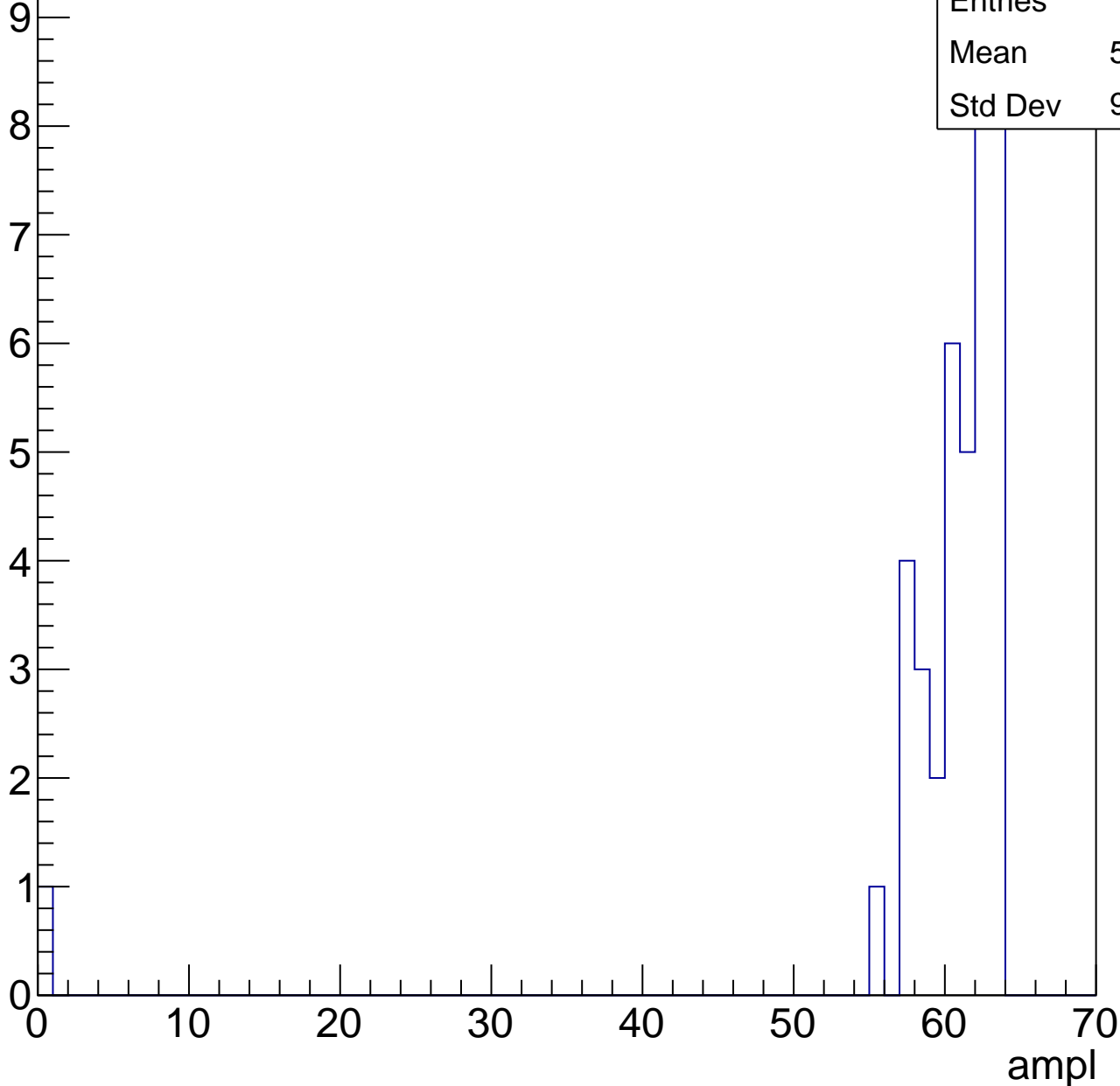


# B1L002S, U17-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	59.05
Std Dev	9.816

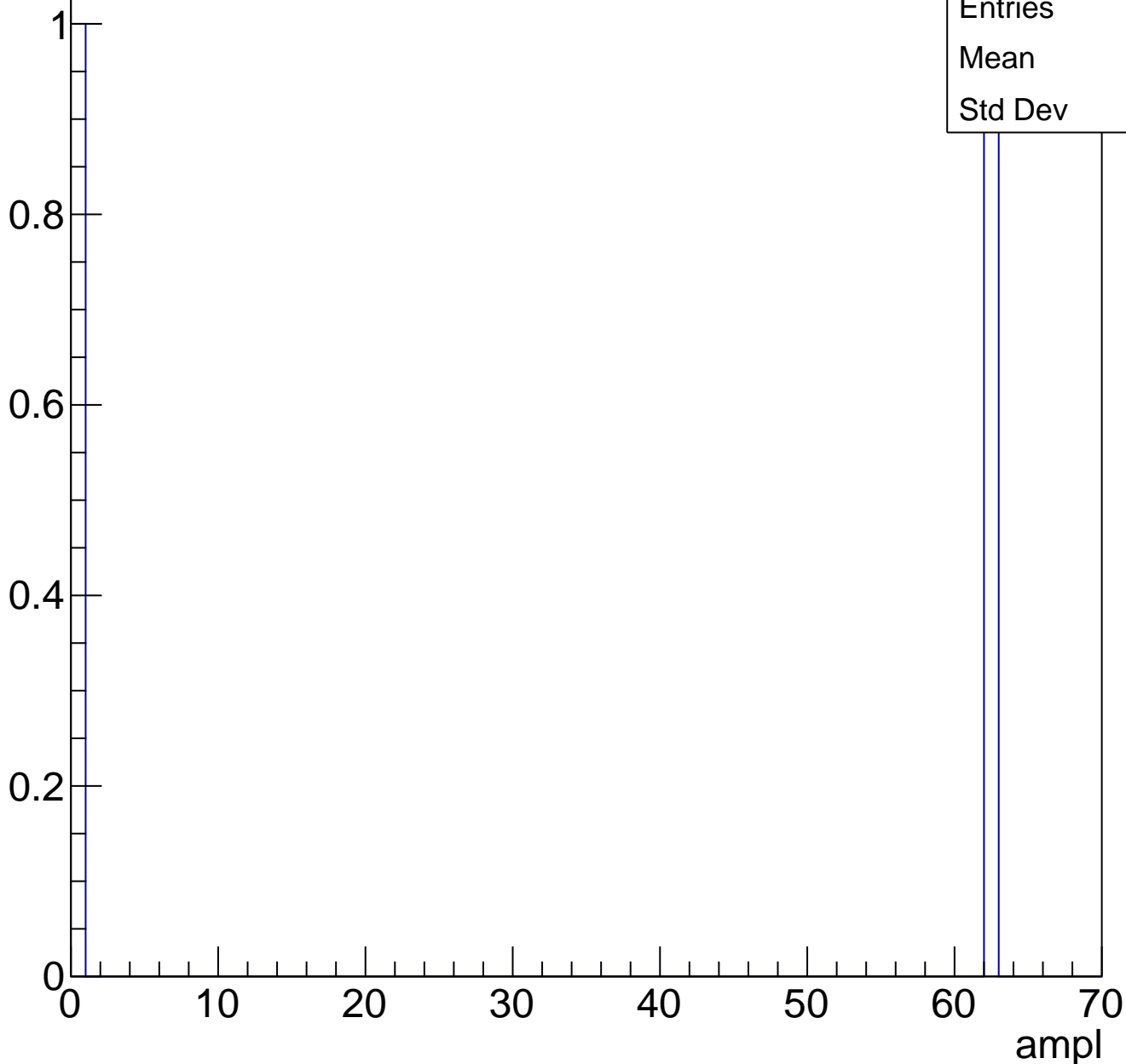




# B1L002S, U17-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch4, adc0

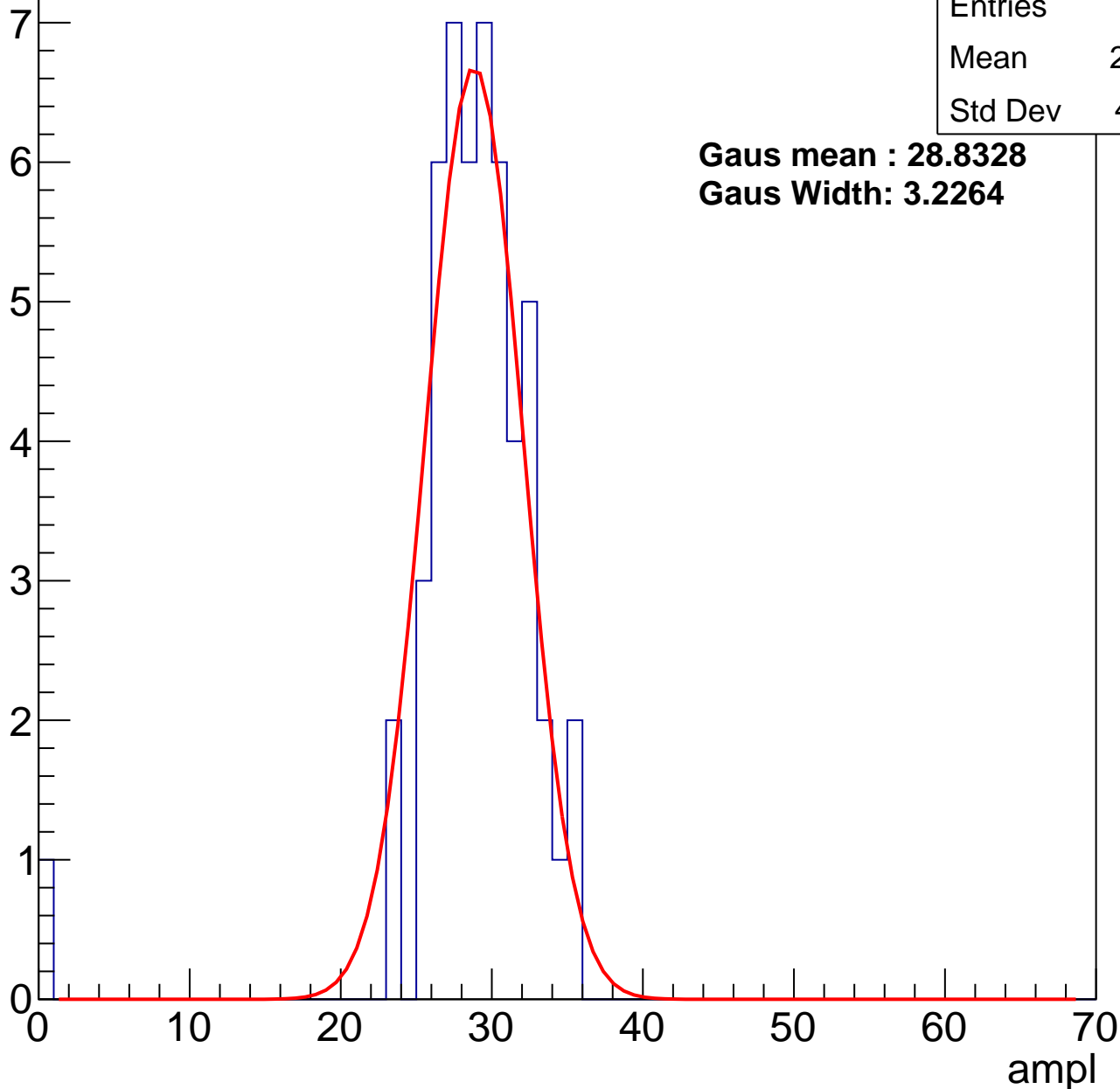
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	28.29
Std Dev	4.841

**Gaus mean : 28.8328**

**Gaus Width: 3.2264**



# B1L002S, U17-ch4, adc1

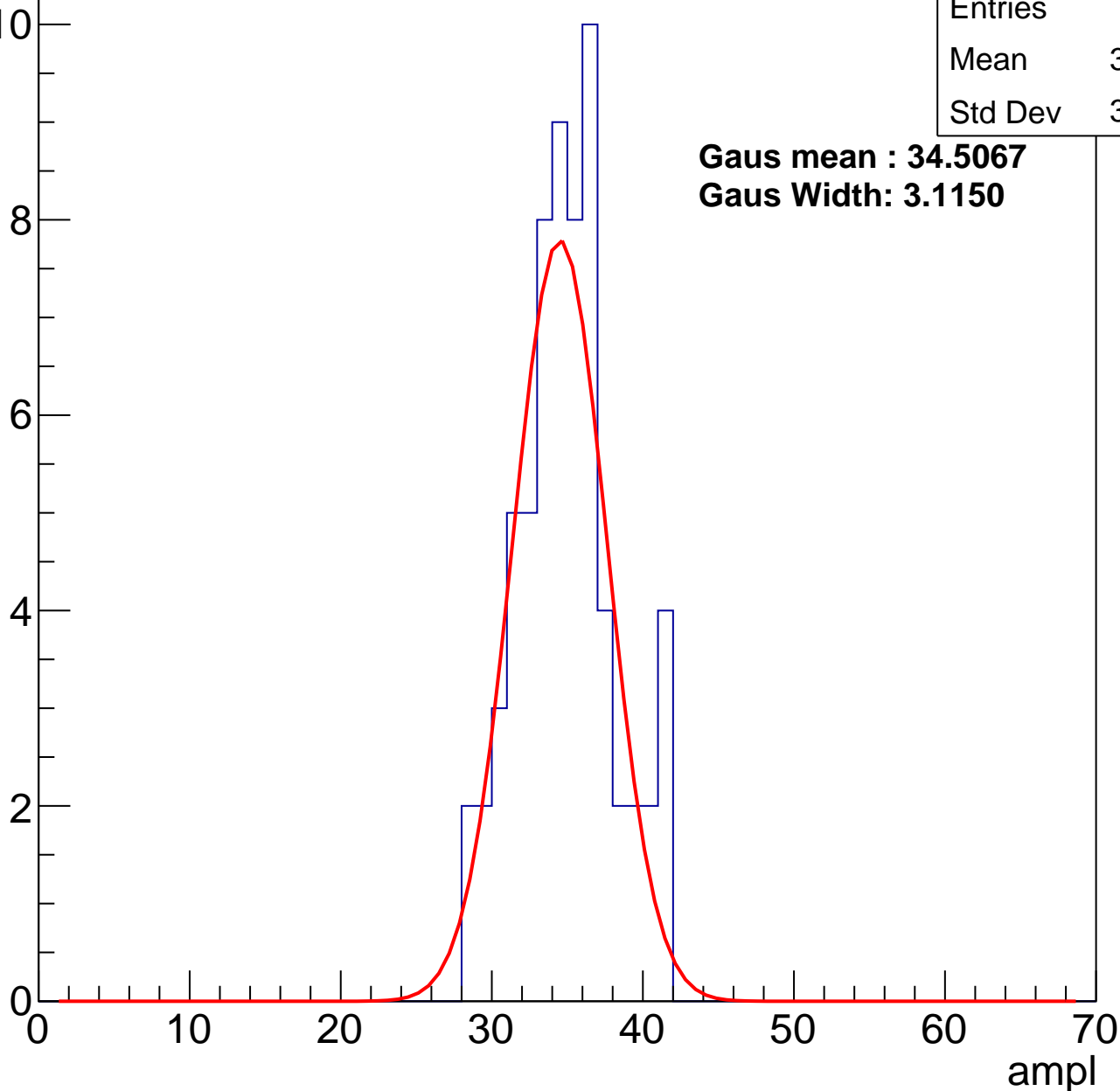
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.47
Std Dev	3.168

**Gaus mean : 34.5067**

**Gaus Width: 3.1150**



# B1L002S, U17-ch4, adc2

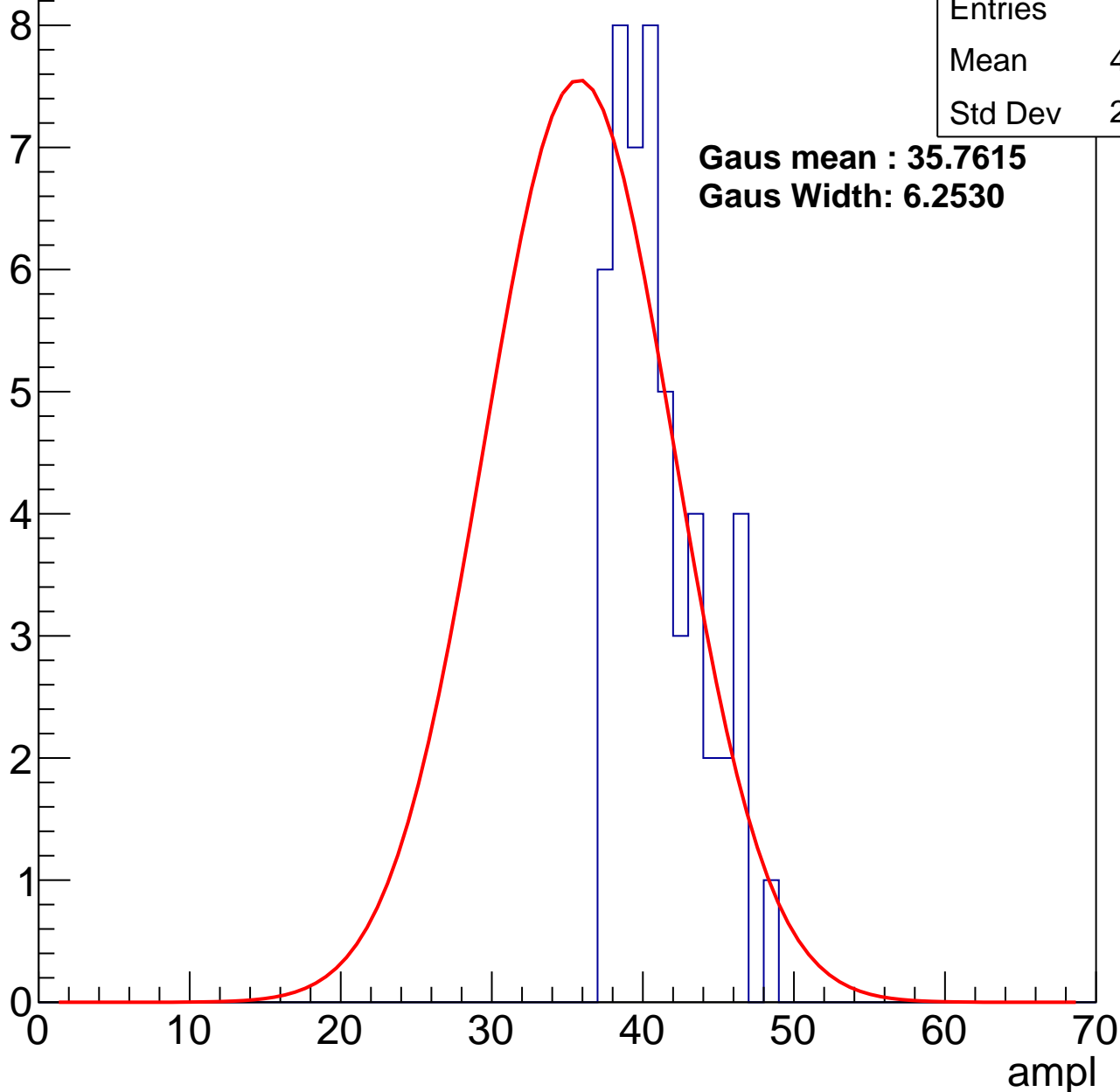
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.64
Std Dev	2.883

**Gaus mean : 35.7615**

**Gaus Width: 6.2530**

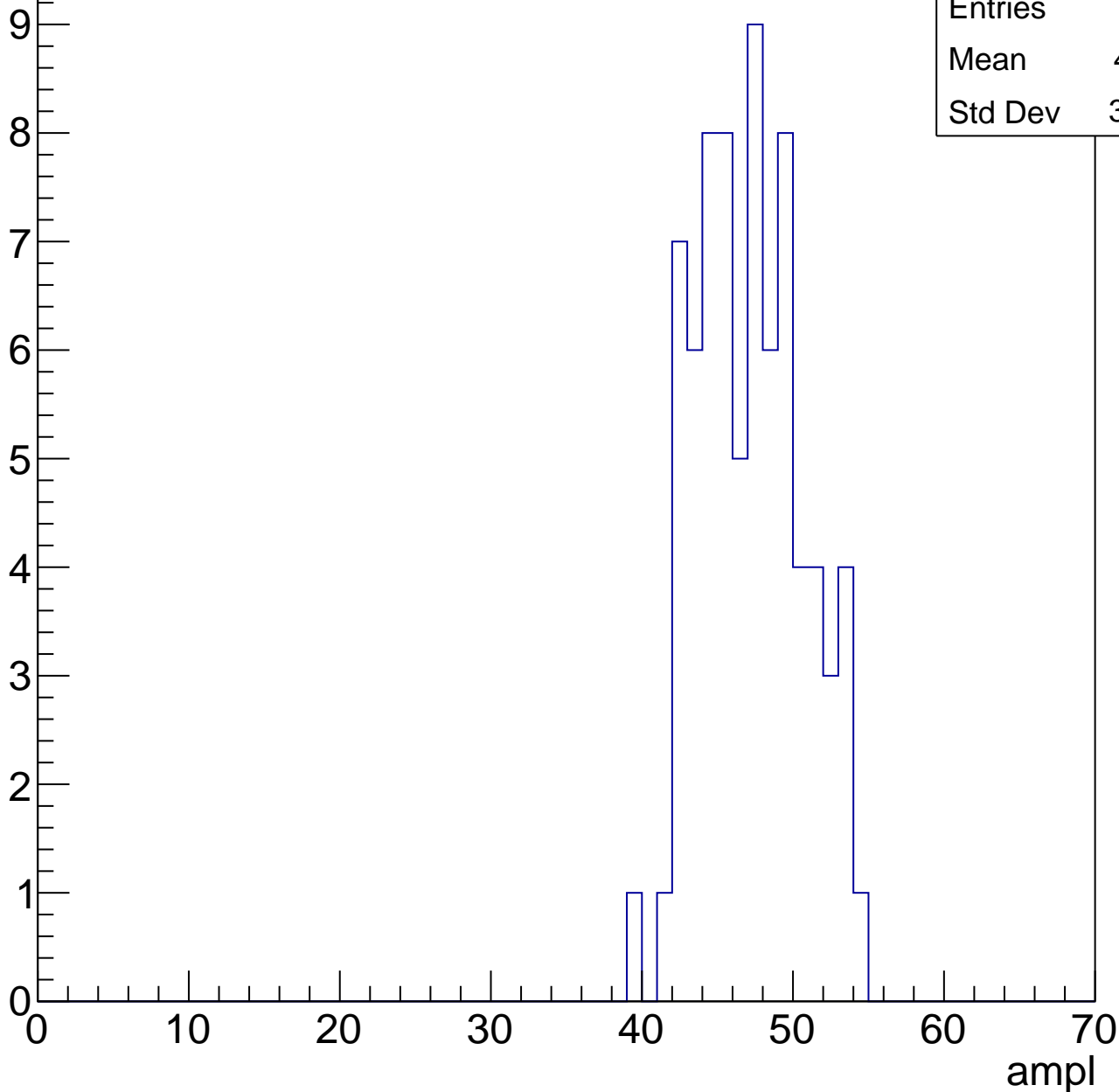


# B1L002S, U17-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

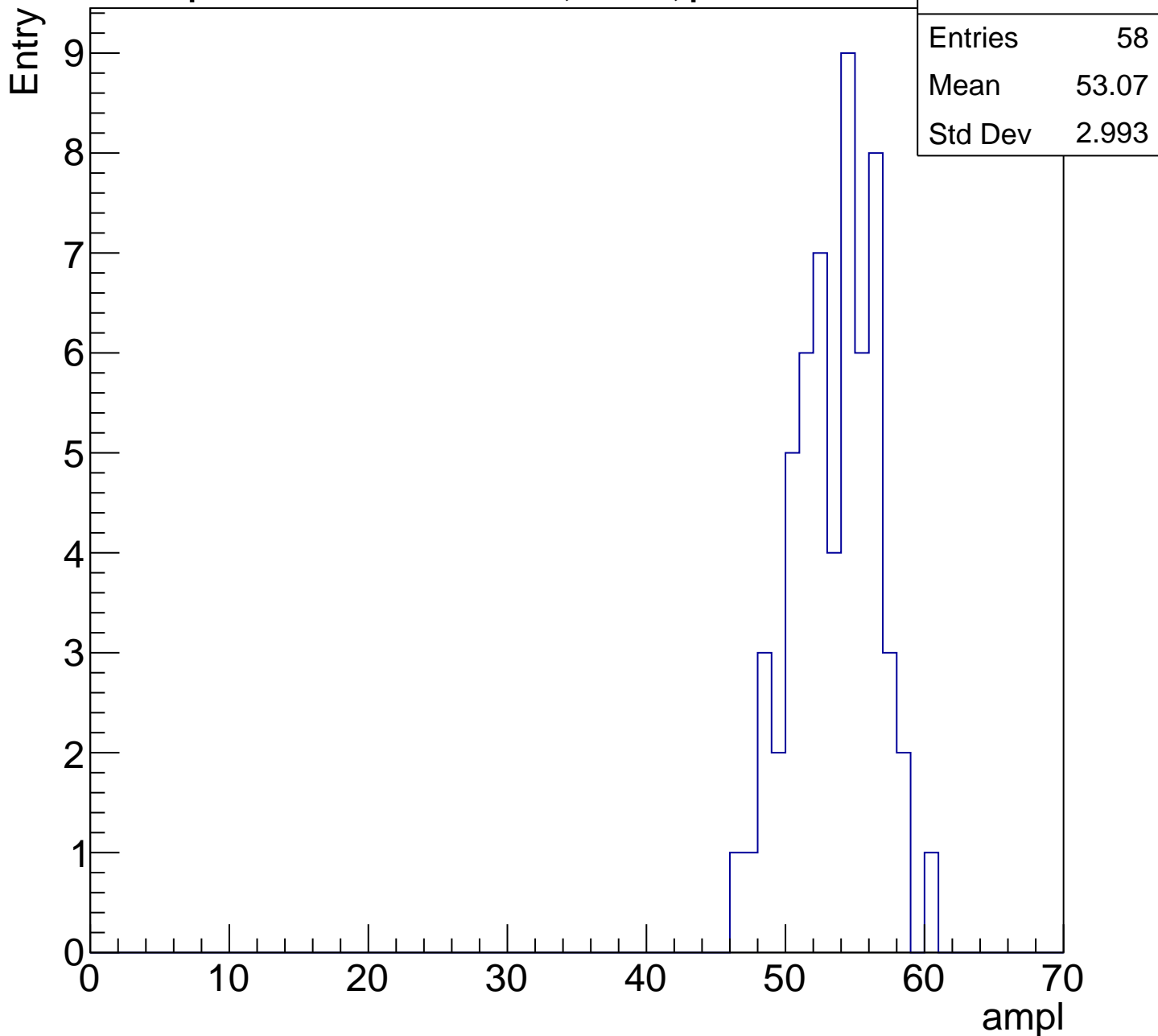
Entry

Entries	75
Mean	46.71
Std Dev	3.425



# B1L002S, U17-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

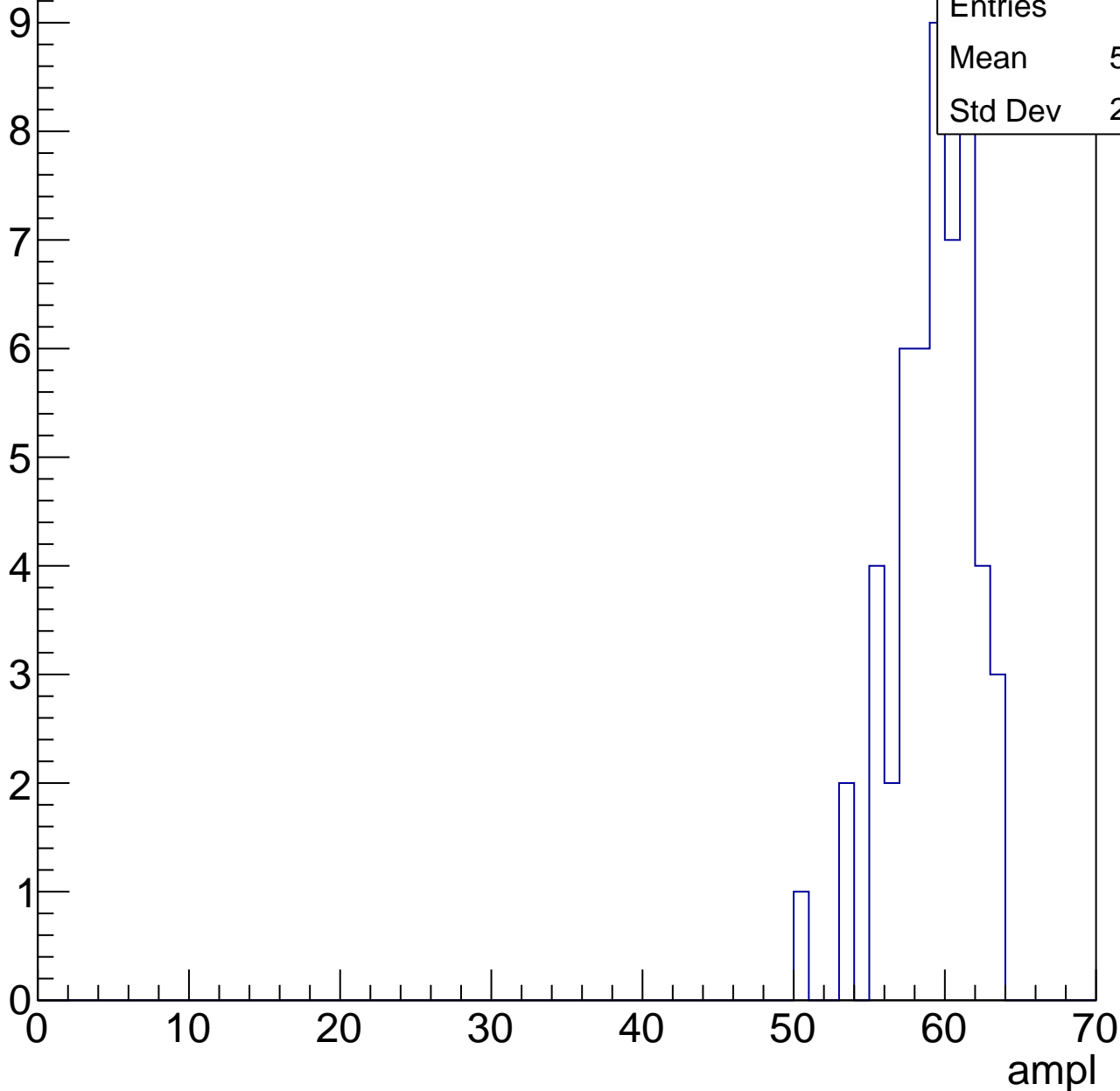


# B1L002S, U17-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.73
Std Dev	2.718

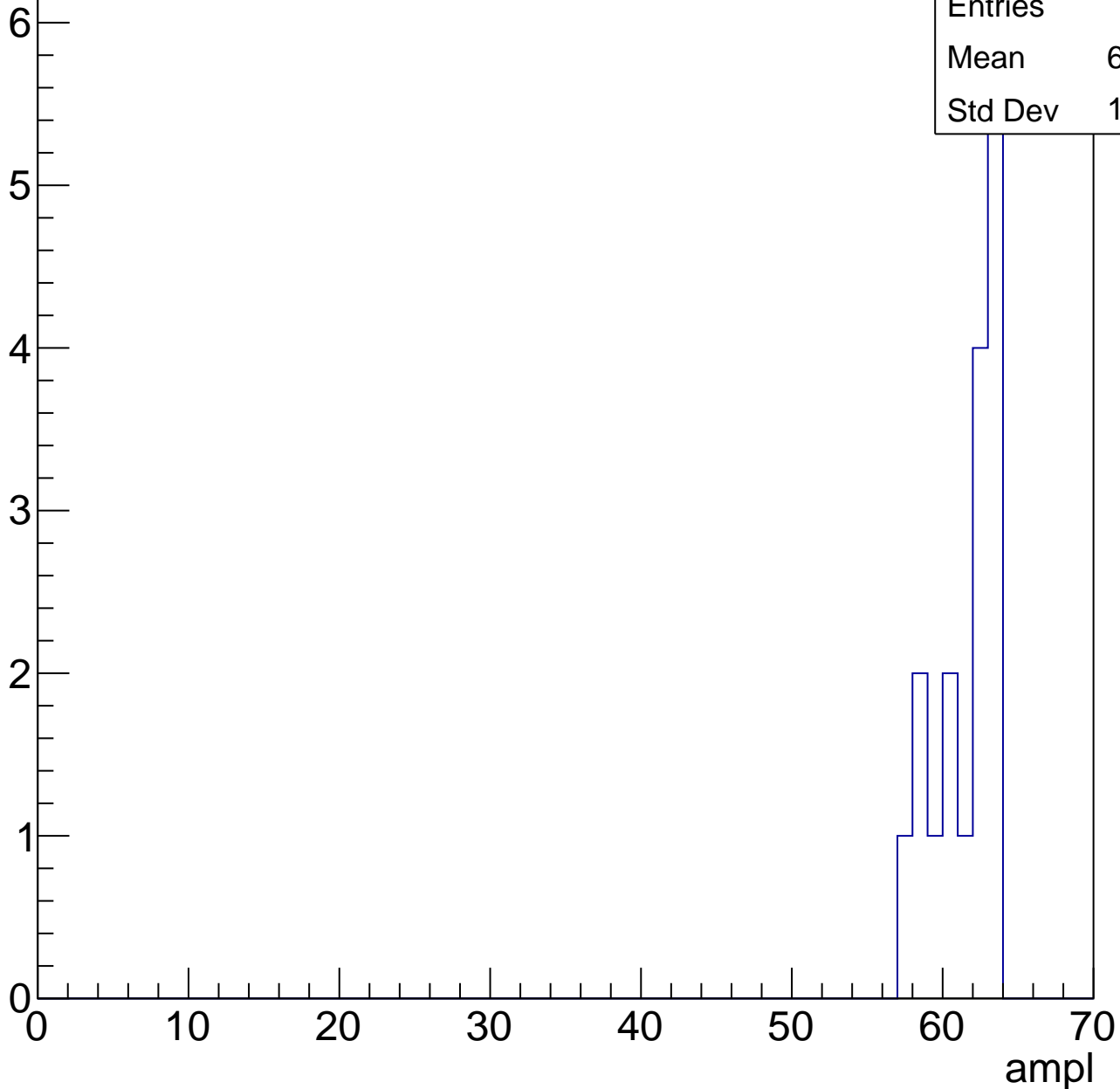


# B1L002S, U17-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.12
Std Dev	1.997

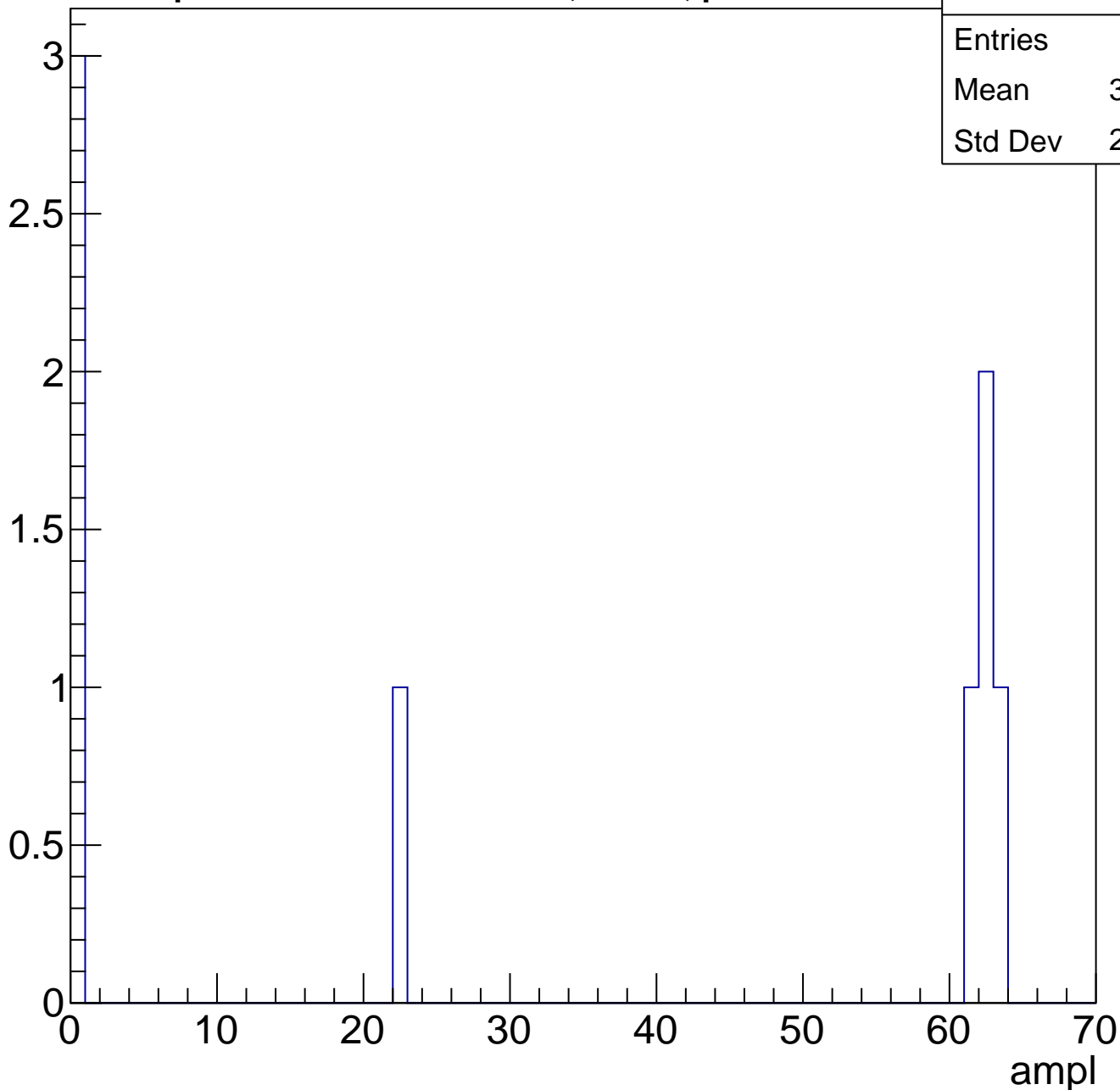




# B1L002S, U17-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	8
Mean	33.75
Std Dev	29.05

# B1L002S, U17-ch5, adc0

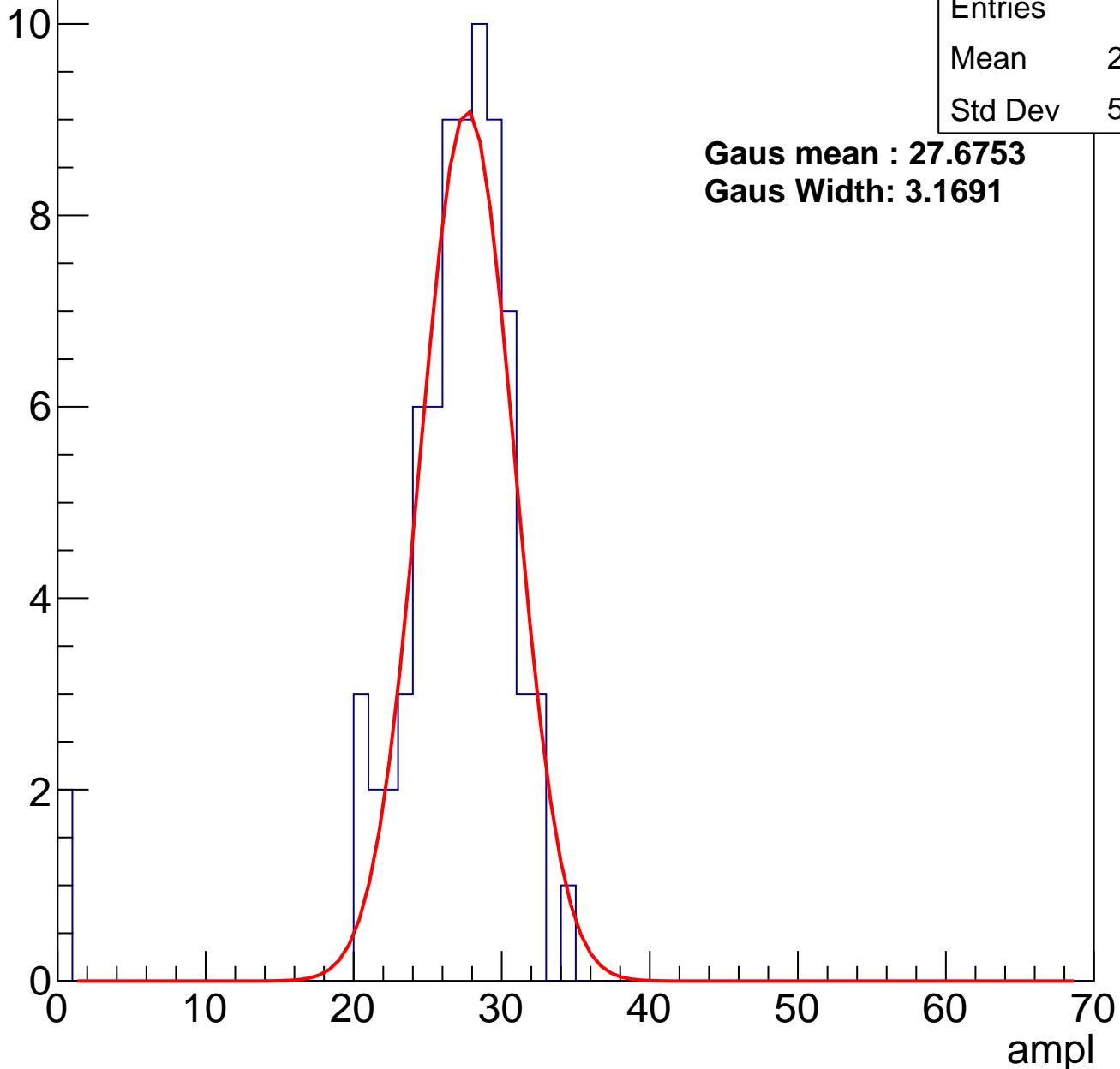
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	26.13
Std Dev	5.275

**Gaus mean : 27.6753**

**Gaus Width: 3.1691**

Entry



# B1L002S, U17-ch5, adc1

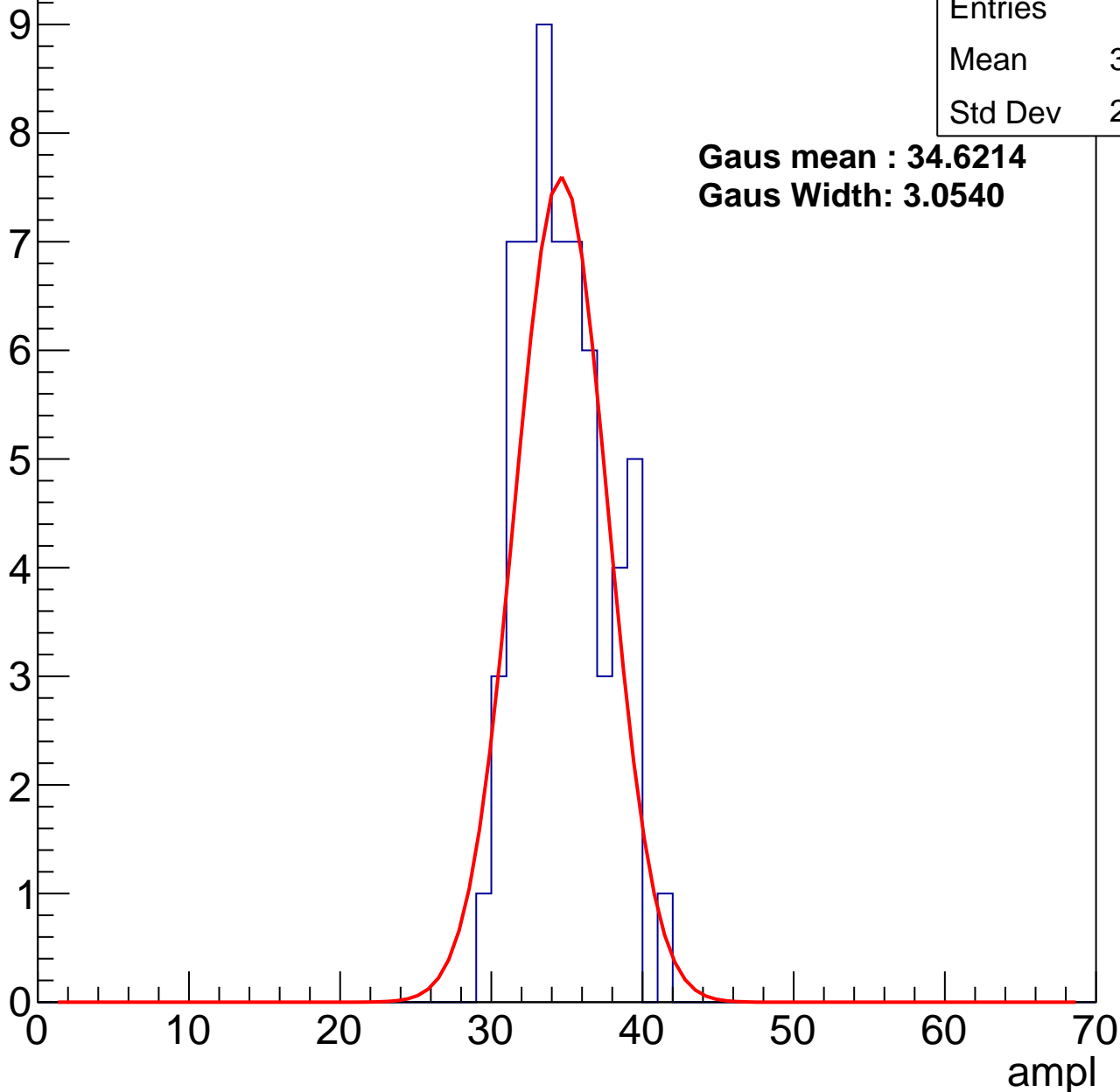
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.25
Std Dev	2.785

**Gaus mean : 34.6214**

**Gaus Width: 3.0540**



# B1L002S, U17-ch5, adc2

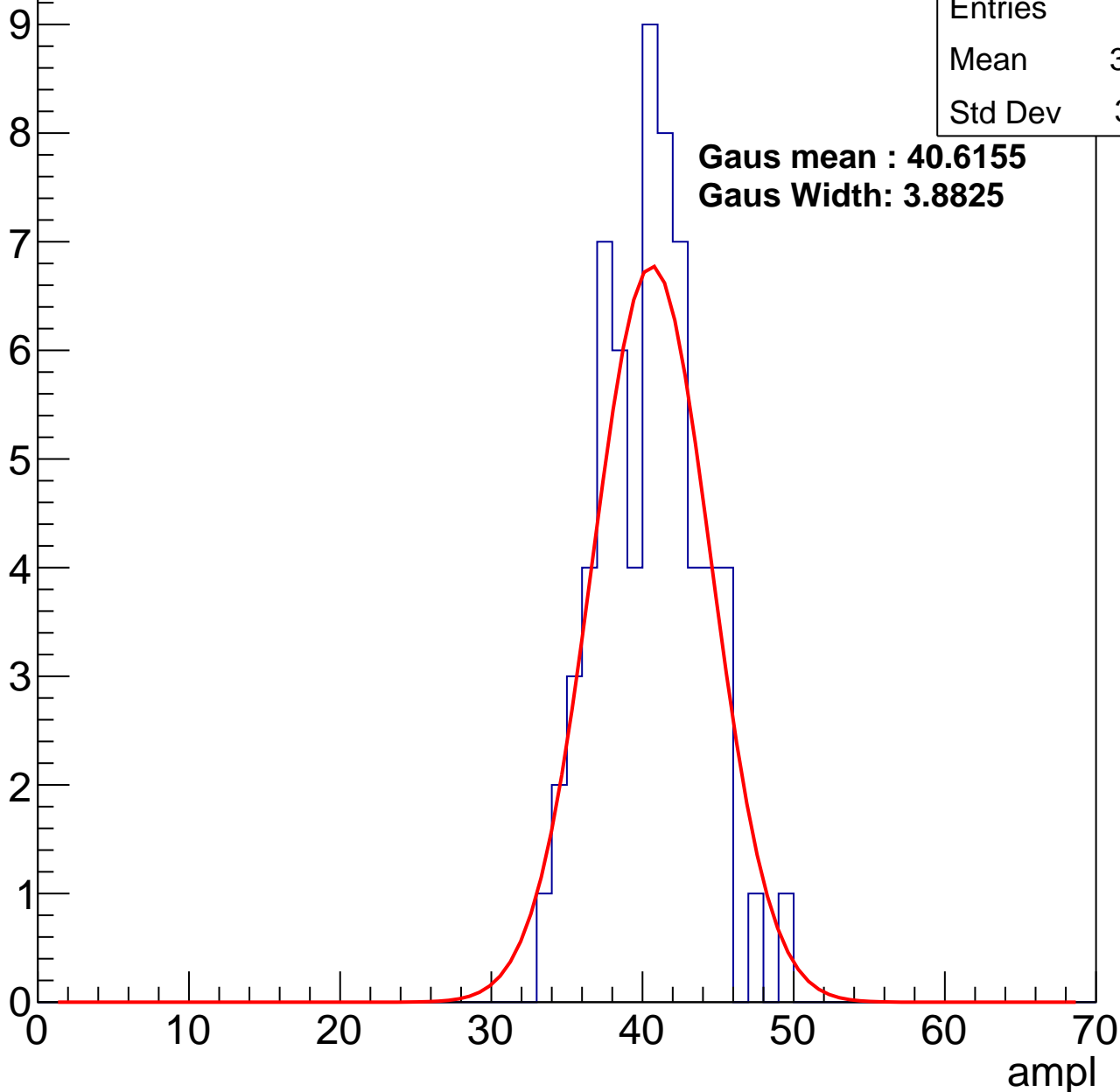
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.98
Std Dev	3.321

**Gaus mean : 40.6155**

**Gaus Width: 3.8825**

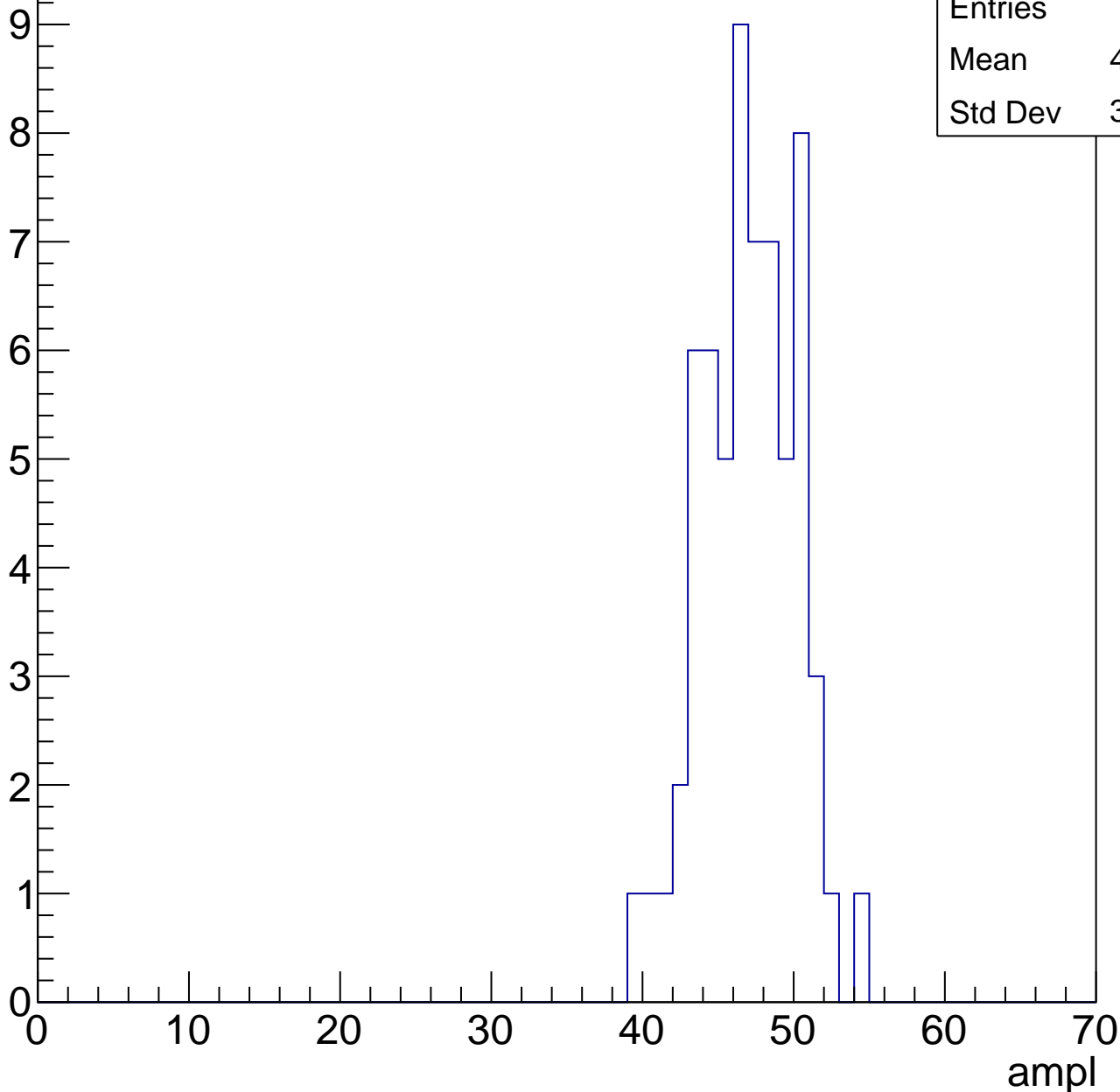


# B1L002S, U17-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.57
Std Dev	3.054

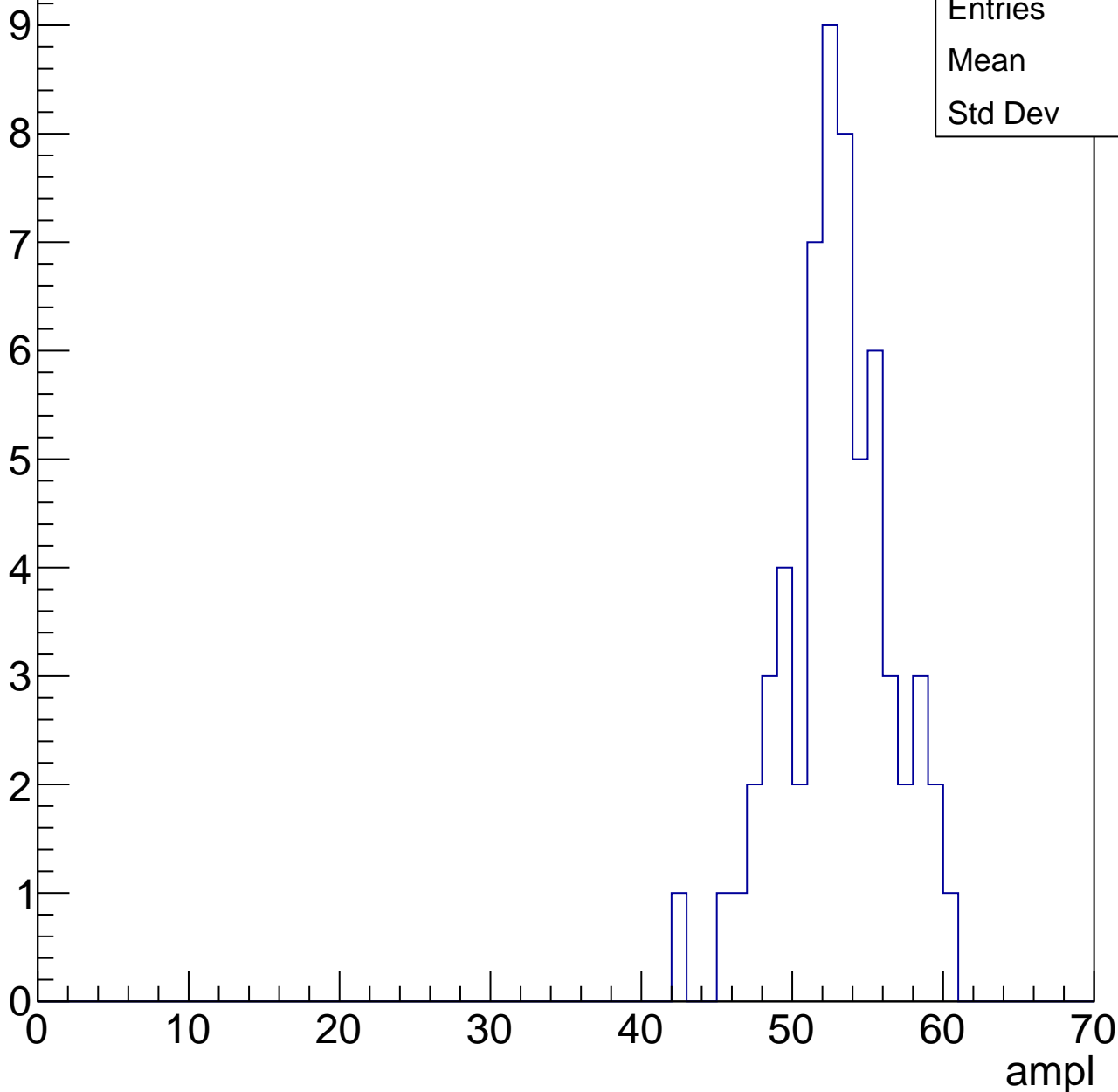


# B1L002S, U17-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.5
Std Dev	3.58



# B1L002S, U17-ch5, adc5

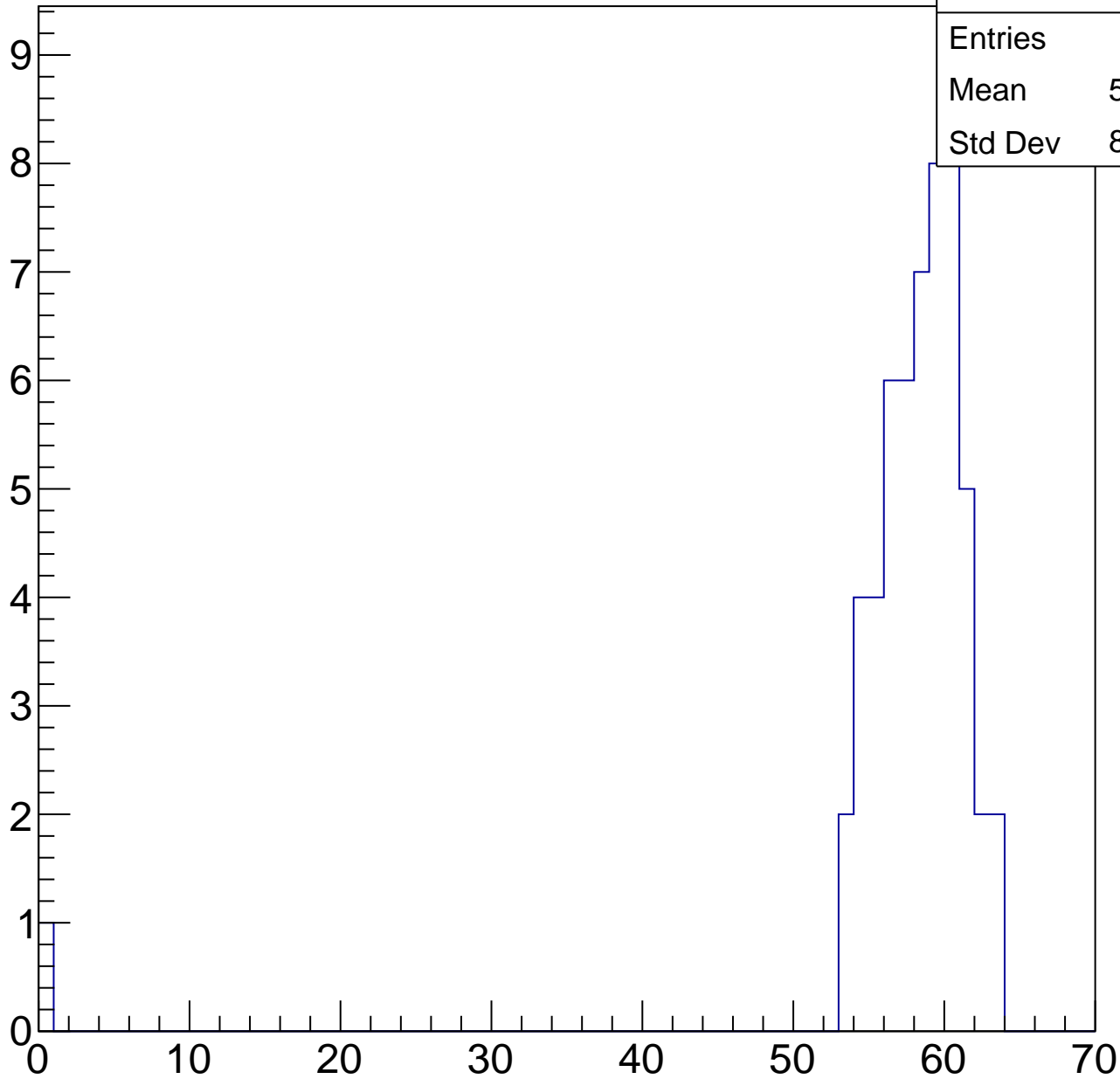
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	57.02
Std Dev	8.085

ampl

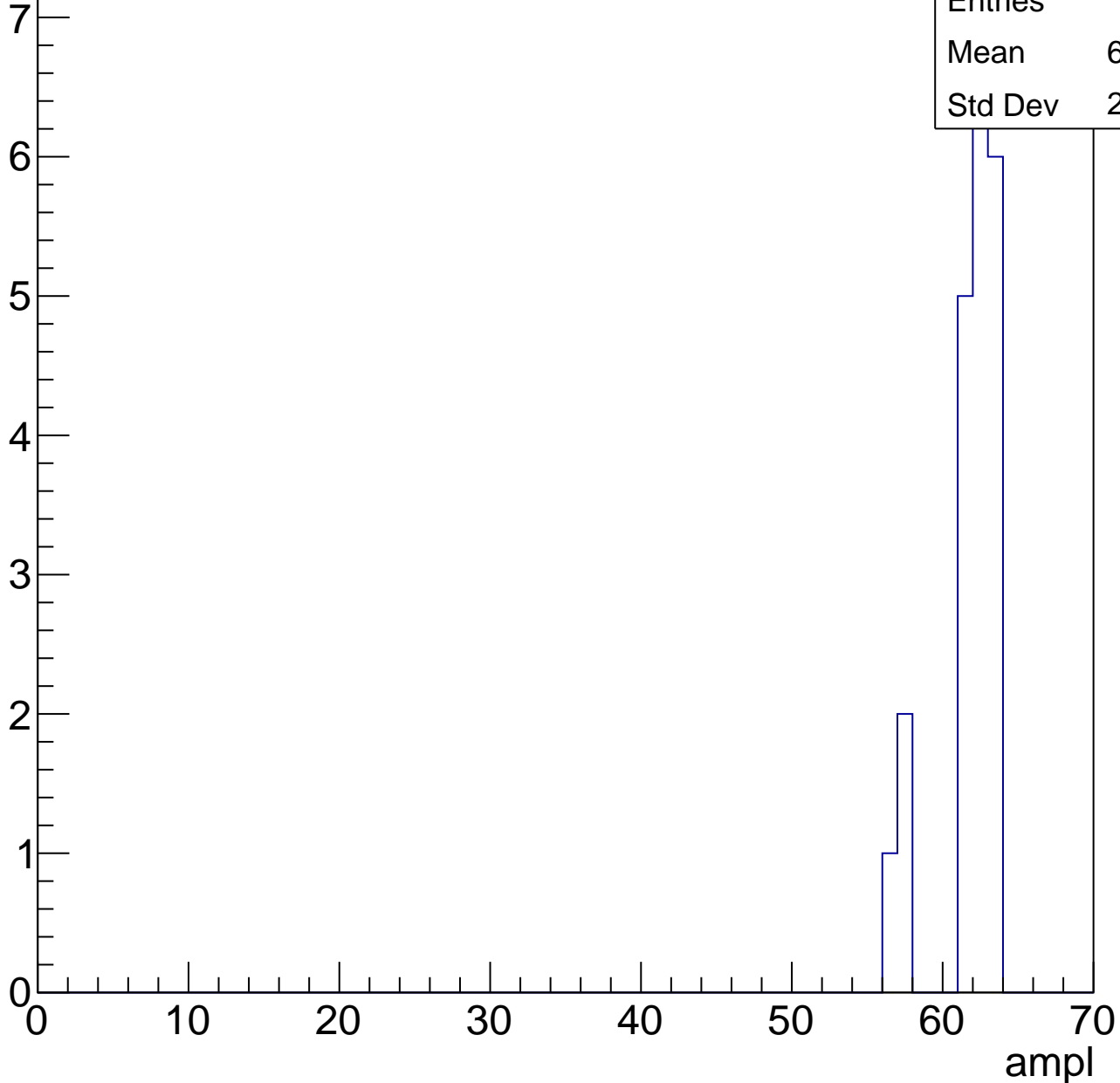


# B1L002S, U17-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.29
Std Dev	2.027





# B1L002S, U17-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch6, adc0

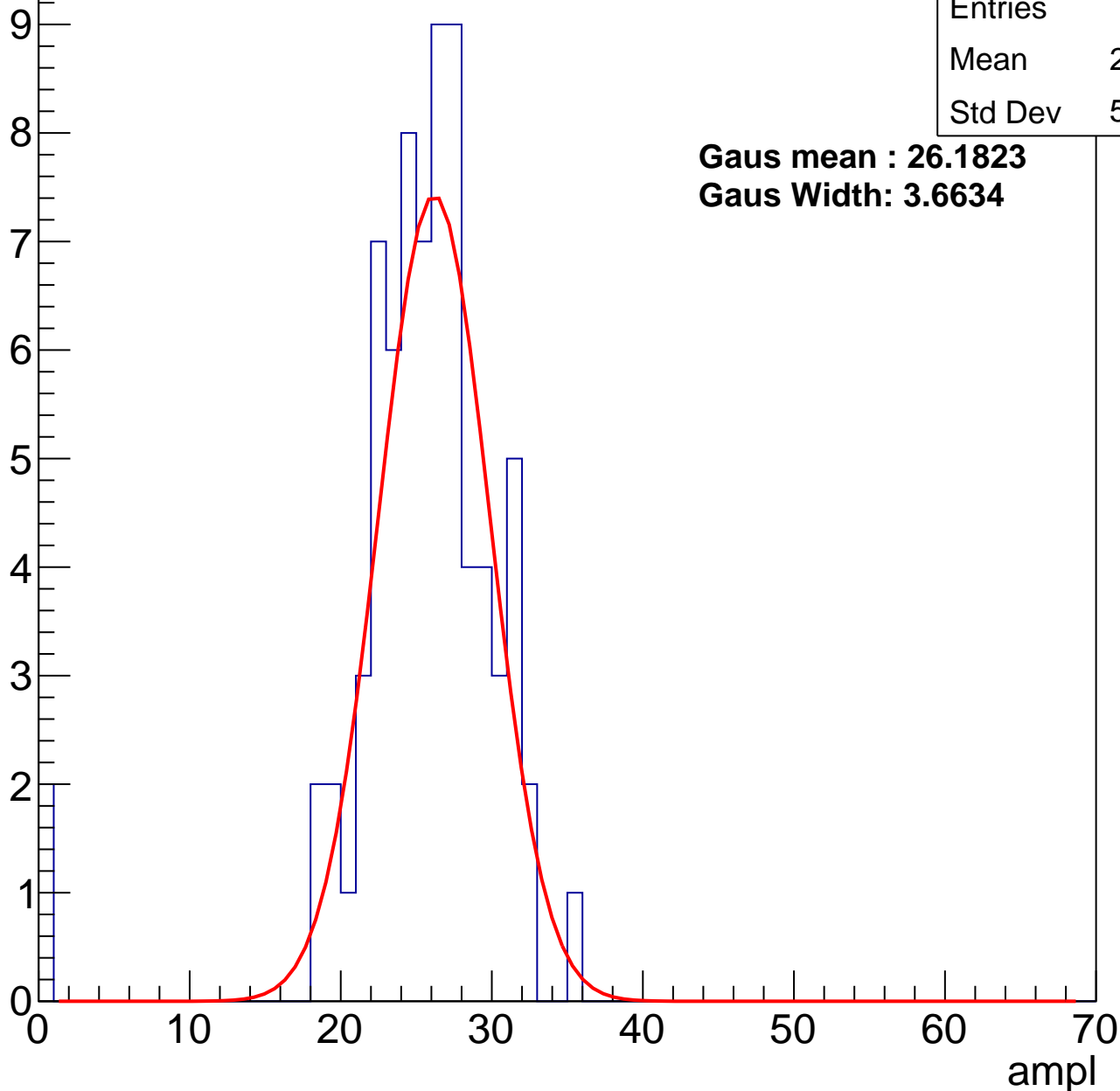
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	24.87
Std Dev	5.397

**Gaus mean : 26.1823**

**Gaus Width: 3.6634**



# B1L002S, U17-ch6, adc1

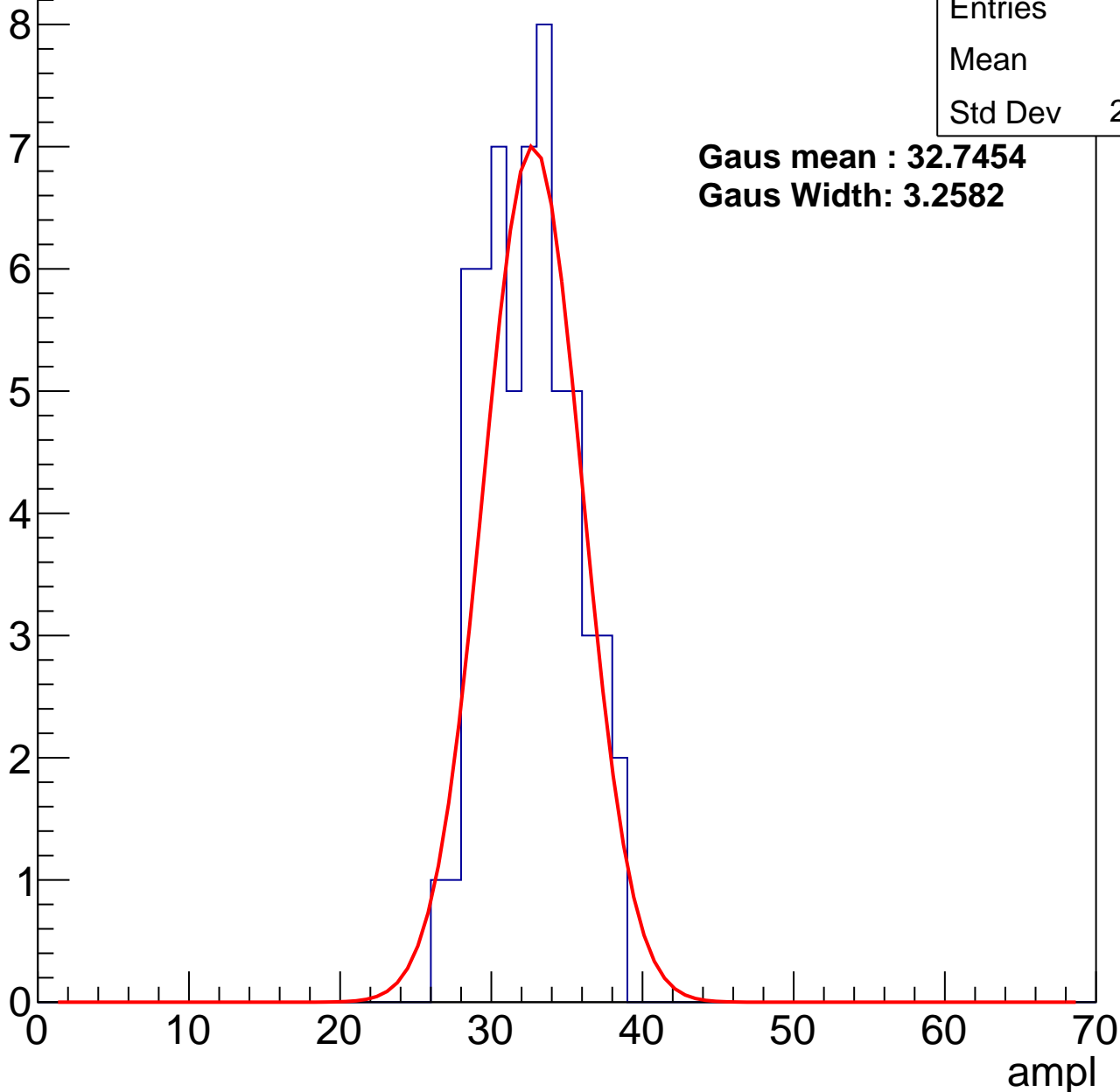
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	32
Std Dev	2.946

**Gaus mean : 32.7454**

**Gaus Width: 3.2582**



# B1L002S, U17-ch6, adc2

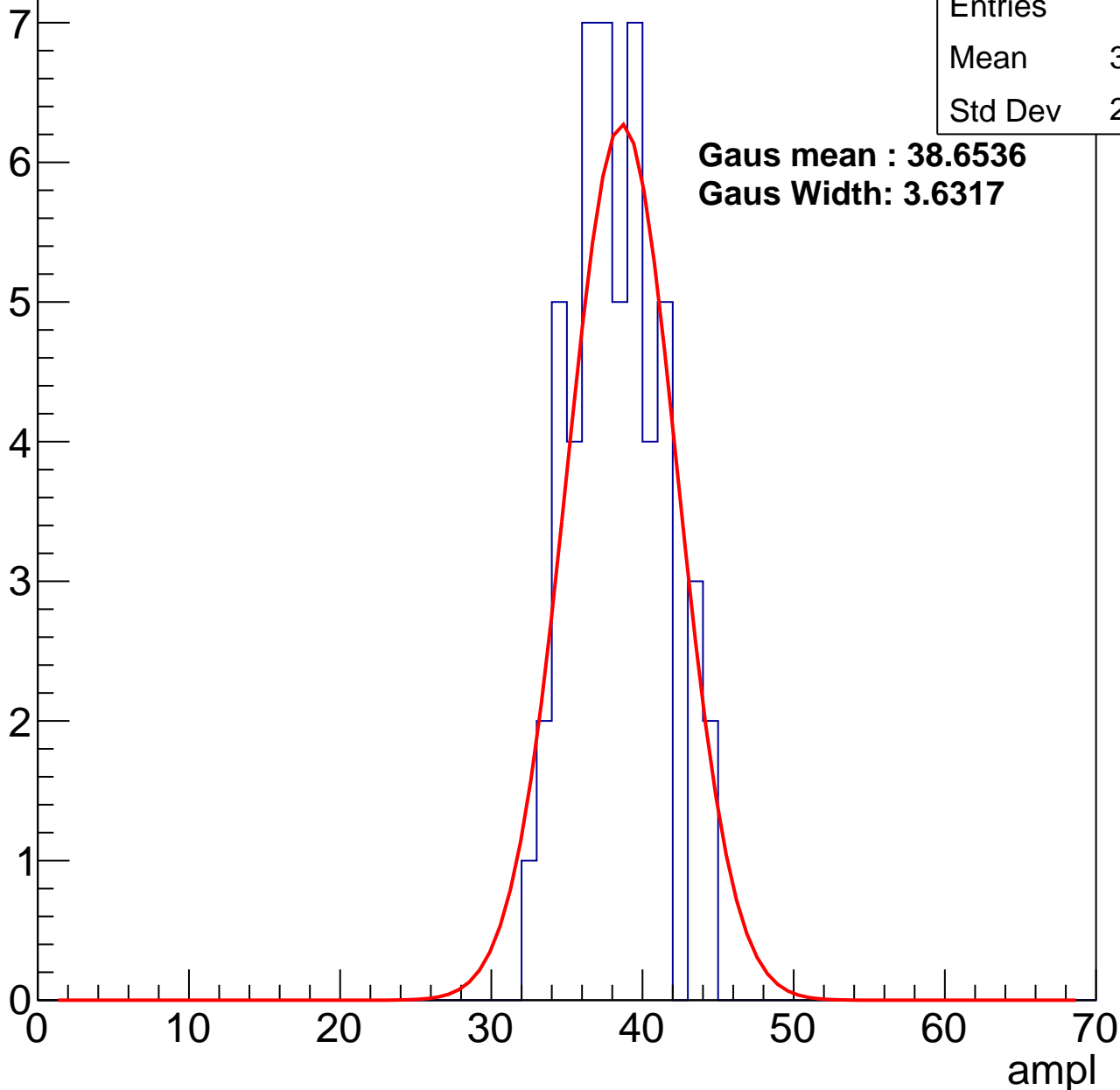
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	37.77
Std Dev	2.939

**Gaus mean : 38.6536**

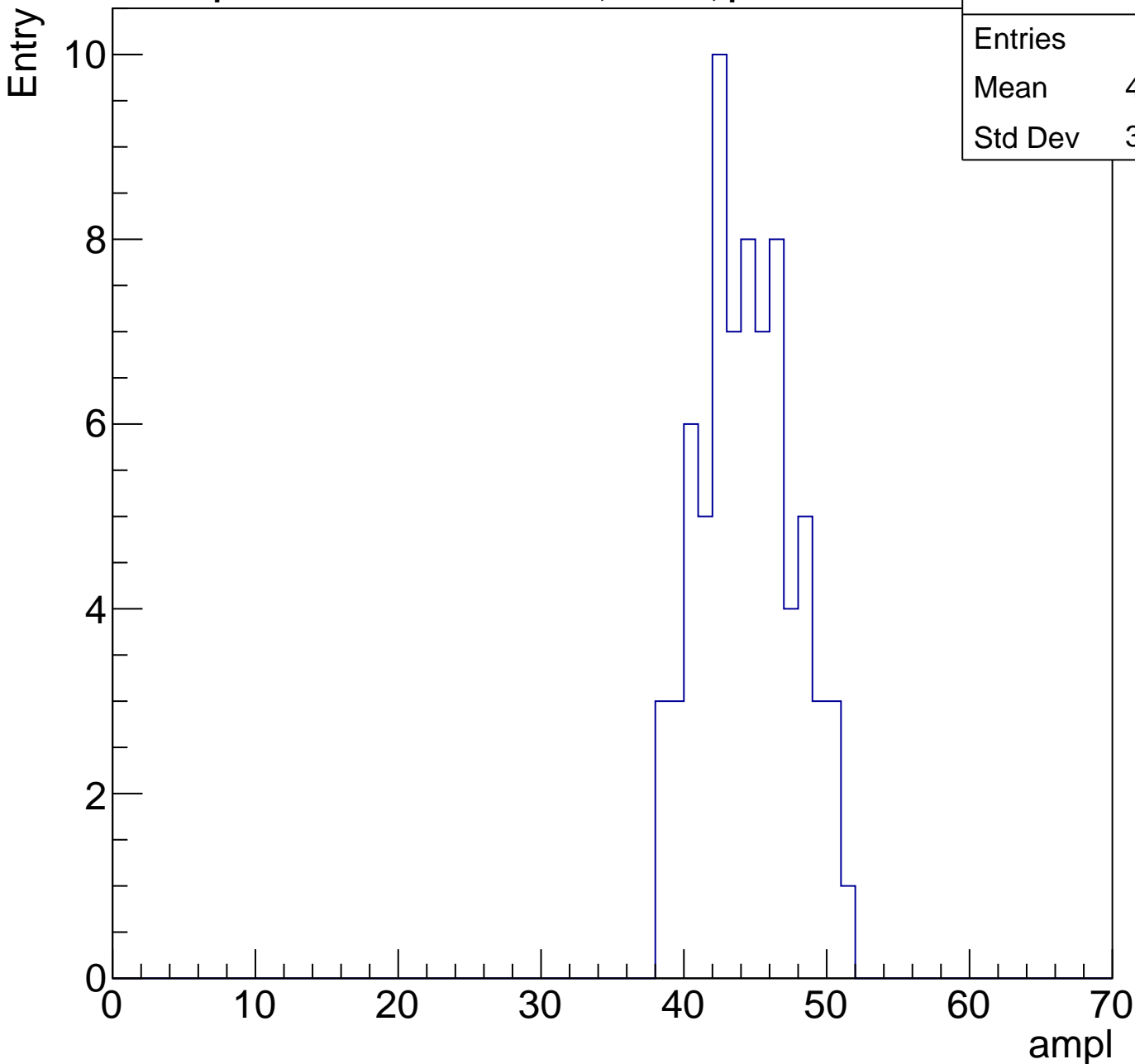
**Gaus Width: 3.6317**



# B1L002S, U17-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	43.95
Std Dev	3.222

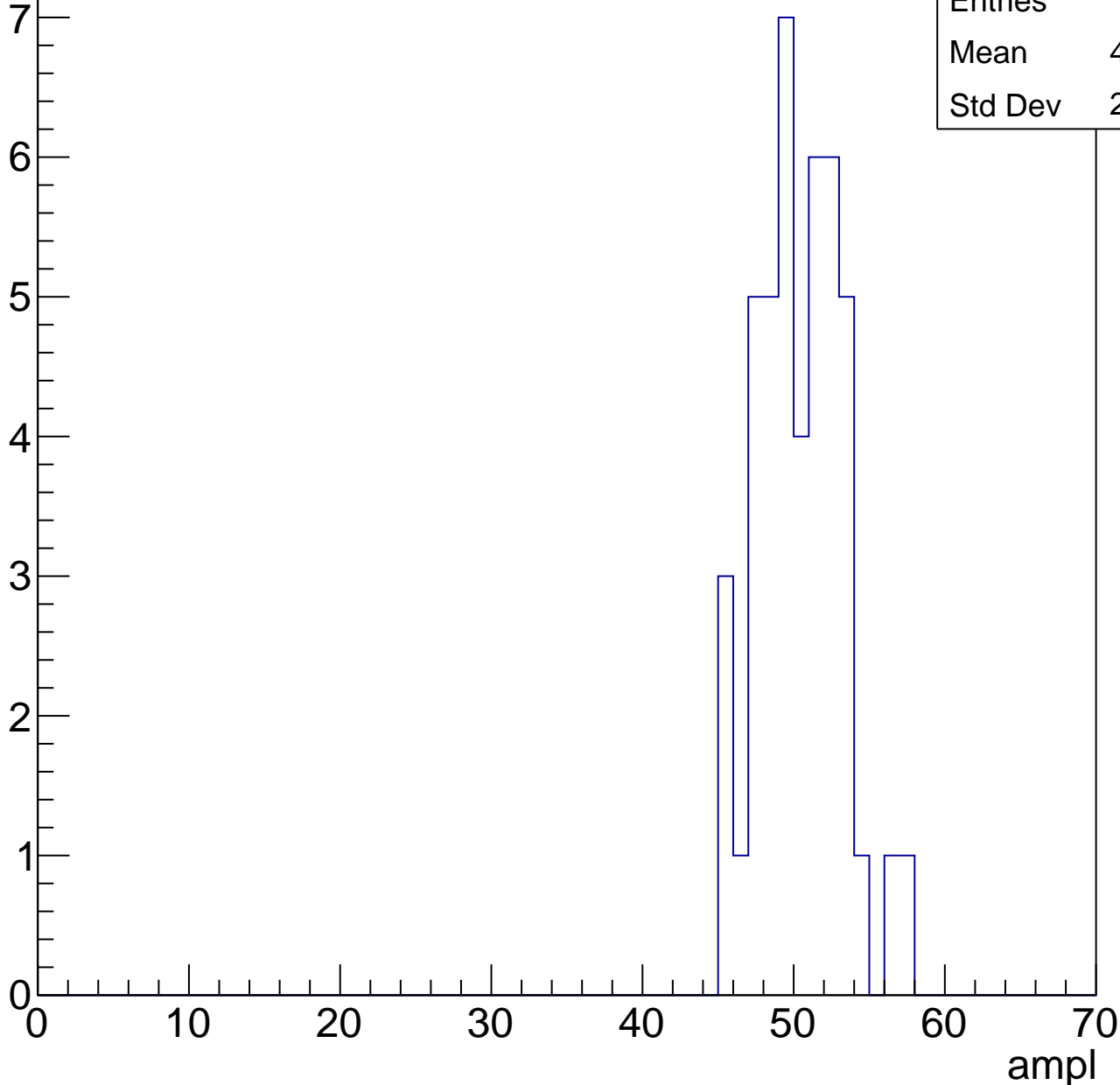


# B1L002S, U17-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

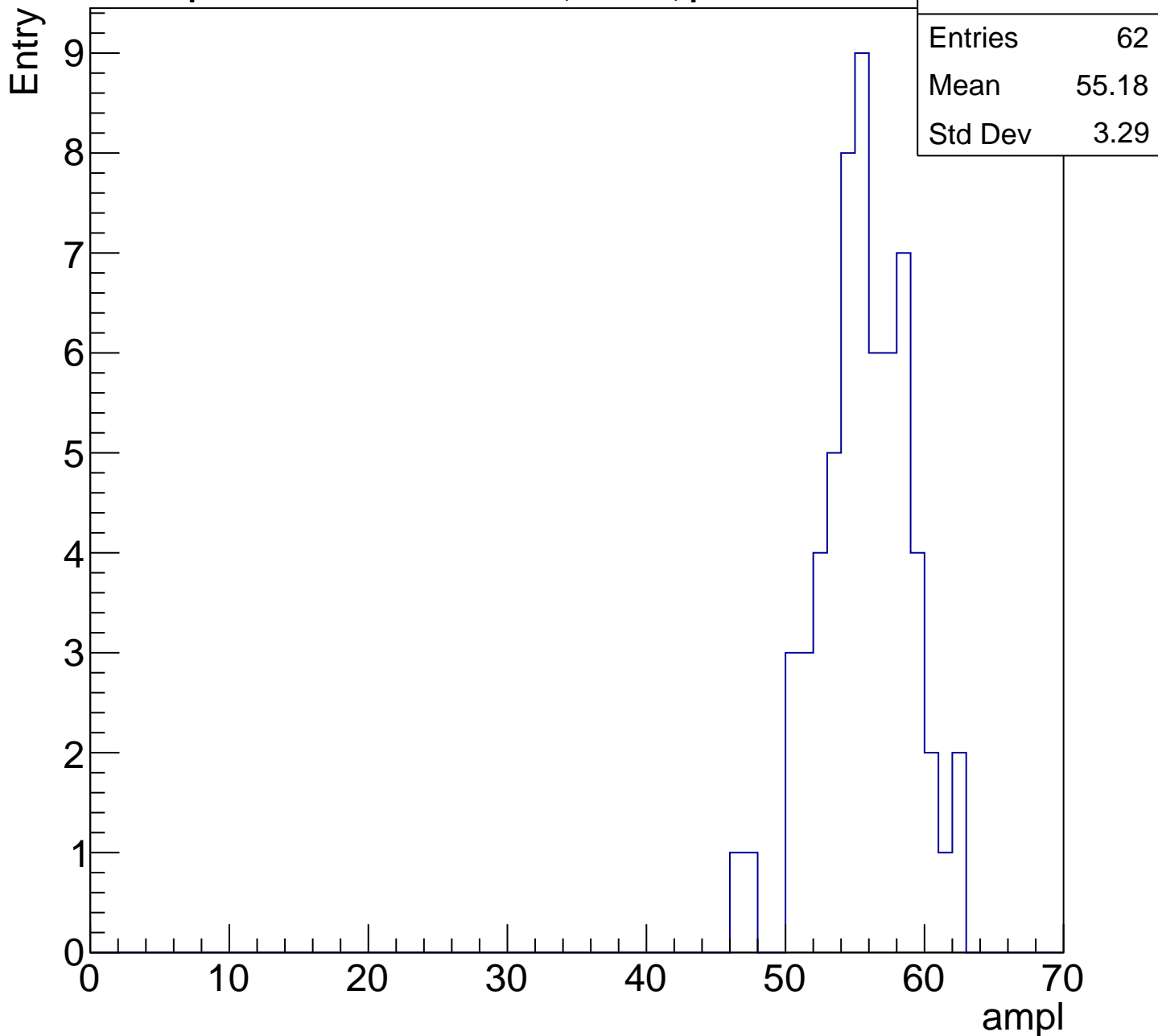
Entry

Entries	45
Mean	49.98
Std Dev	2.745



# B1L002S, U17-ch6, adc5

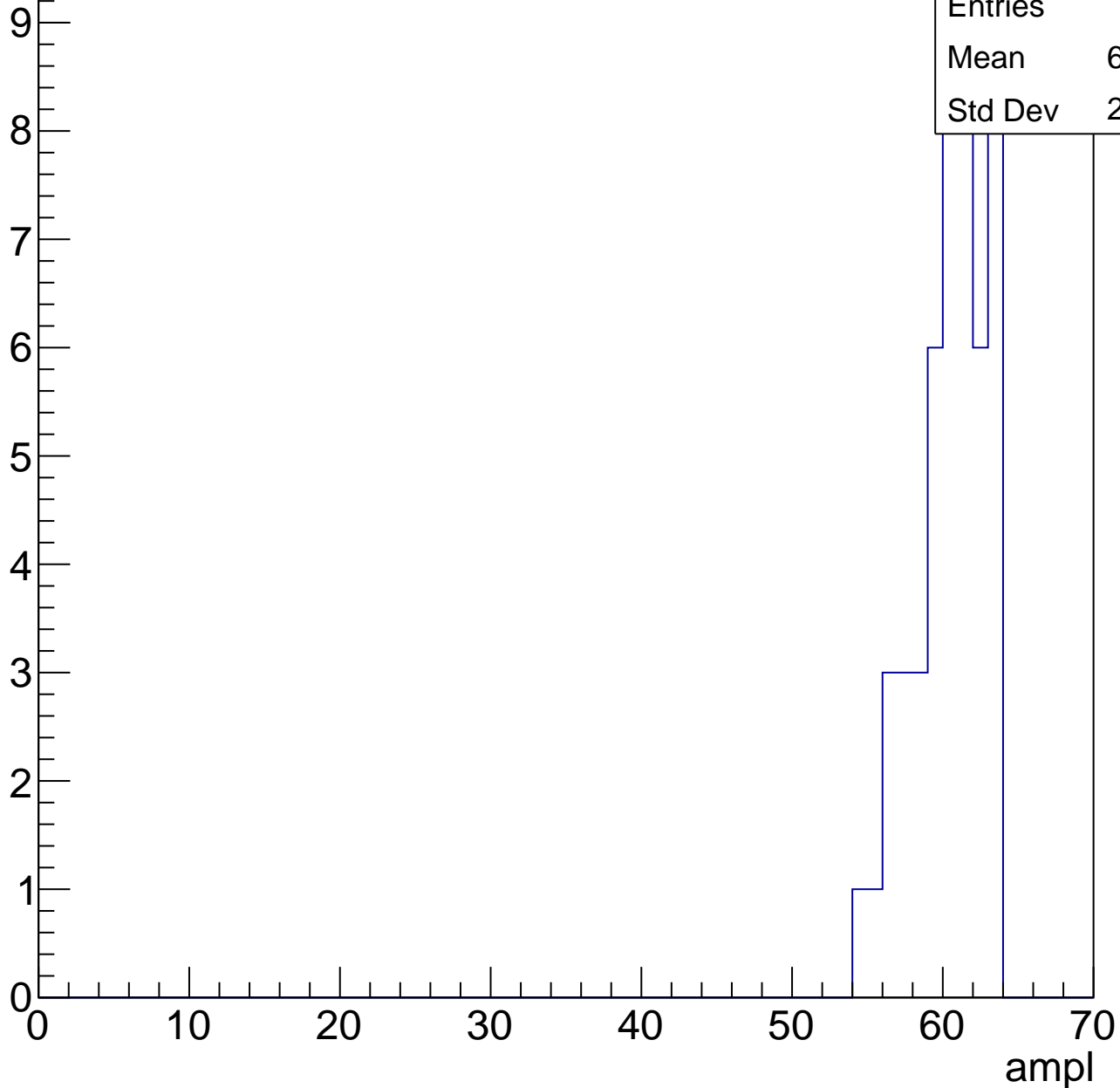
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U17-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



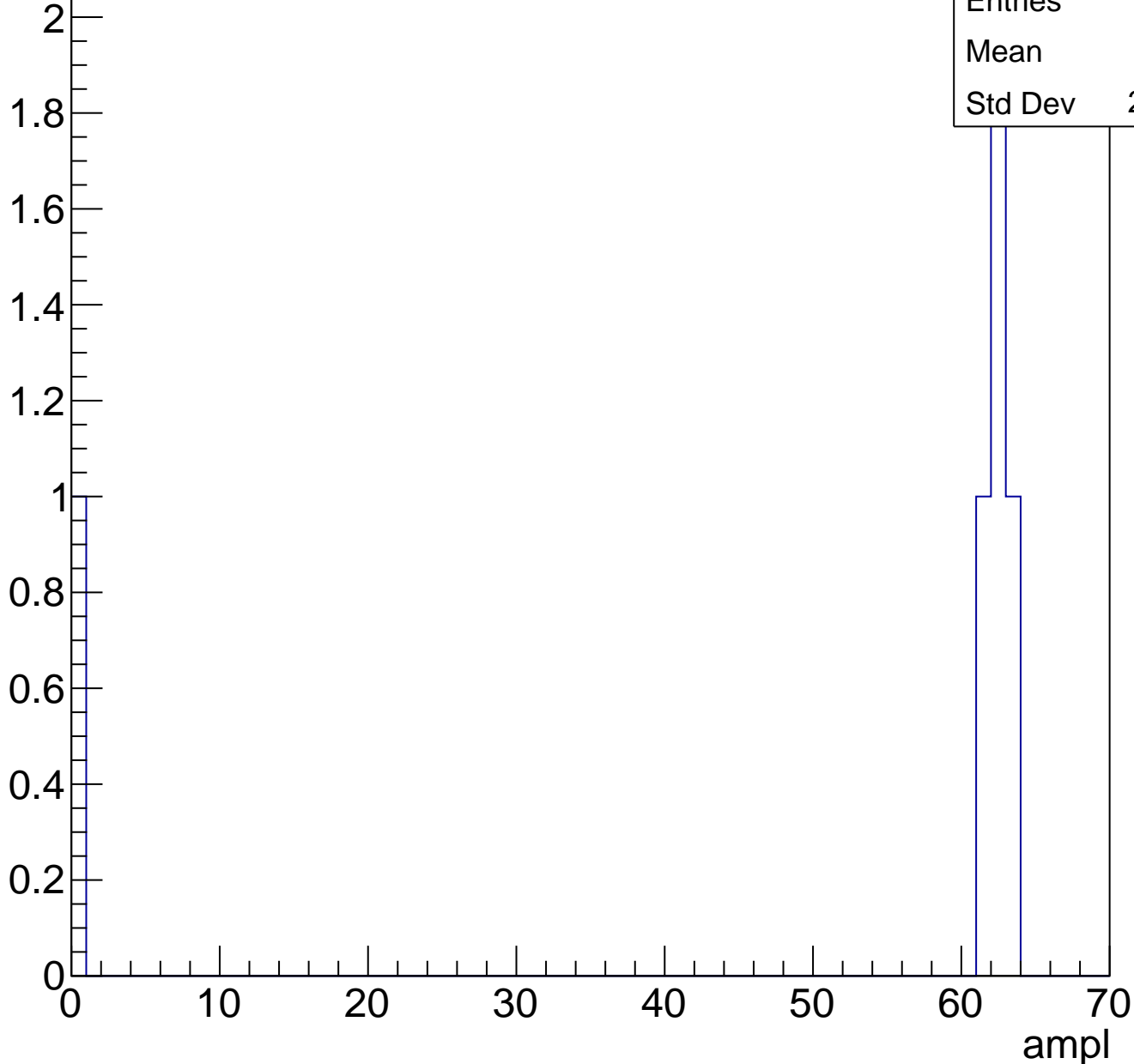
Entries	48
Mean	60.06
Std Dev	2.358



# B1L002S, U17-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch7, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	29.61
Std Dev	3.1

**Gaus mean : 29.7626**

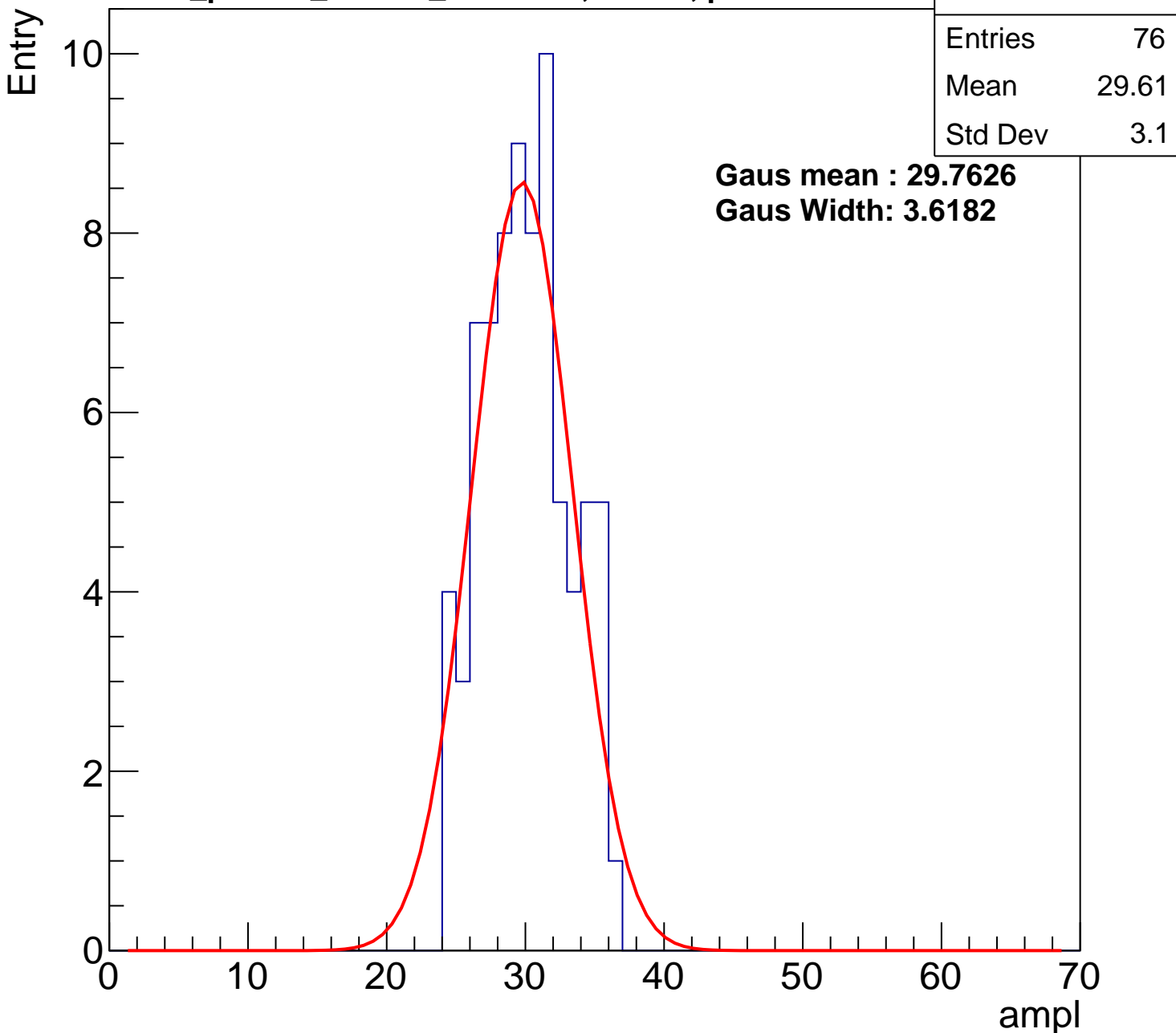
**Gaus Width: 3.6182**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	36.1
Std Dev	2.85

**Gaus mean : 37.0267**

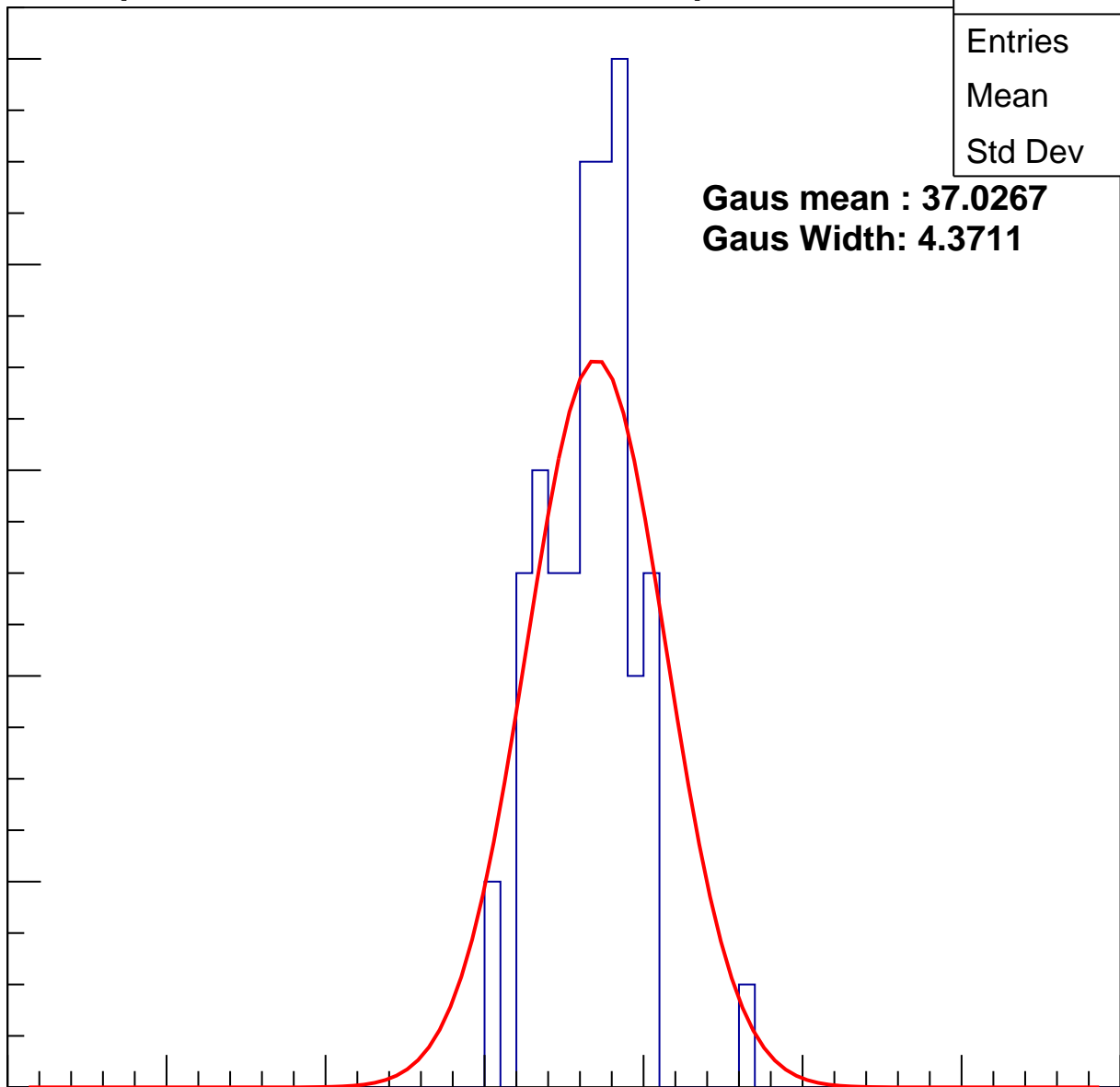
**Gaus Width: 4.3711**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch7, adc2

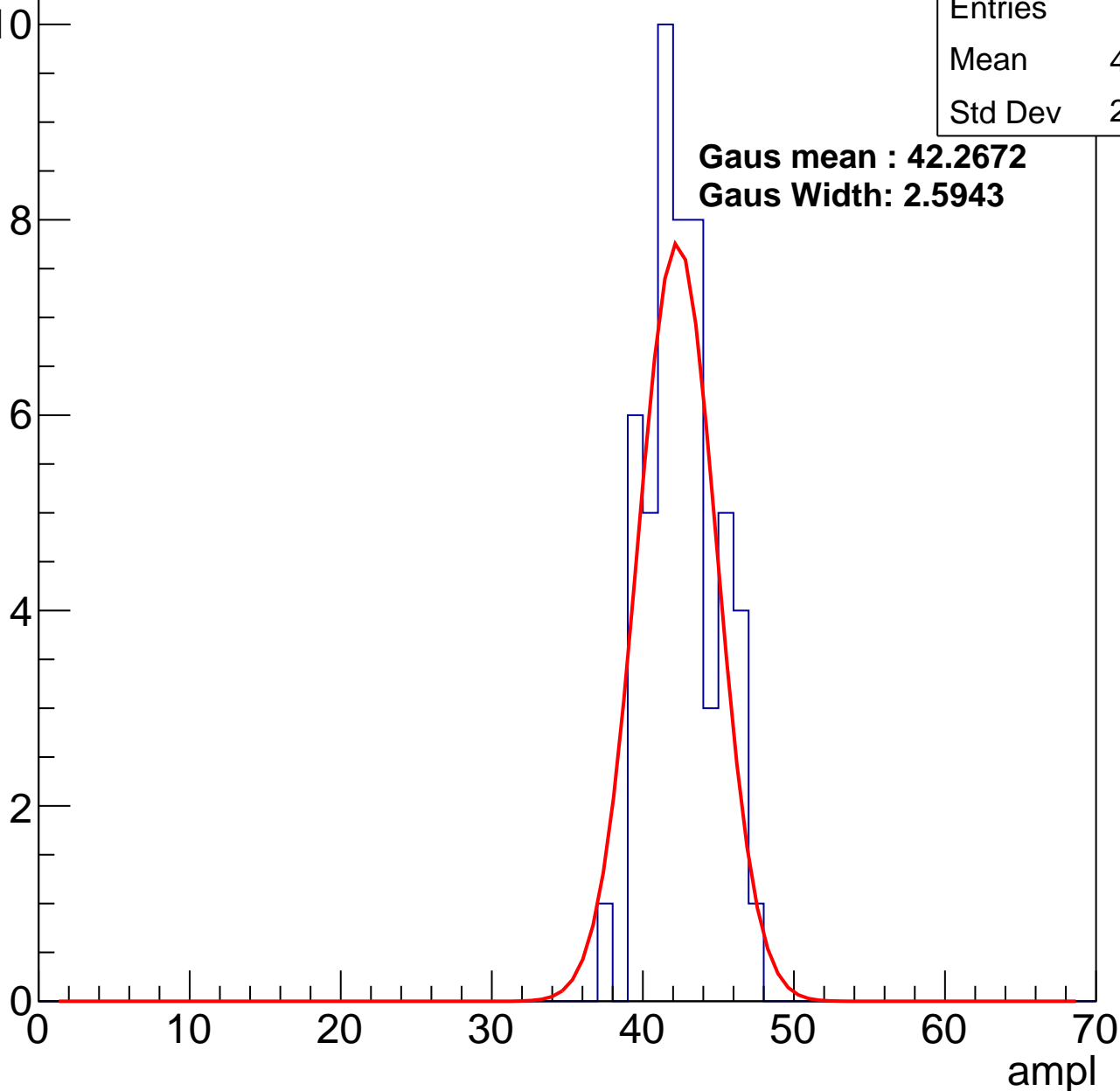
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	42.14
Std Dev	2.267

**Gaus mean : 42.2672**

**Gaus Width: 2.5943**

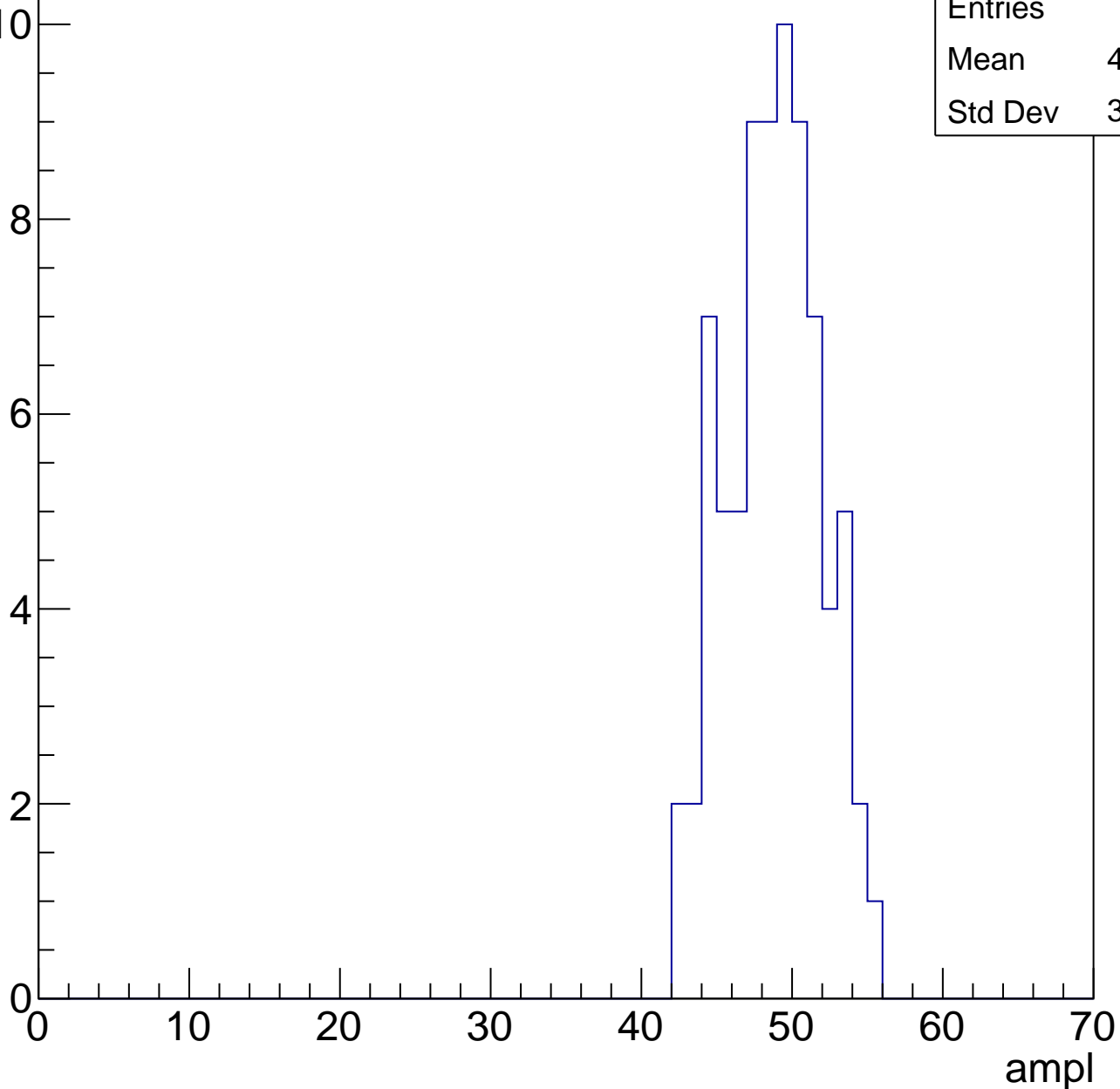


# B1L002S, U17-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

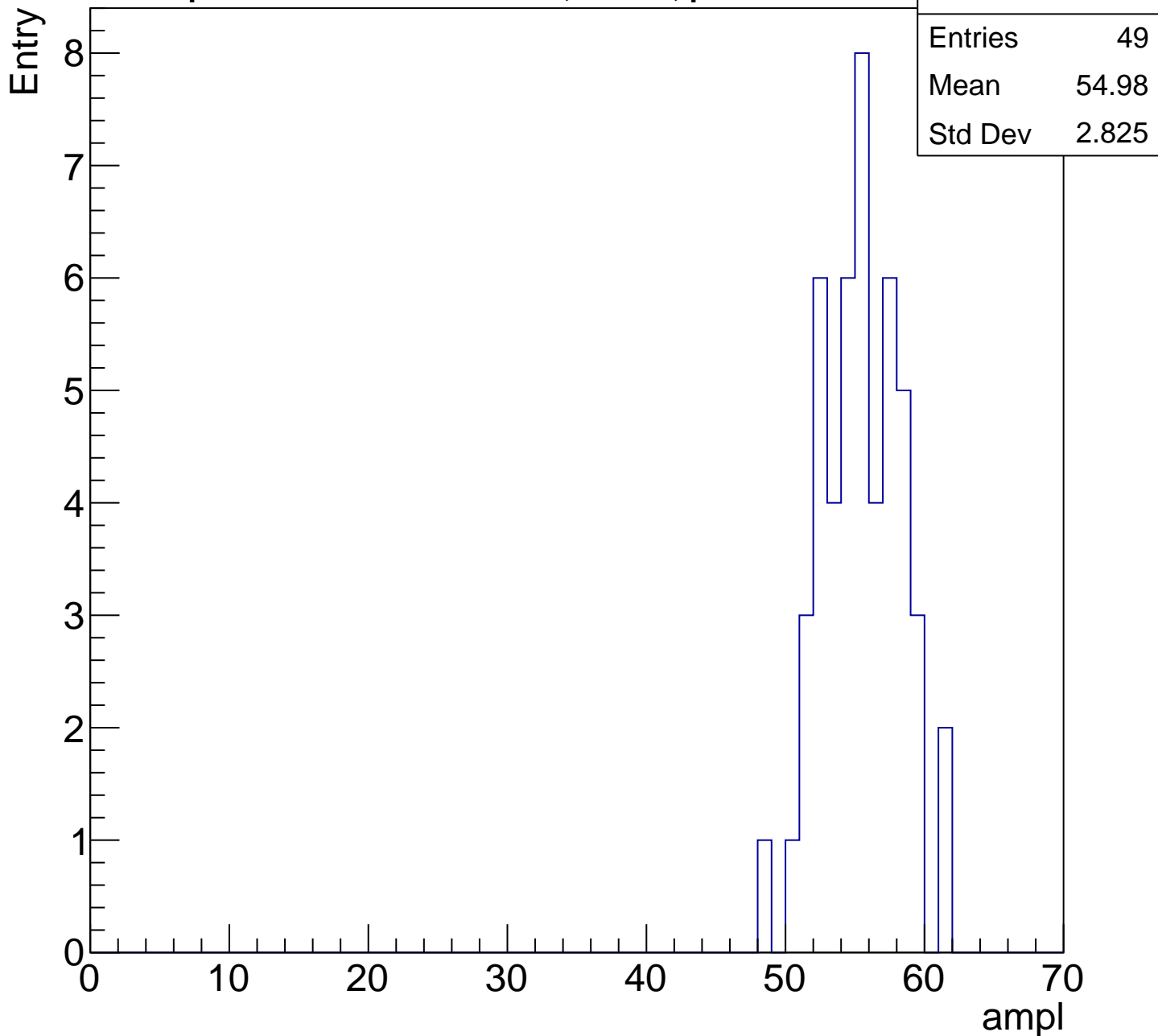
Entry

Entries	77
Mean	48.32
Std Dev	3.056



# B1L002S, U17-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

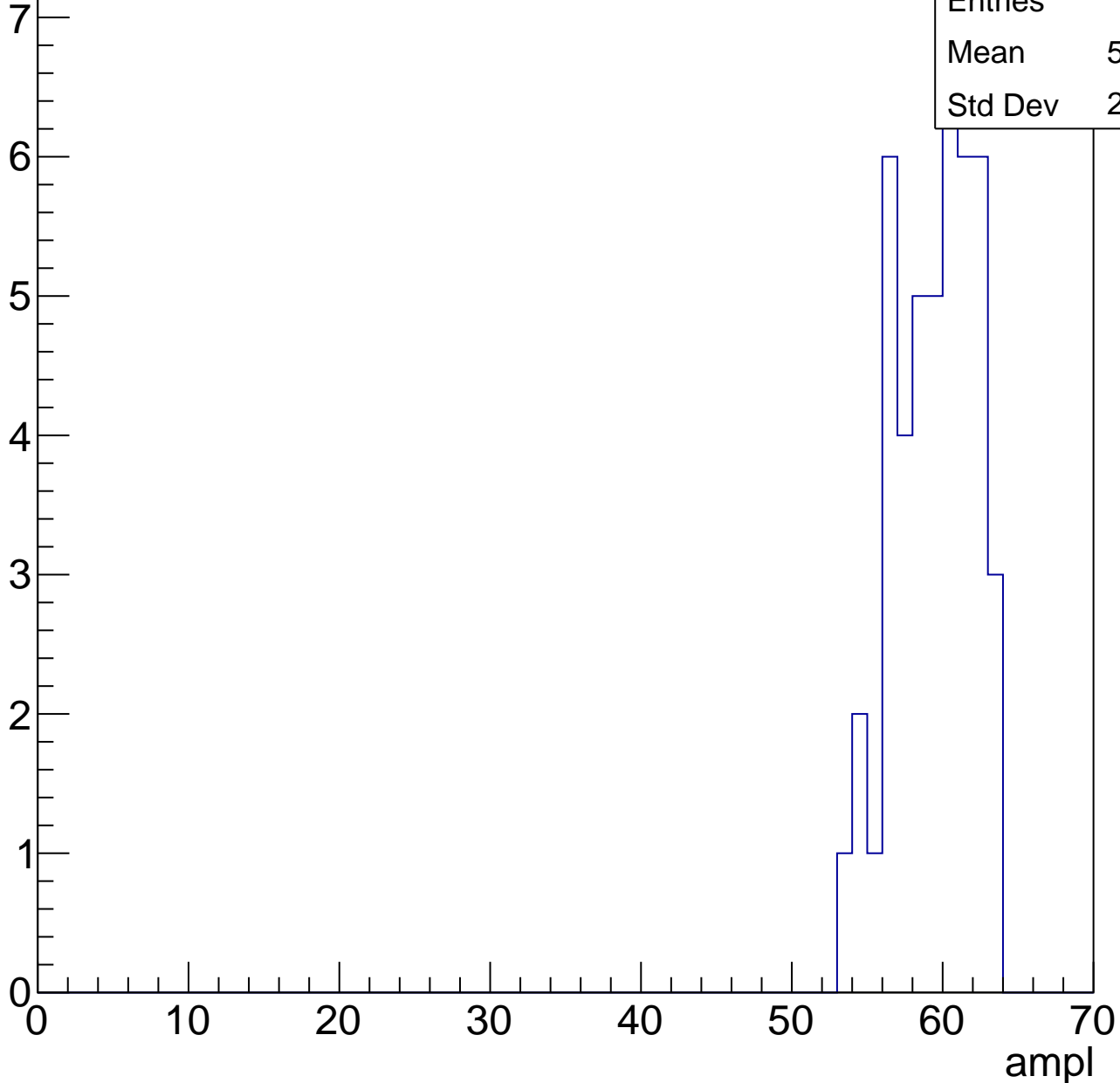


# B1L002S, U17-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	58.96
Std Dev	2.596

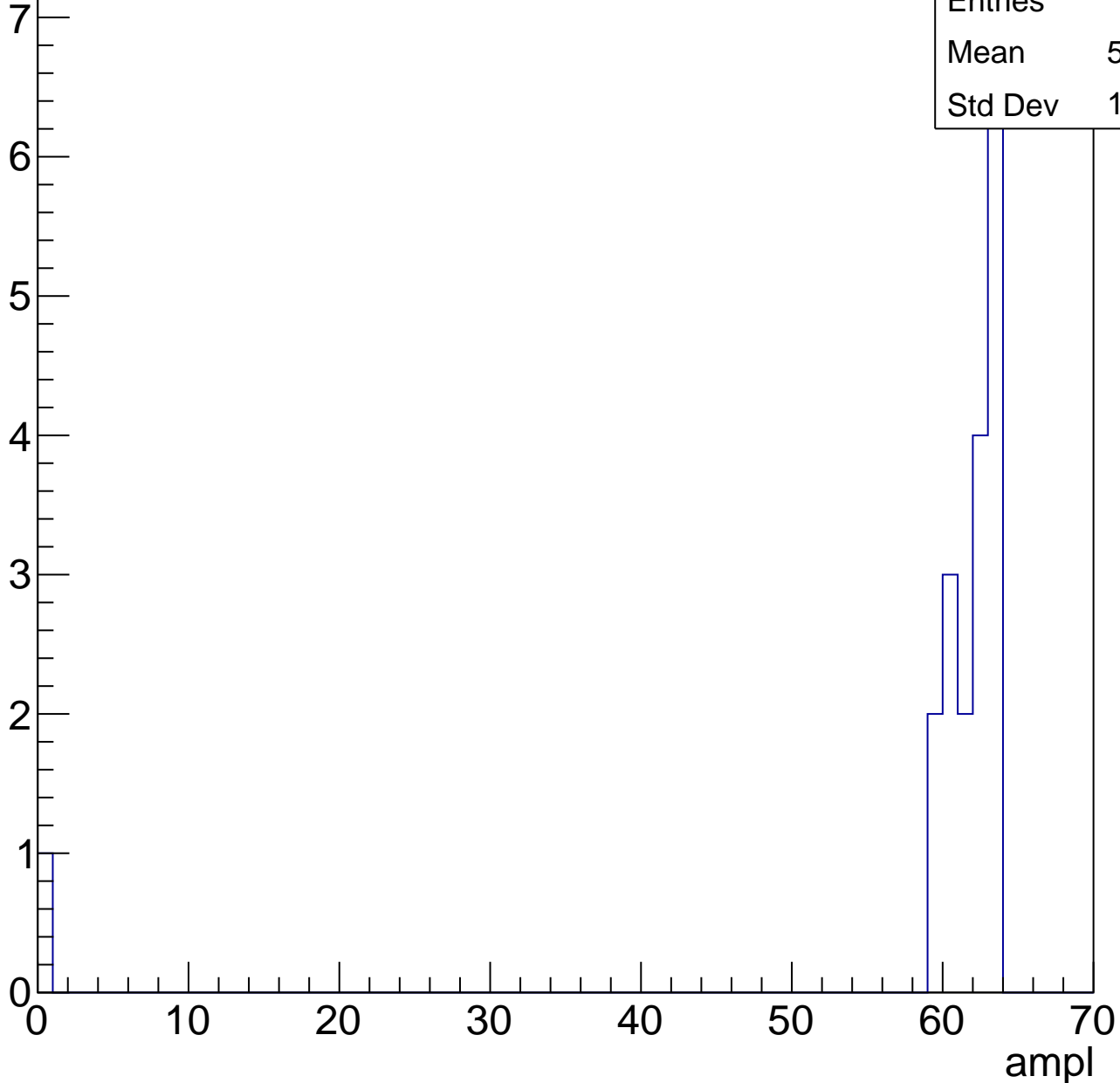


# B1L002S, U17-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.37
Std Dev	13.83





# B1L002S, U17-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7.667
Std Dev	10.84

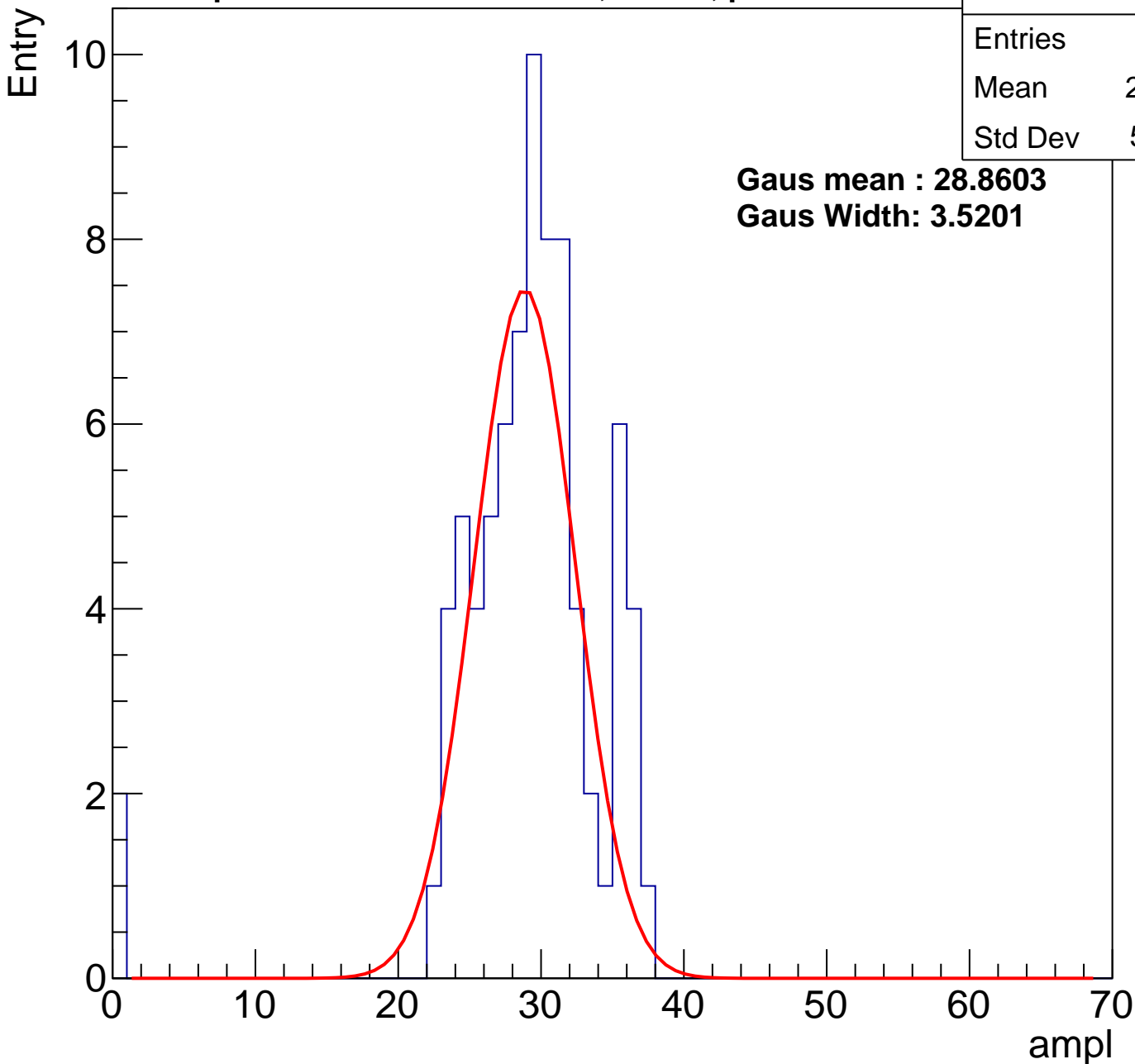
# B1L002S, U17-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	28.45
Std Dev	5.911

**Gaus mean : 28.8603**

**Gaus Width: 3.5201**



# B1L002S, U17-ch8, adc1

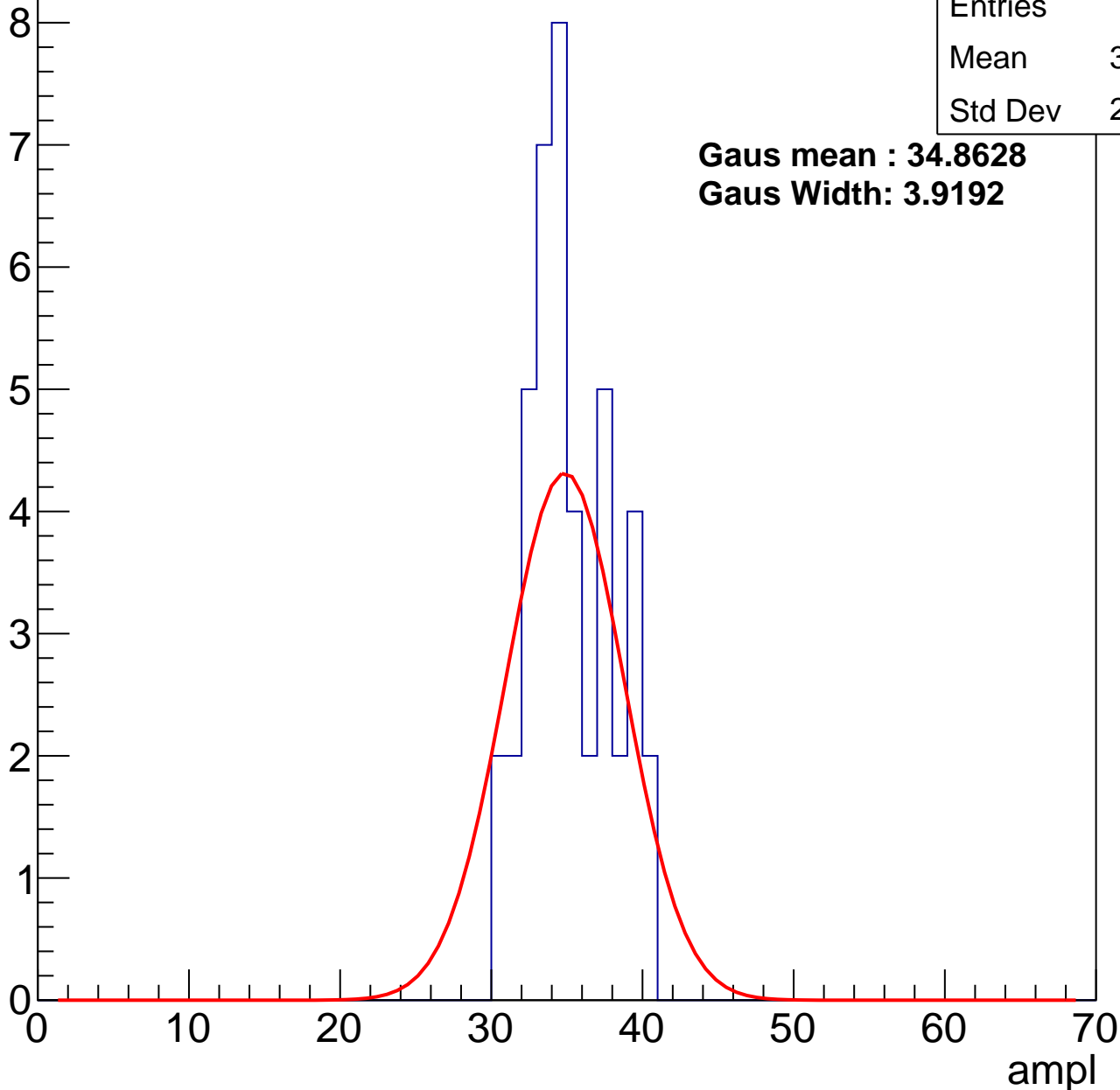
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	34.74
Std Dev	2.703

**Gaus mean : 34.8628**

**Gaus Width: 3.9192**



# B1L002S, U17-ch8, adc2

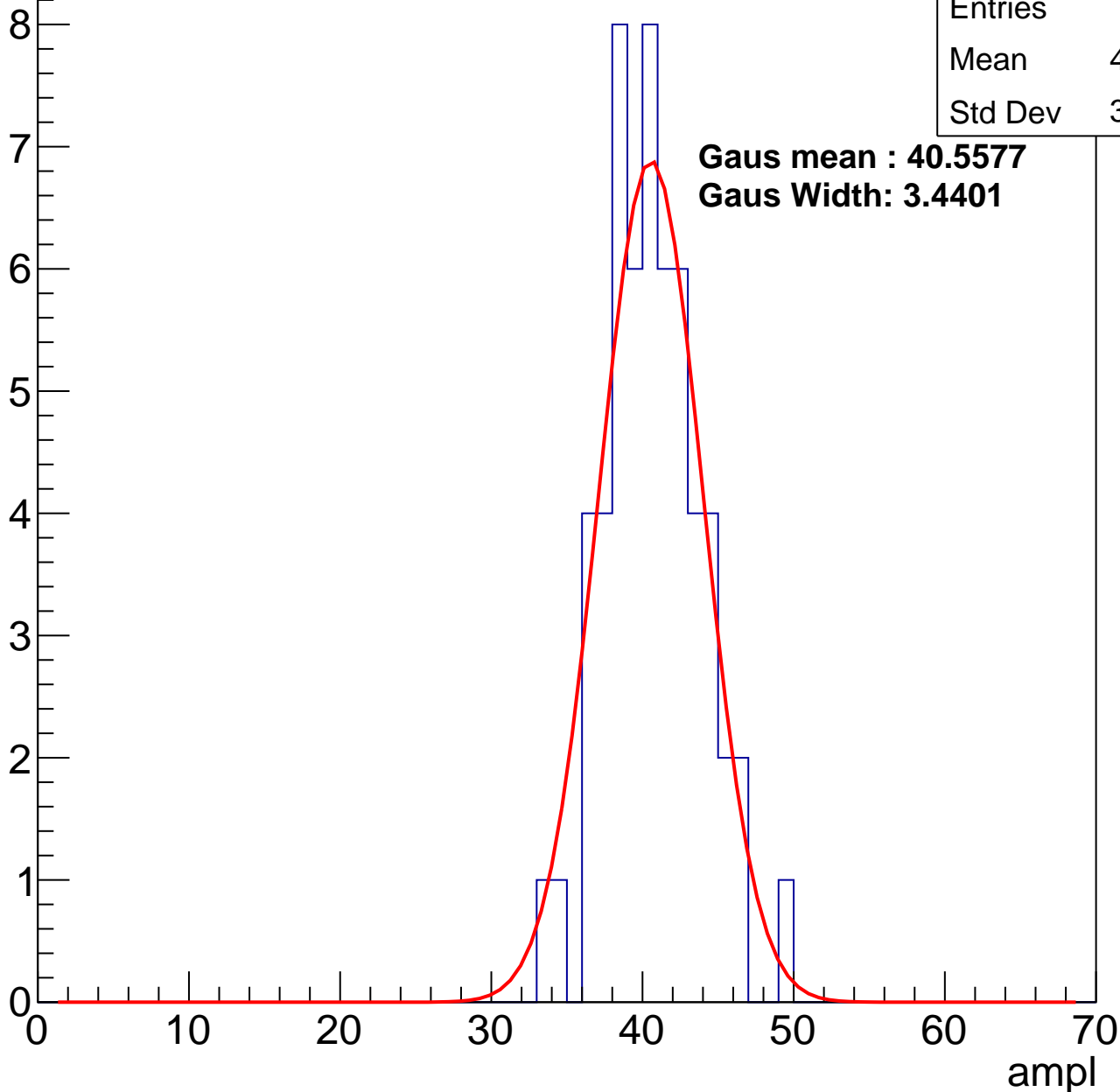
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.25
Std Dev	3.114

**Gaus mean : 40.5577**

**Gaus Width: 3.4401**

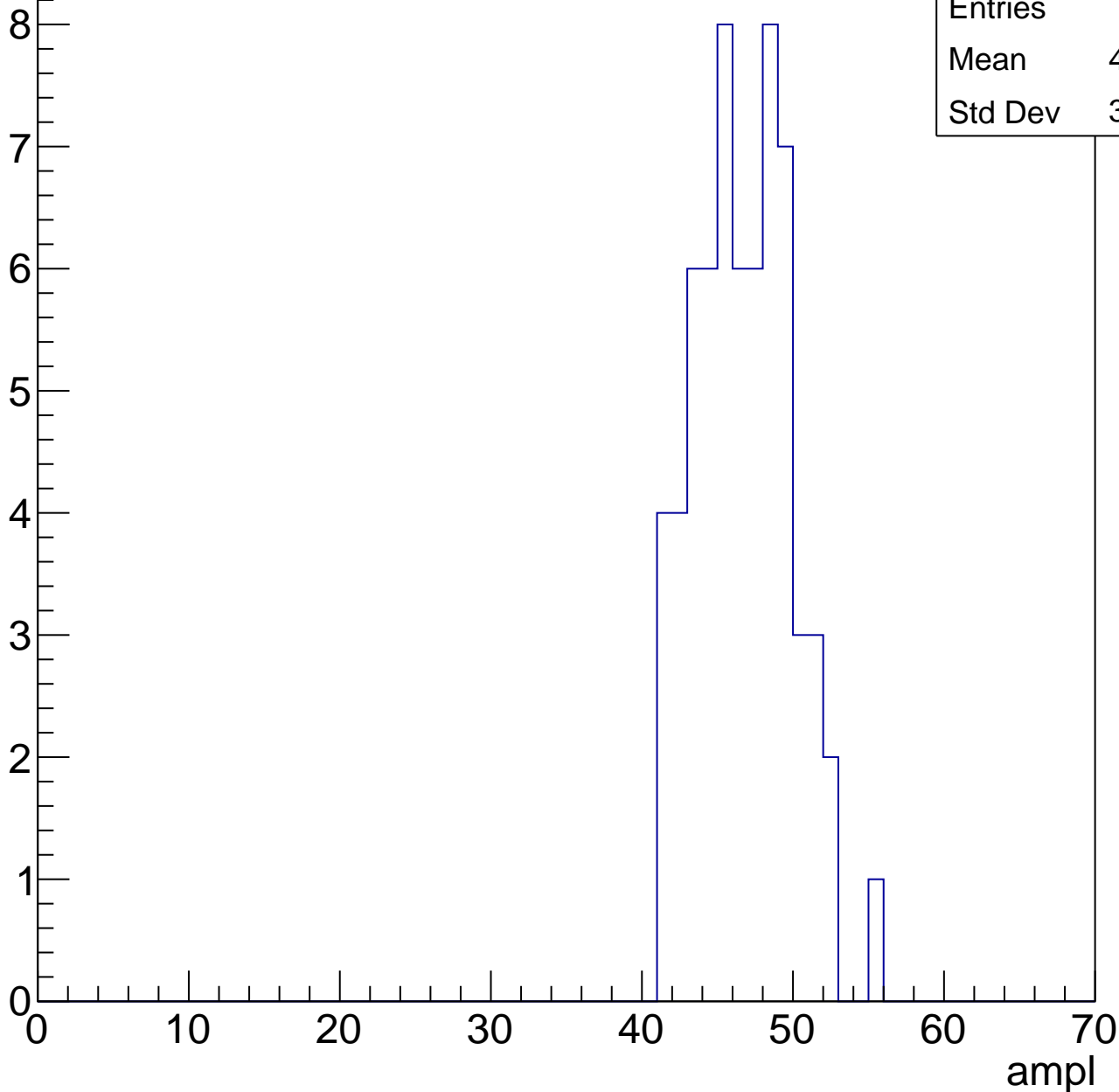


# B1L002S, U17-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.27
Std Dev	3.119

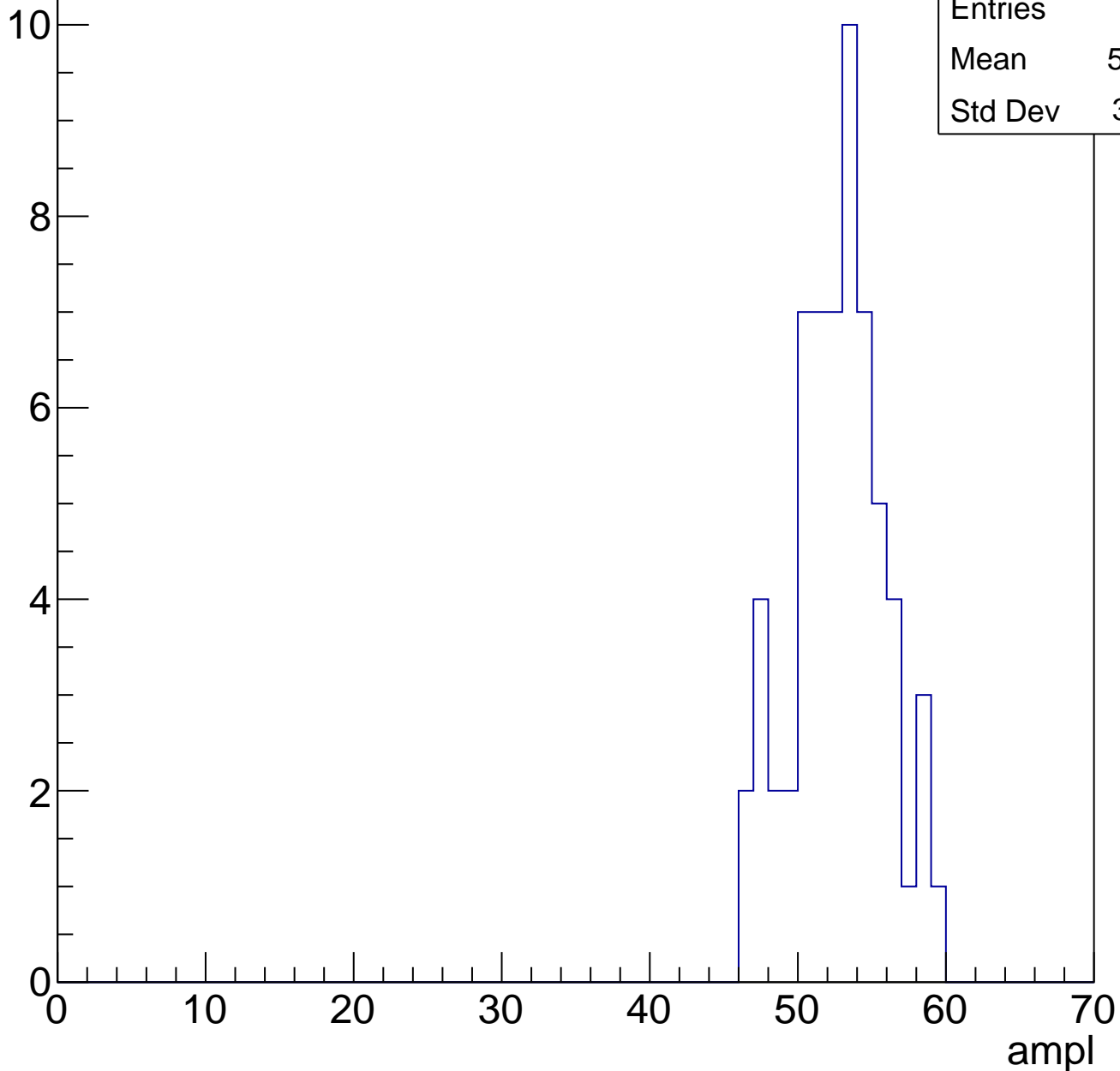


# B1L002S, U17-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

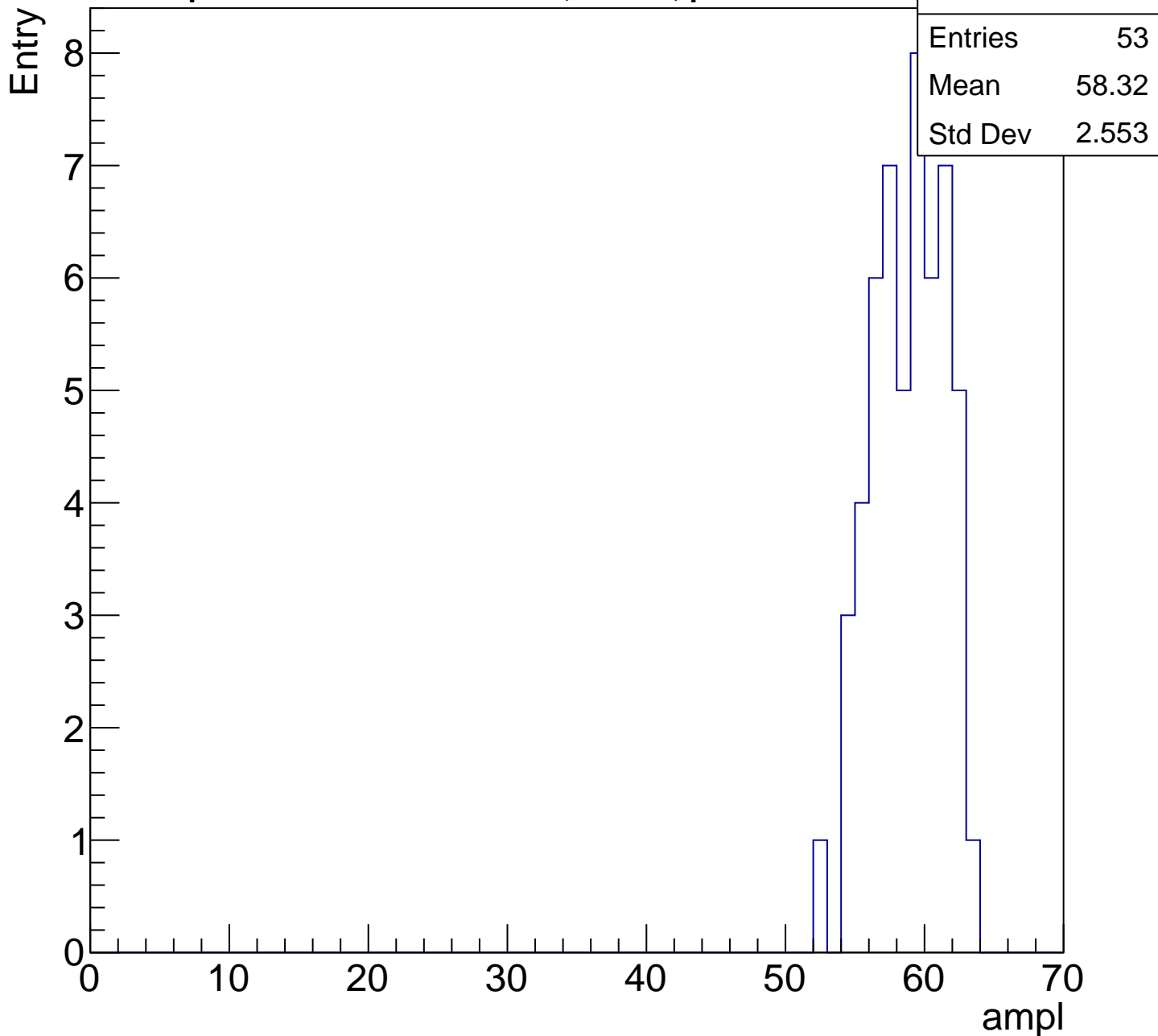
Entries	62
Mean	52.29
Std Dev	3.061

Entry



# B1L002S, U17-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

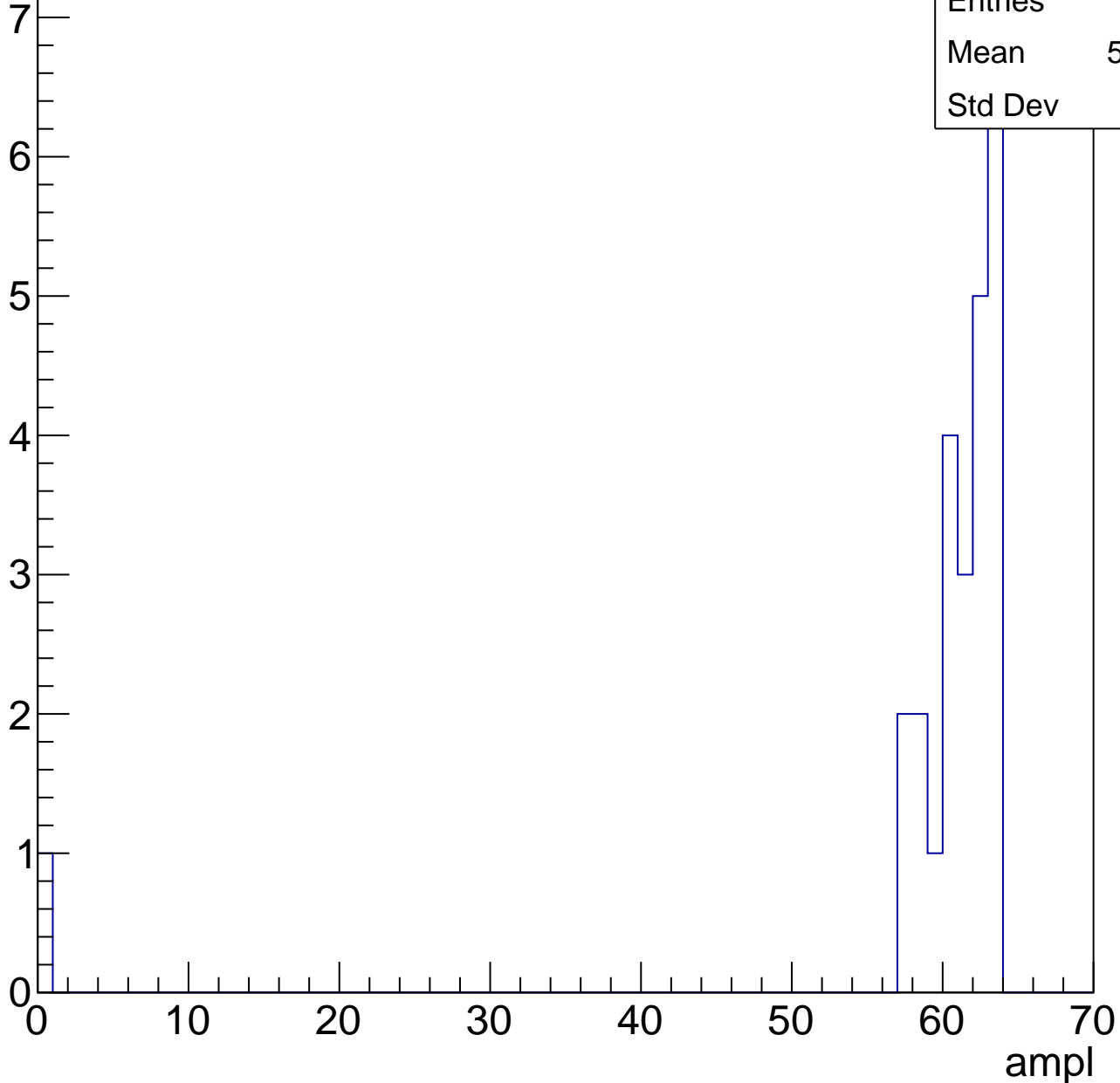


# B1L002S, U17-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.52
Std Dev	12.1

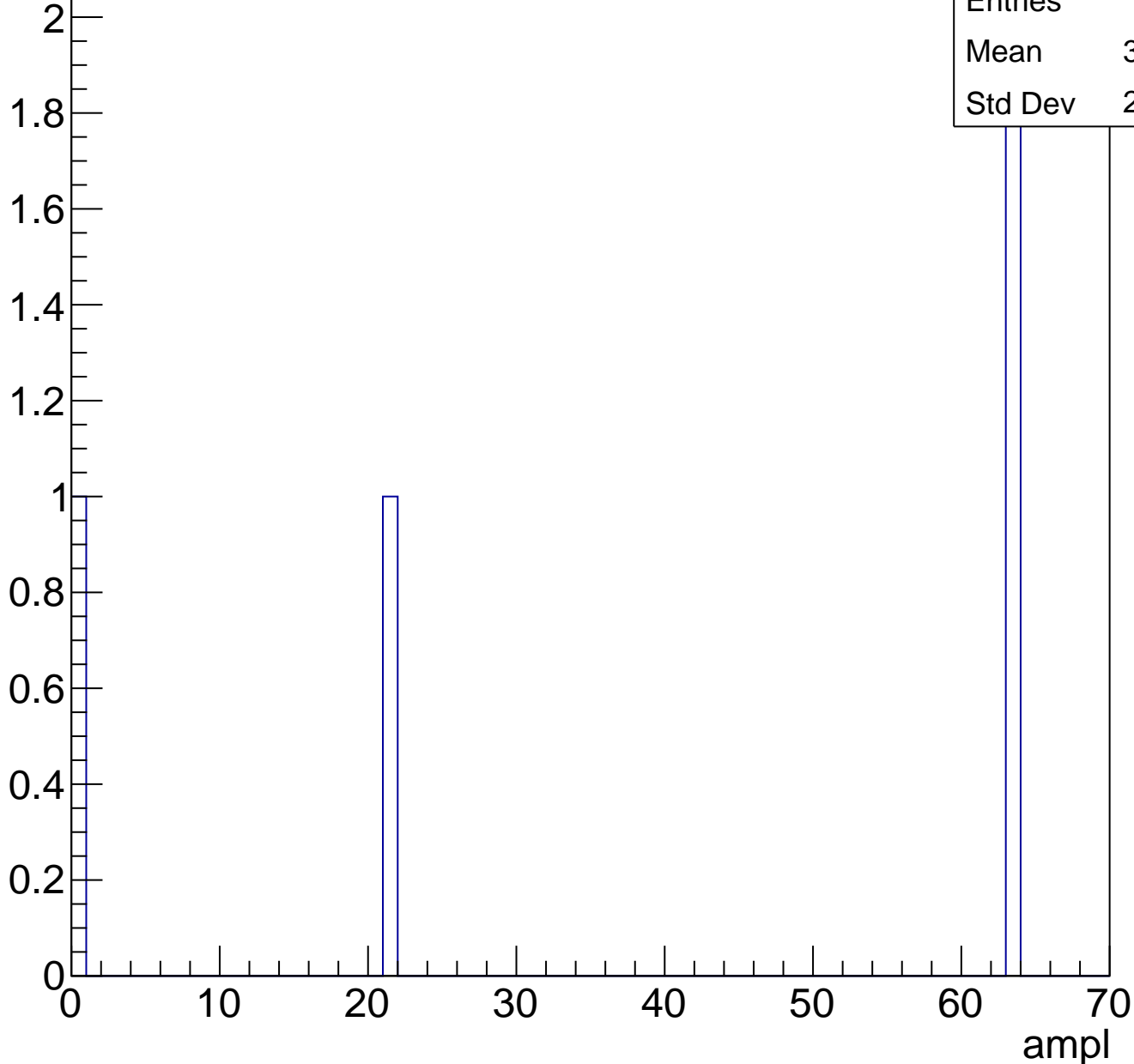




# B1L002S, U17-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch9, adc0

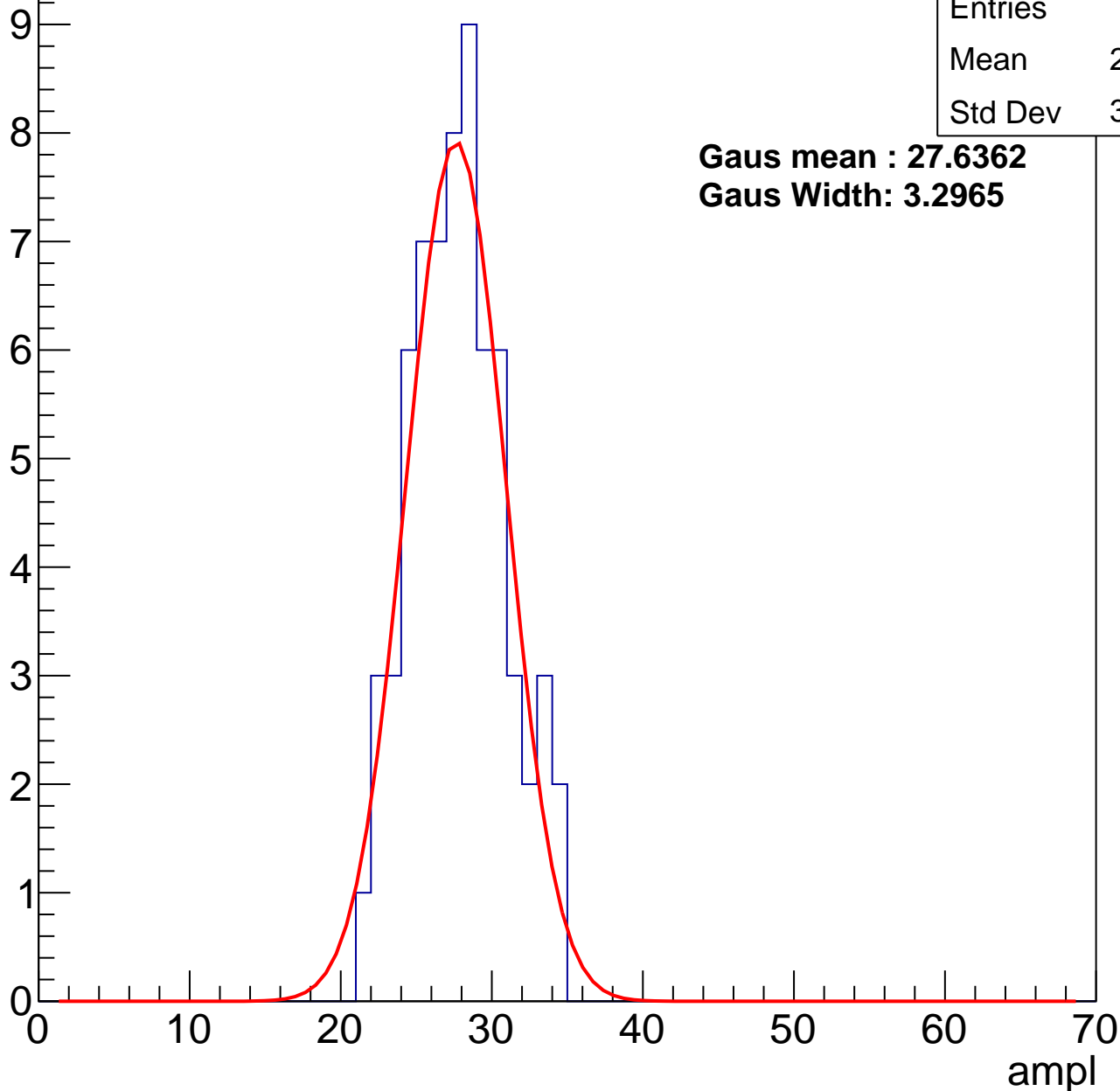
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.32
Std Dev	3.095

**Gaus mean : 27.6362**

**Gaus Width: 3.2965**



# B1L002S, U17-ch9, adc1

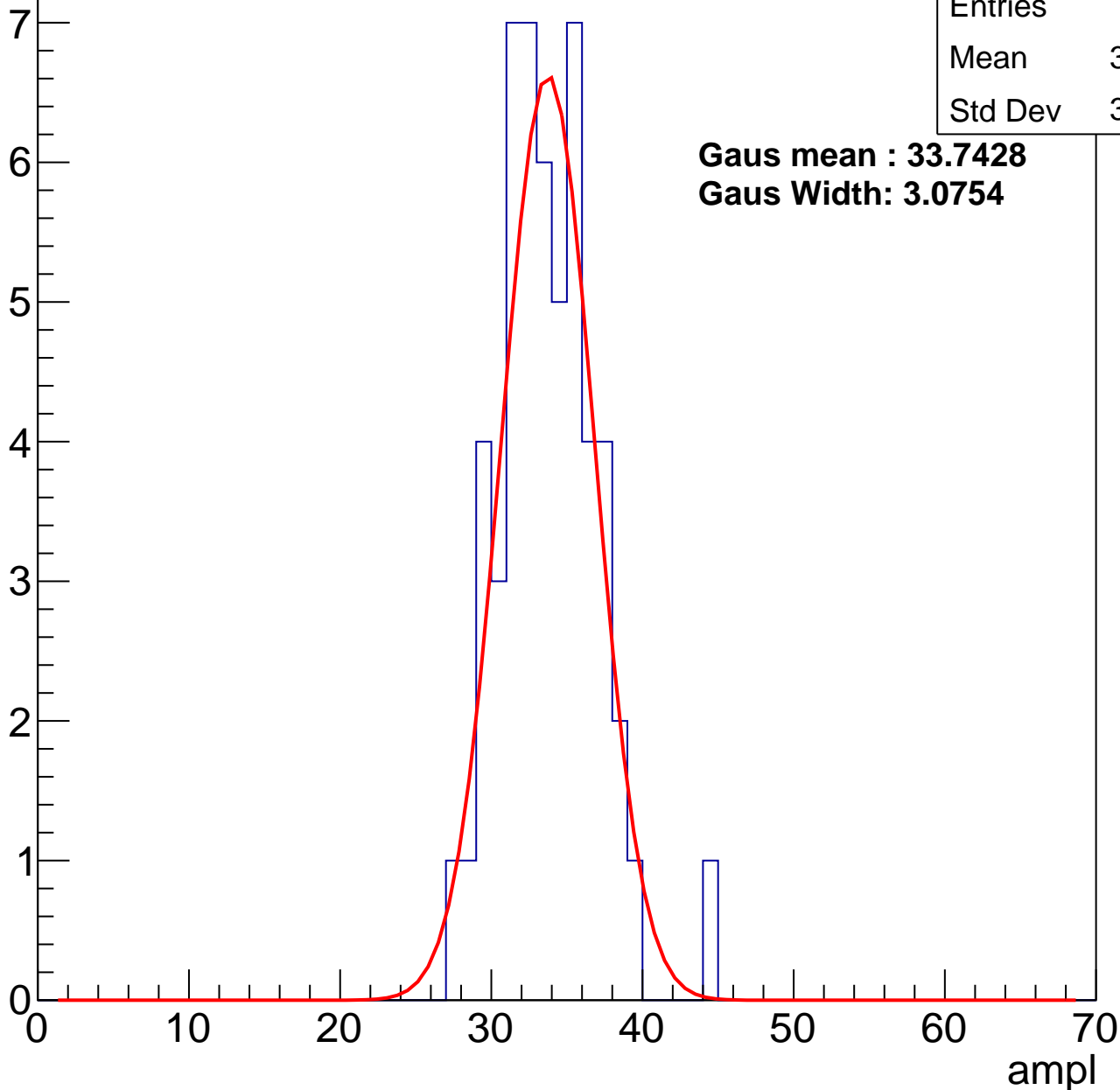
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	33.32
Std Dev	3.137

**Gaus mean : 33.7428**

**Gaus Width: 3.0754**



# B1L002S, U17-ch9, adc2

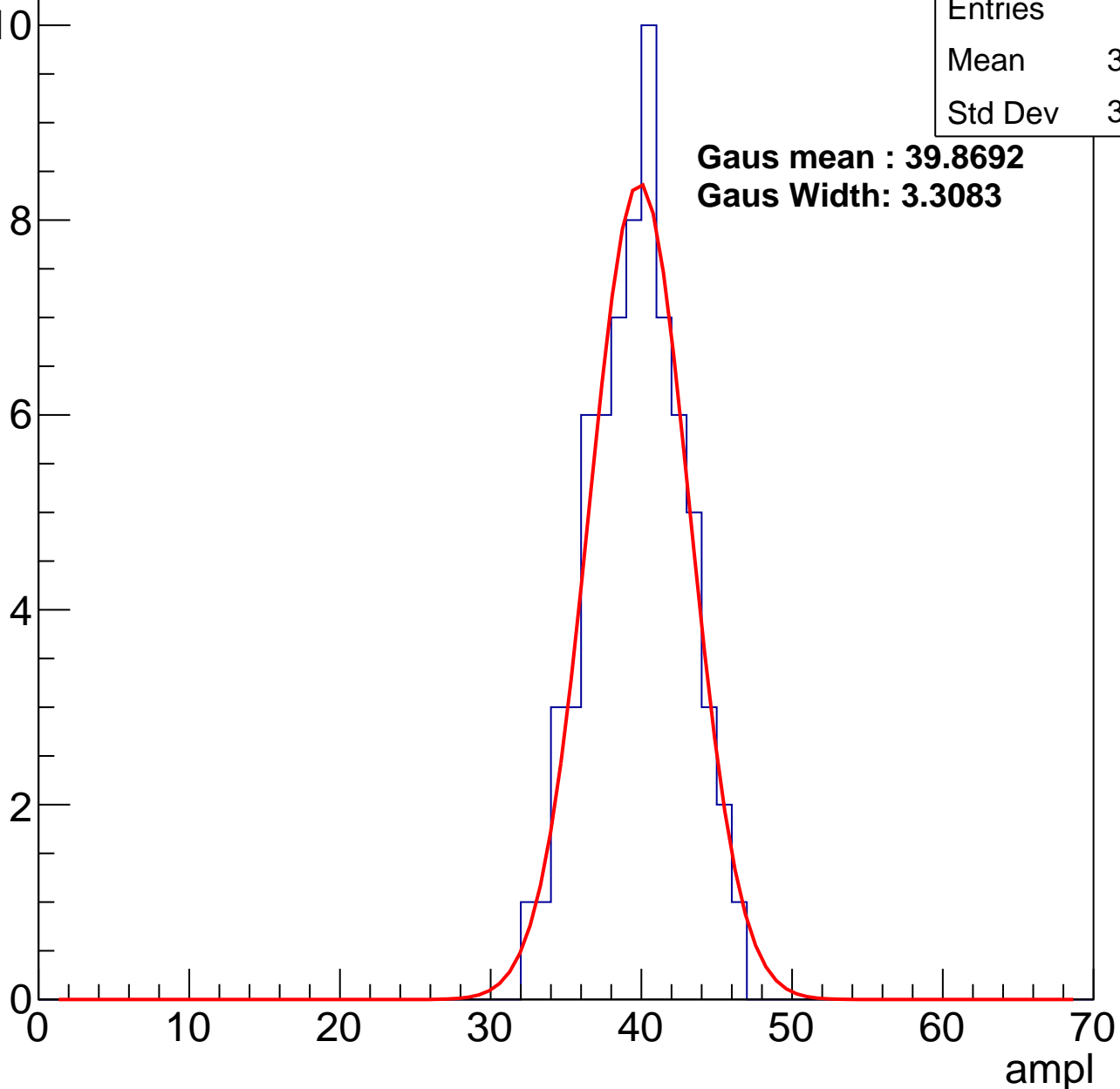
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.28
Std Dev	3.083

**Gaus mean : 39.8692**

**Gaus Width: 3.3083**

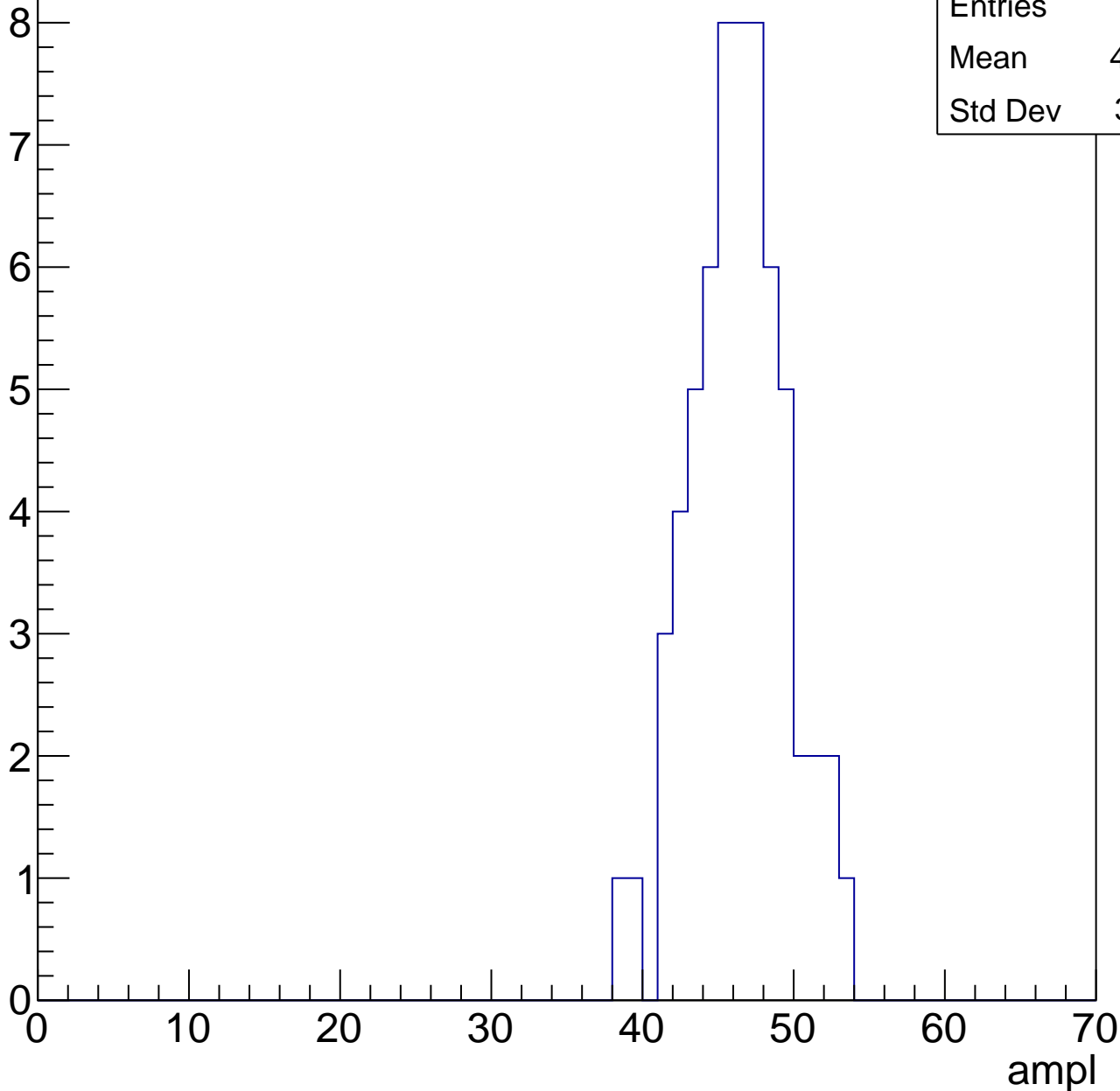


# B1L002S, U17-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

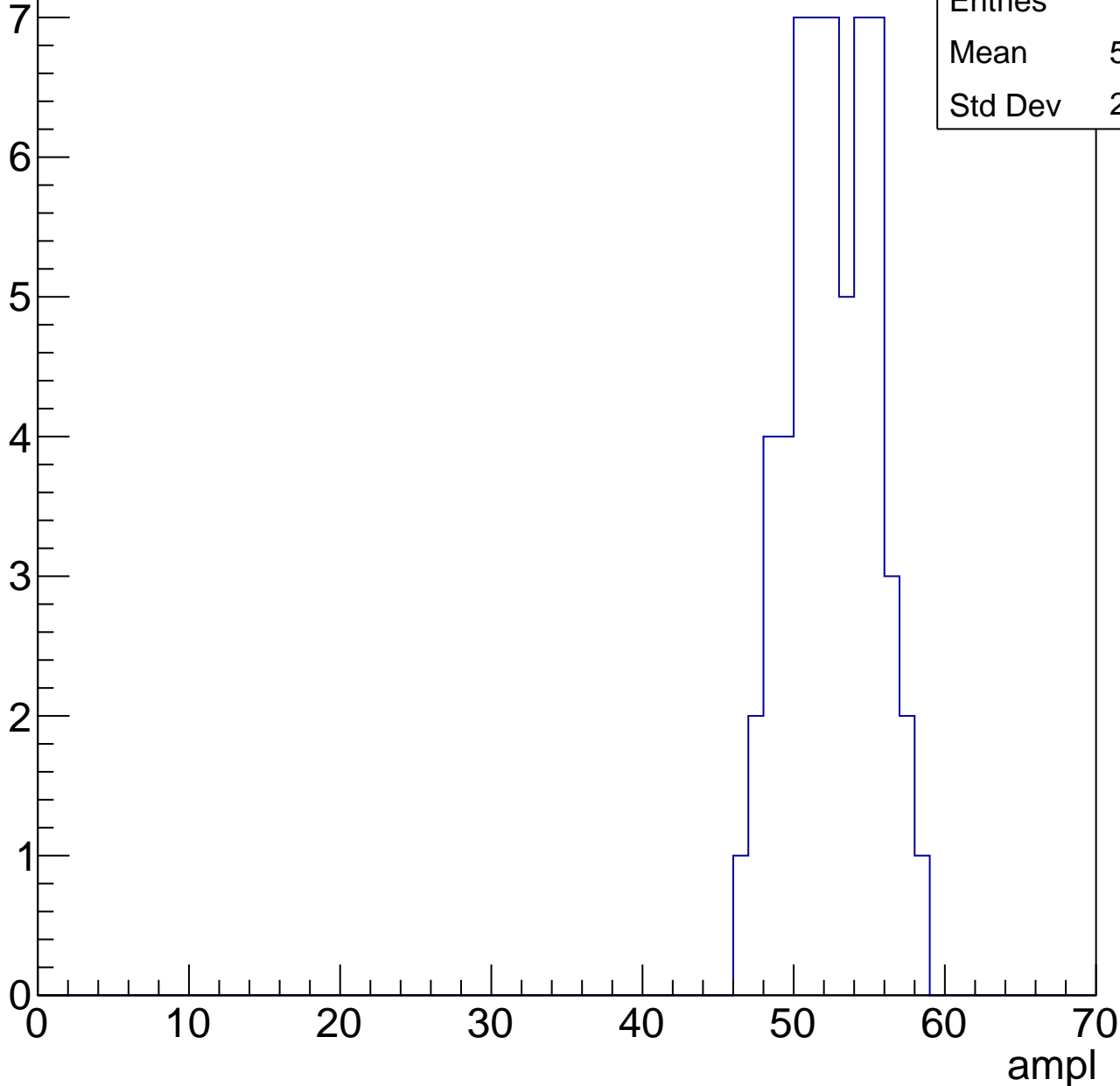
Entries	62
Mean	45.85
Std Dev	3.131



# B1L002S, U17-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



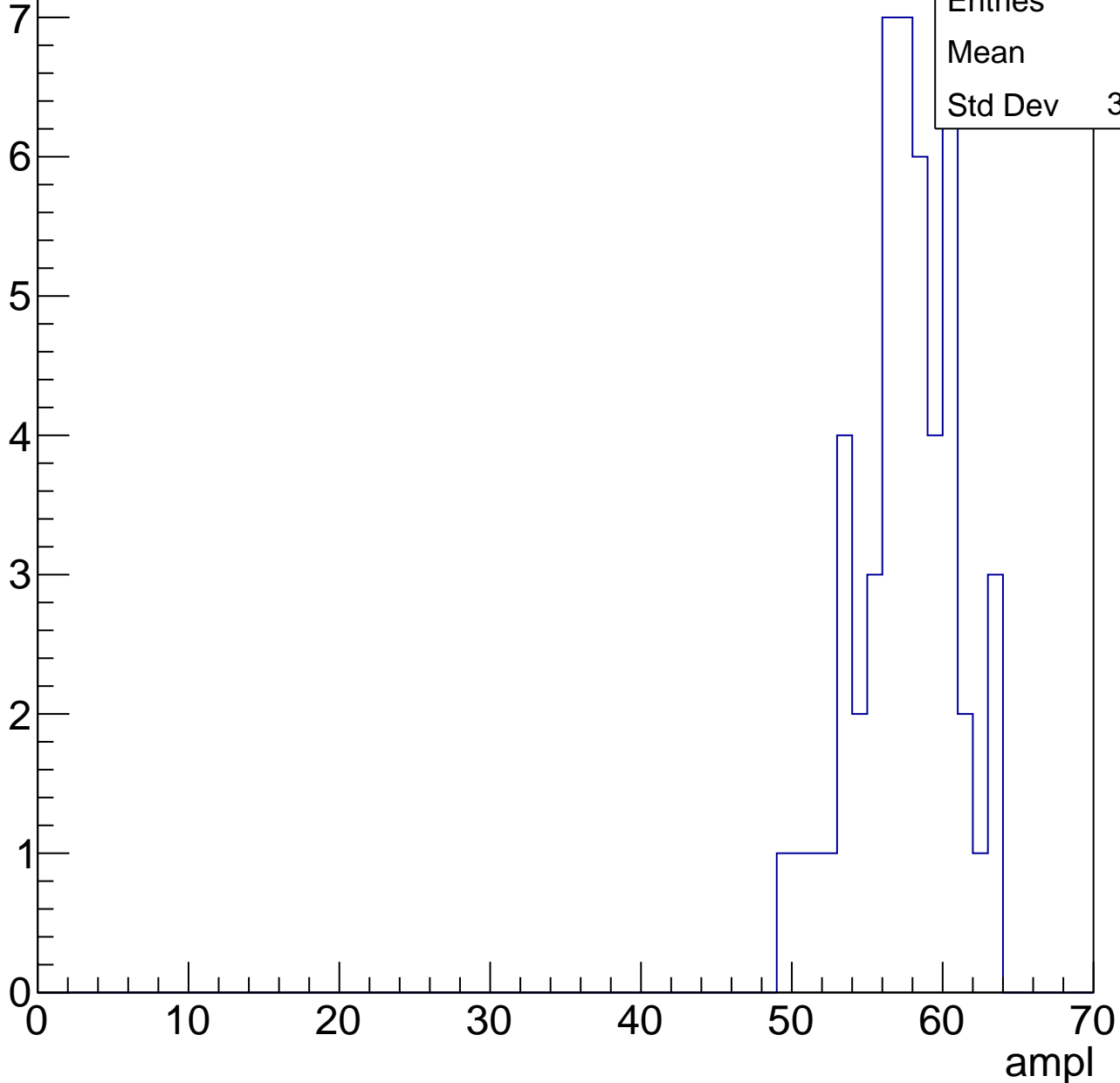
Entries	57
Mean	52.05
Std Dev	2.812

# B1L002S, U17-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	57.1
Std Dev	3.239

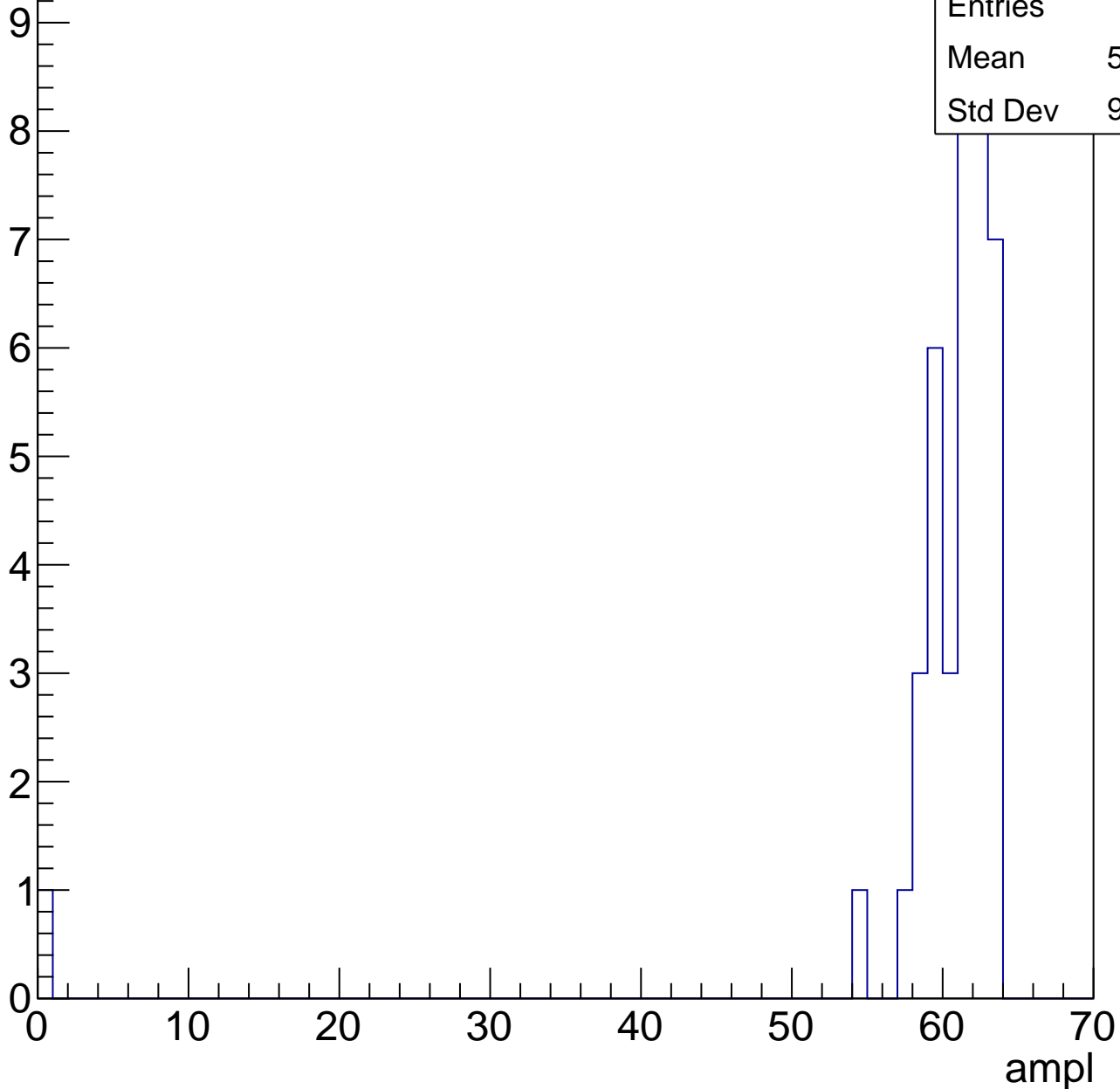


# B1L002S, U17-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	59.13
Std Dev	9.793





# B1L002S, U17-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch10, adc0

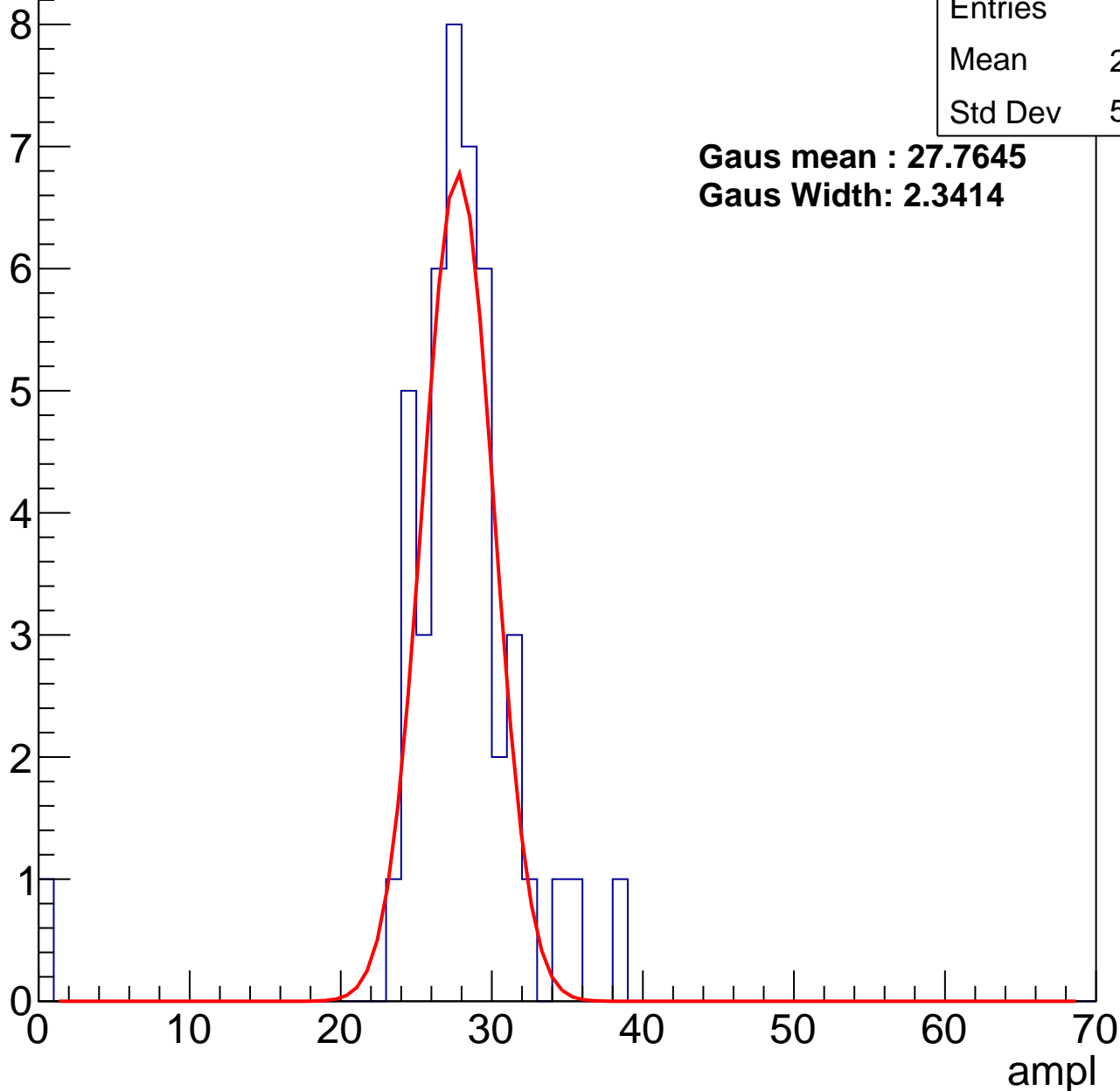
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	27.22
Std Dev	5.026

**Gaus mean : 27.7645**

**Gaus Width: 2.3414**



# B1L002S, U17-ch10, adc1

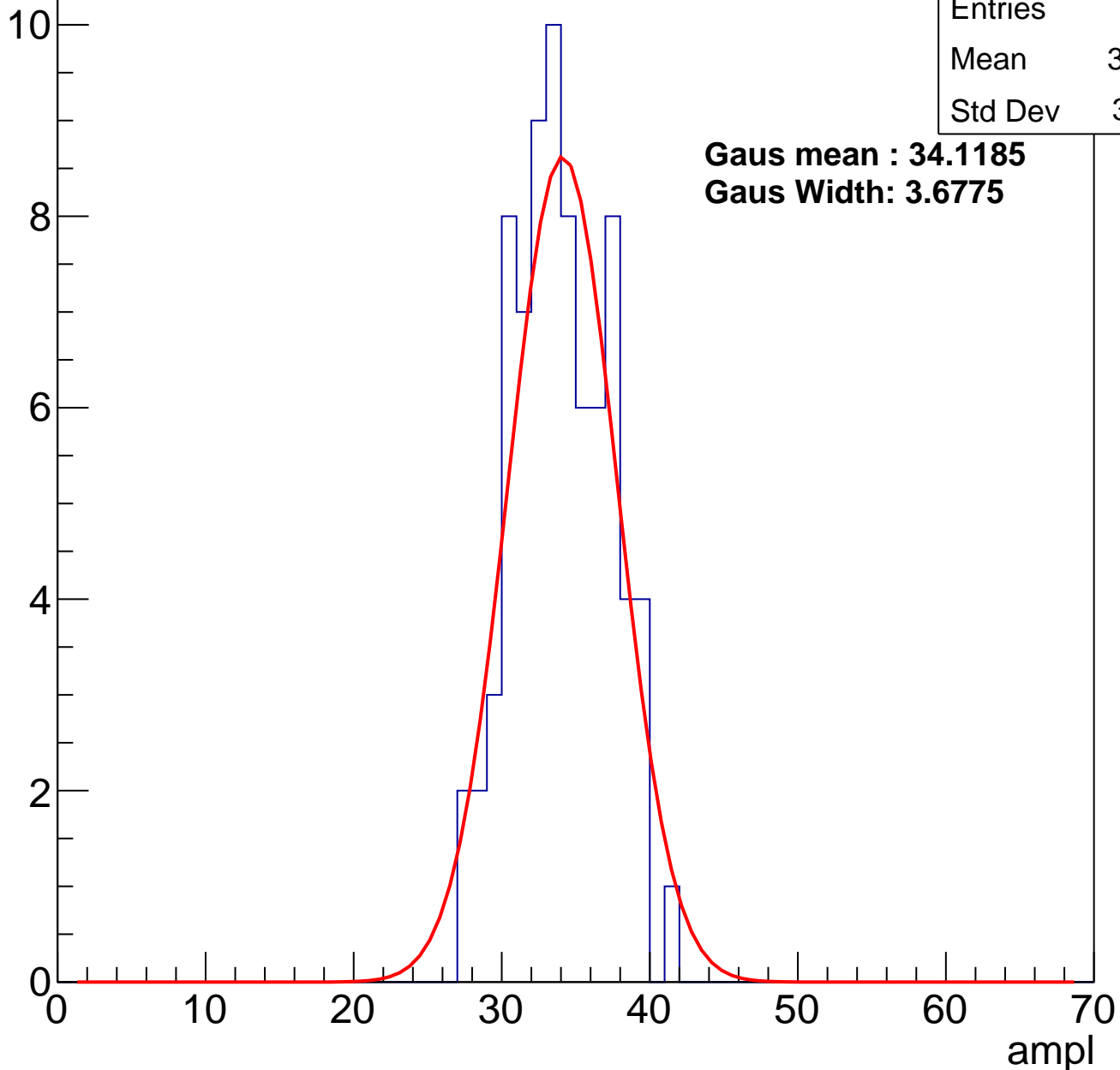
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	33.53
Std Dev	3.161

**Gaus mean : 34.1185**

**Gaus Width: 3.6775**

Entry



# B1L002S, U17-ch10, adc2

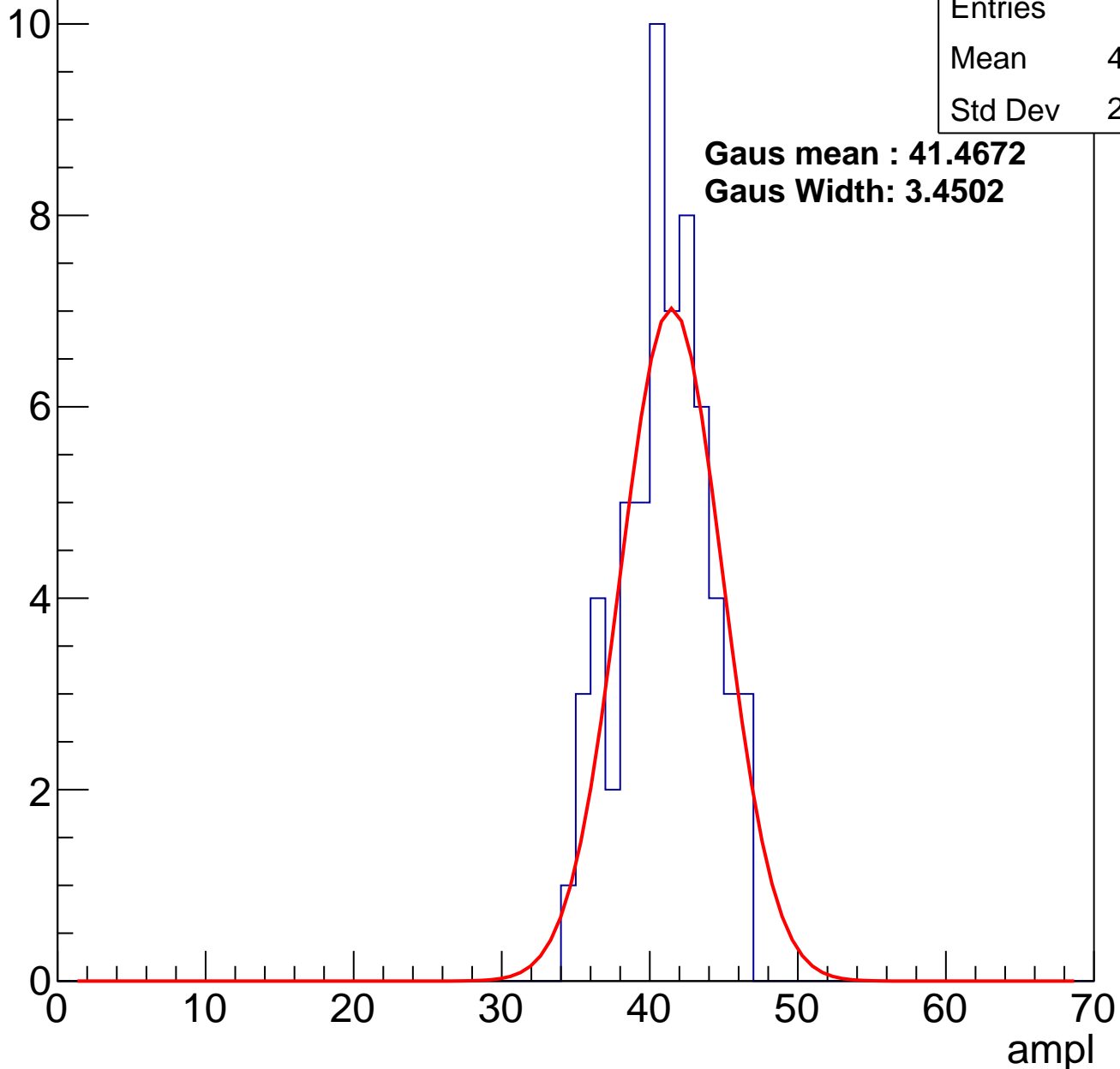
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	40.52
Std Dev	2.979

**Gaus mean : 41.4672**

**Gaus Width: 3.4502**

Entry



# B1L002S, U17-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

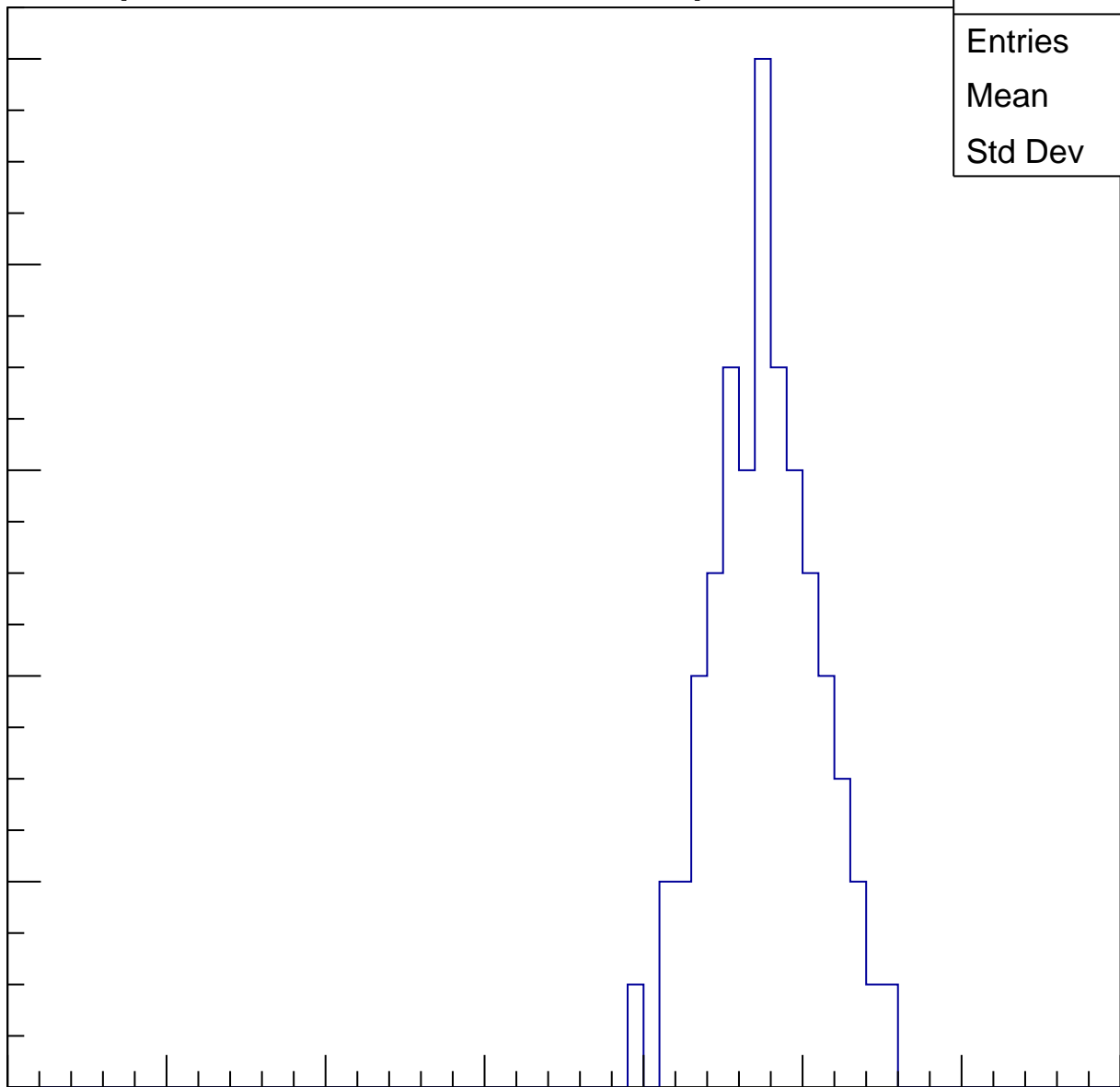
Entries	66
Mean	47.17
Std Dev	3.319

Entry

10  
8  
6  
4  
2  
0

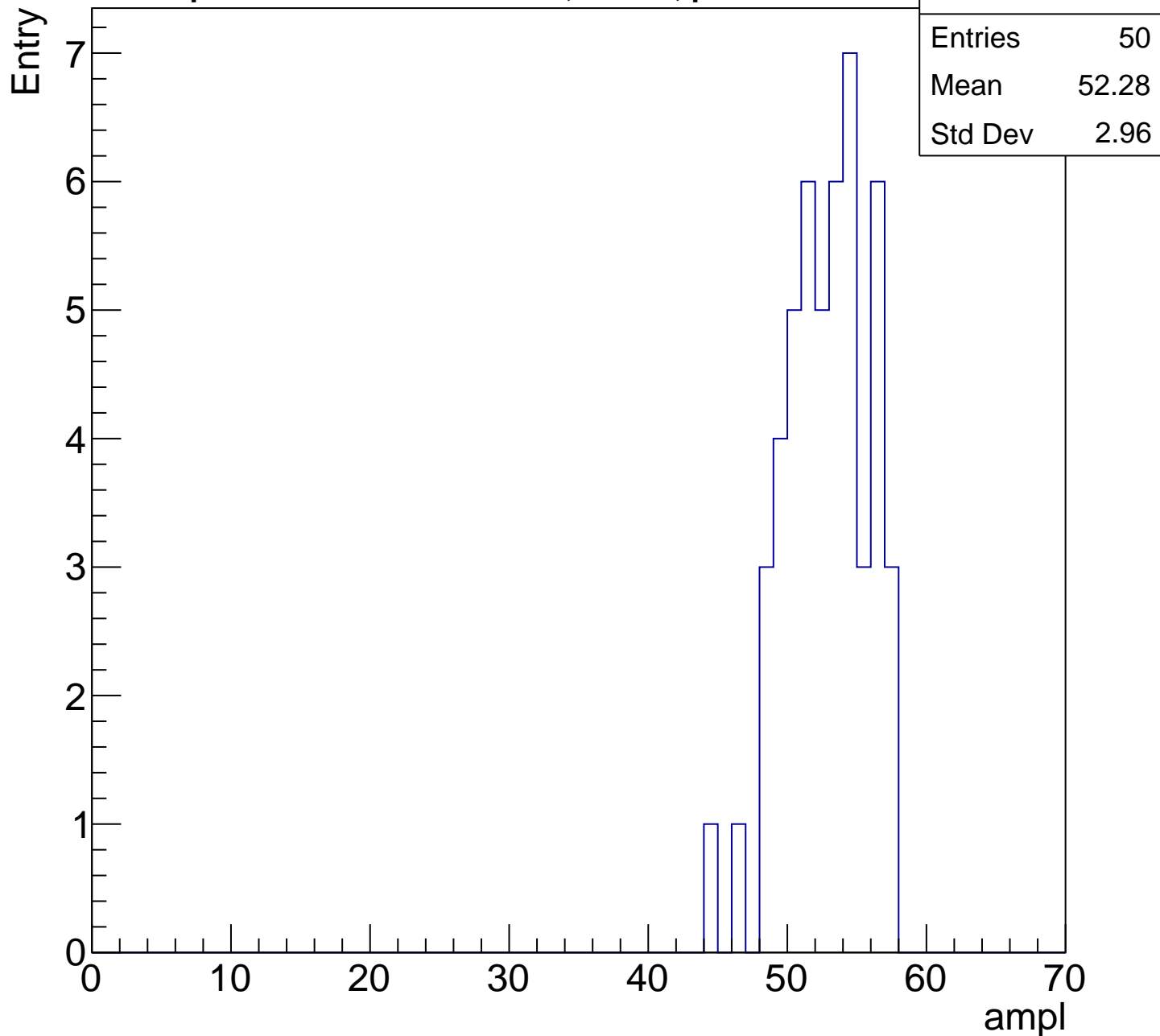
0 10 20 30 40 50 60 70

ampl



# B1L002S, U17-ch10, adc4

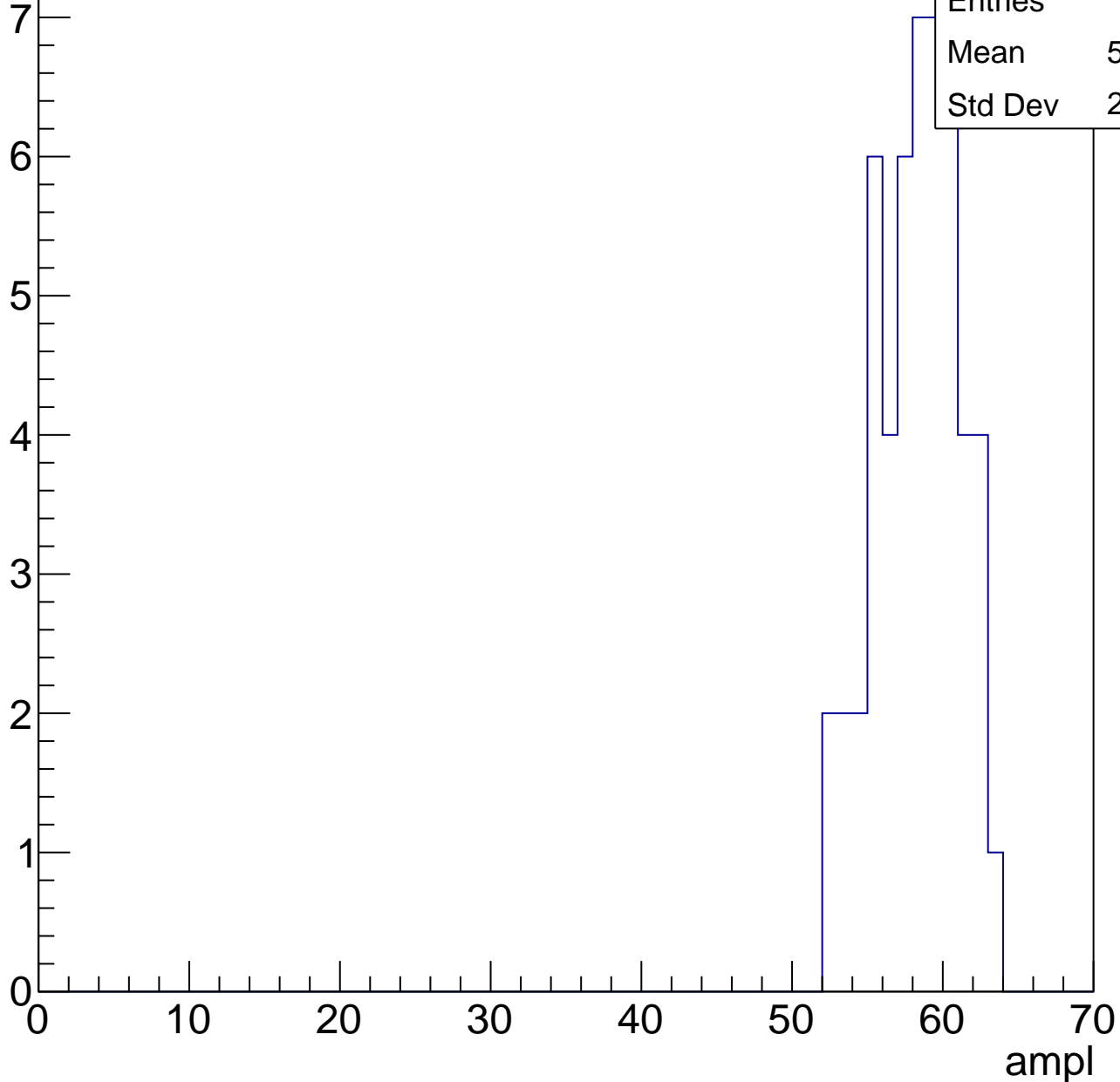
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U17-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

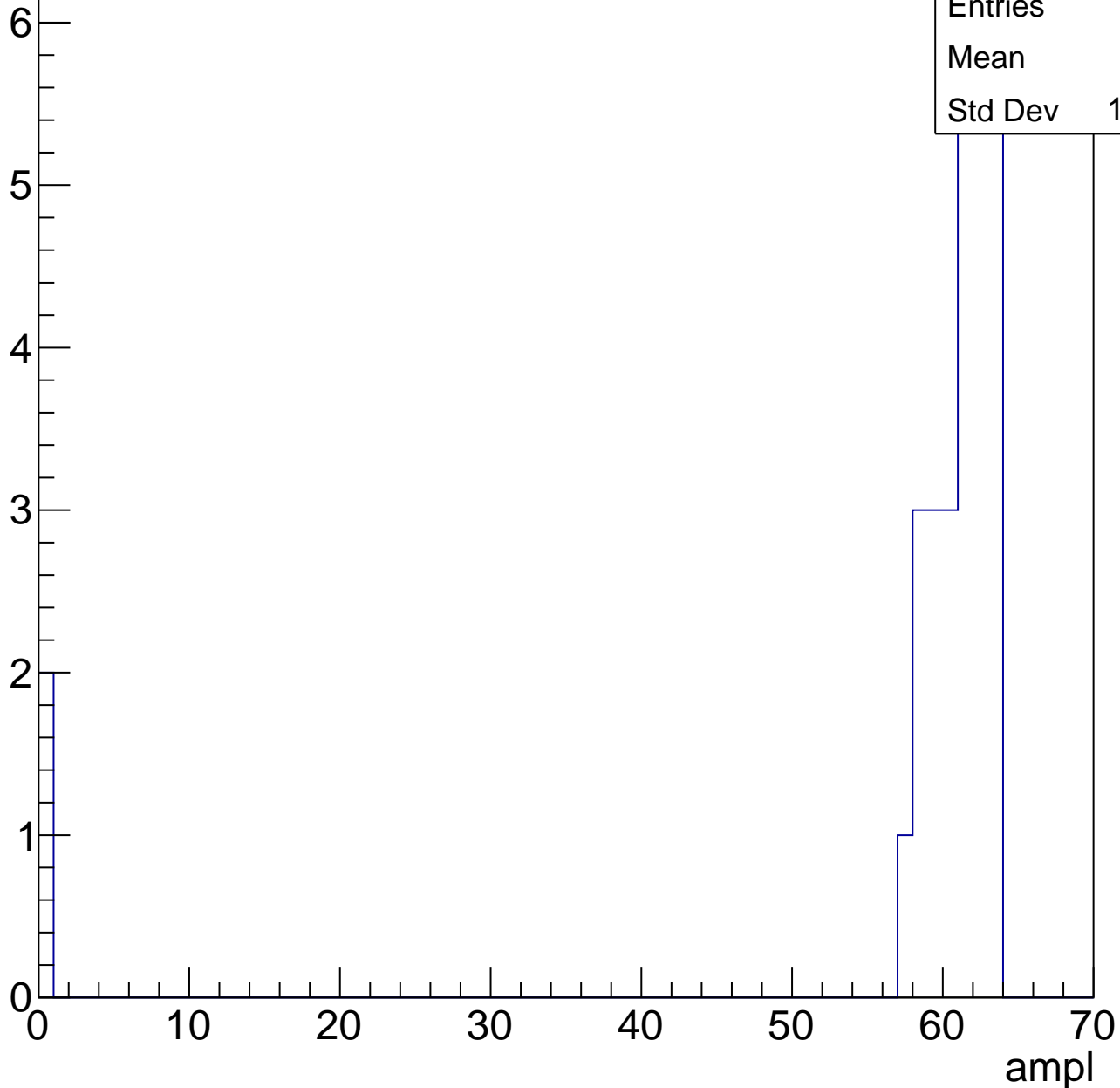


Entries	52
Mean	57.85
Std Dev	2.734

# B1L002S, U17-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

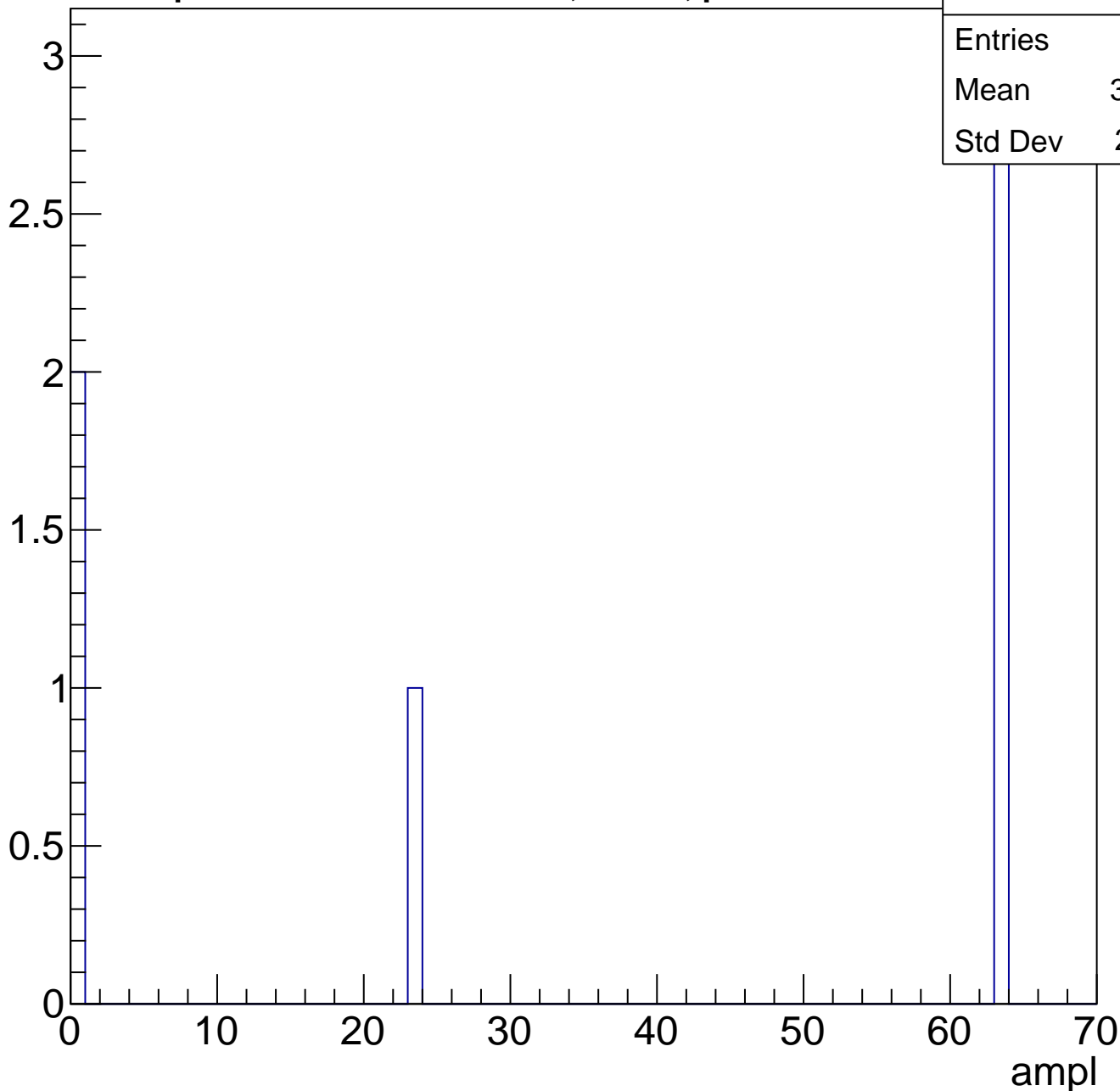




# B1L002S, U17-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	35.33
Std Dev	28.71

# B1L002S, U17-ch11, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	27.48
Std Dev	2.881

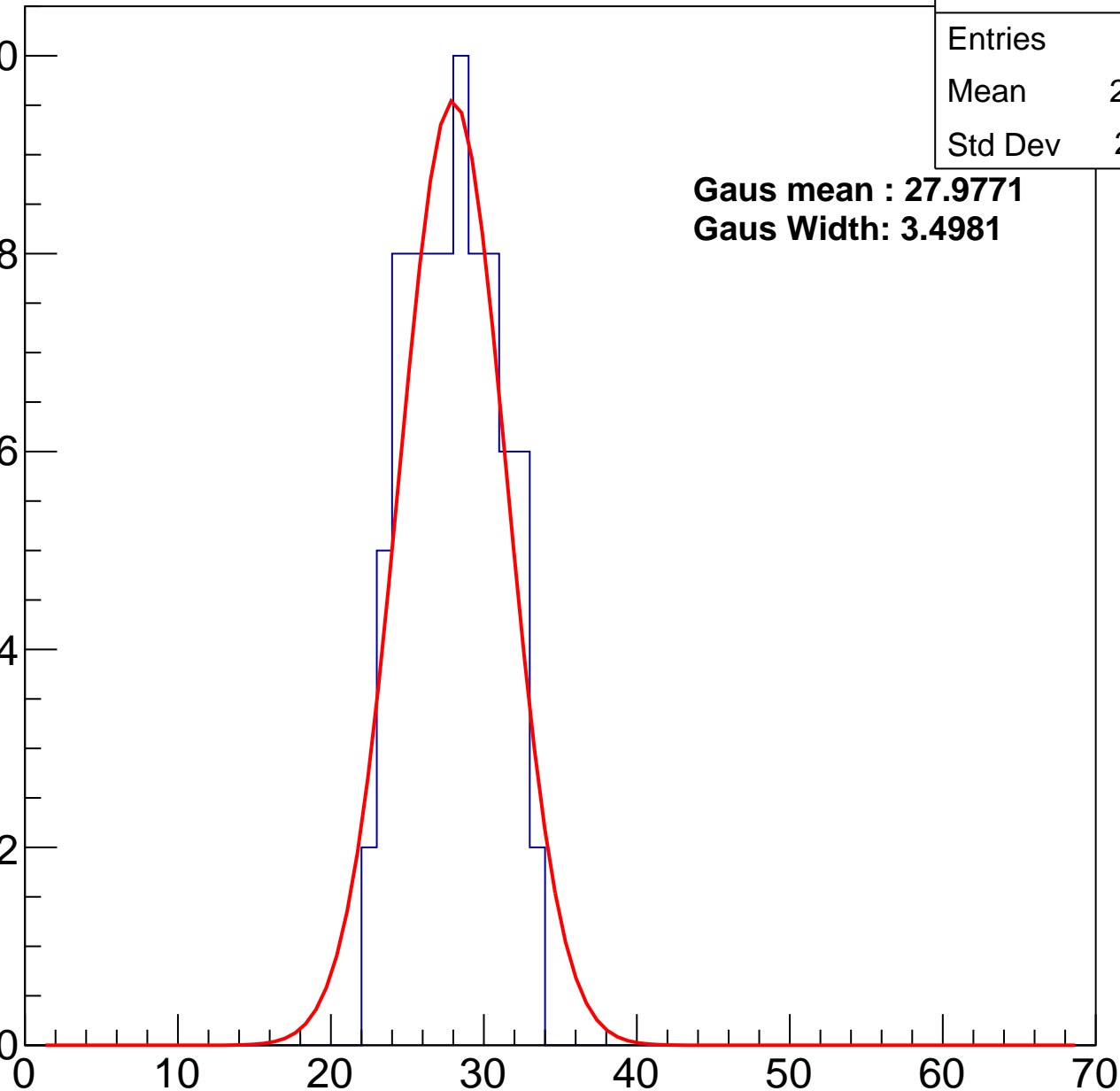
**Gaus mean : 27.9771**

**Gaus Width: 3.4981**

Entry

10  
8  
6  
4  
2  
0

ampl



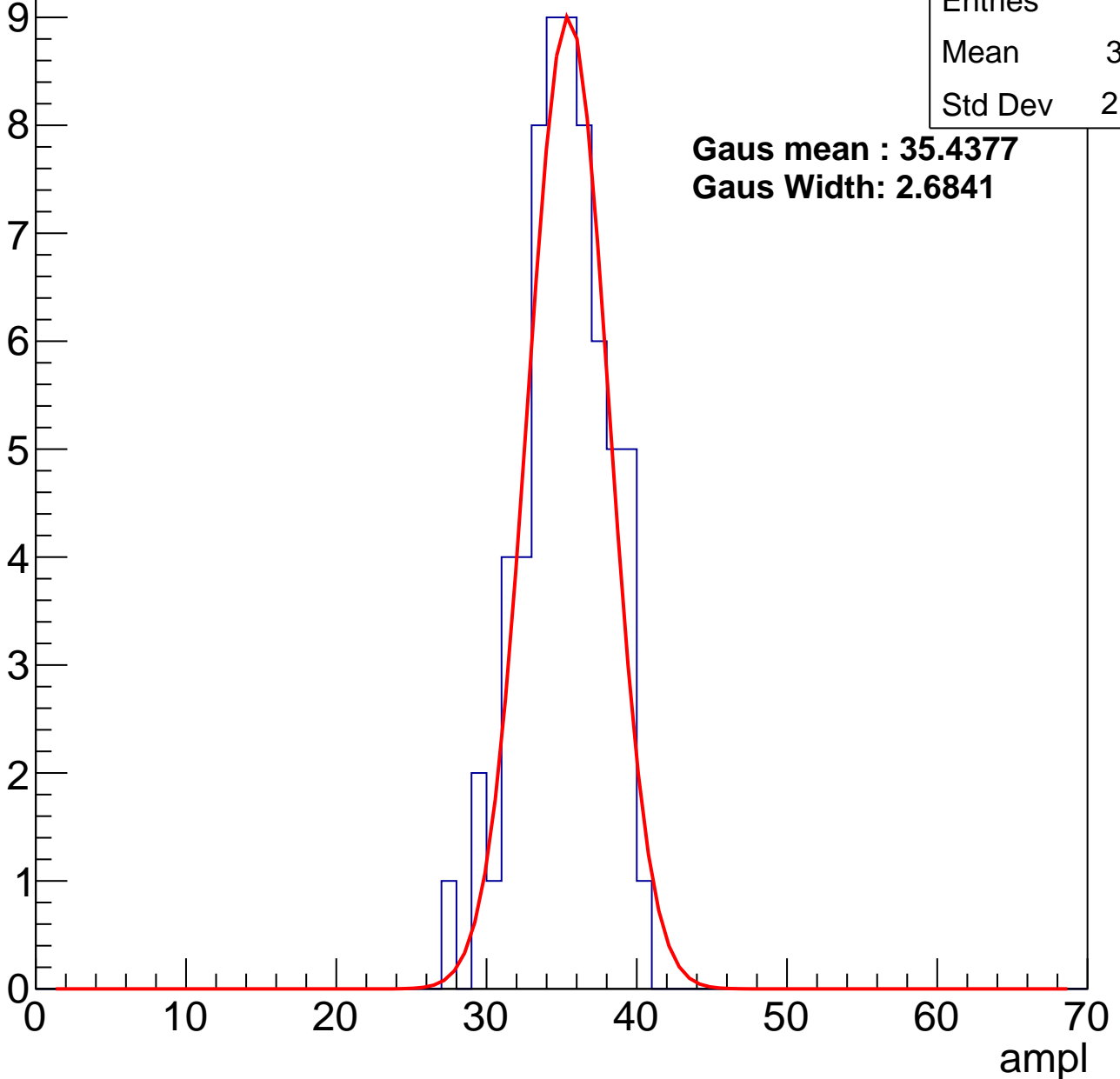
# B1L002S, U17-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.71
Std Dev	2.757

**Gaus mean : 35.4377**  
**Gaus Width: 2.6841**



# B1L002S, U17-ch11, adc2

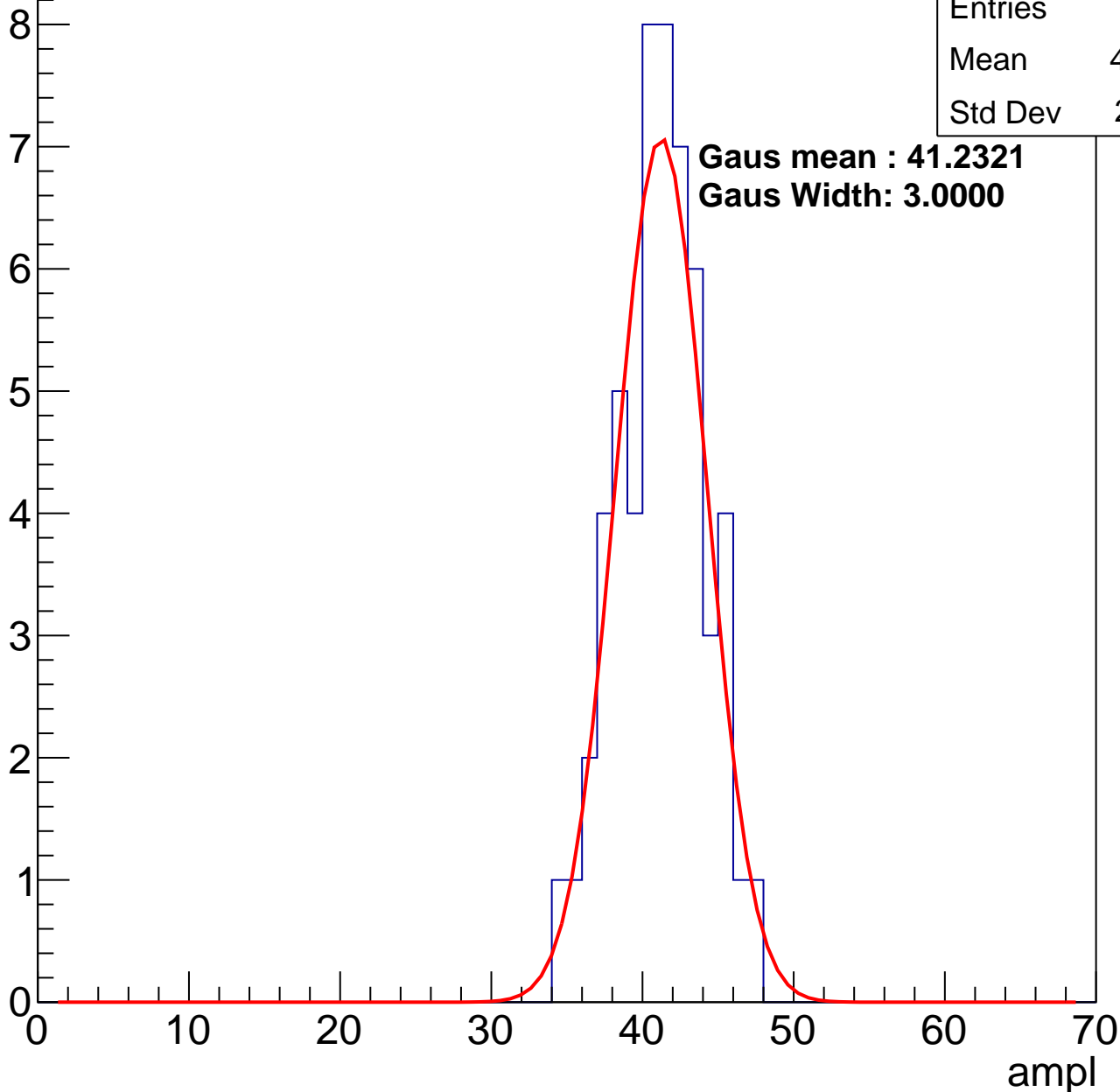
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.73
Std Dev	2.851

**Gaus mean : 41.2321**

**Gaus Width: 3.0000**

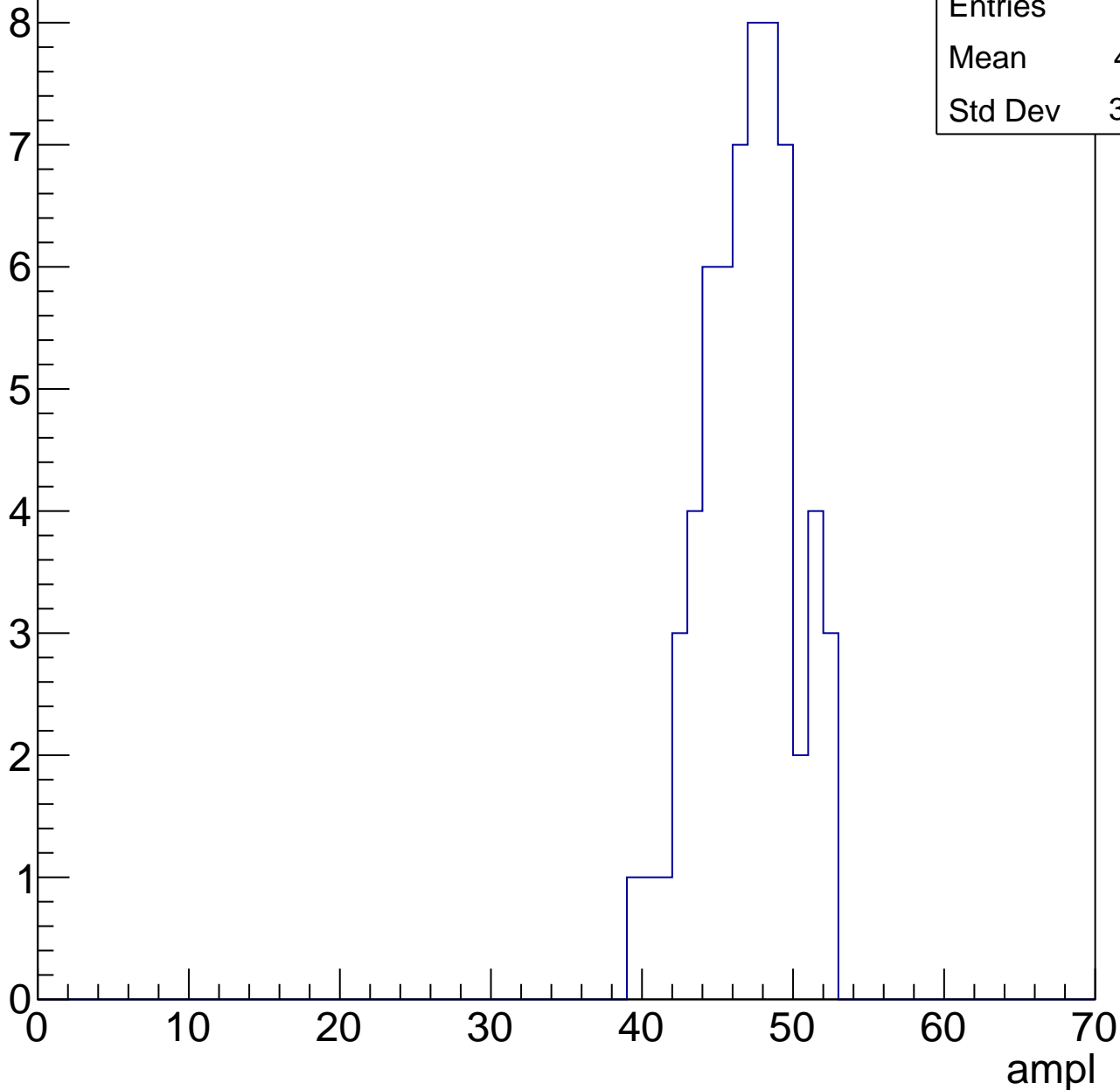


# B1L002S, U17-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.51
Std Dev	3.006

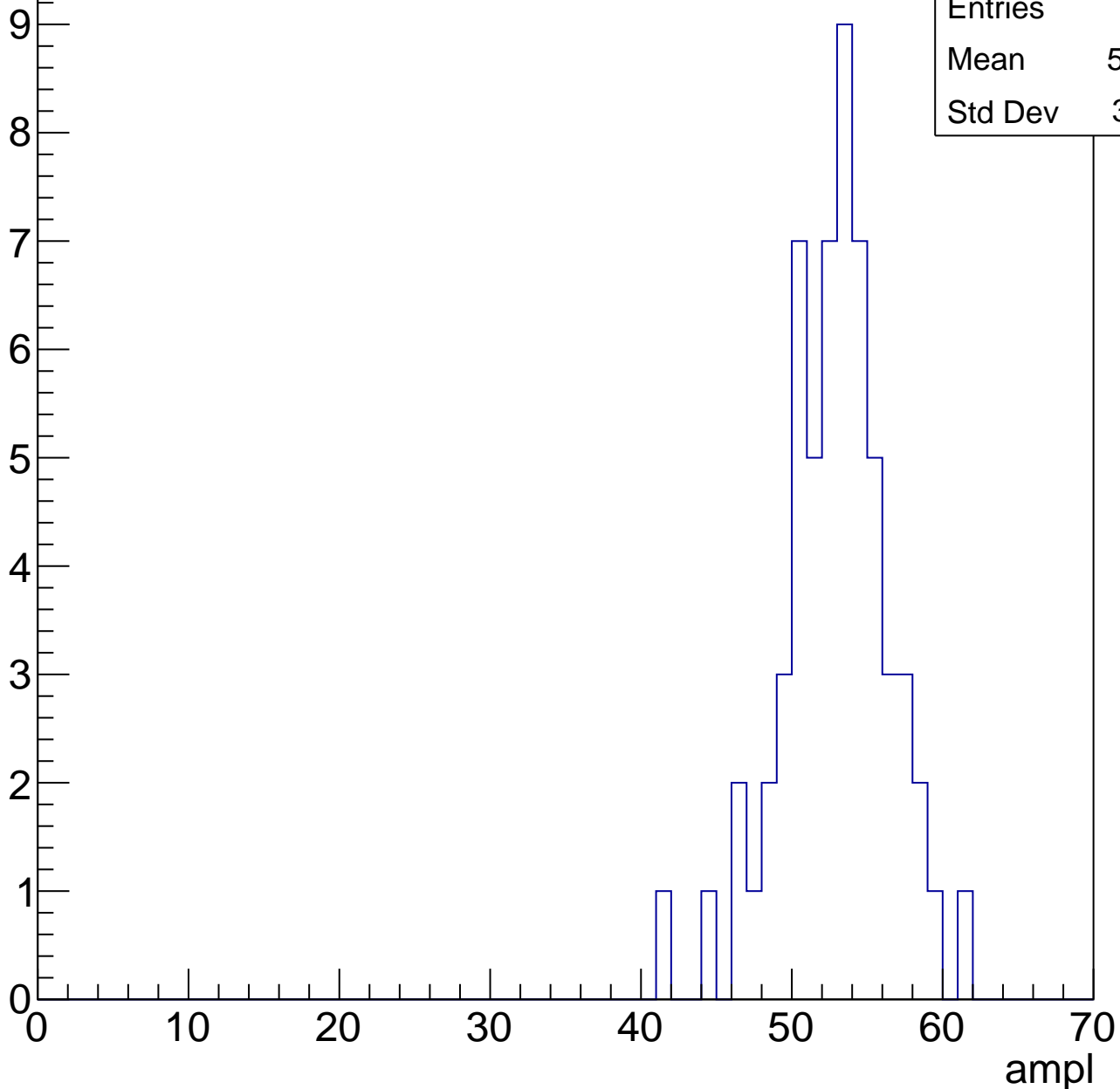


# B1L002S, U17-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.35
Std Dev	3.591

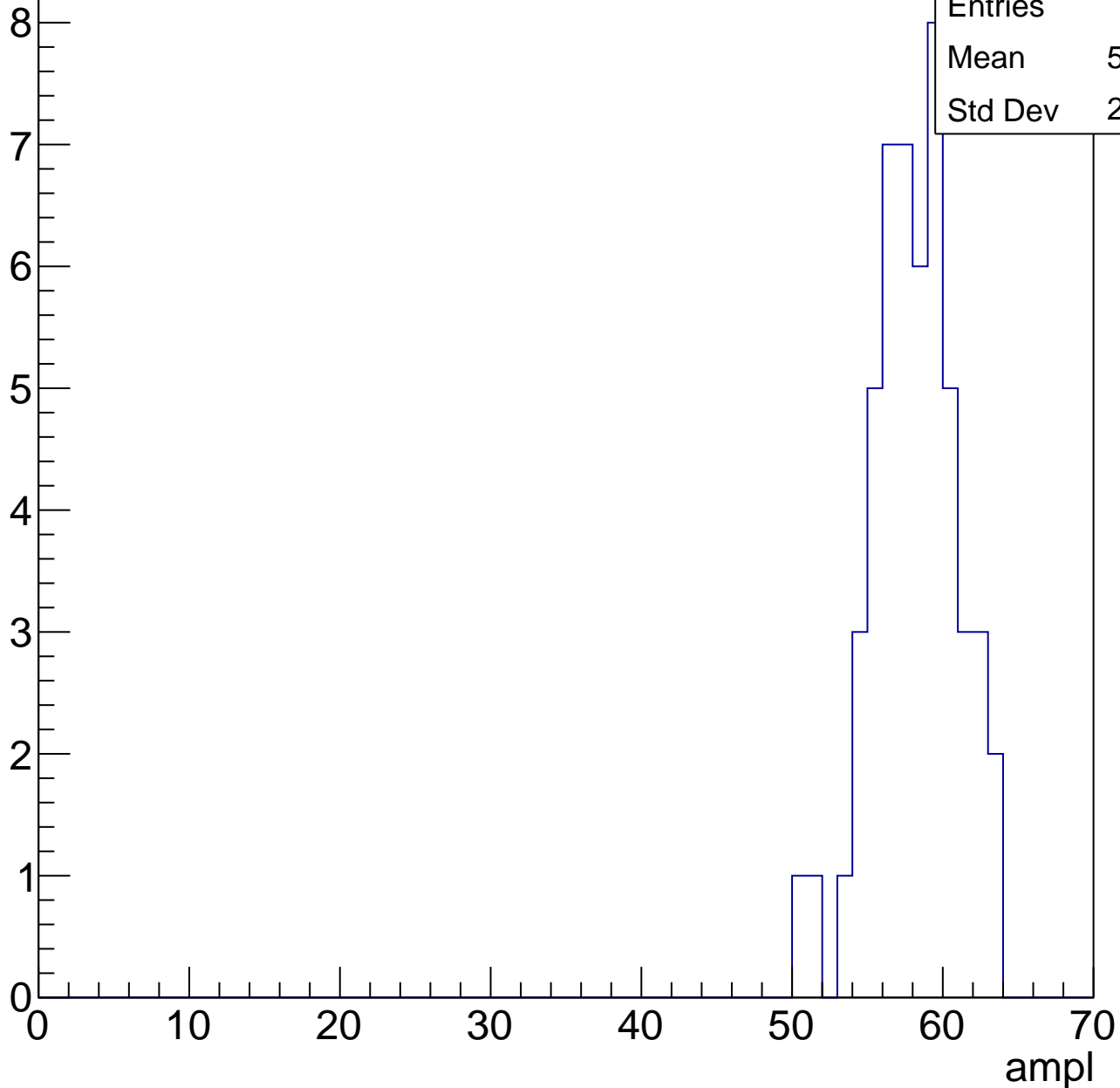


# B1L002S, U17-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	57.63
Std Dev	2.815

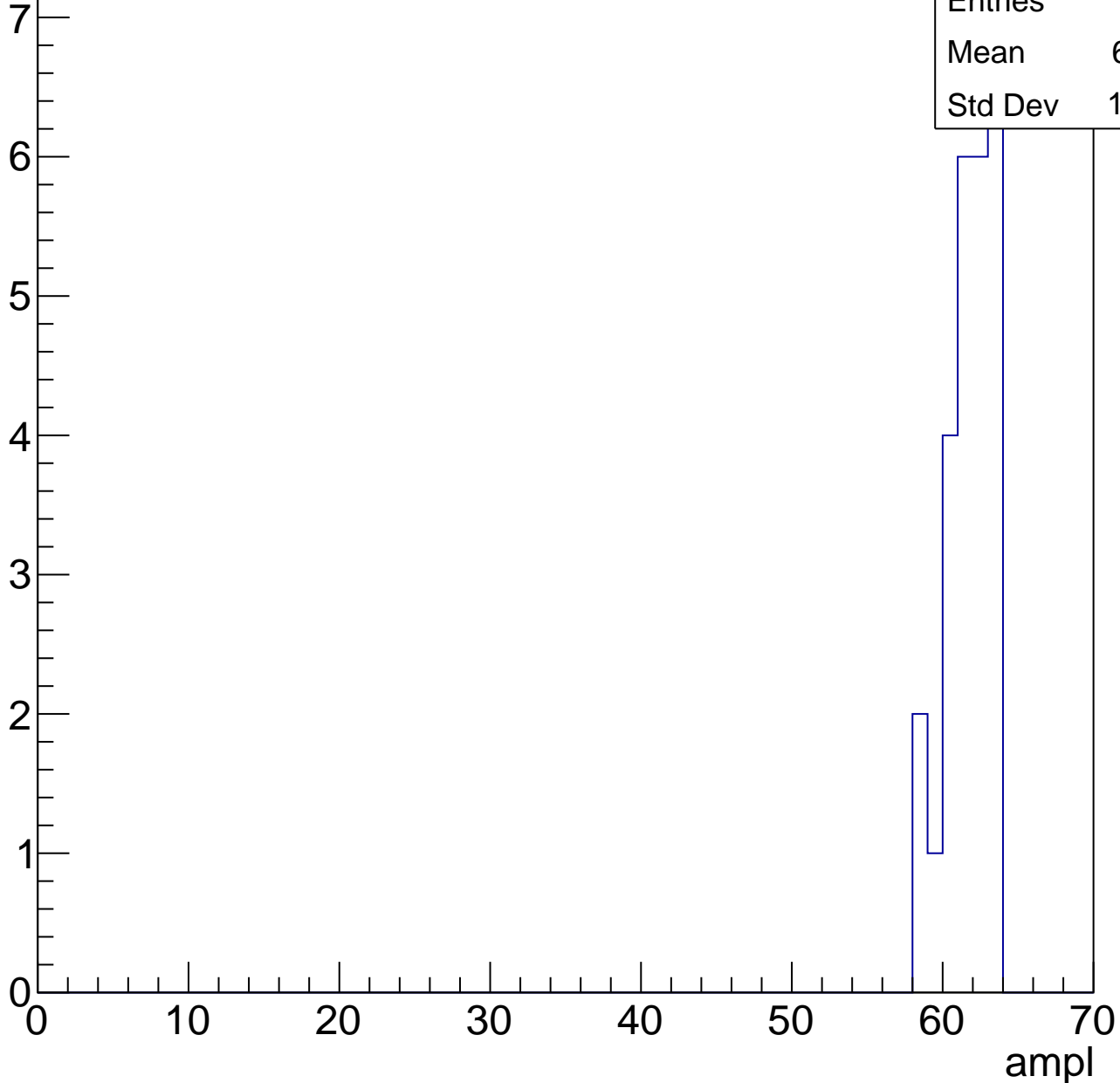


# B1L002S, U17-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.31
Std Dev	1.488

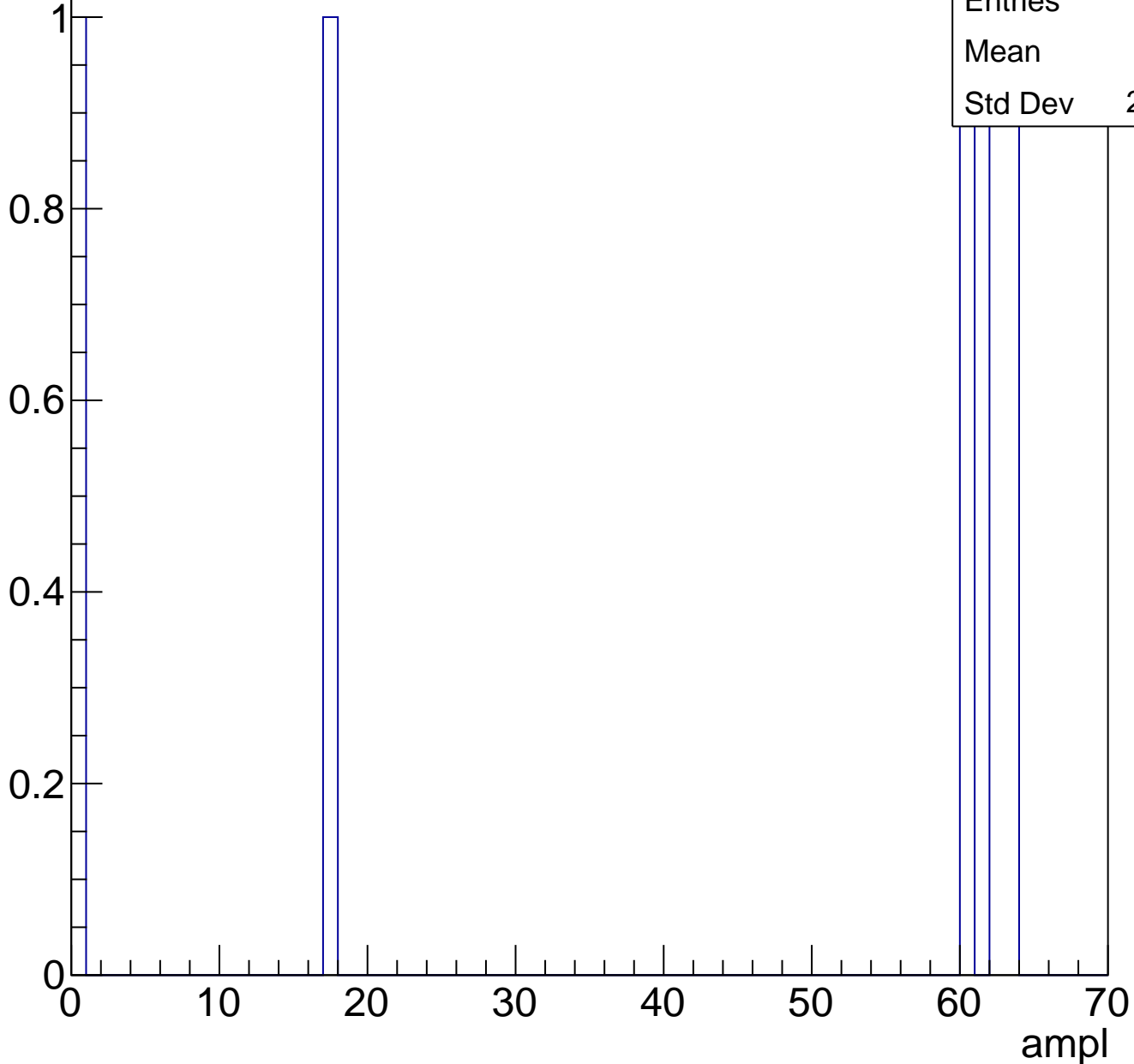




# B1L002S, U17-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch12, adc0

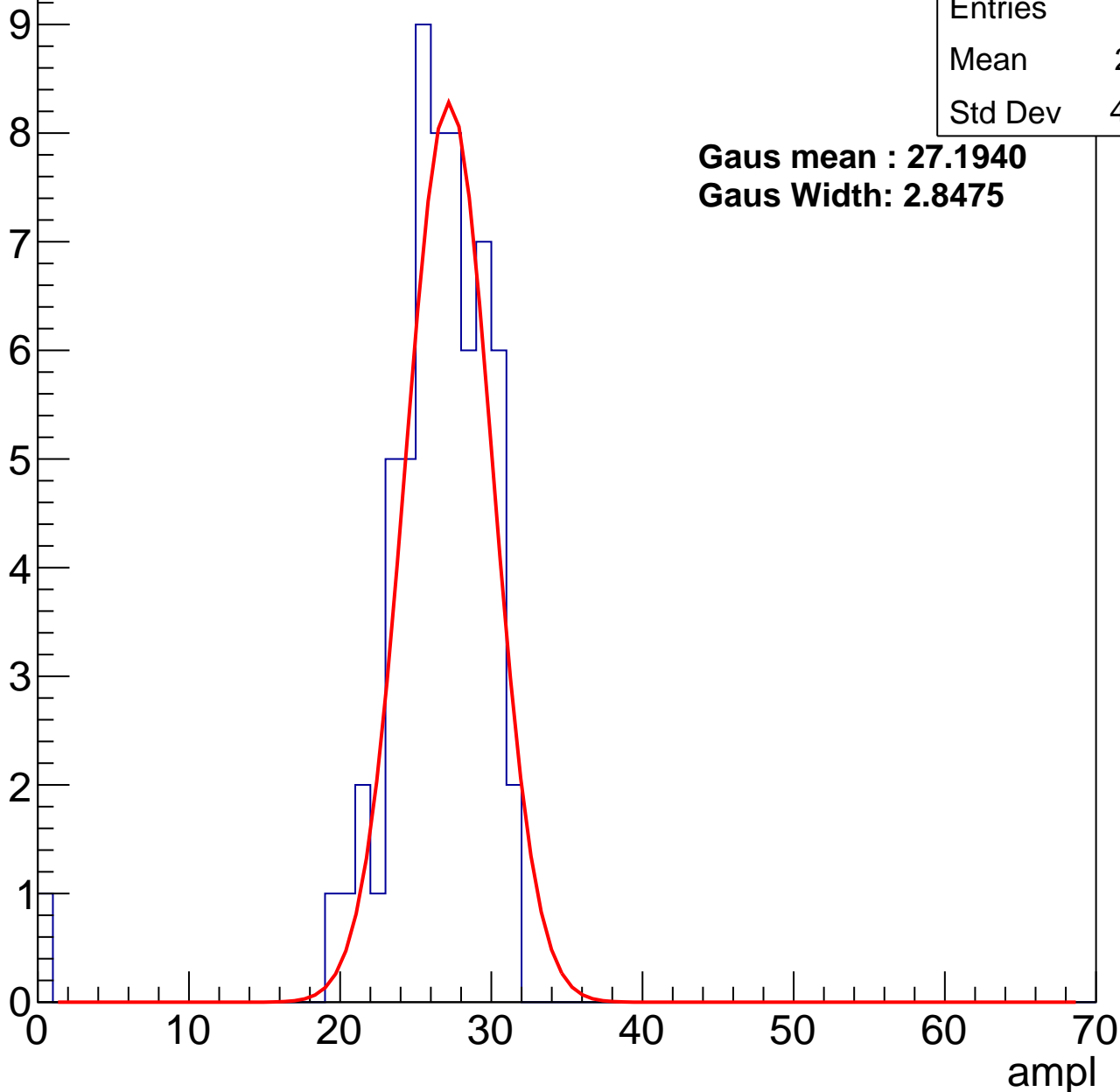
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	25.81
Std Dev	4.287

**Gaus mean : 27.140**

**Gaus Width: 2.8475**



# B1L002S, U17-ch12, adc1

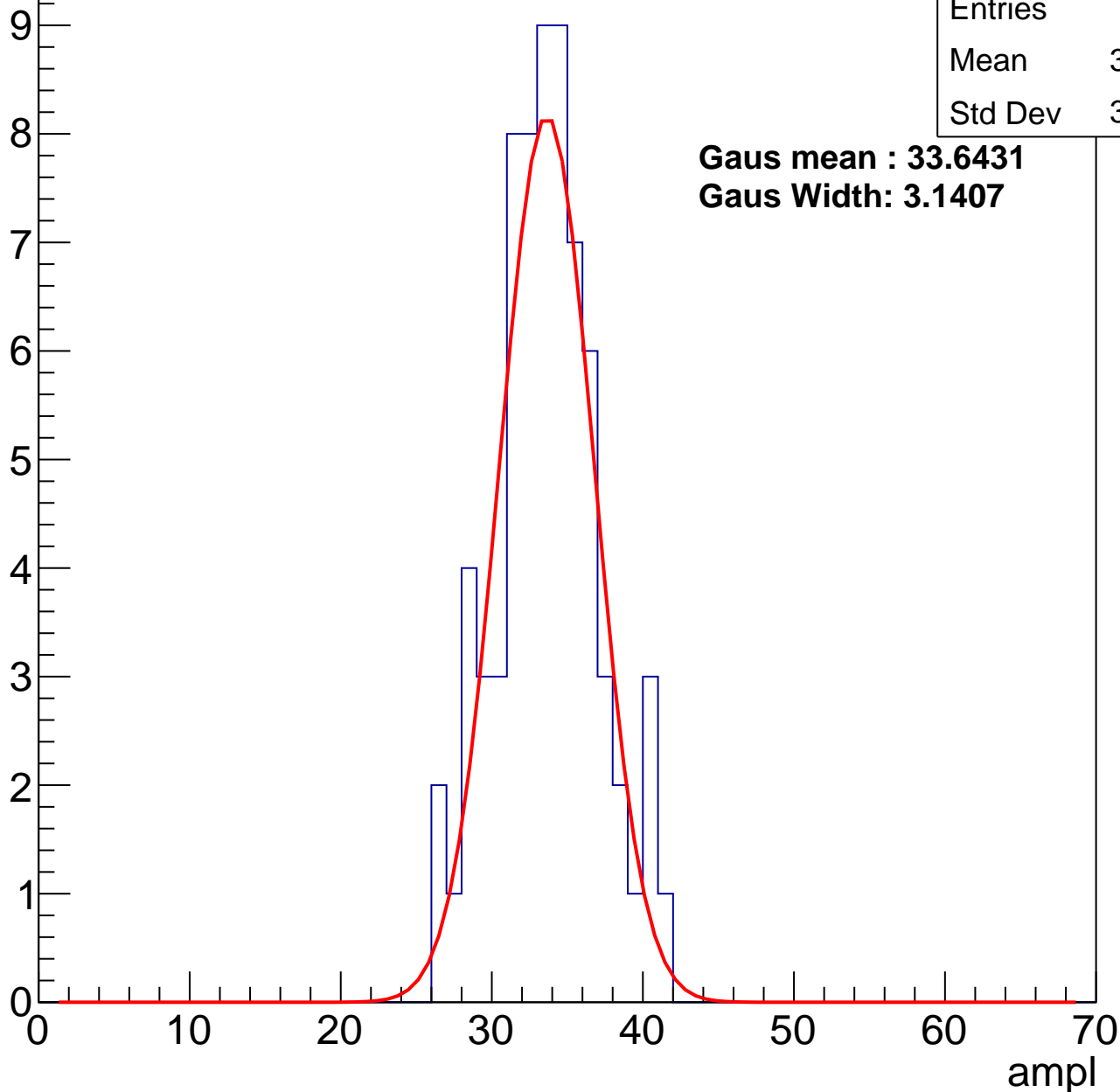
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	33.19
Std Dev	3.344

**Gaus mean : 33.6431**

**Gaus Width: 3.1407**



# B1L002S, U17-ch12, adc2

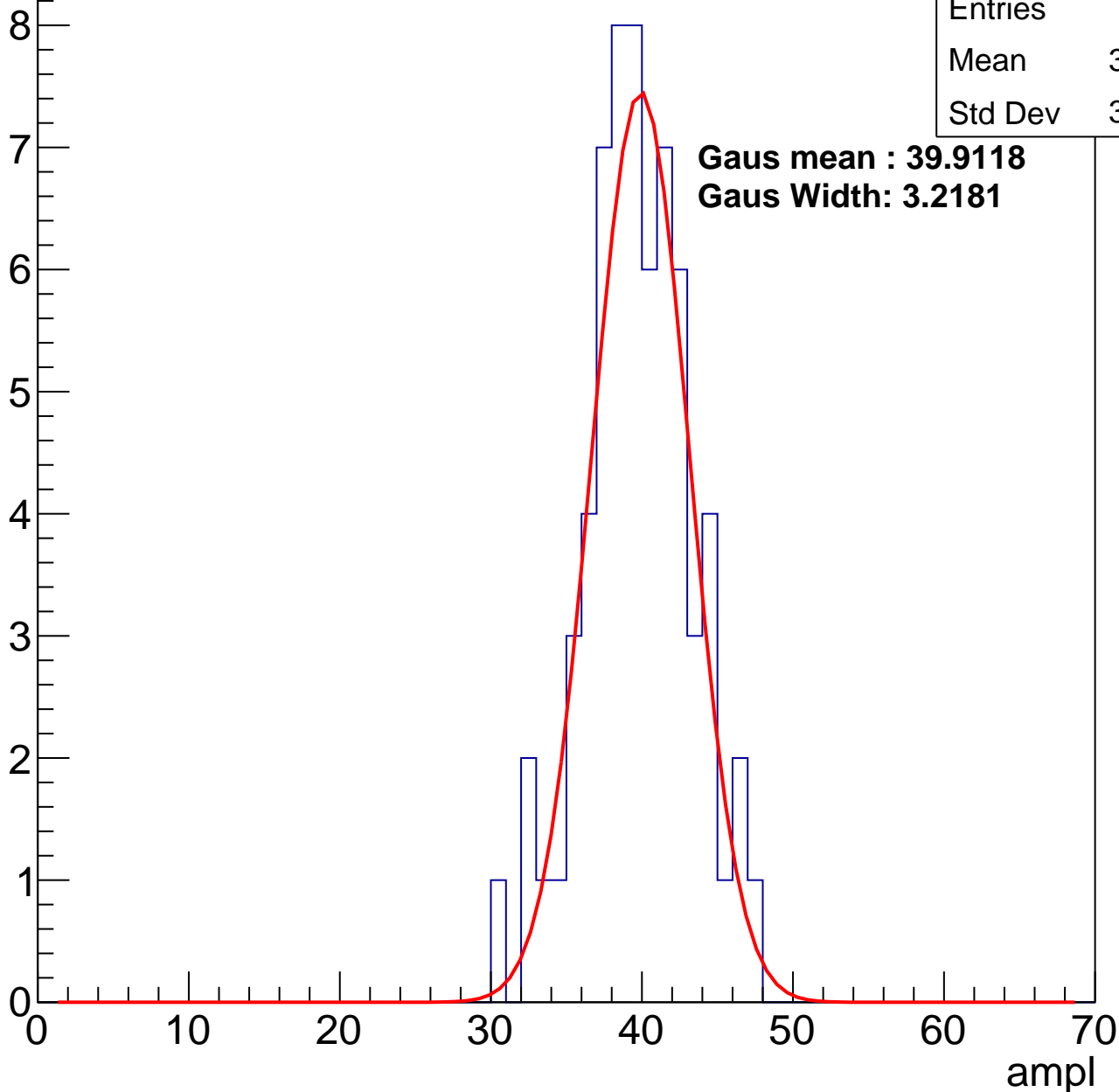
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.28
Std Dev	3.484

**Gaus mean : 39.9118**

**Gaus Width: 3.2181**

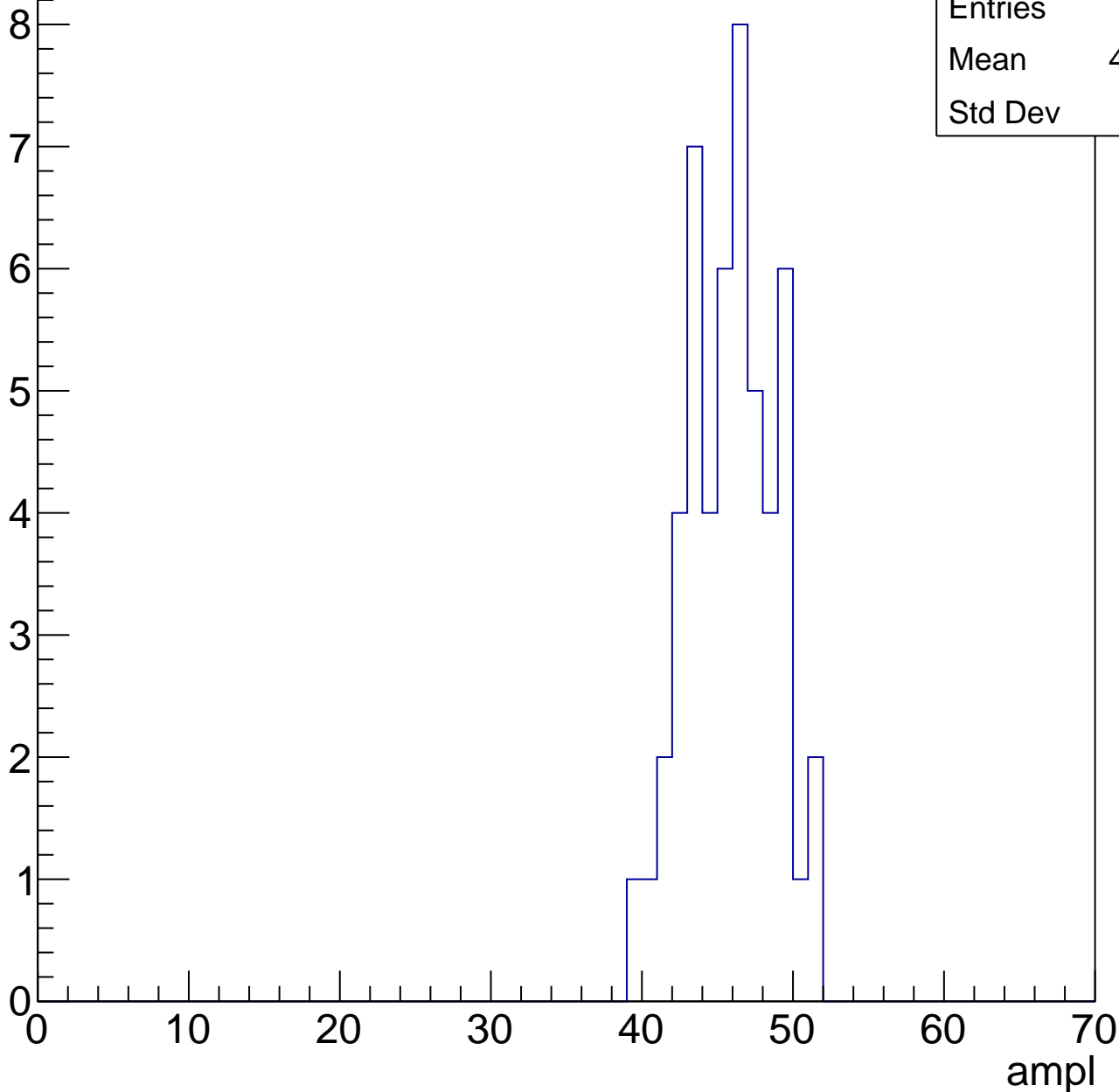


# B1L002S, U17-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	45.43
Std Dev	2.83

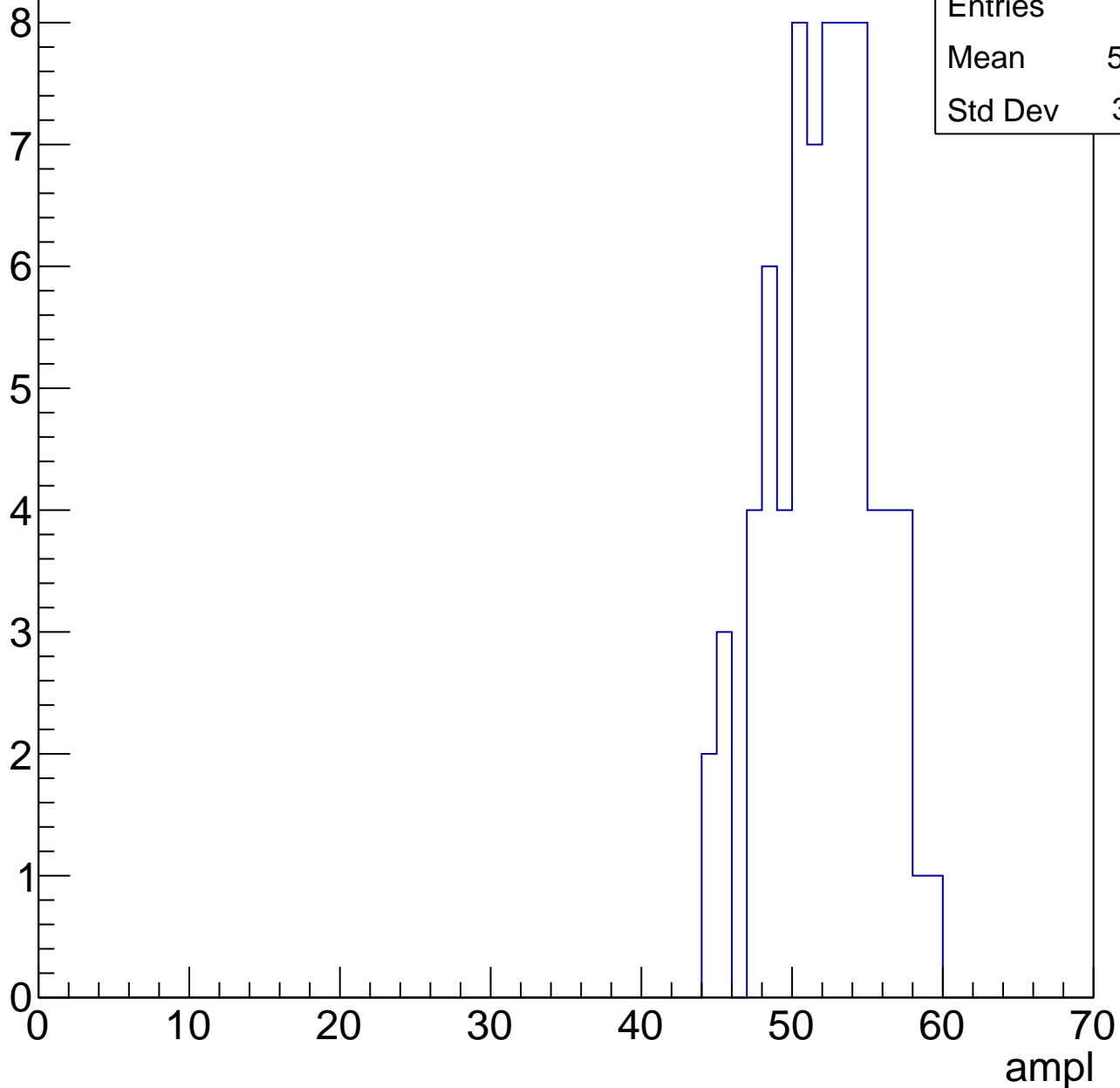


# B1L002S, U17-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	51.57
Std Dev	3.451

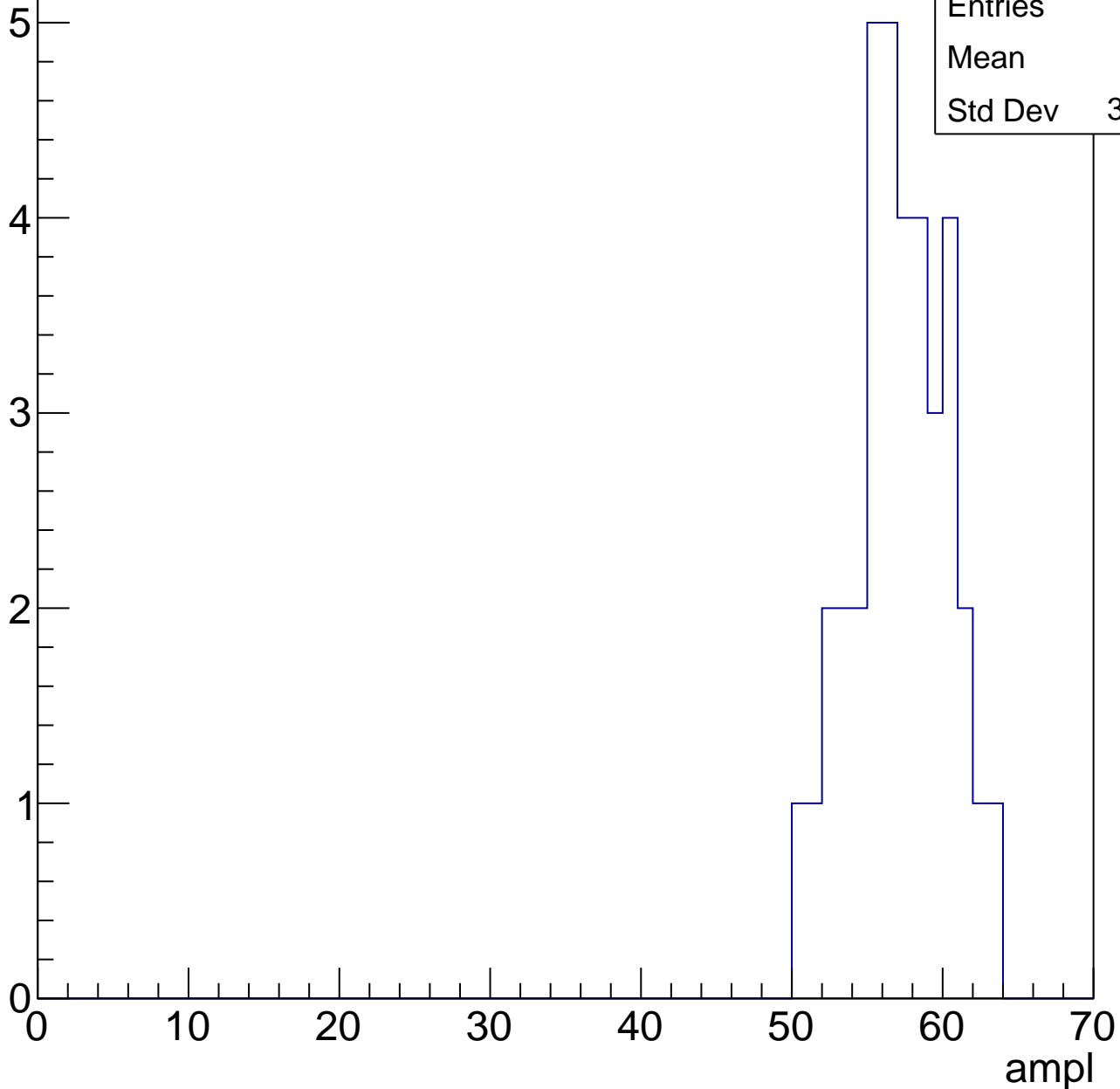


# B1L002S, U17-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	56.7
Std Dev	3.083

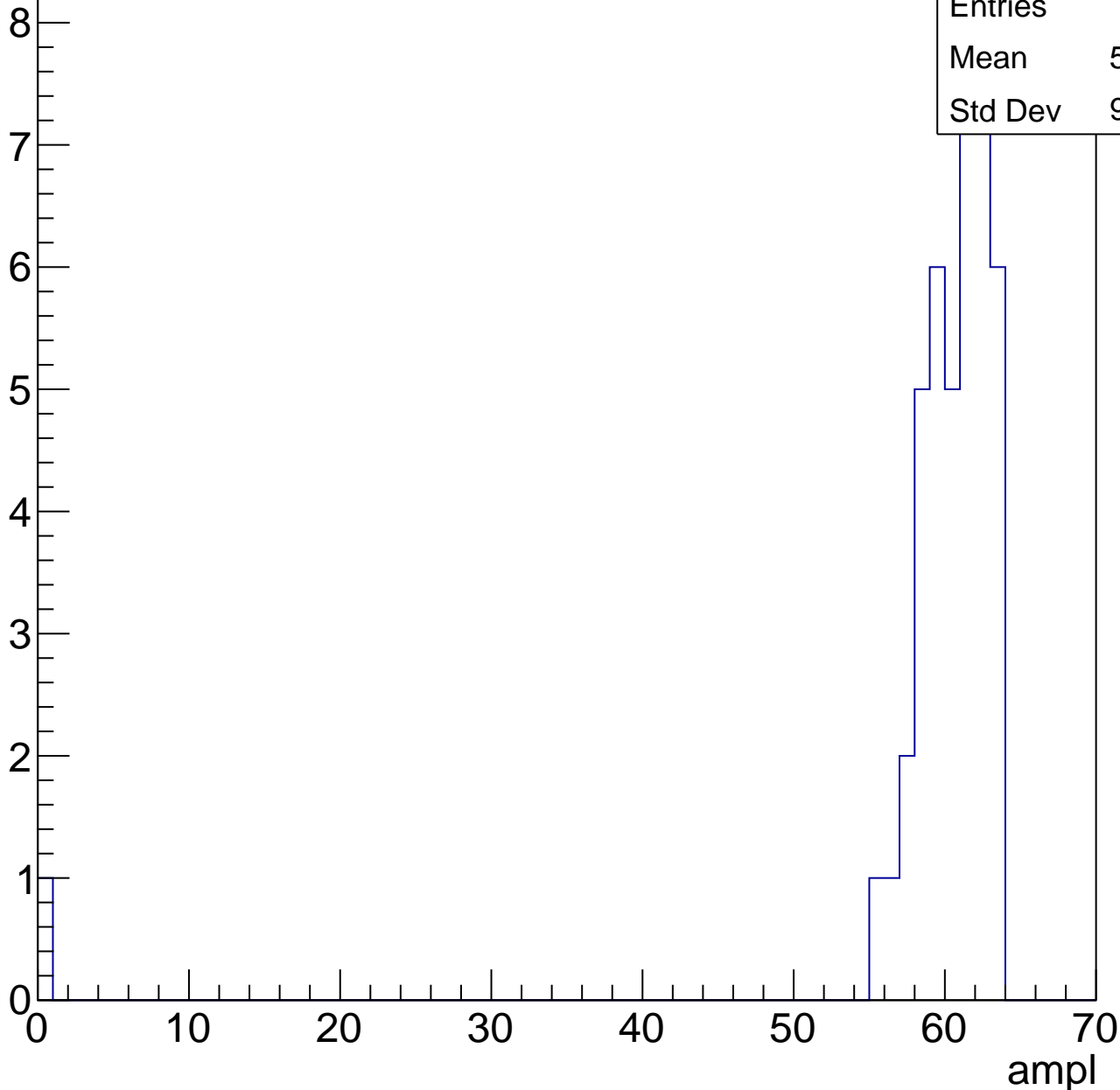


# B1L002S, U17-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	58.86
Std Dev	9.305

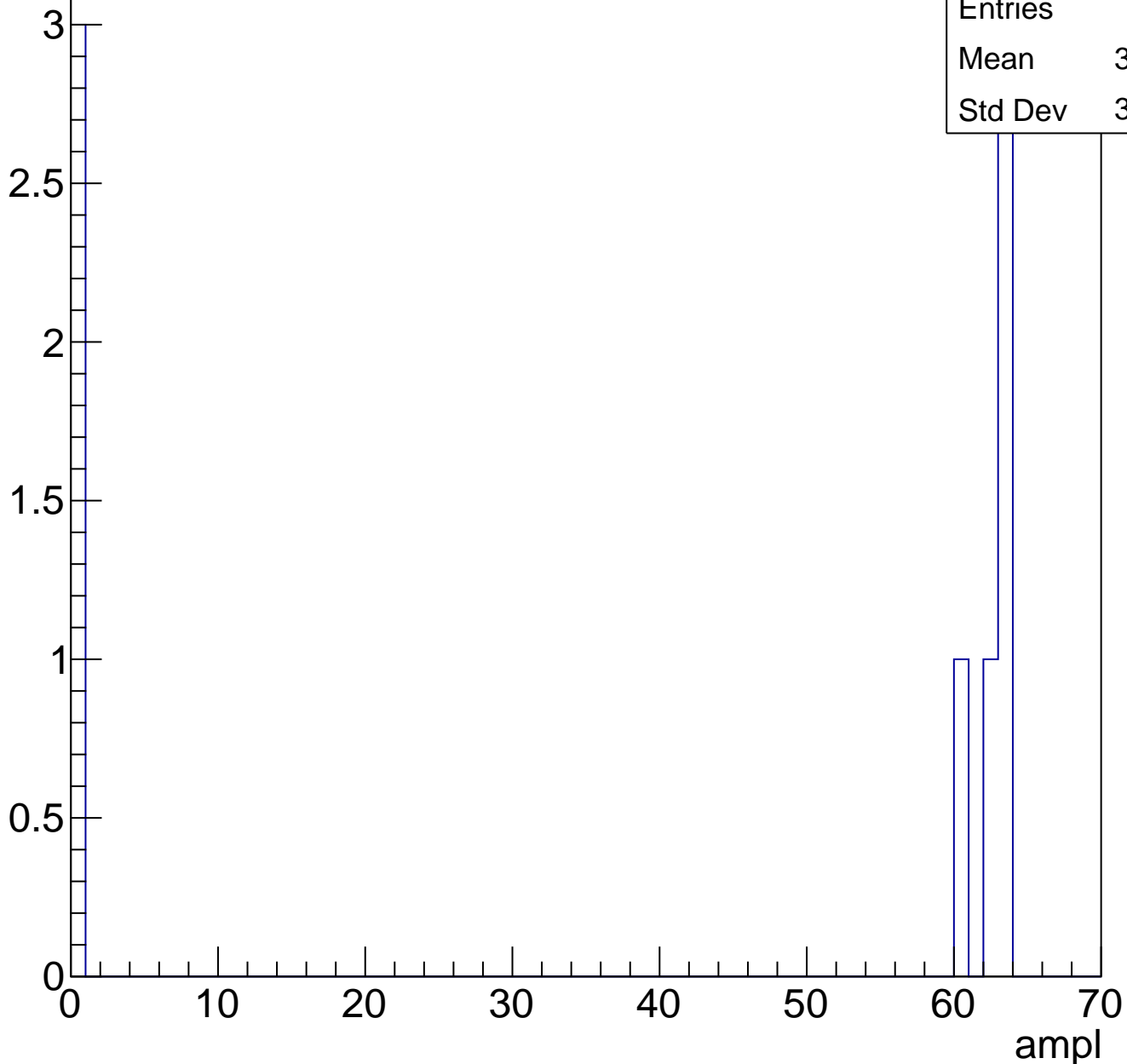




# B1L002S, U17-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch13, adc0

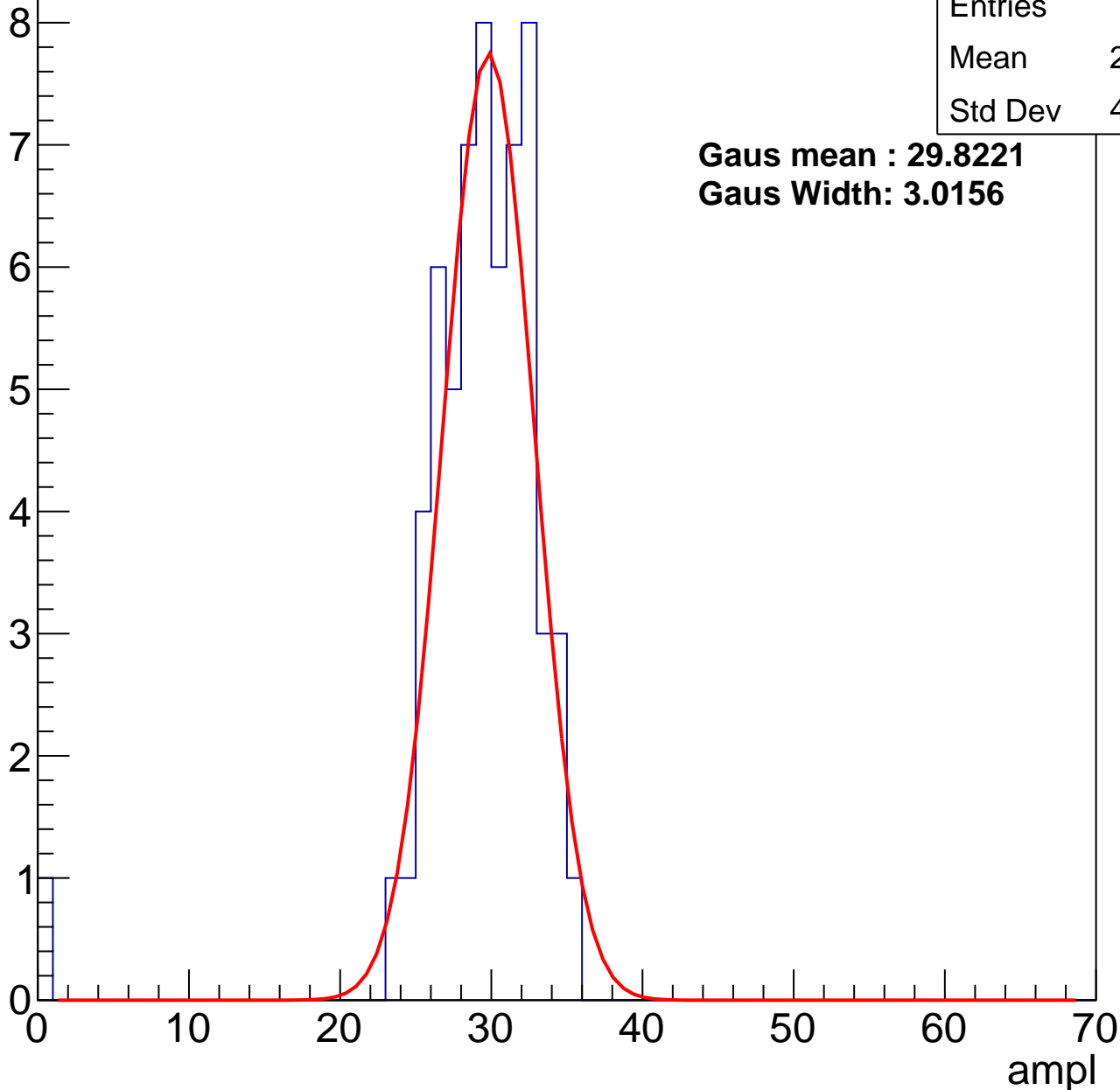
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.77
Std Dev	4.632

**Gaus mean : 29.8221**

**Gaus Width: 3.0156**



# B1L002S, U17-ch13, adc1

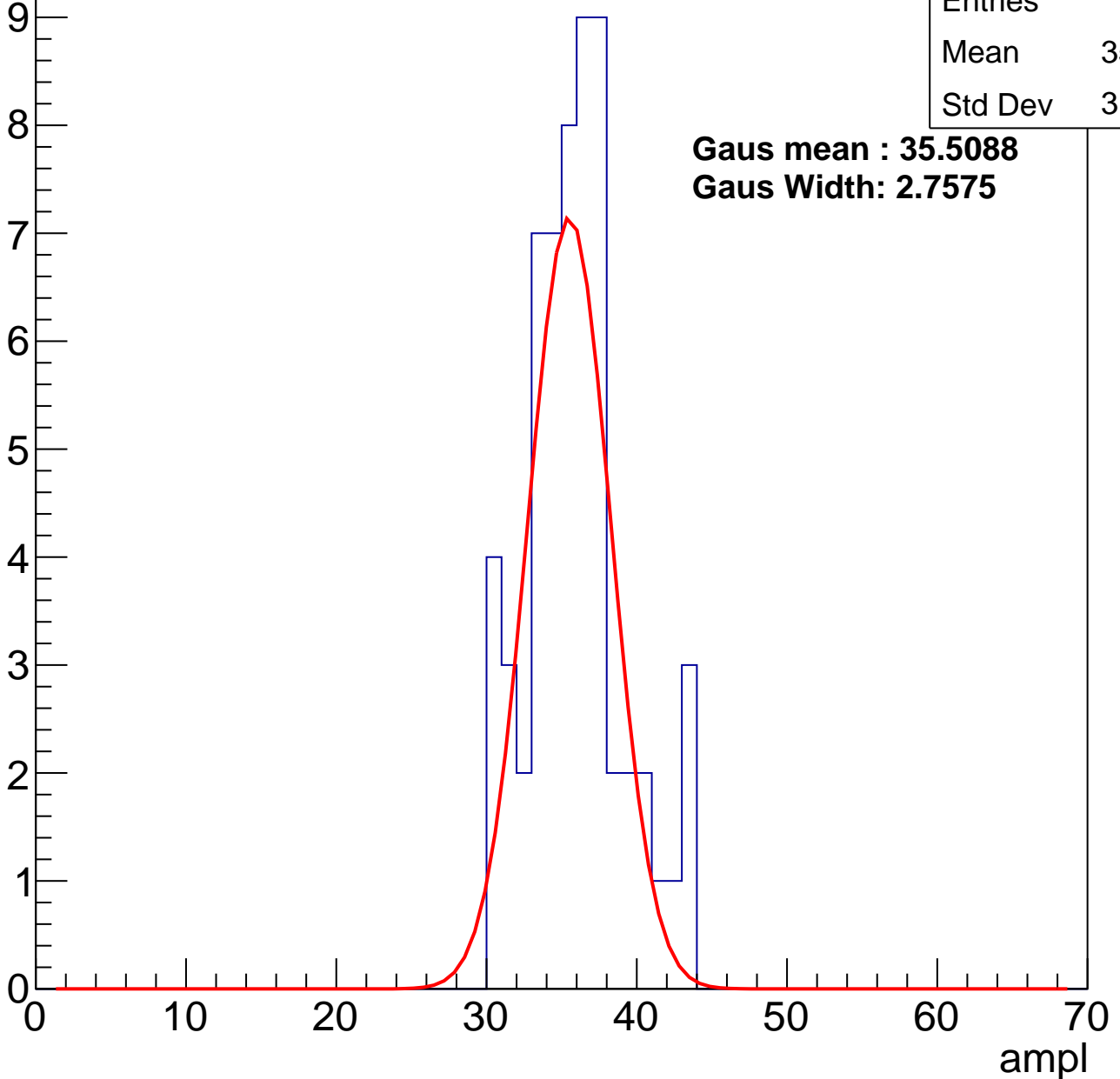
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.48
Std Dev	3.186

**Gaus mean : 35.5088**

**Gaus Width: 2.7575**



# B1L002S, U17-ch13, adc2

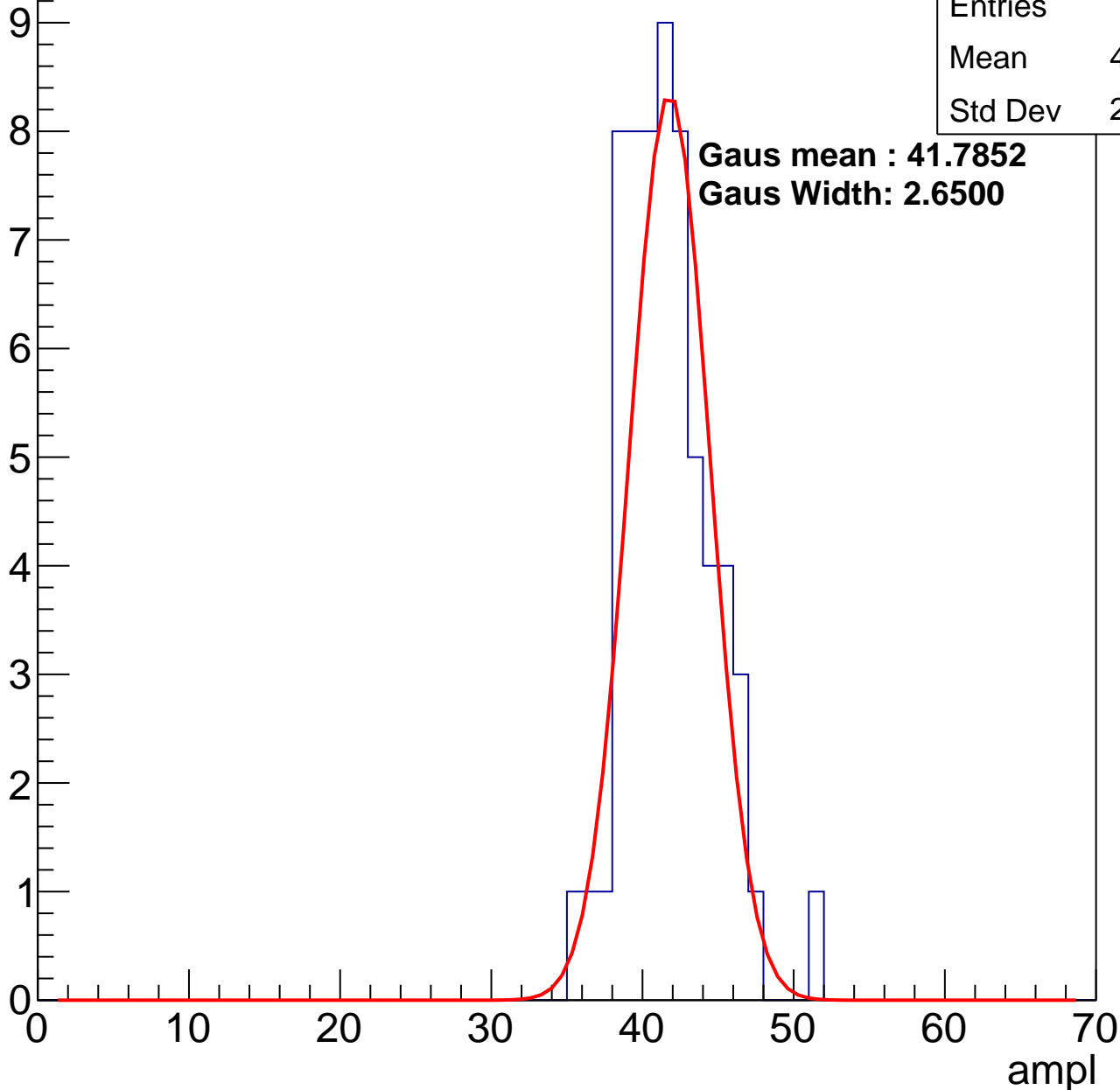
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.23
Std Dev	2.909

**Gaus mean : 41.7852**

**Gaus Width: 2.6500**

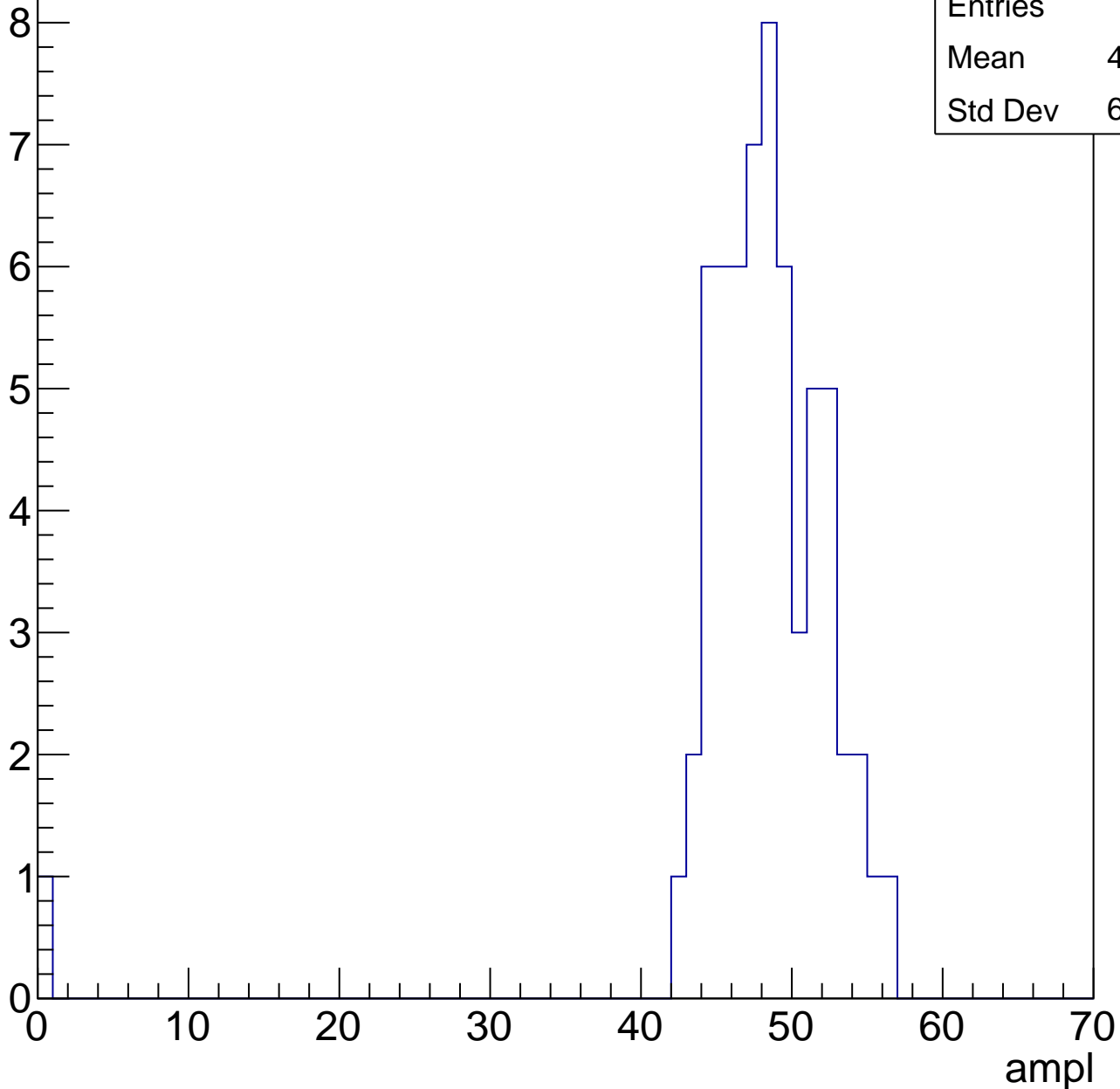


# B1L002S, U17-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	47.34
Std Dev	6.865

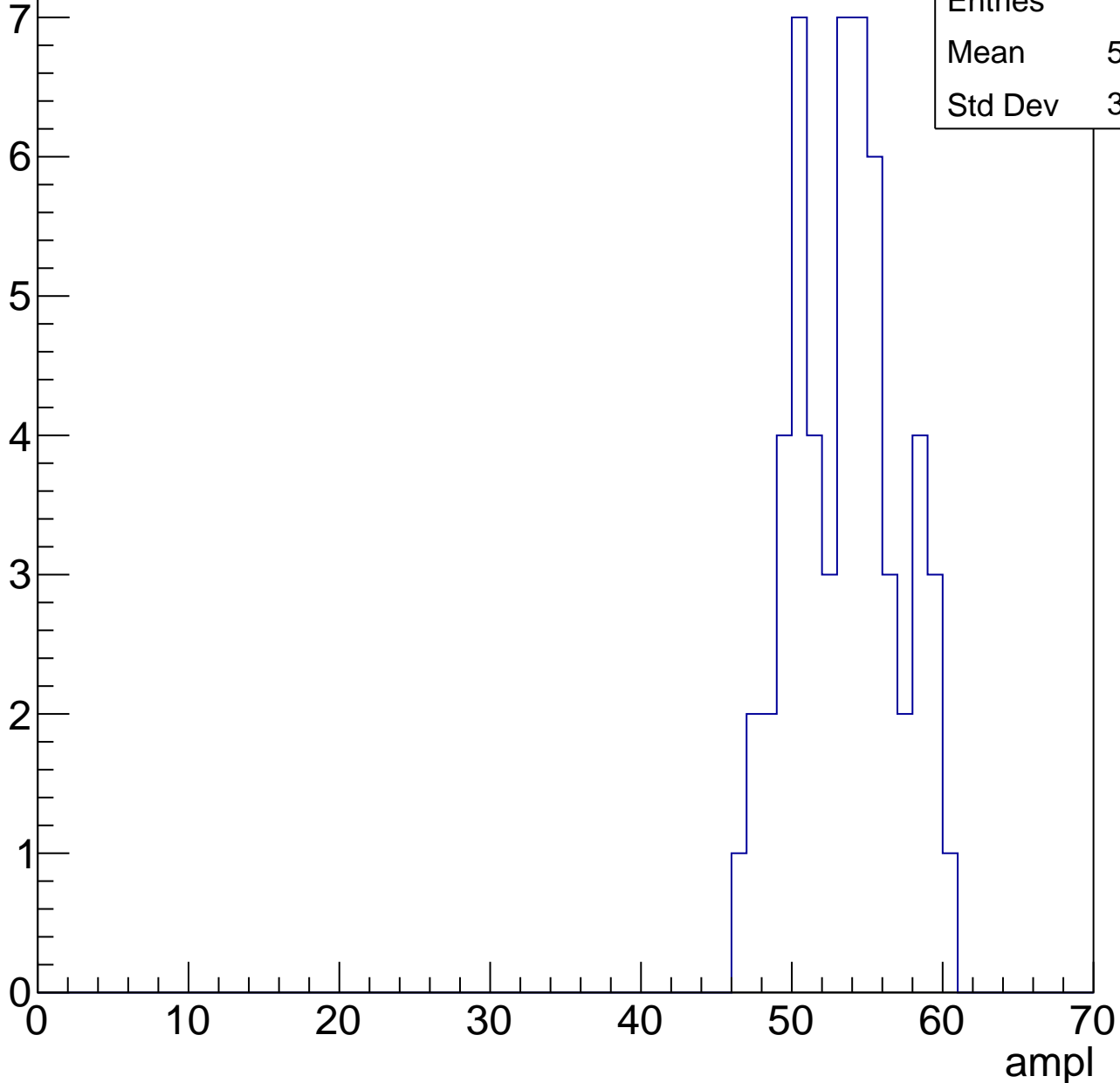


# B1L002S, U17-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.07
Std Dev	3.443

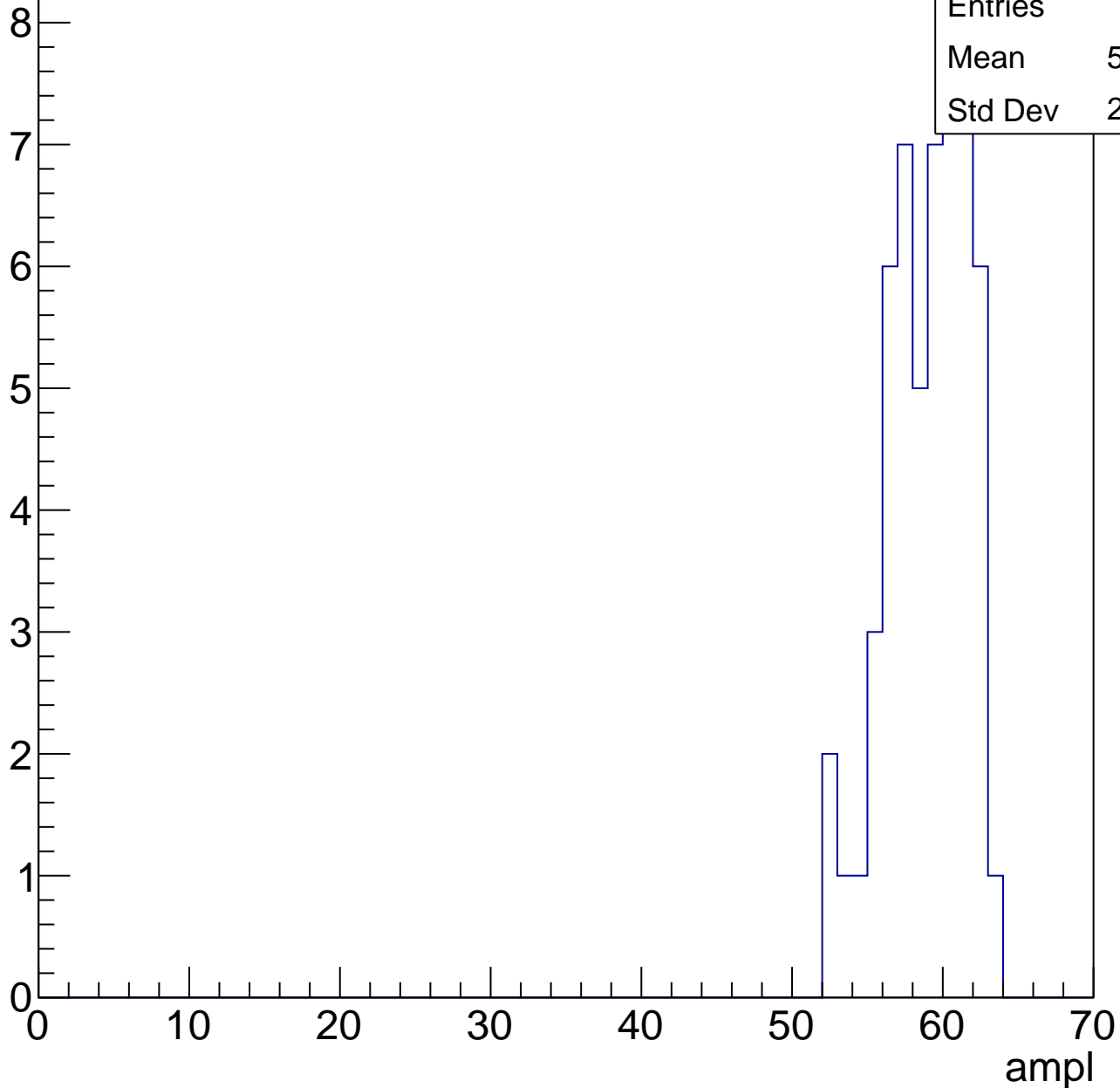


# B1L002S, U17-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	58.49
Std Dev	2.662

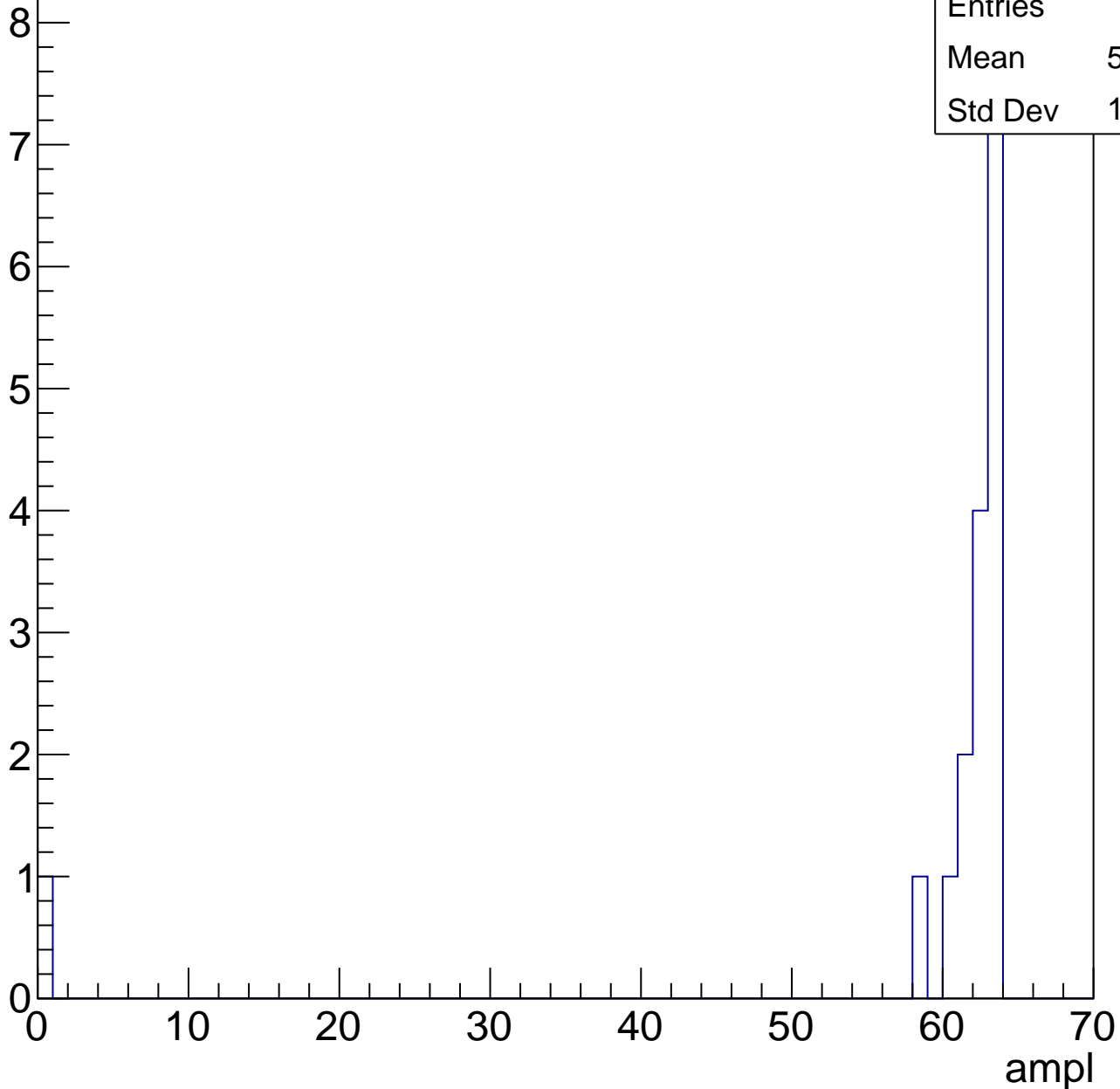


# B1L002S, U17-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58.35
Std Dev	14.65

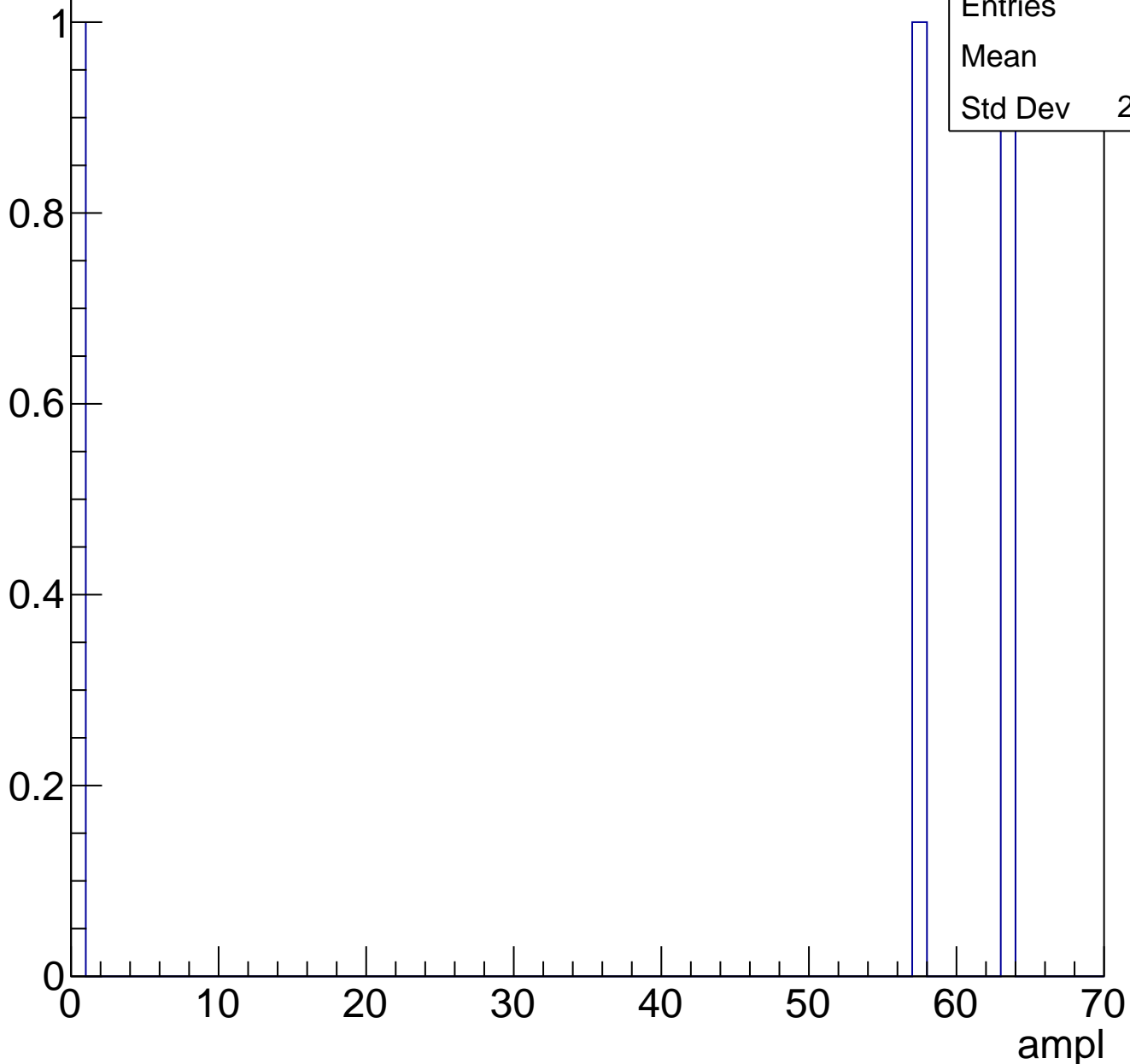




# B1L002S, U17-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	40
Std Dev	28.39

# B1L002S, U17-ch14, adc0

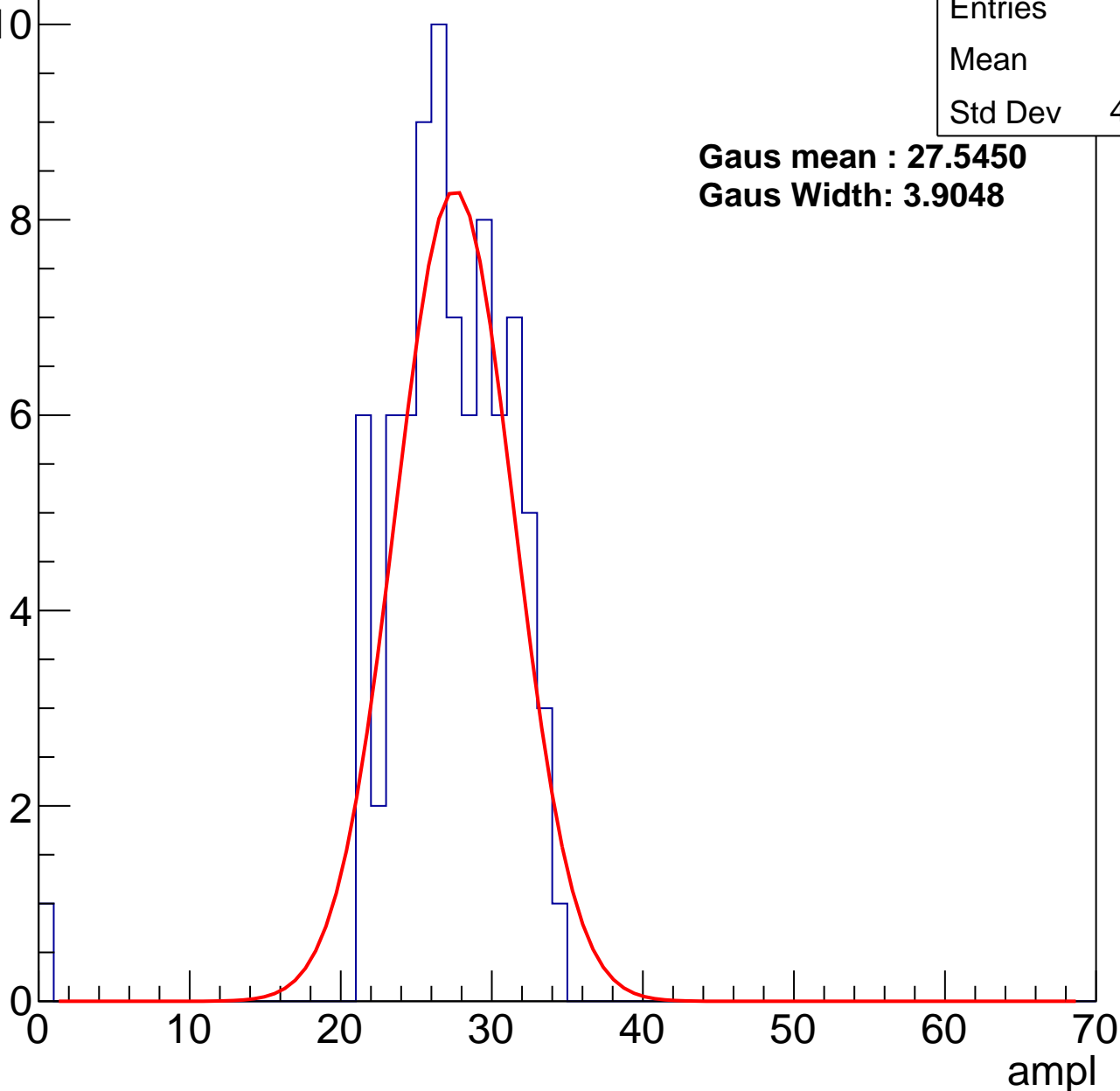
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	26.7
Std Dev	4.485

**Gaus mean : 27.5450**

**Gaus Width: 3.9048**



# B1L002S, U17-ch14, adc1

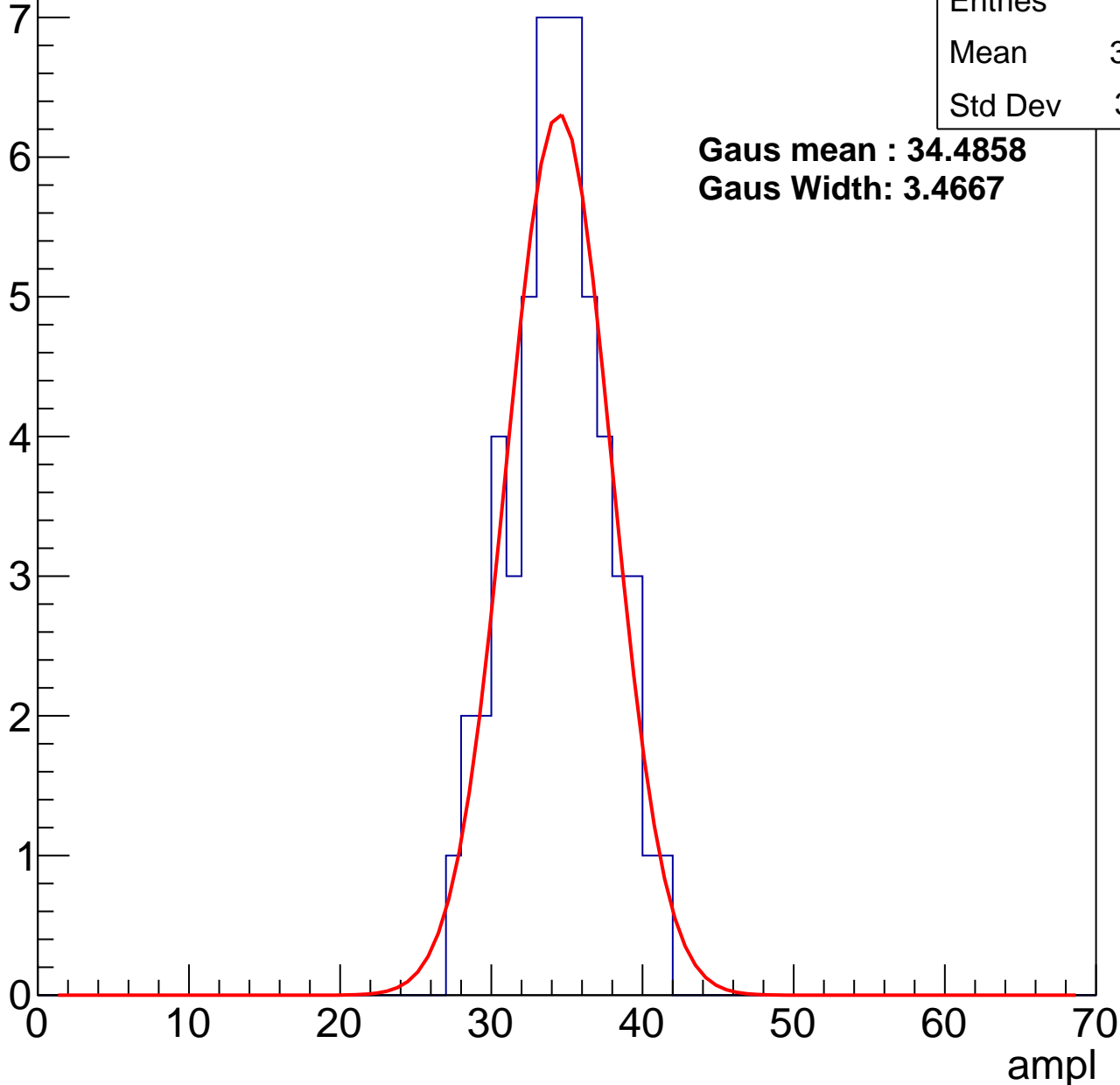
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	33.96
Std Dev	3.191

**Gaus mean : 34.4858**

**Gaus Width: 3.4667**



# B1L002S, U17-ch14, adc2

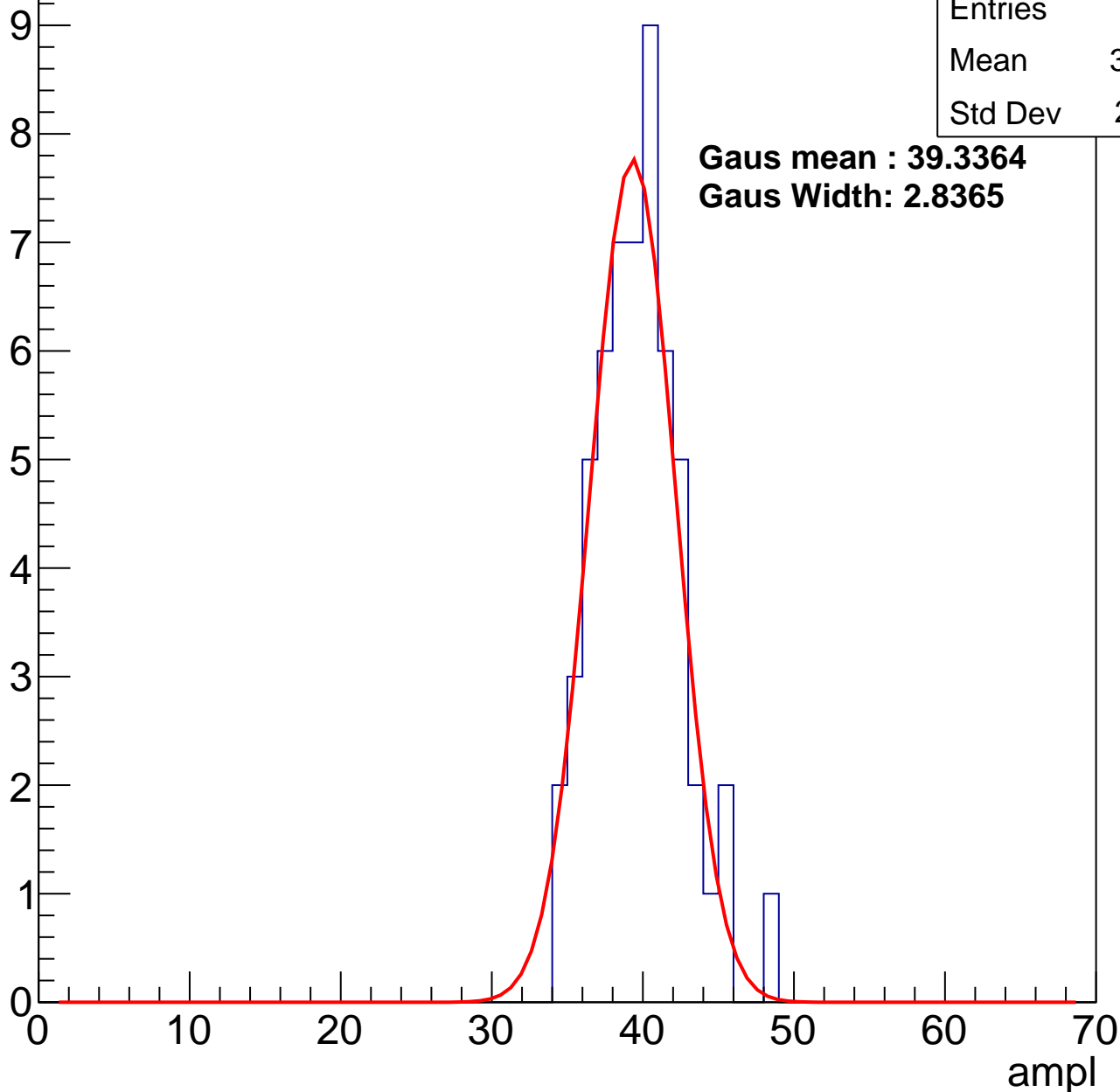
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	39.25
Std Dev	2.861

**Gaus mean : 39.3364**

**Gaus Width: 2.8365**

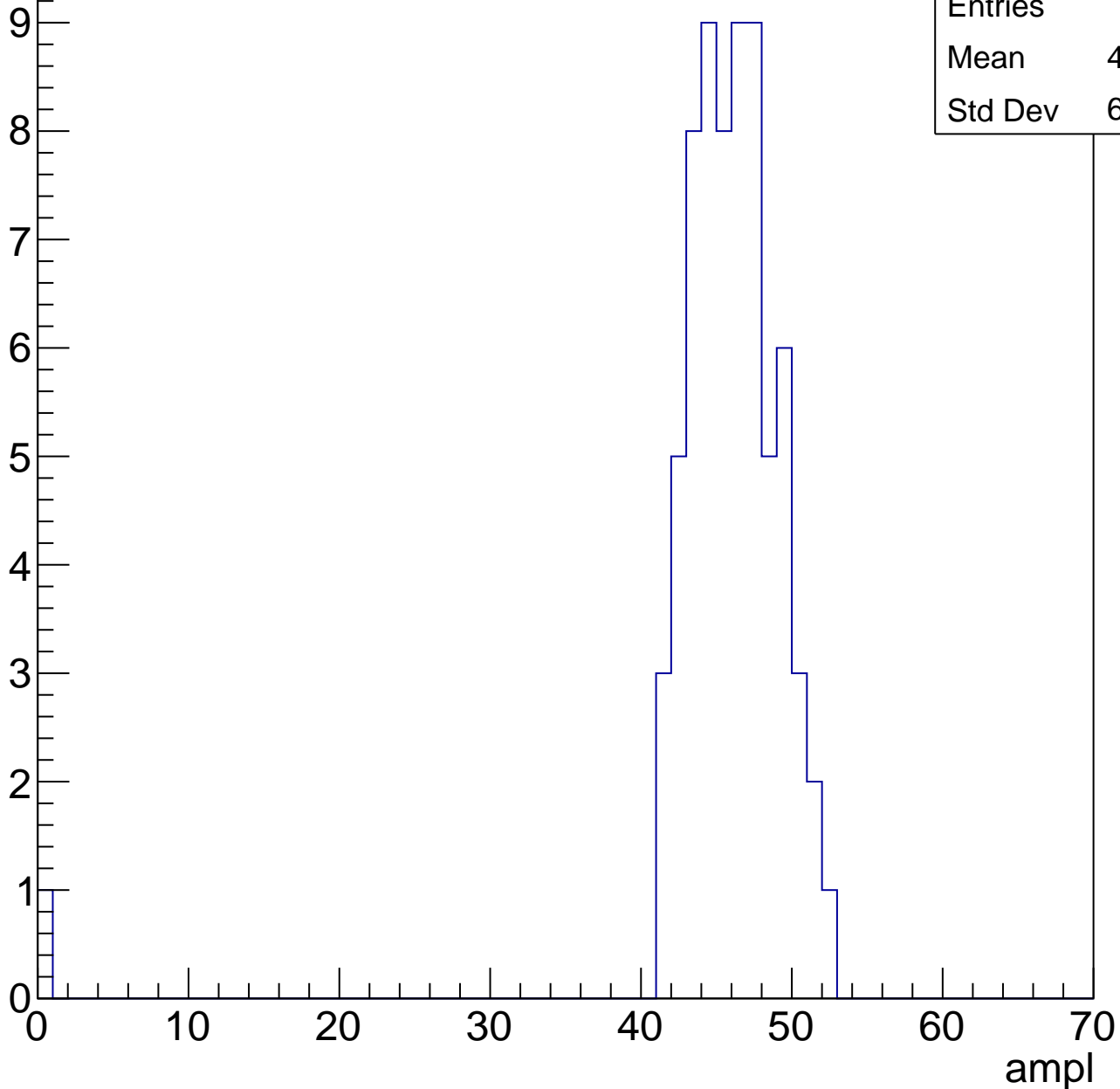


# B1L002S, U17-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.04
Std Dev	6.068

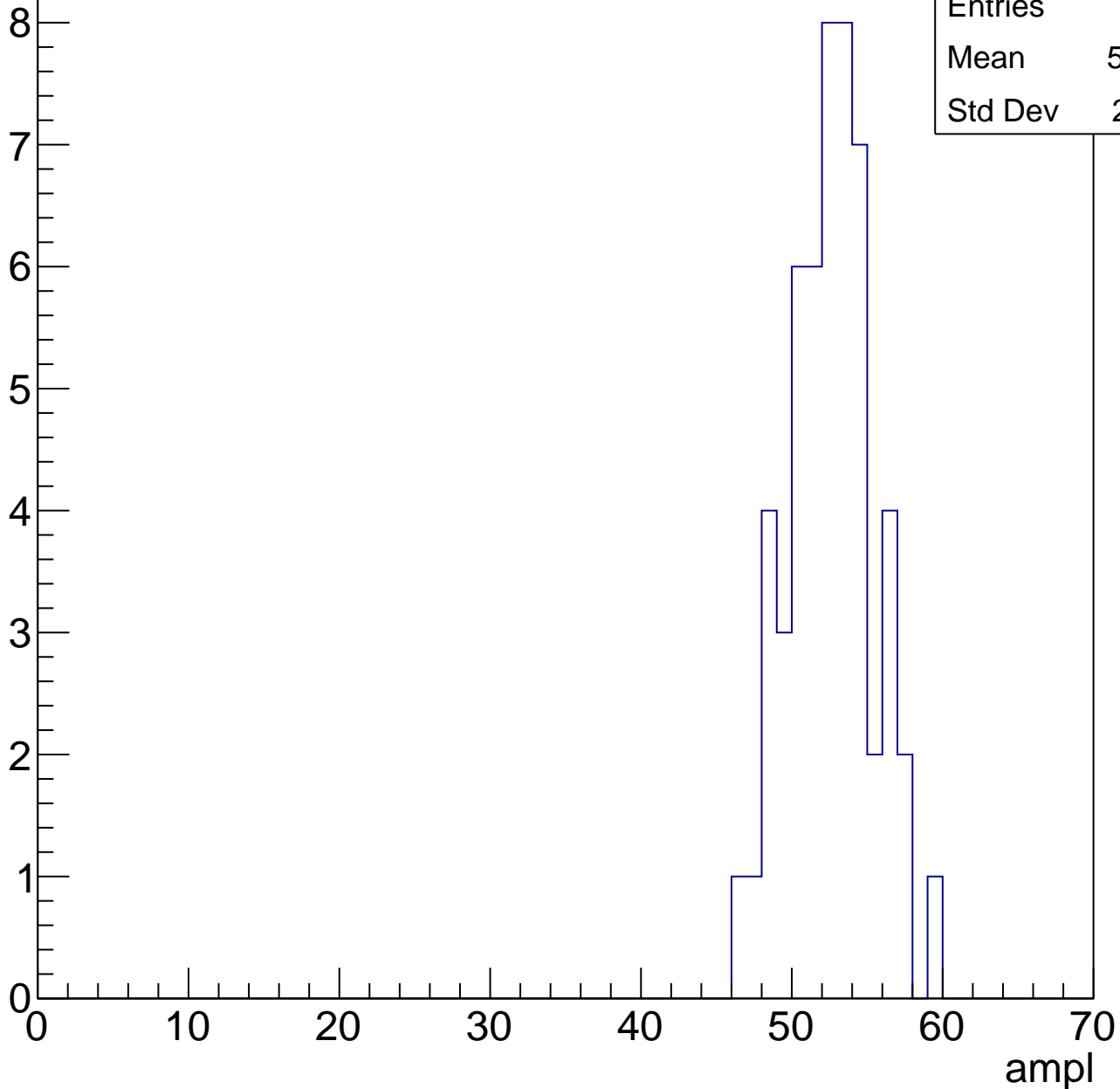


# B1L002S, U17-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.13
Std Dev	2.741

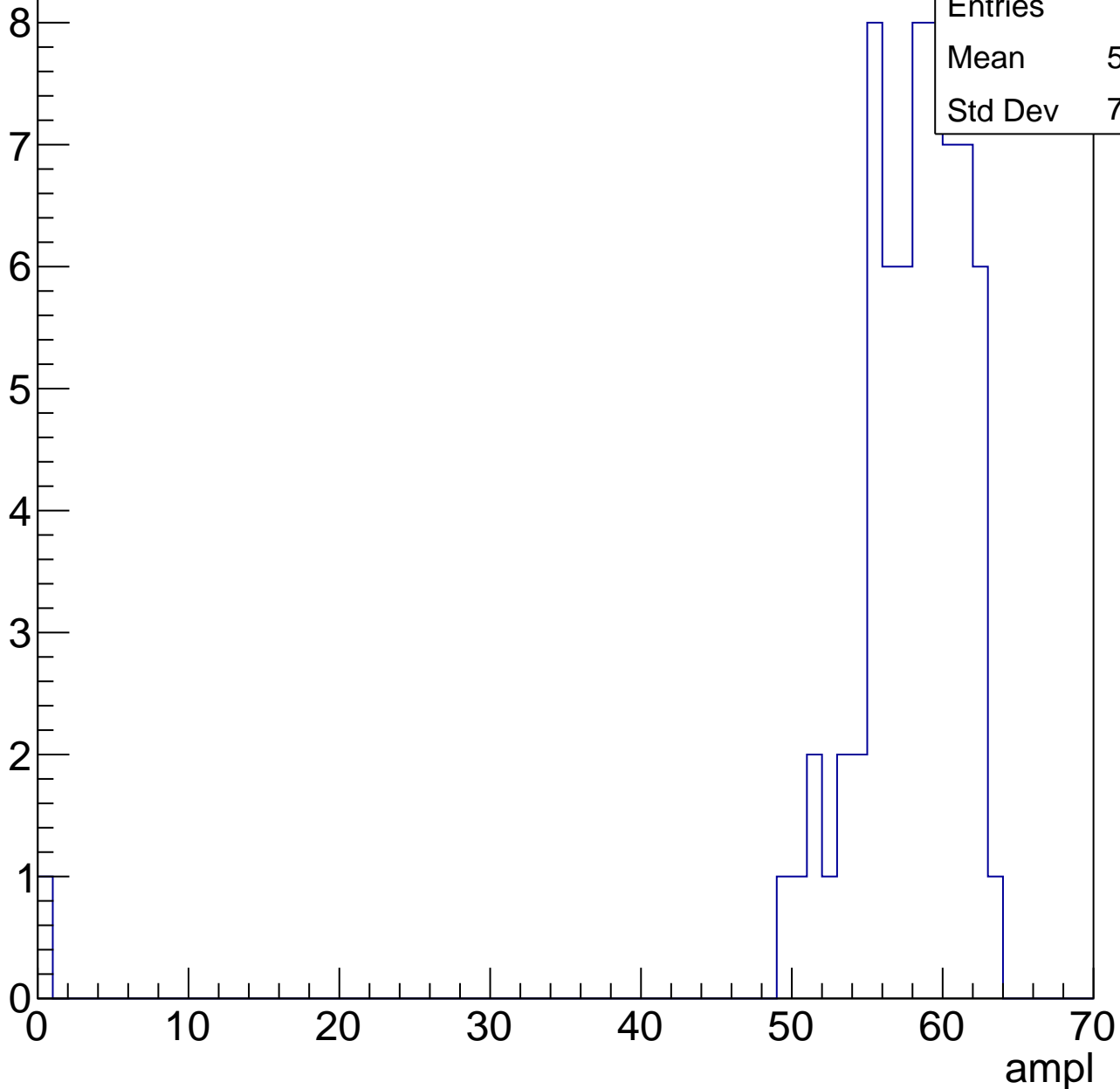


# B1L002S, U17-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	56.76
Std Dev	7.673

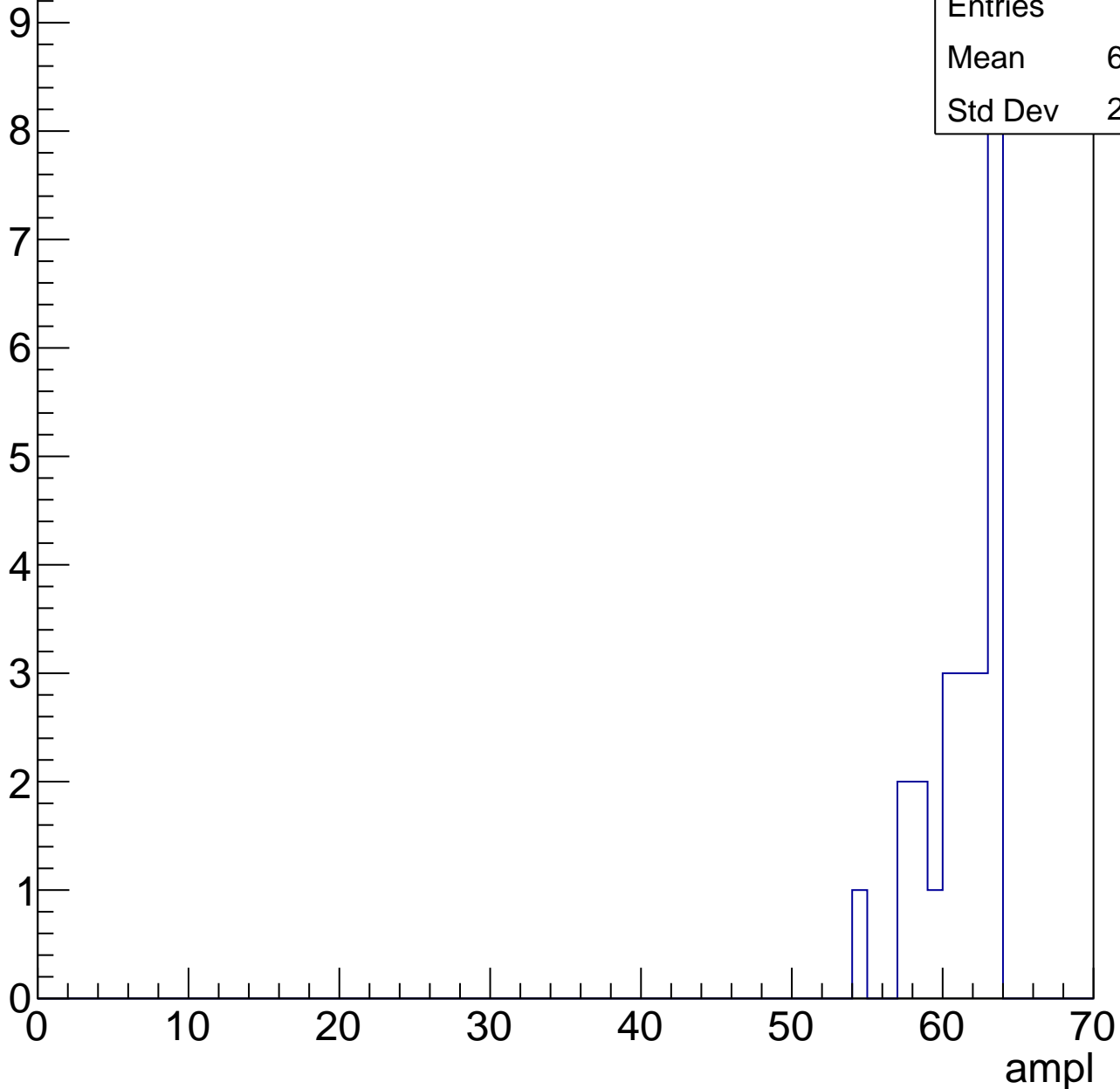


# B1L002S, U17-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	60.79
Std Dev	2.449

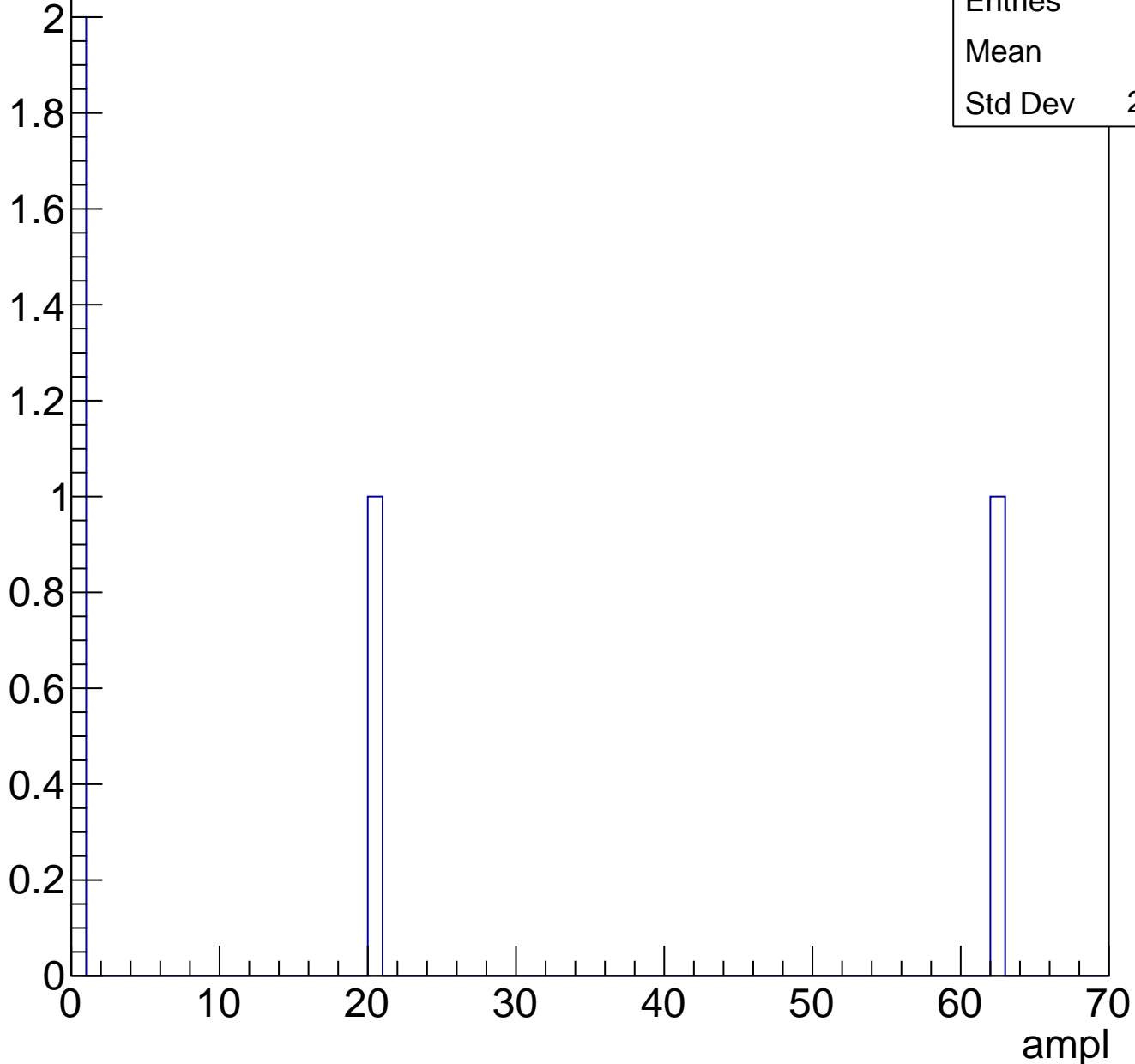




# B1L002S, U17-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.5
Std Dev	25.31

# B1L002S, U17-ch15, adc0

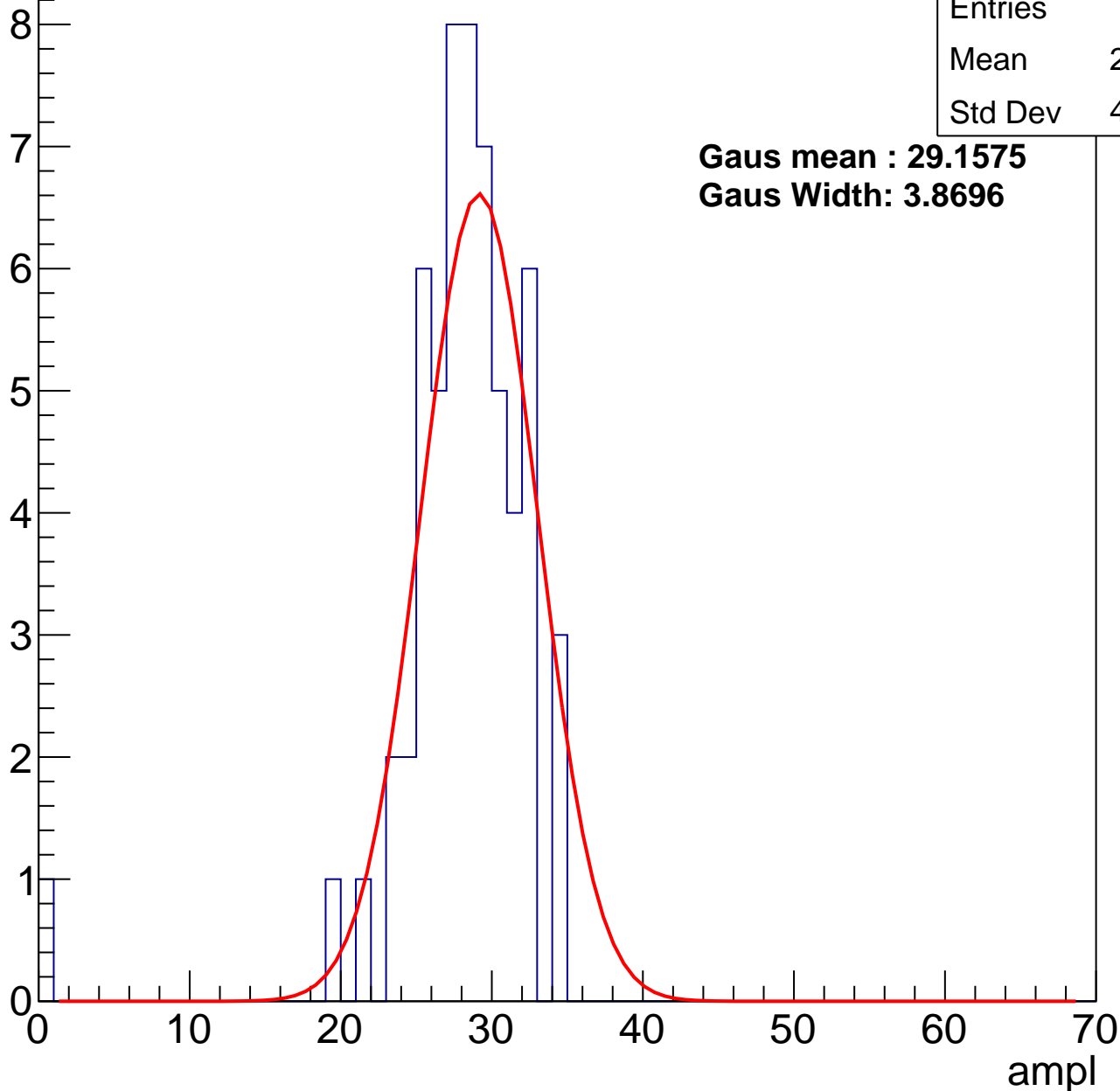
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.54
Std Dev	4.753

**Gaus mean : 29.1575**

**Gaus Width: 3.8696**



# B1L002S, U17-ch15, adc1

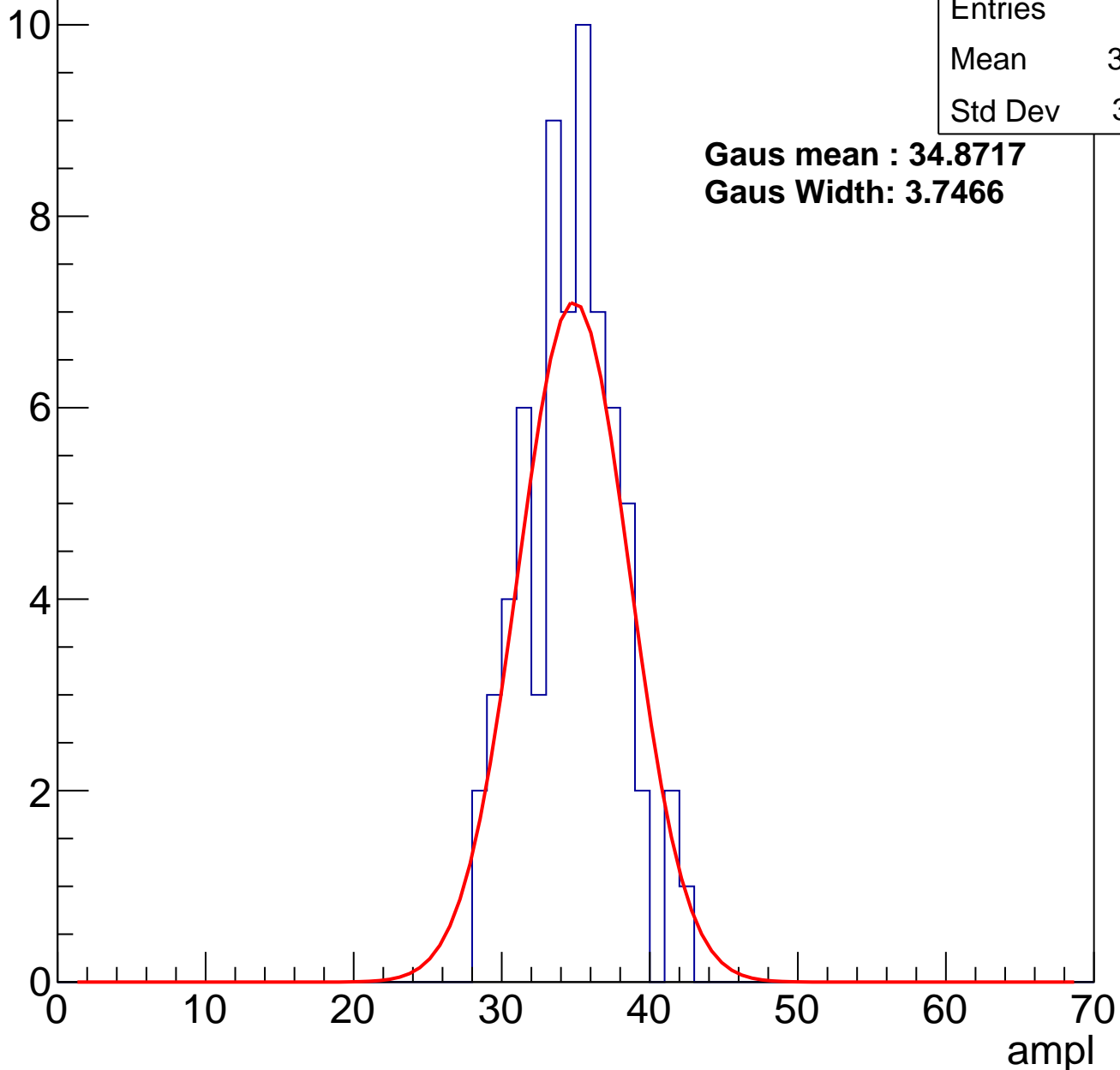
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	34.27
Std Dev	3.151

**Gaus mean : 34.8717**

**Gaus Width: 3.7466**

Entry



# B1L002S, U17-ch15, adc2

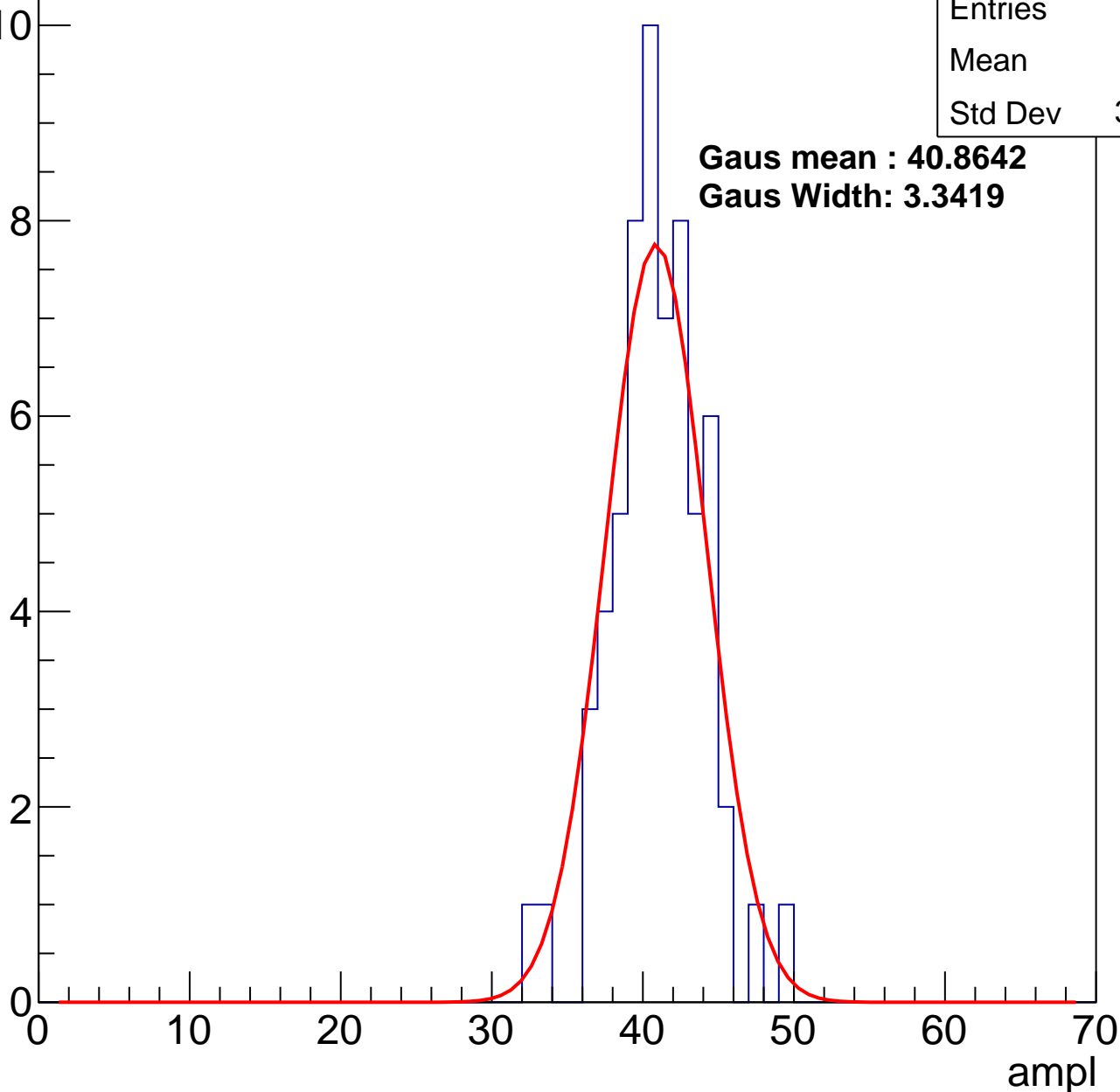
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	40.5
Std Dev	3.031

**Gaus mean : 40.8642**

**Gaus Width: 3.3419**

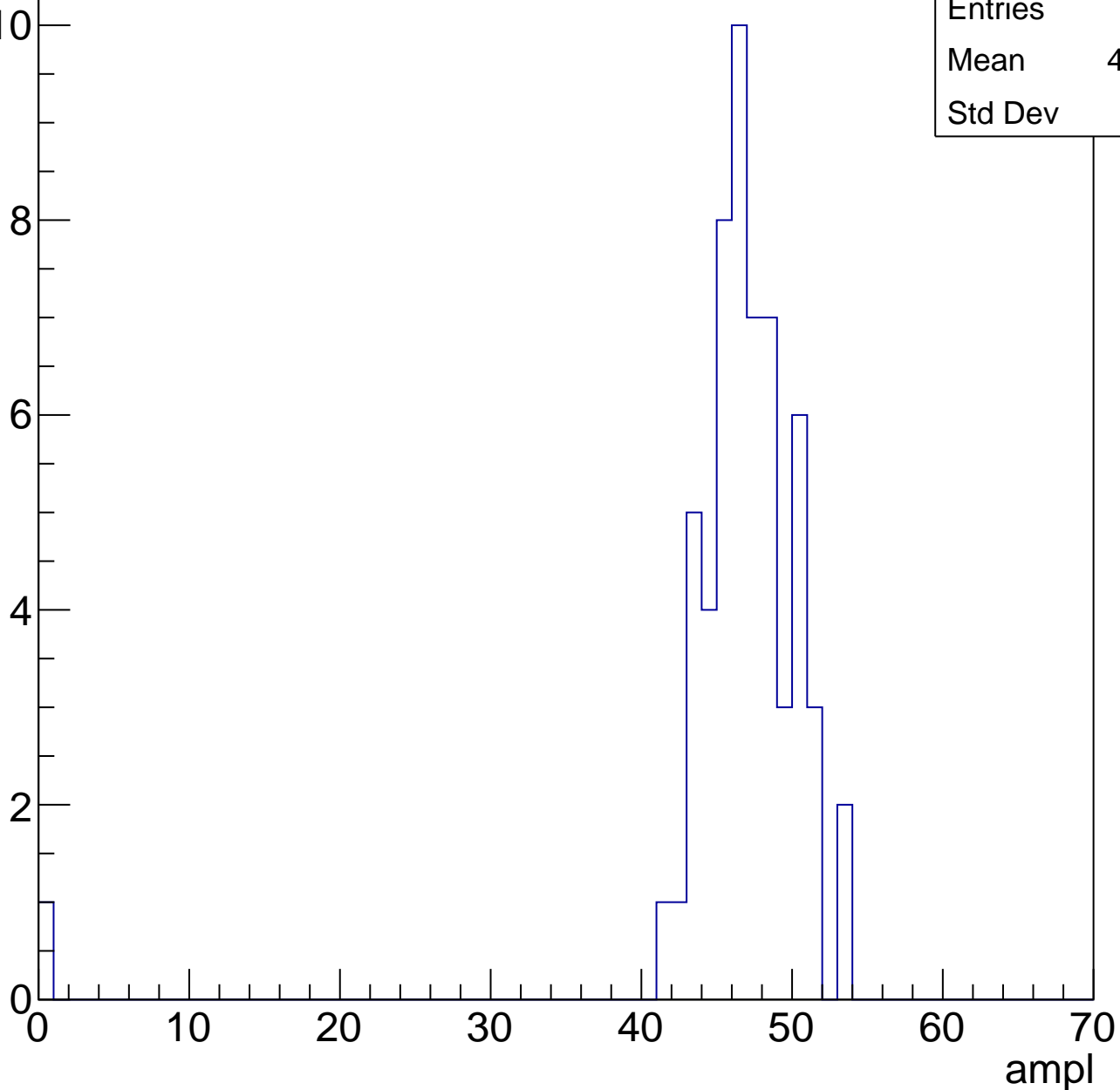


# B1L002S, U17-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.95
Std Dev	6.64

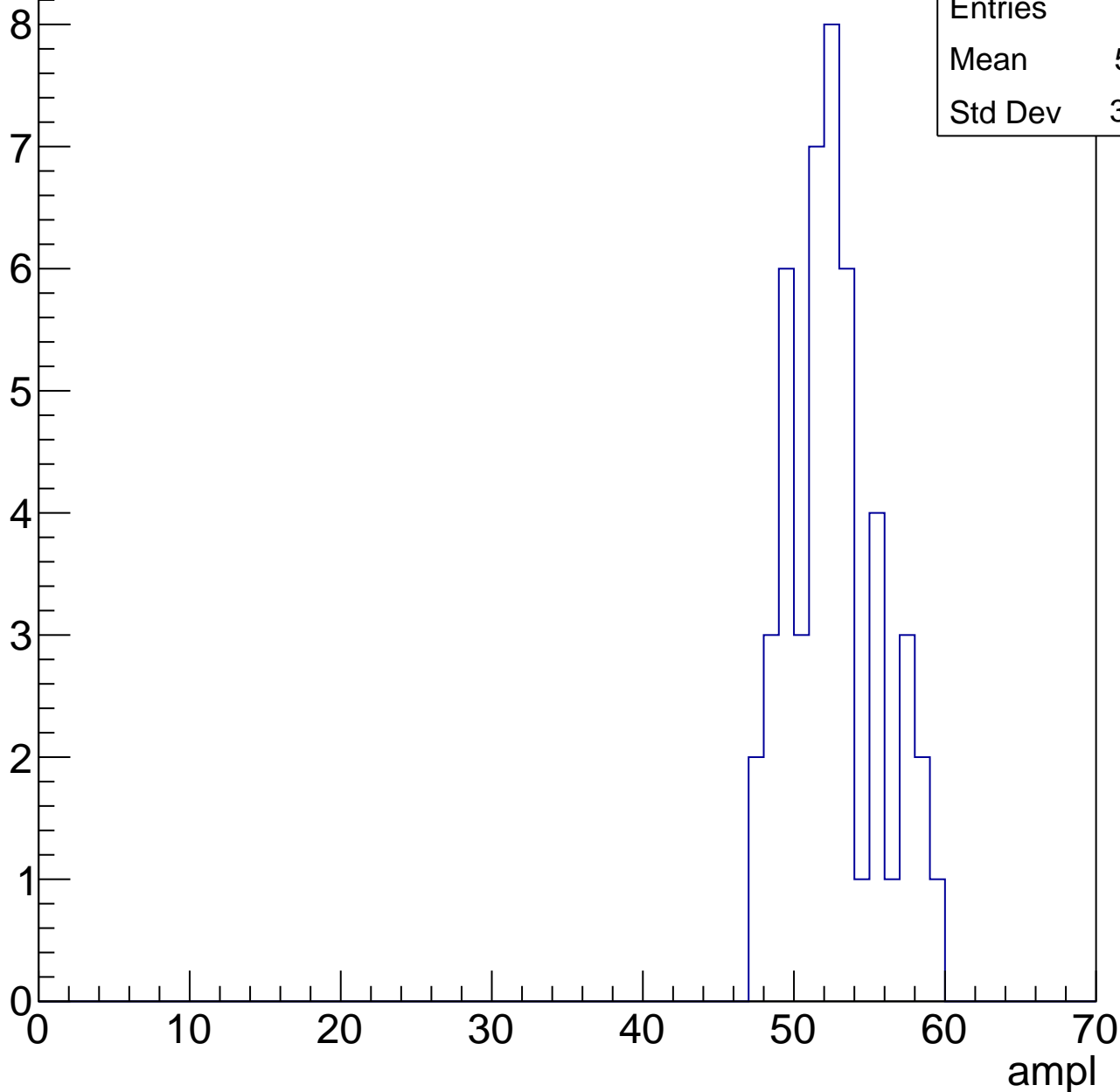


# B1L002S, U17-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	52.11
Std Dev	3.019



# B1L002S, U17-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

8

6

4

2

0

0

10

20

30

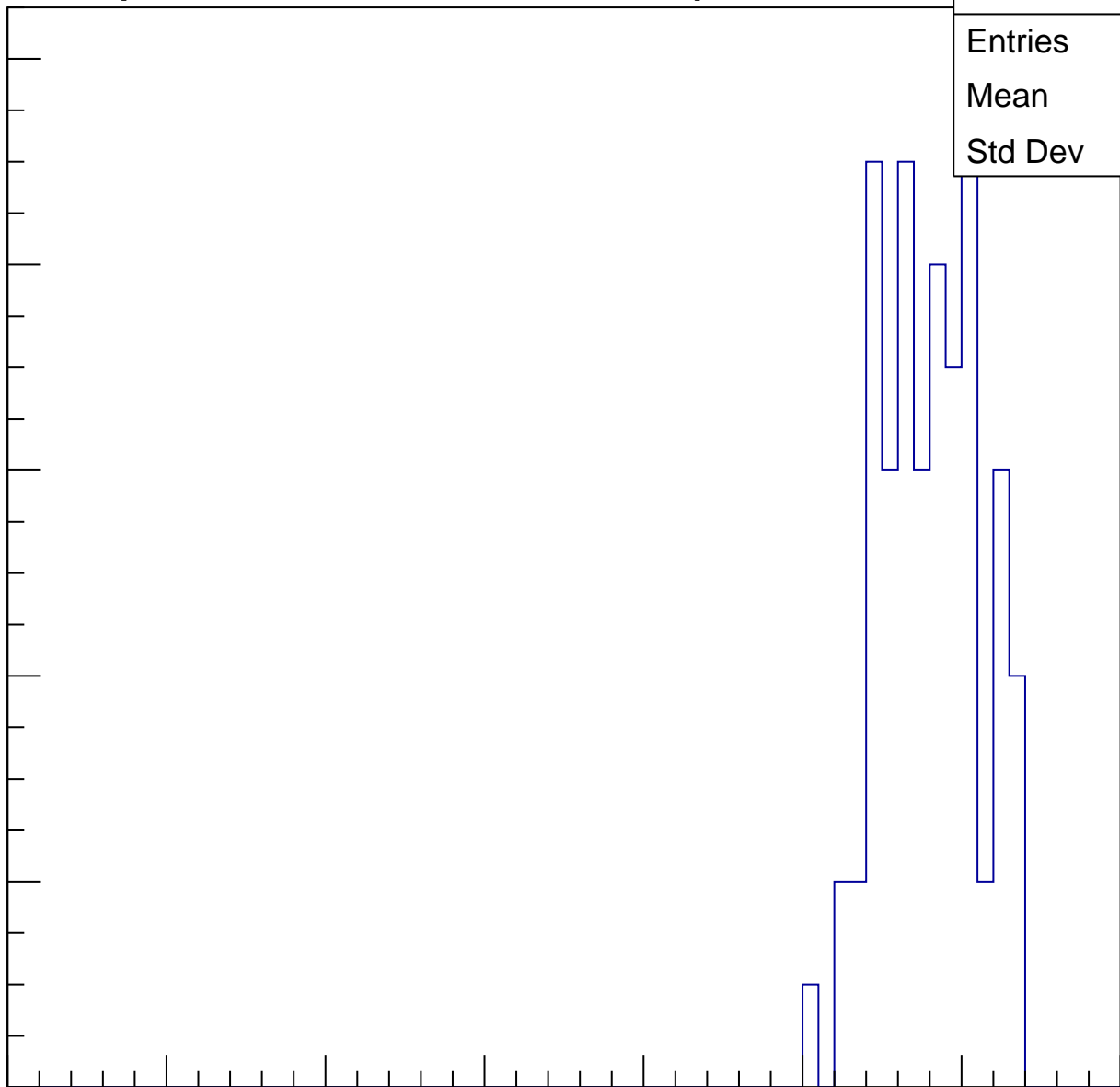
40

50

60

ampl

Entries	72
Mean	57.57
Std Dev	3.059

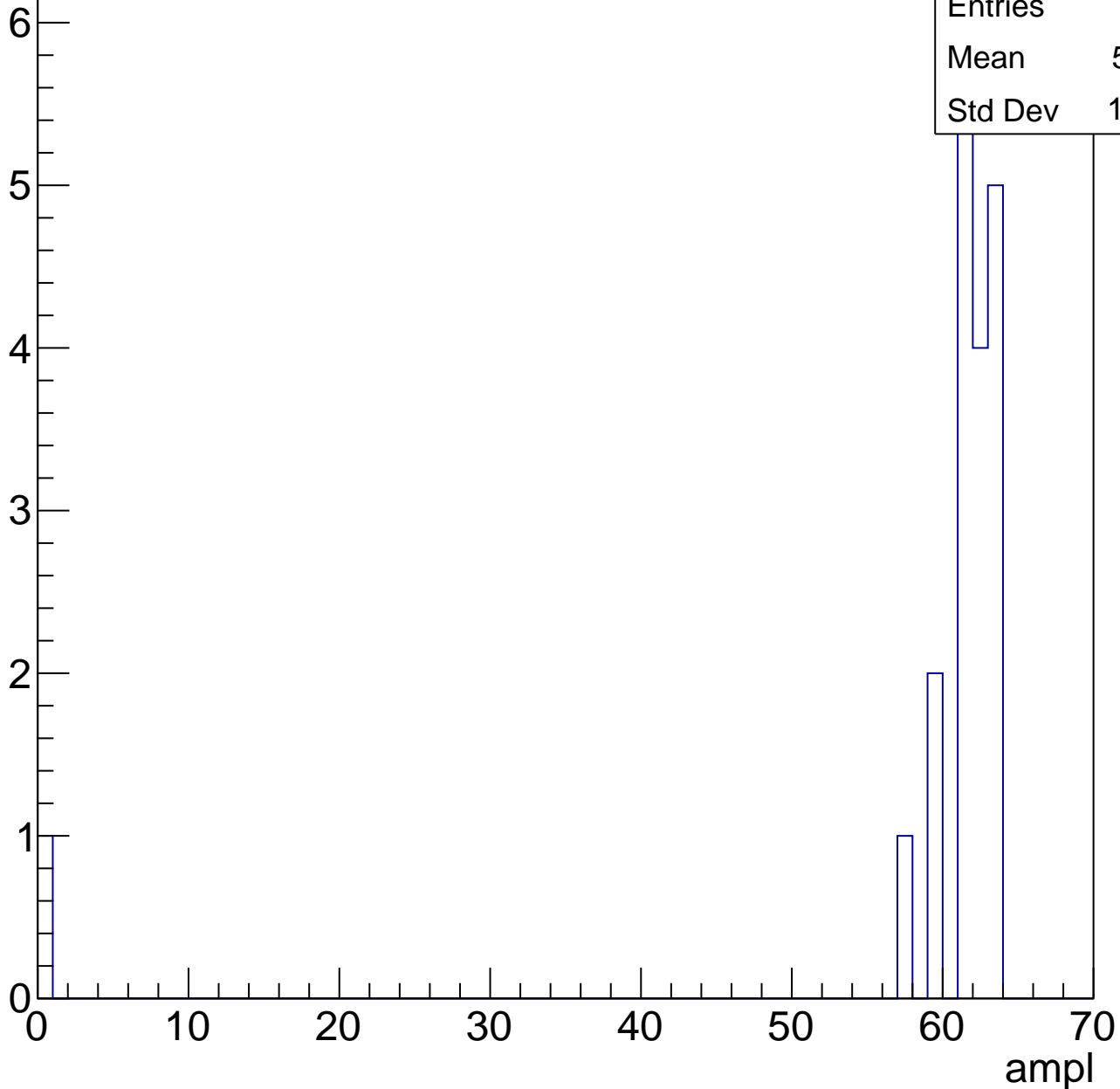


# B1L002S, U17-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.11
Std Dev	13.78

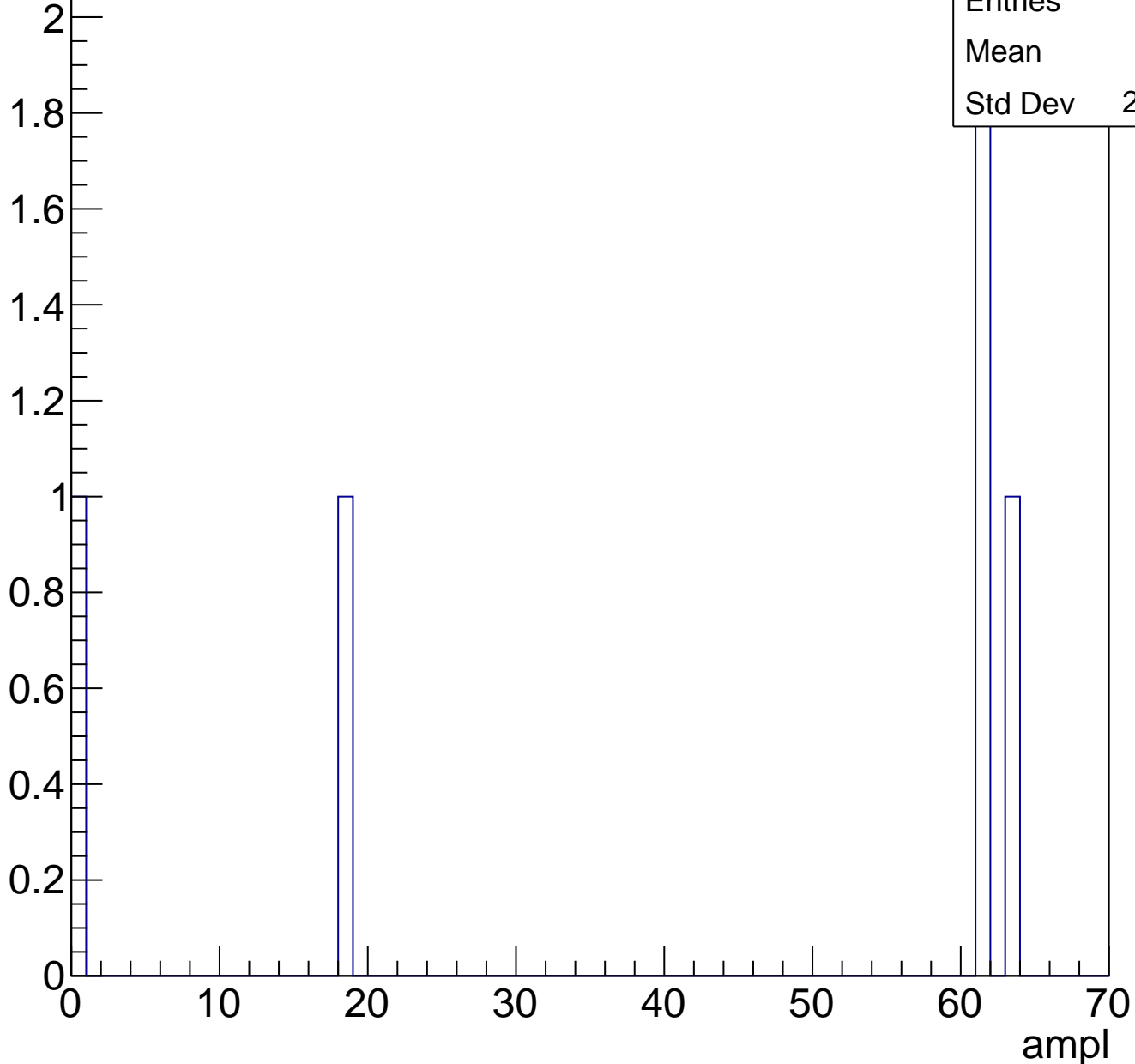




# B1L002S, U17-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	40.6
Std Dev	26.43

# B1L002S, U17-ch16, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	51
Mean	25.86
Std Dev	5.938

**Gaus mean : 26.5568**

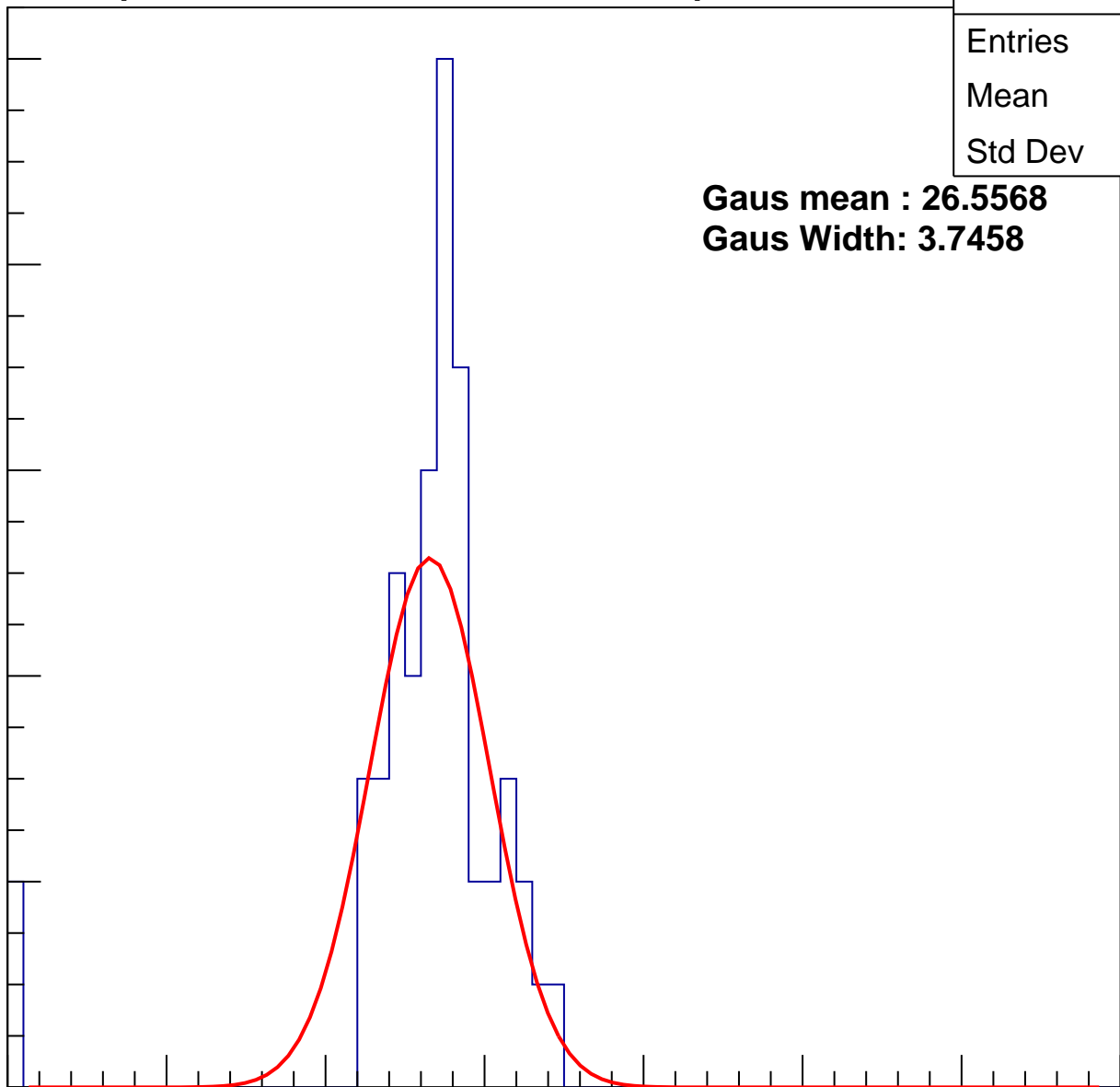
**Gaus Width: 3.7458**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch16, adc1

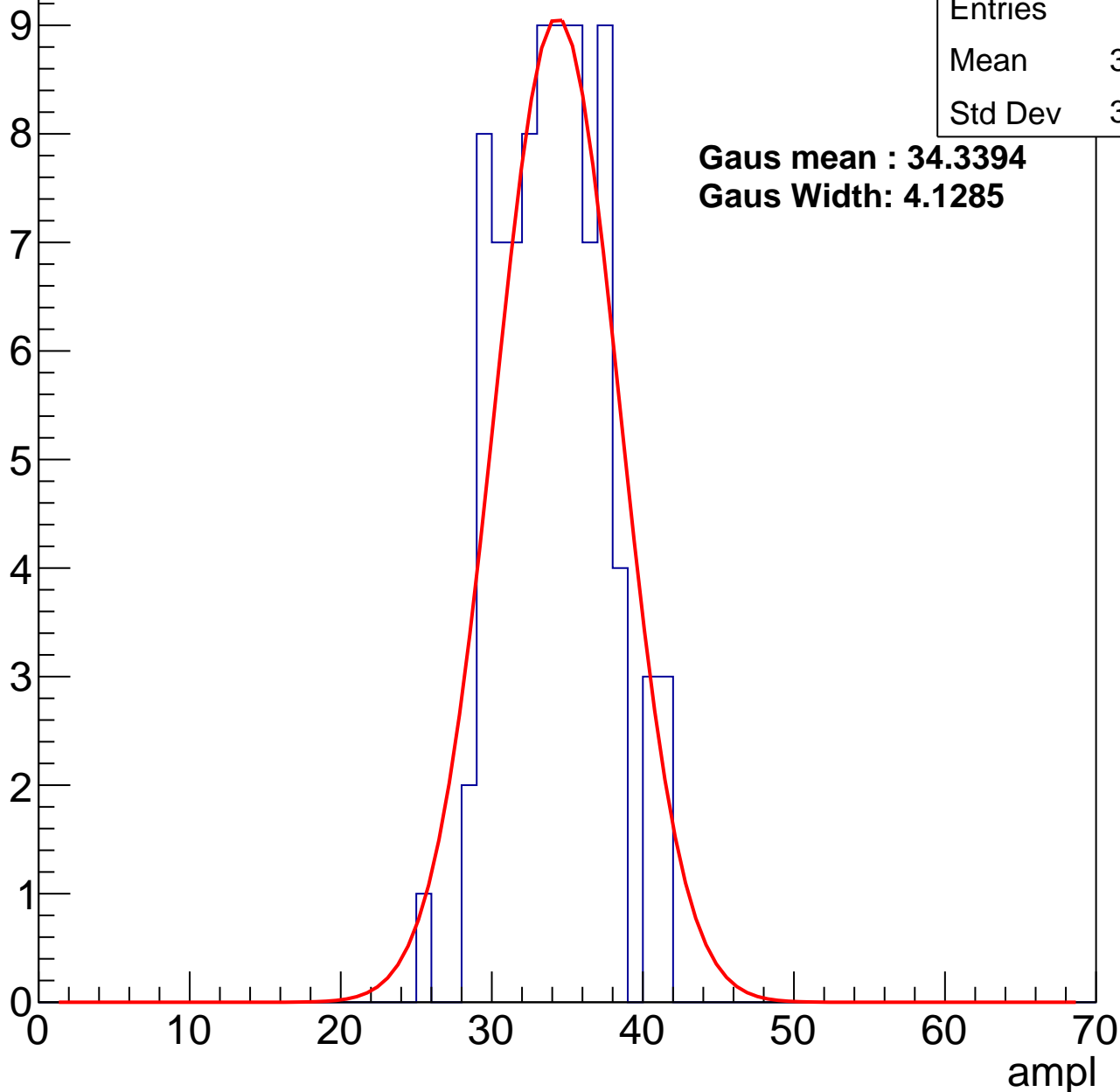
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	86
Mean	33.65
Std Dev	3.402

**Gaus mean : 34.3394**

**Gaus Width: 4.1285**



# B1L002S, U17-ch16, adc2

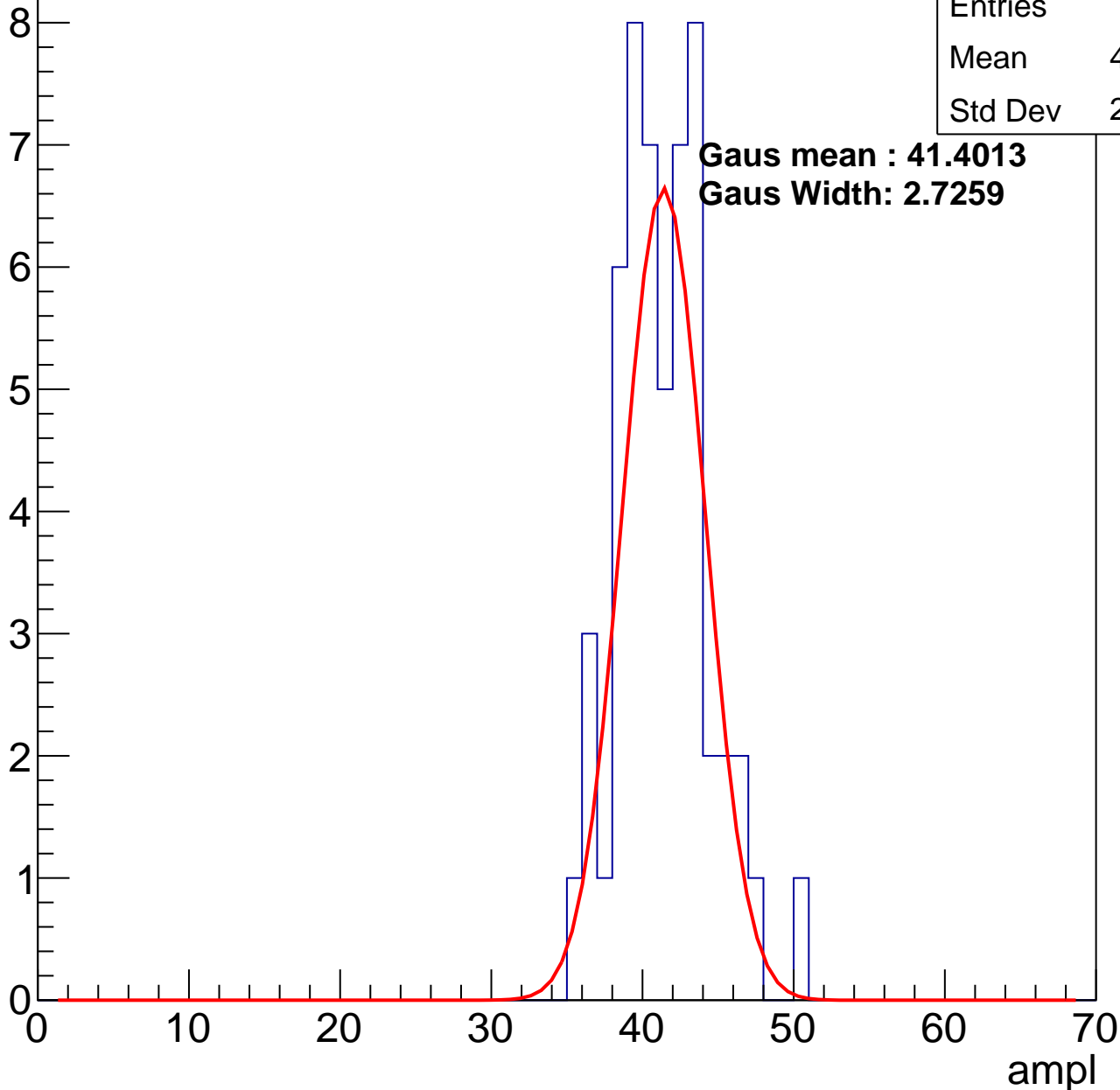
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.93
Std Dev	2.968

**Gaus mean : 41.4013**

**Gaus Width: 2.7259**

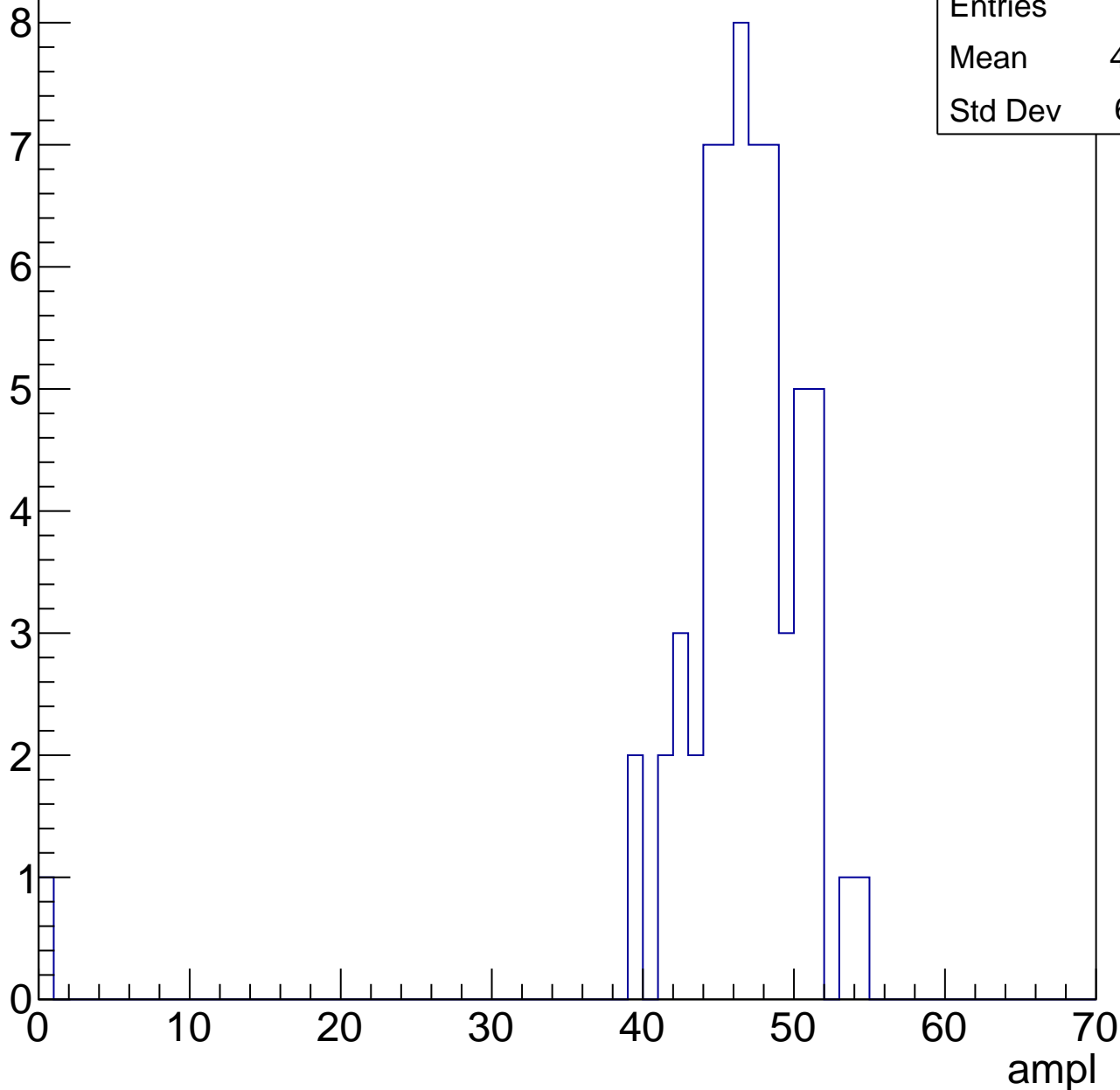


# B1L002S, U17-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.69
Std Dev	6.701

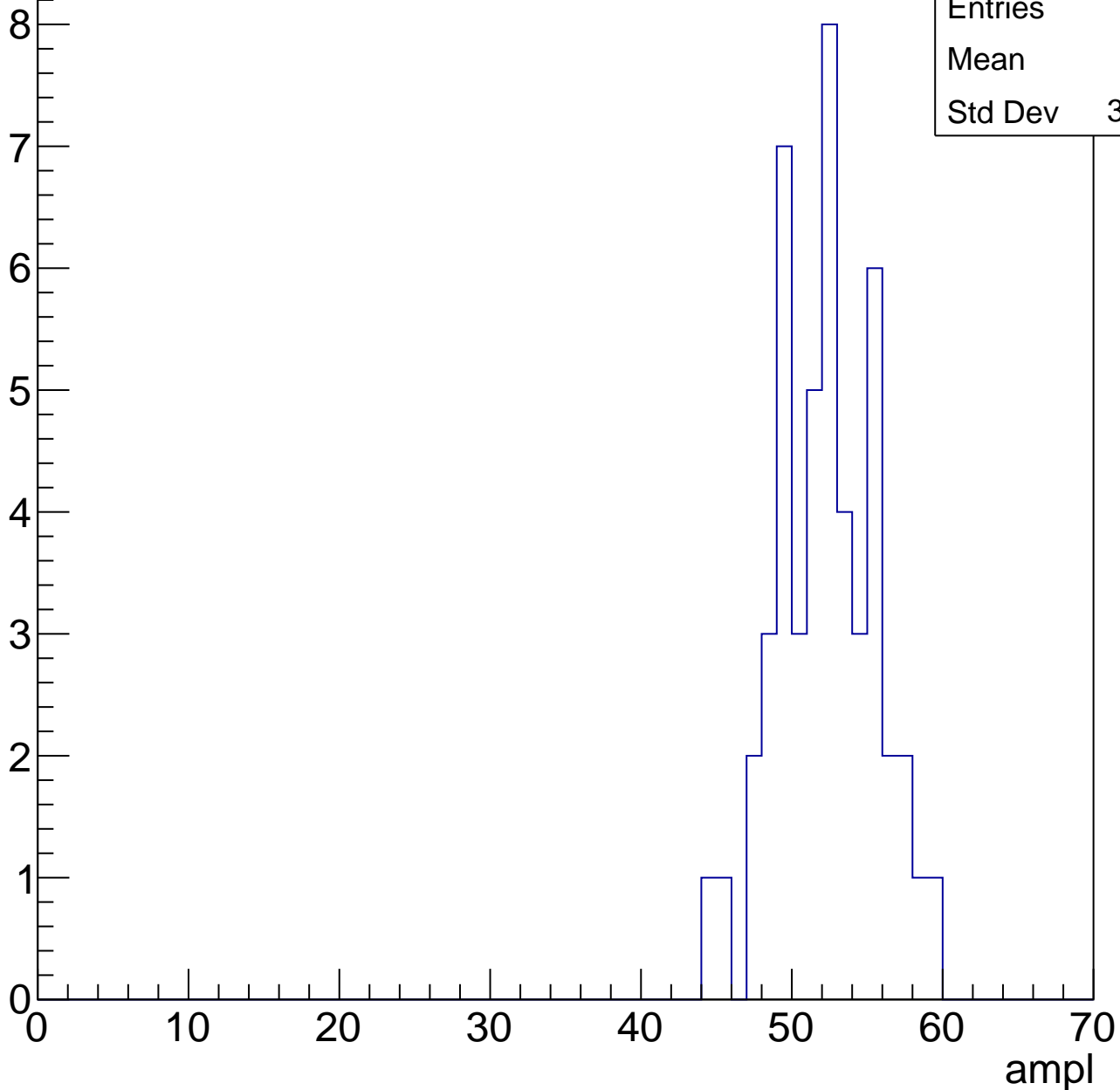


# B1L002S, U17-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	51.8
Std Dev	3.276

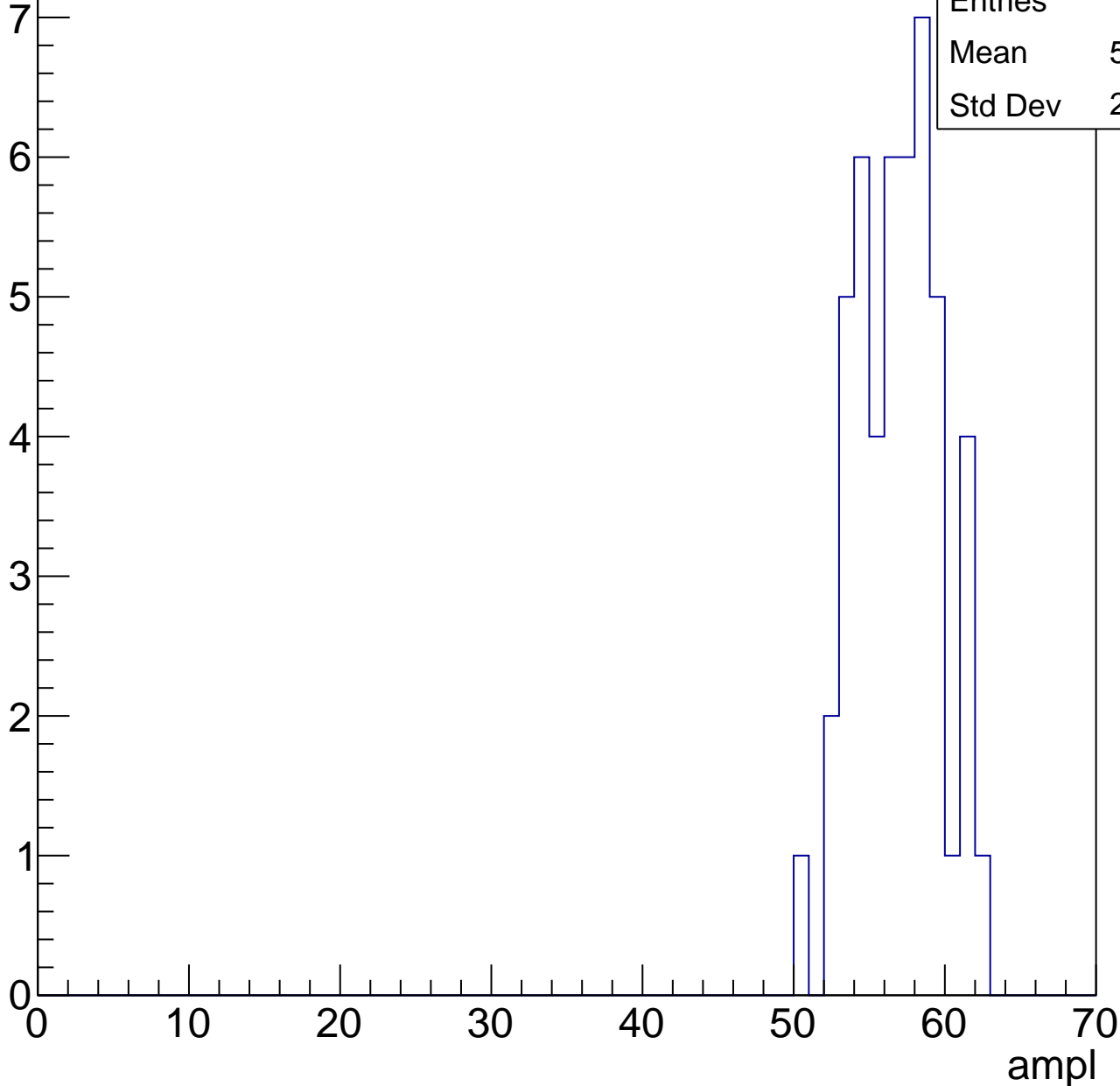


# B1L002S, U17-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

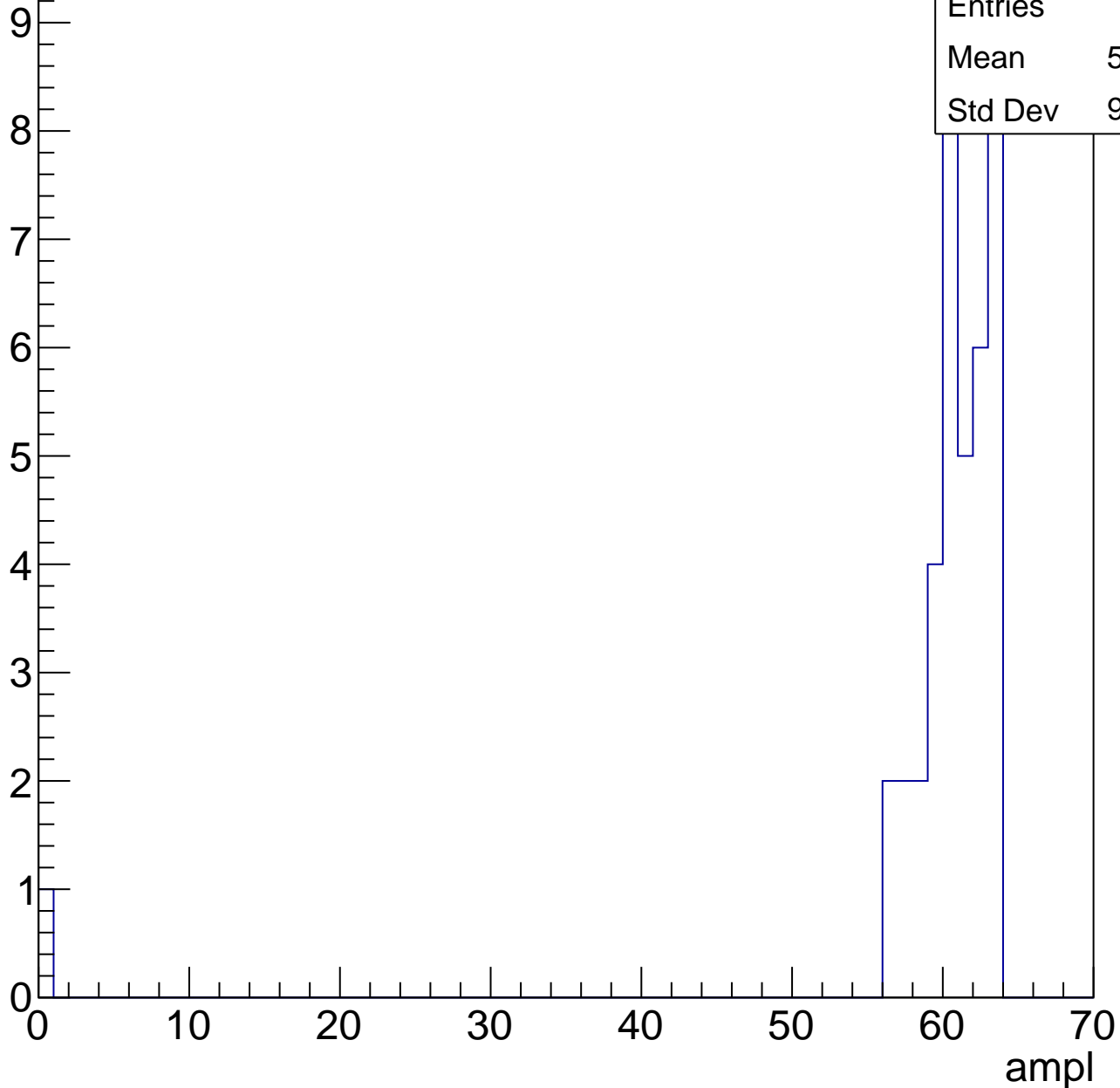
Entries	48
Mean	56.42
Std Dev	2.753



# B1L002S, U17-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

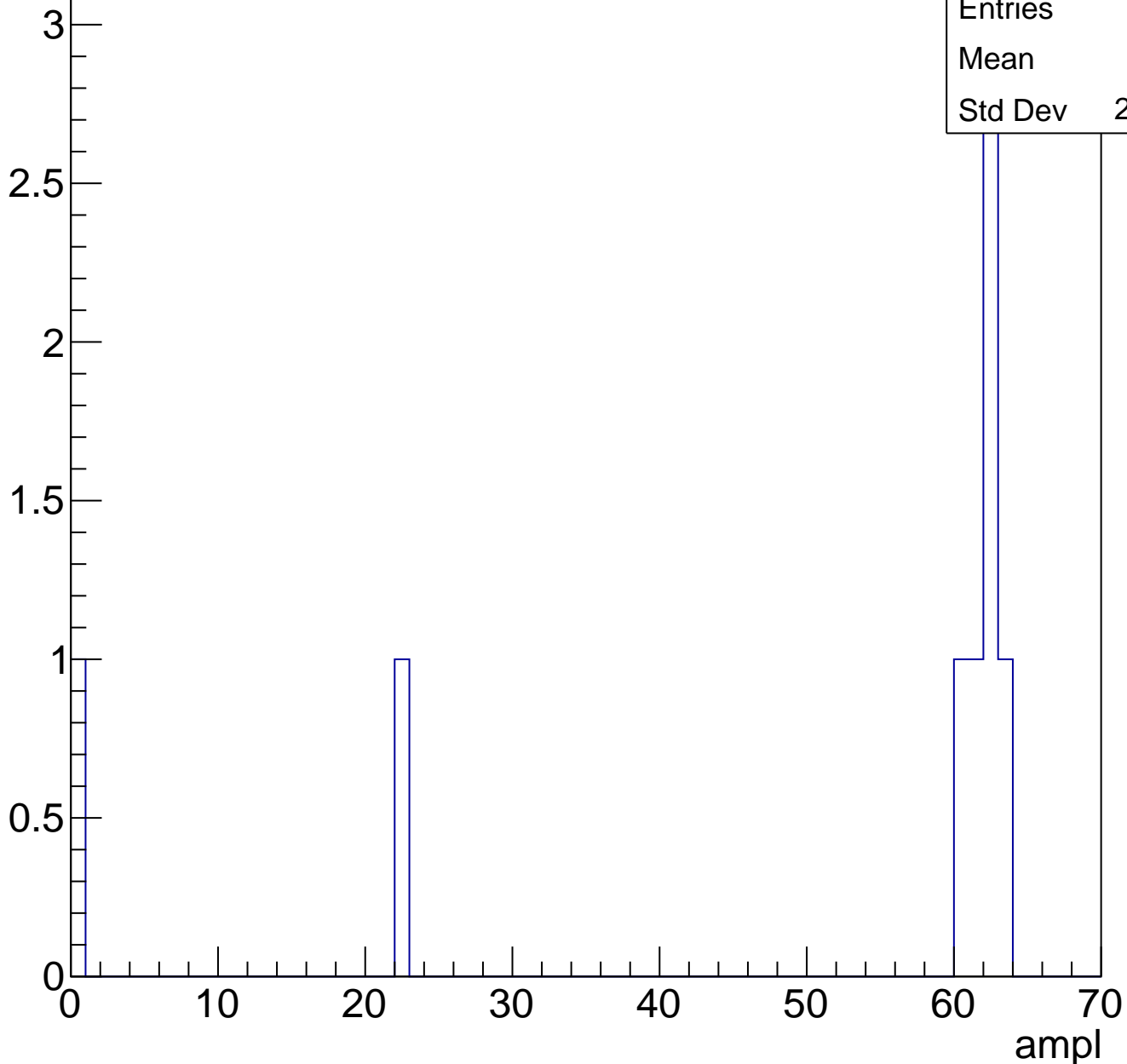




# B1L002S, U17-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch17, adc0

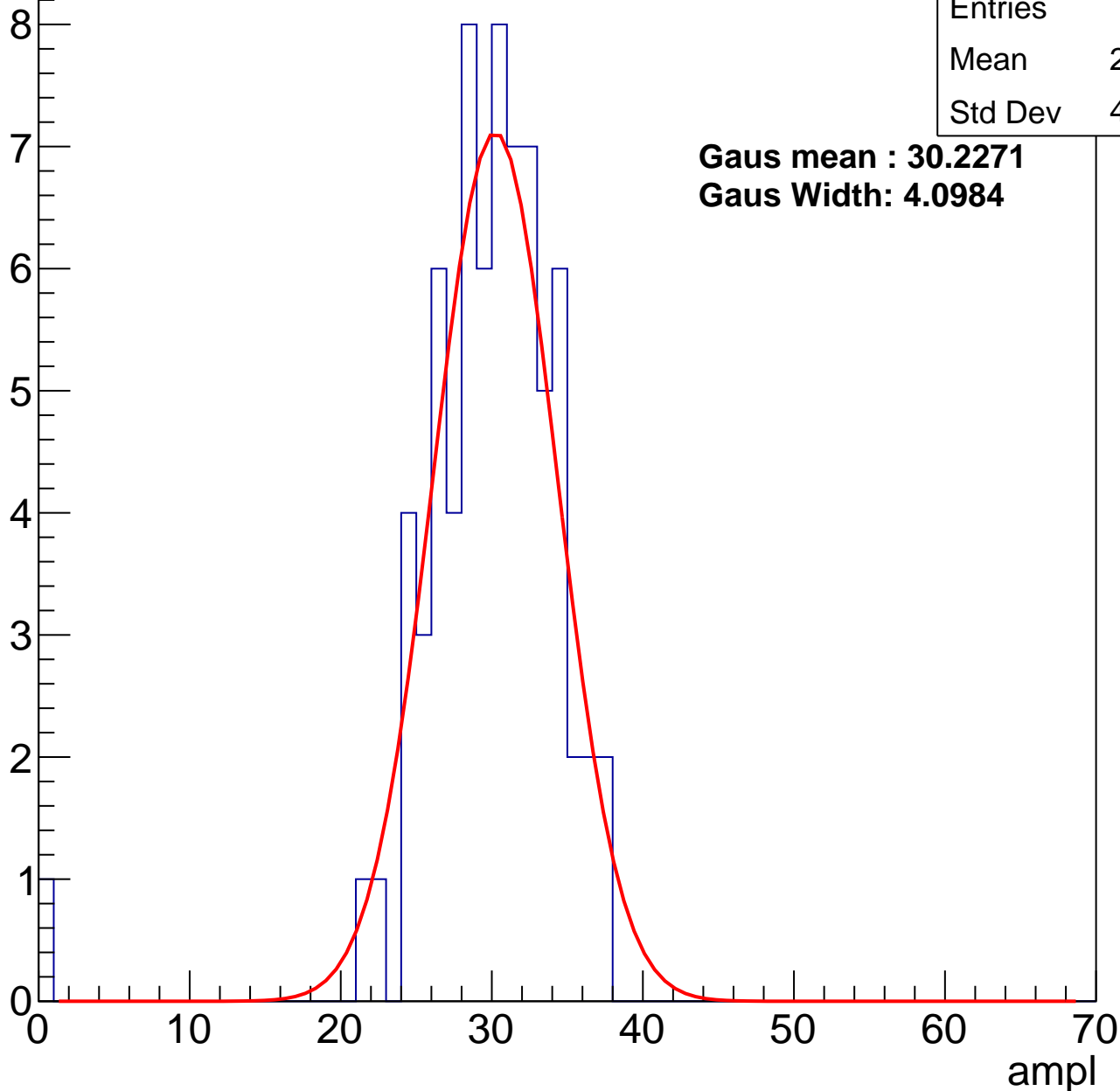
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	29.34
Std Dev	4.963

**Gaus mean : 30.2271**

**Gaus Width: 4.0984**



# B1L002S, U17-ch17, adc1

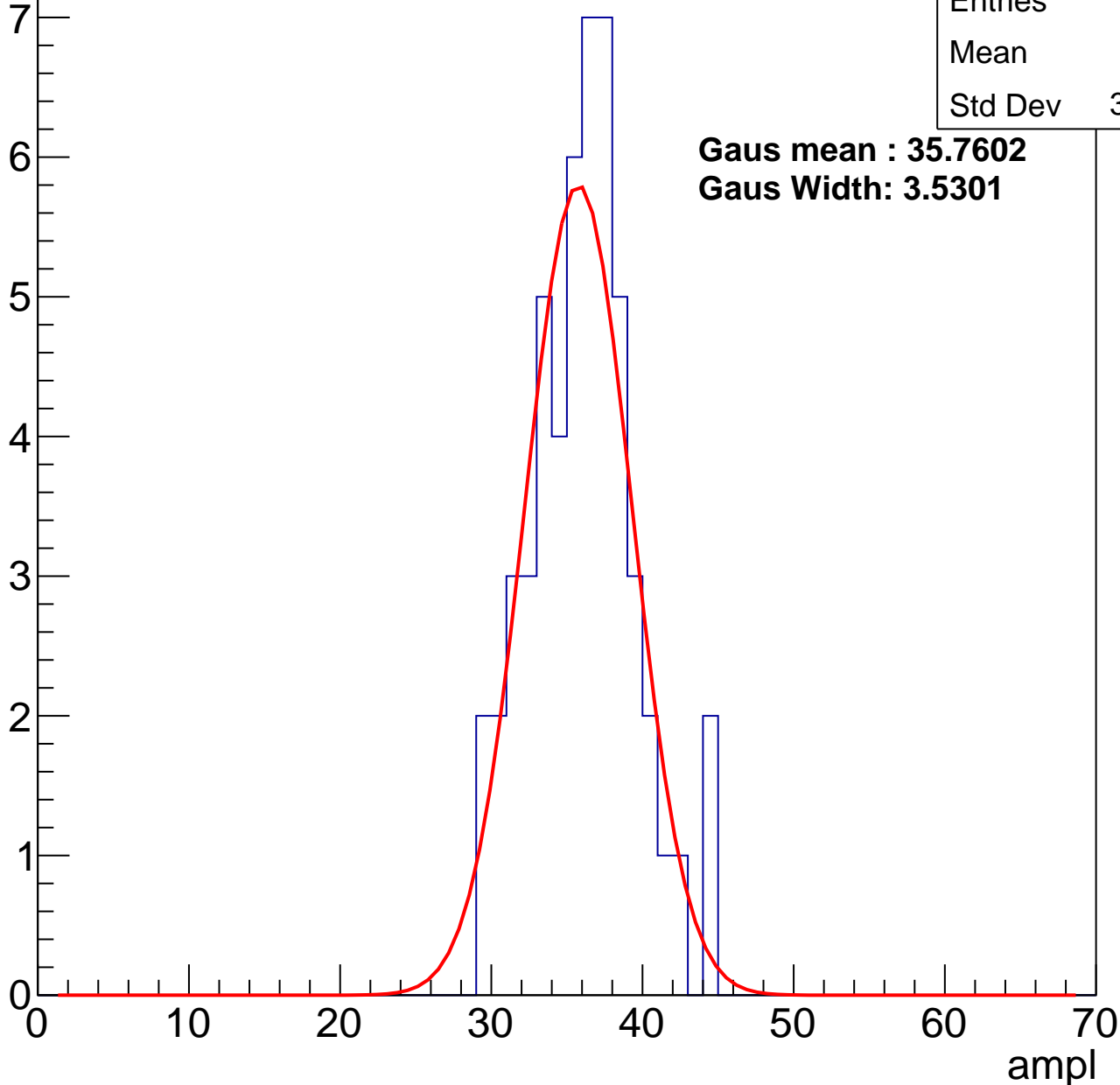
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	35.6
Std Dev	3.439

**Gaus mean : 35.7602**

**Gaus Width: 3.5301**



# B1L002S, U17-ch17, adc2

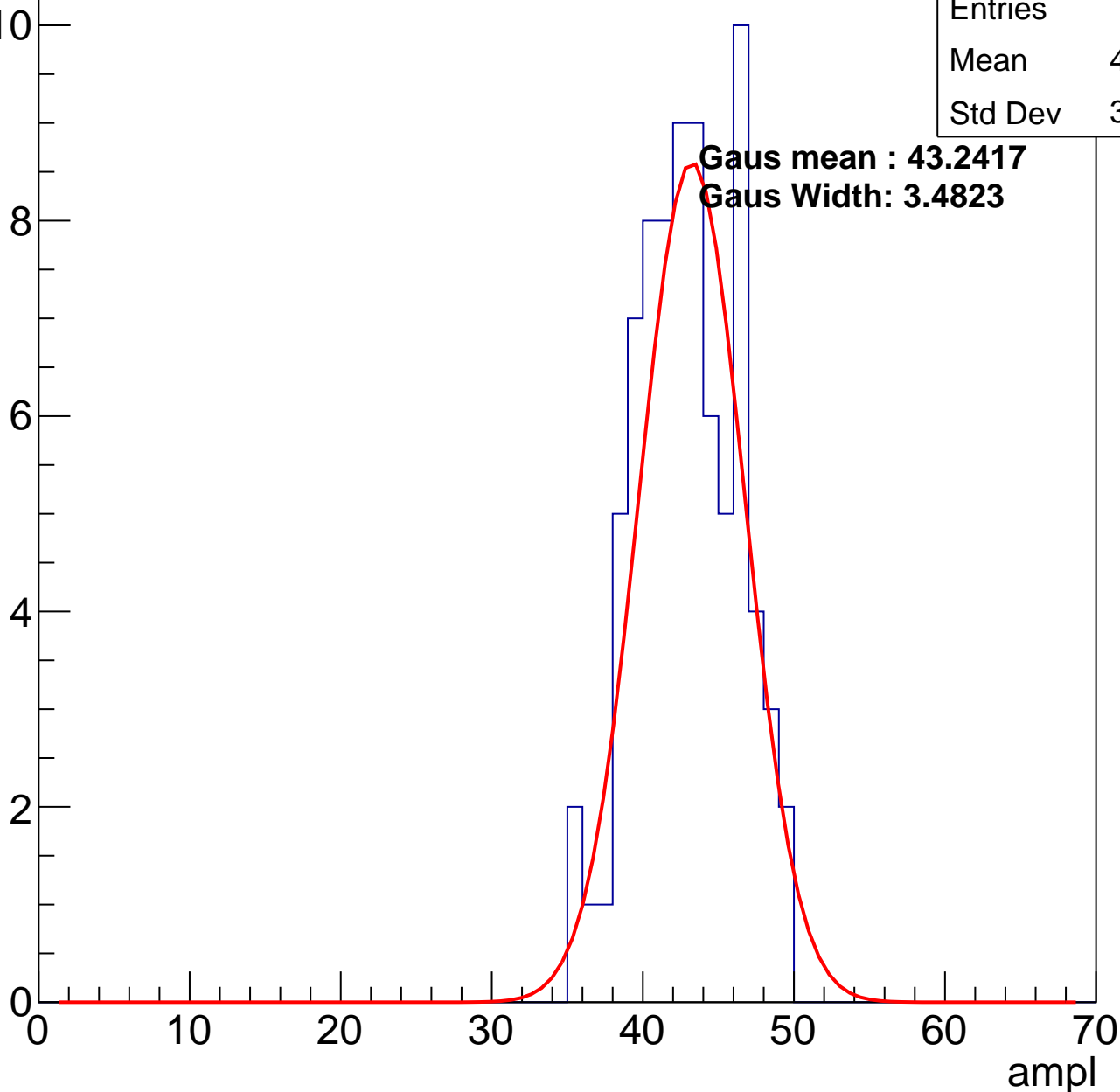
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	42.48
Std Dev	3.294

**Gaus mean : 43.2417**

**Gaus Width: 3.4823**

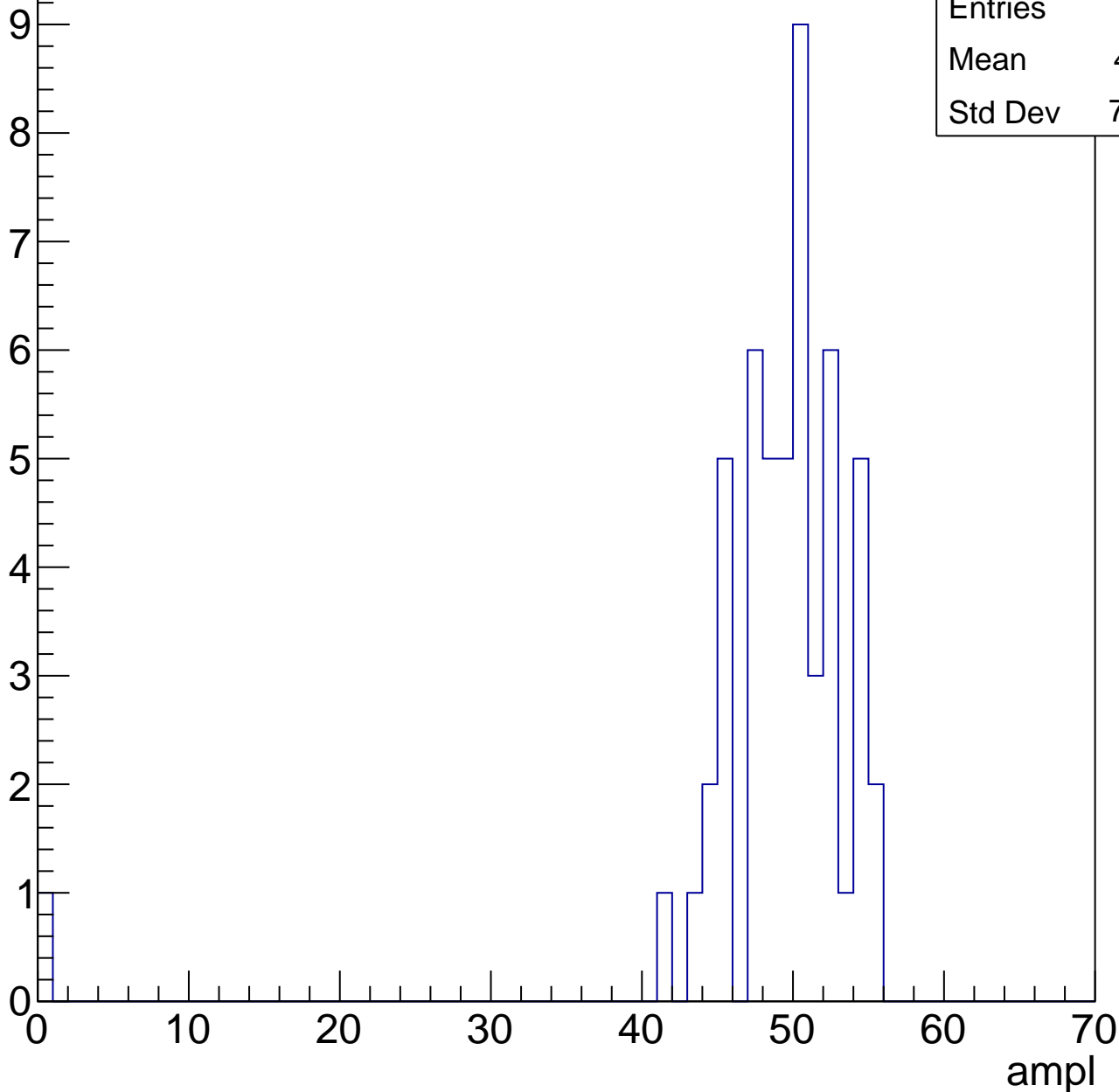


# B1L002S, U17-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	48.31
Std Dev	7.498

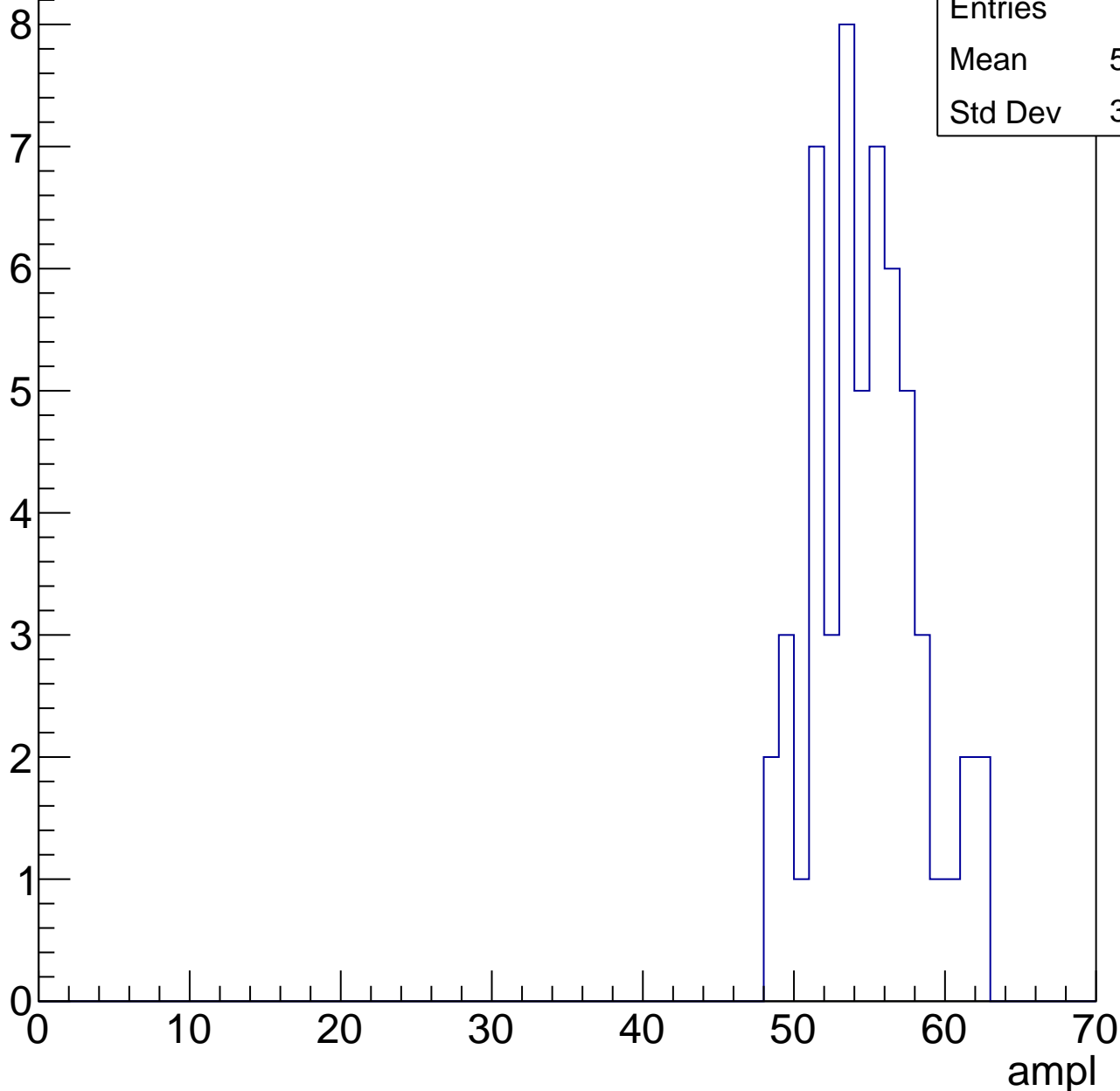


# B1L002S, U17-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	54.38
Std Dev	3.405

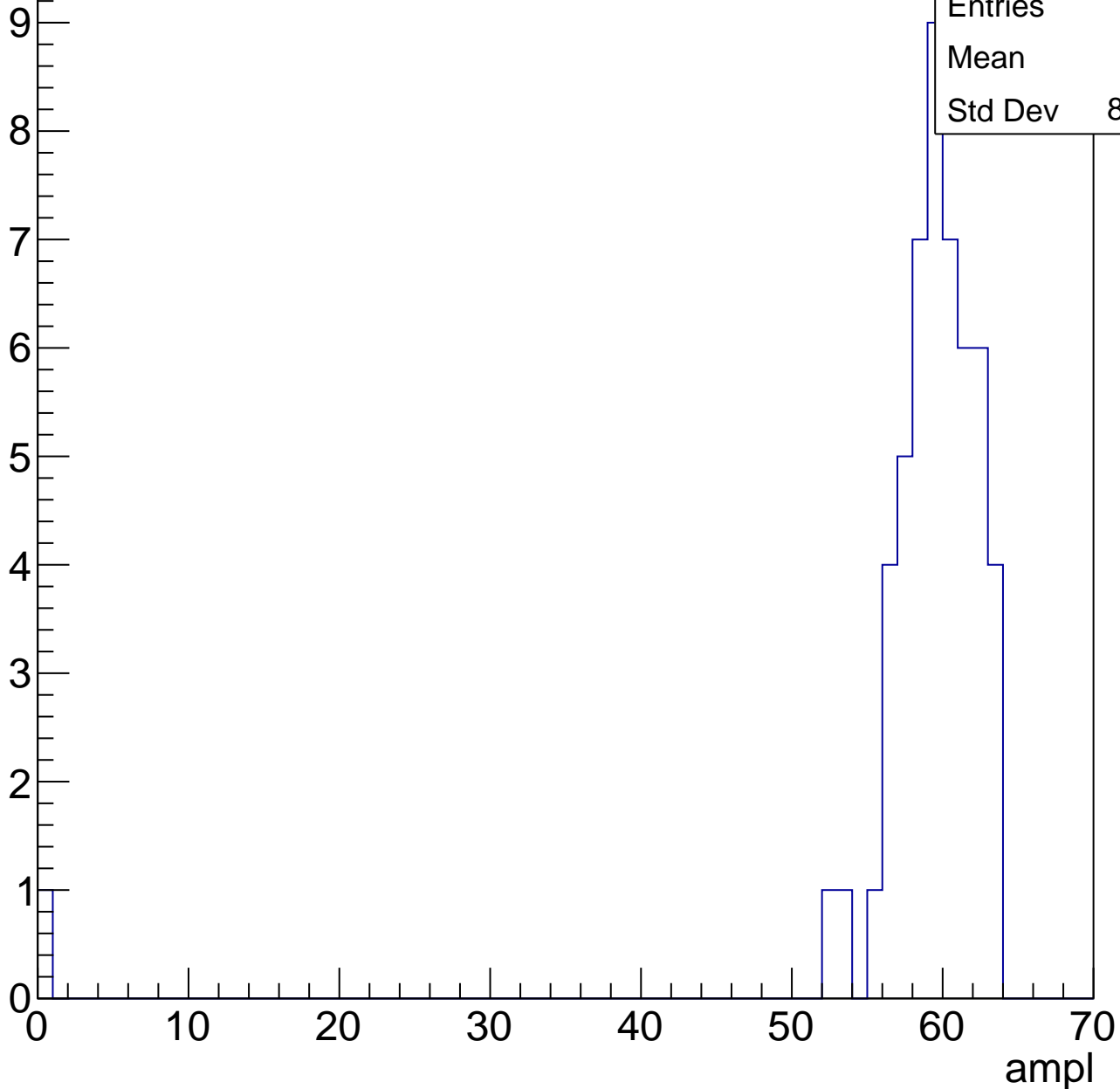


# B1L002S, U17-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58
Std Dev	8.483

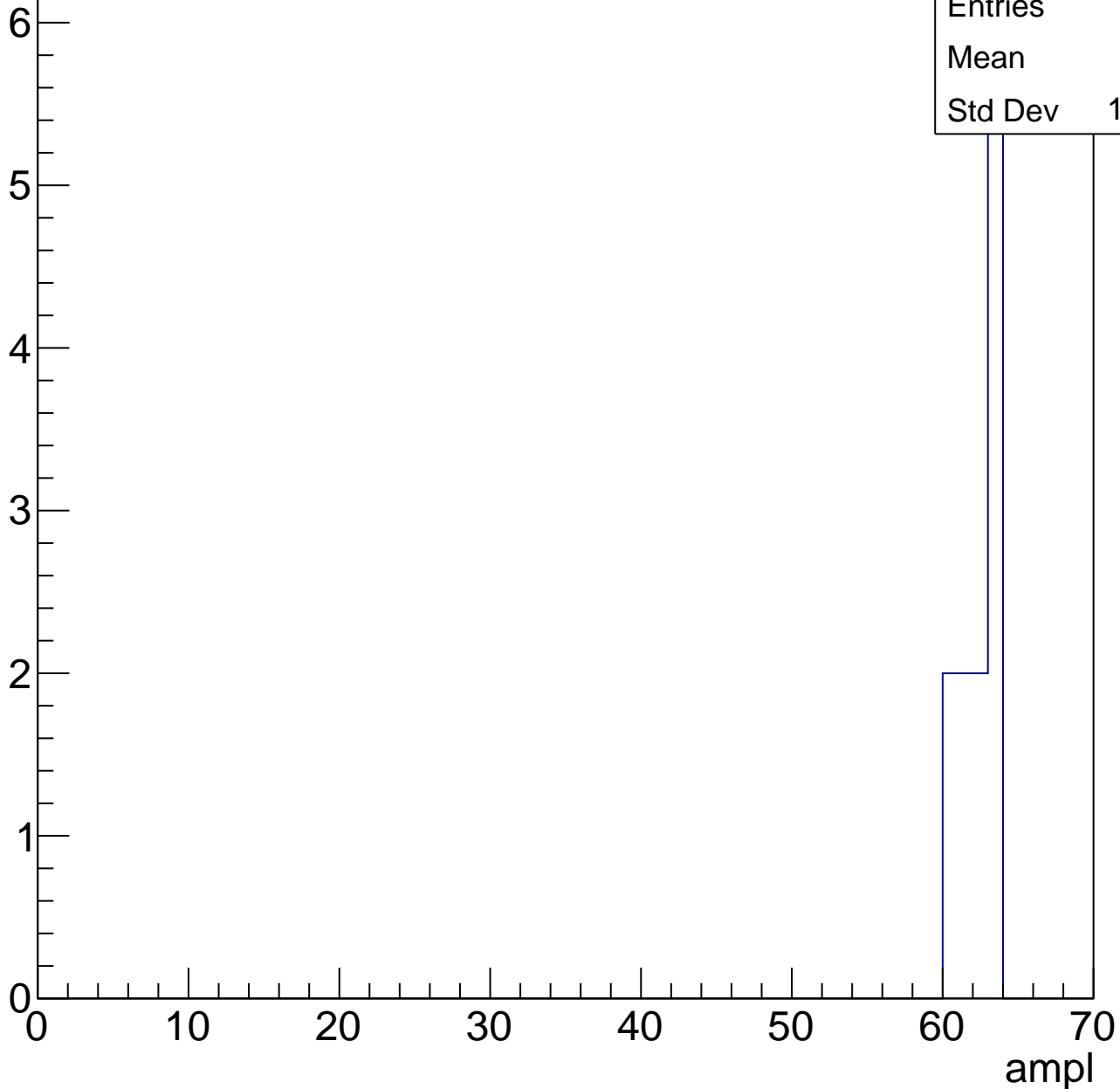


# B1L002S, U17-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	12
Mean	62
Std Dev	1.155





# B1L002S, U17-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch18, adc0

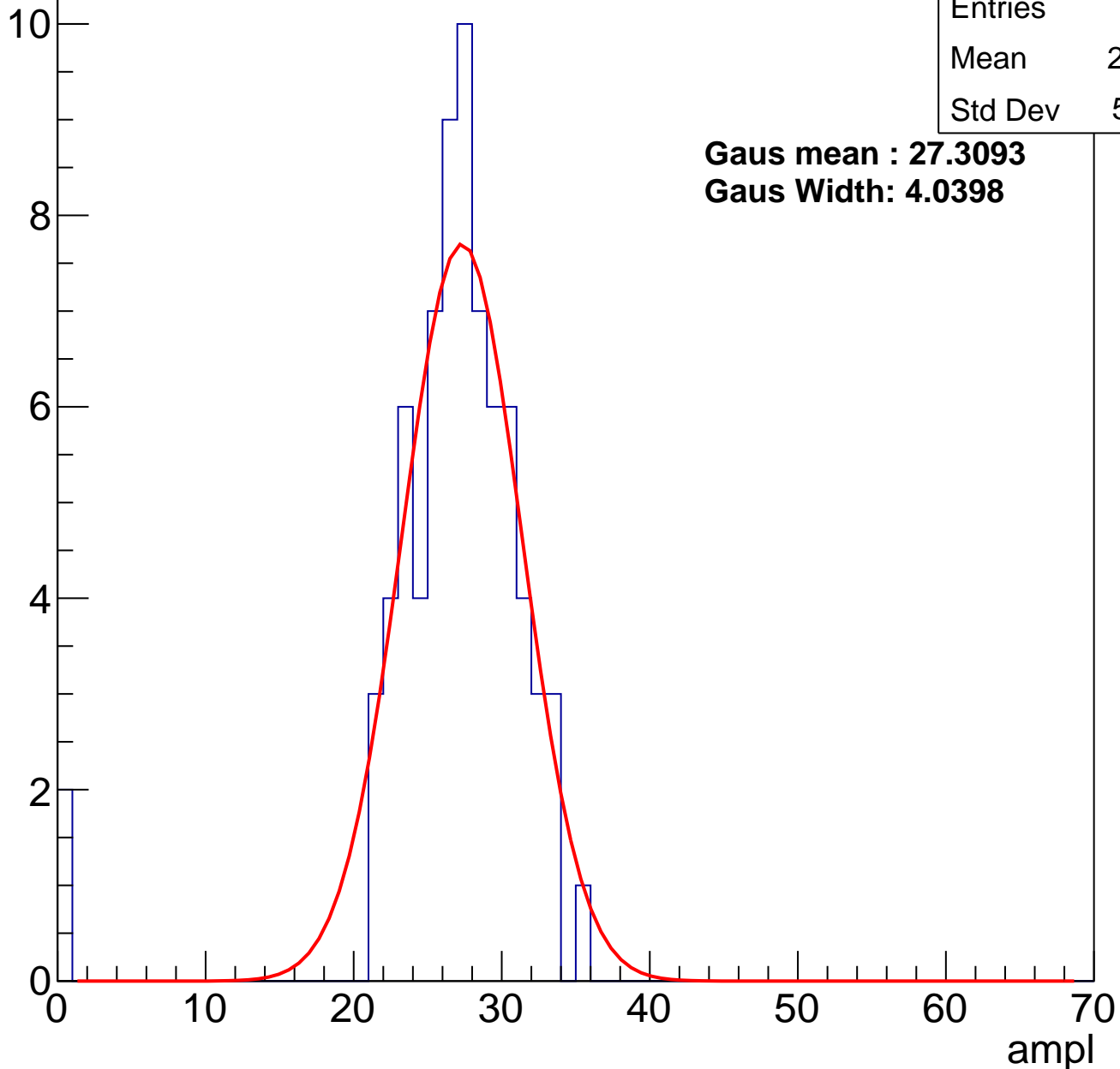
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	26.24
Std Dev	5.401

**Gaus mean : 27.3093**

**Gaus Width: 4.0398**

Entry



# B1L002S, U17-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	33.95
Std Dev	3.364

**Gaus mean : 33.9497**

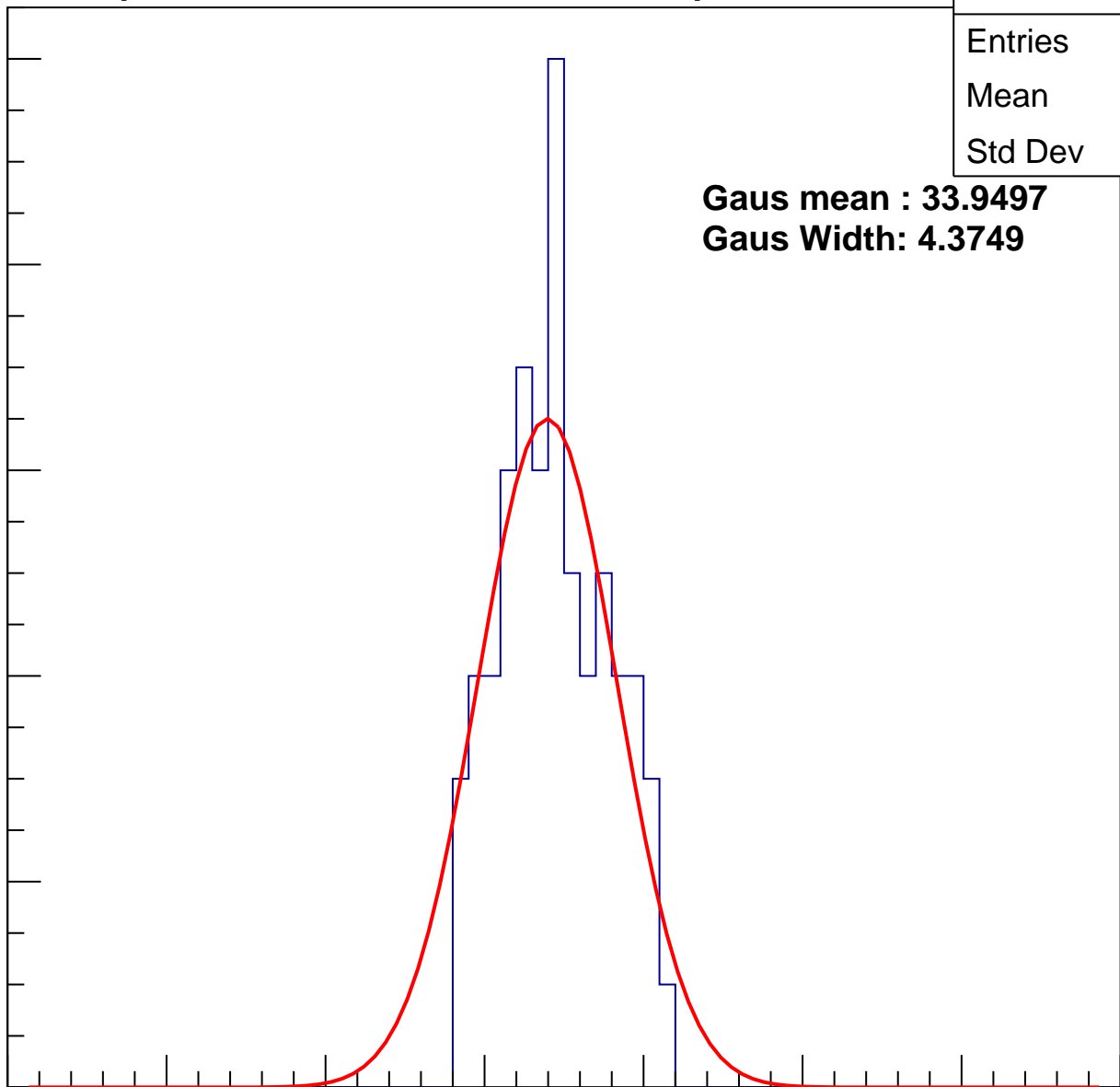
**Gaus Width: 4.3749**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch18, adc2

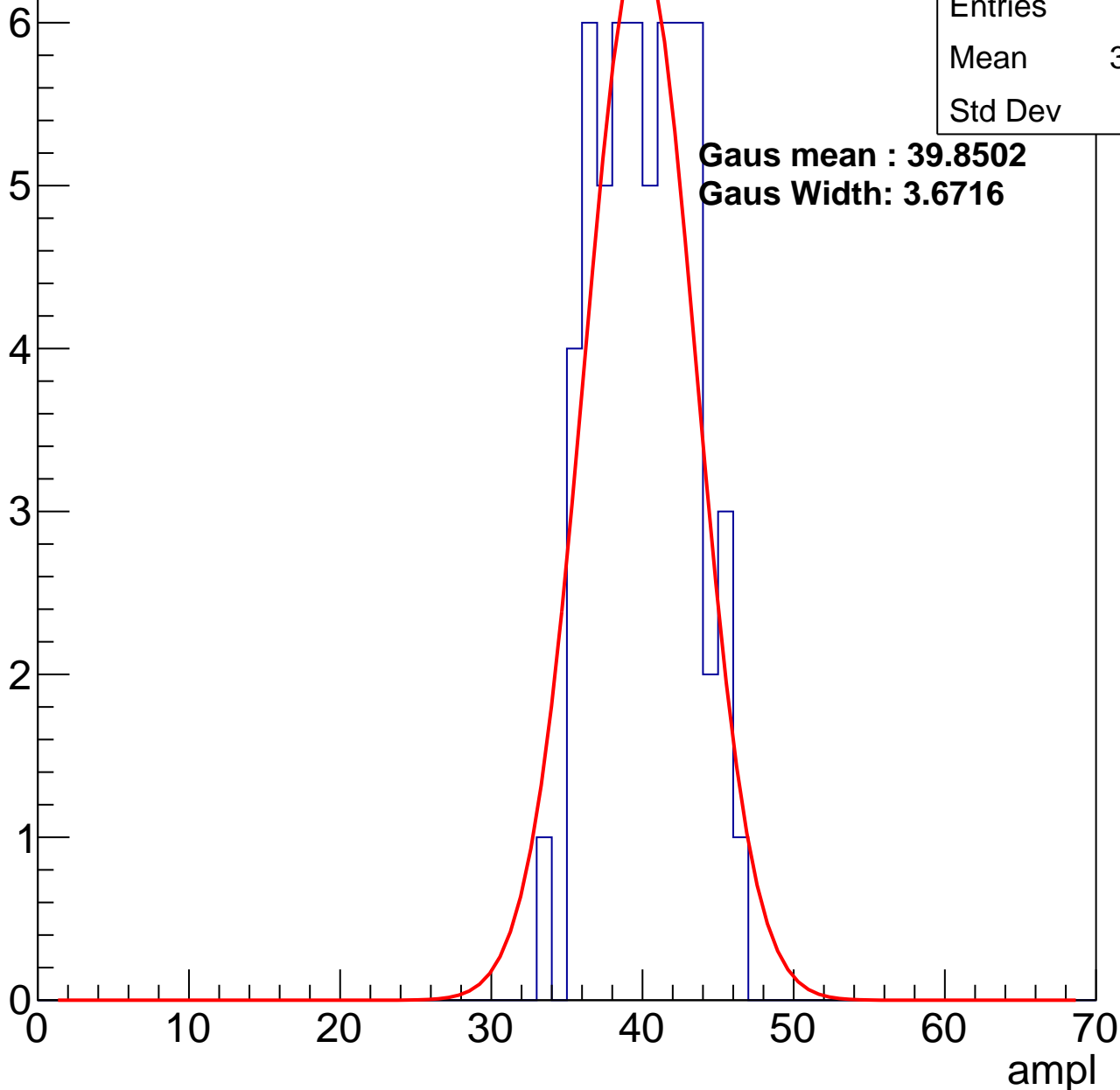
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.67
Std Dev	3.08

**Gaus mean : 39.8502**

**Gaus Width: 3.6716**

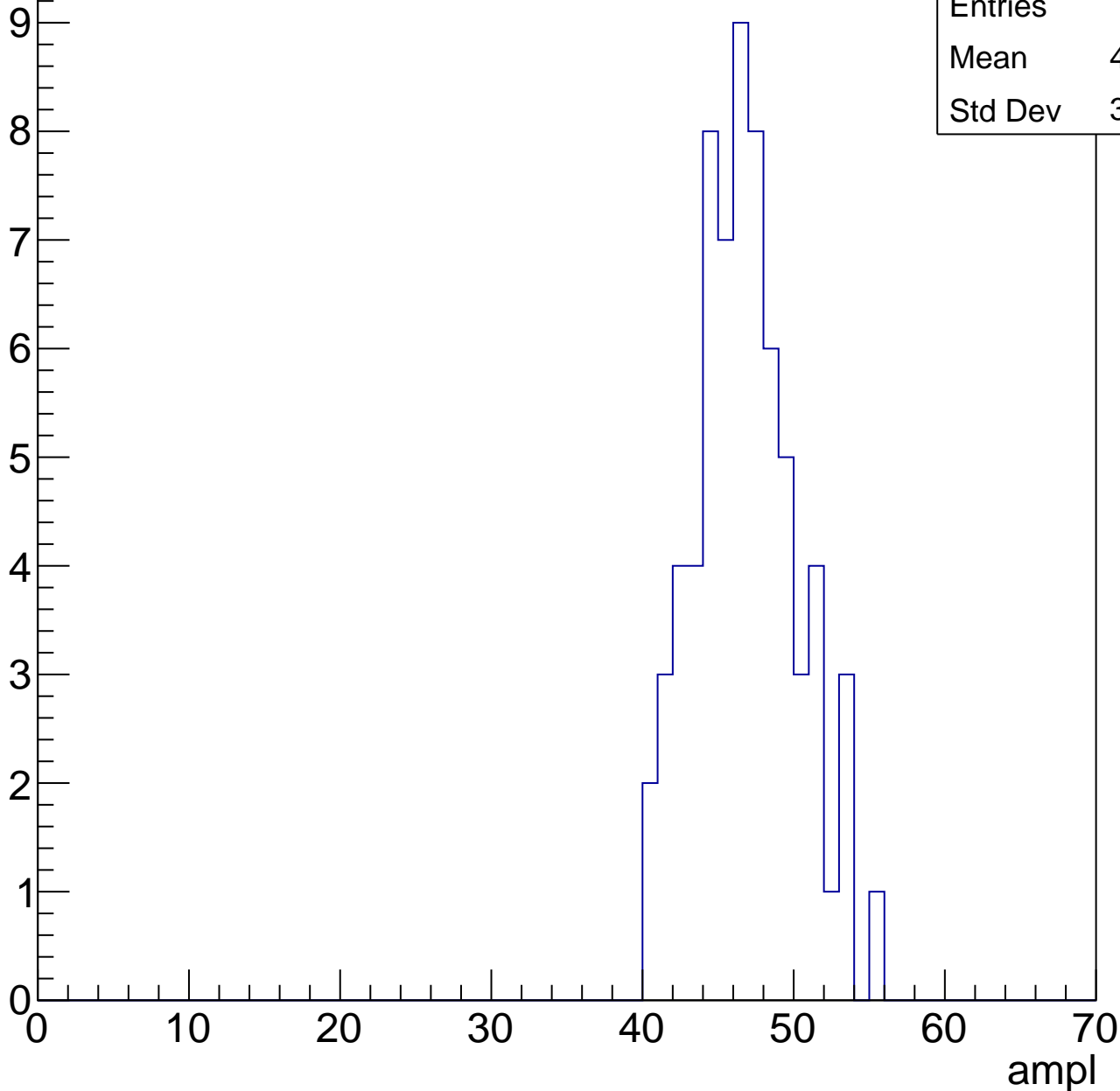


# B1L002S, U17-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	46.37
Std Dev	3.356

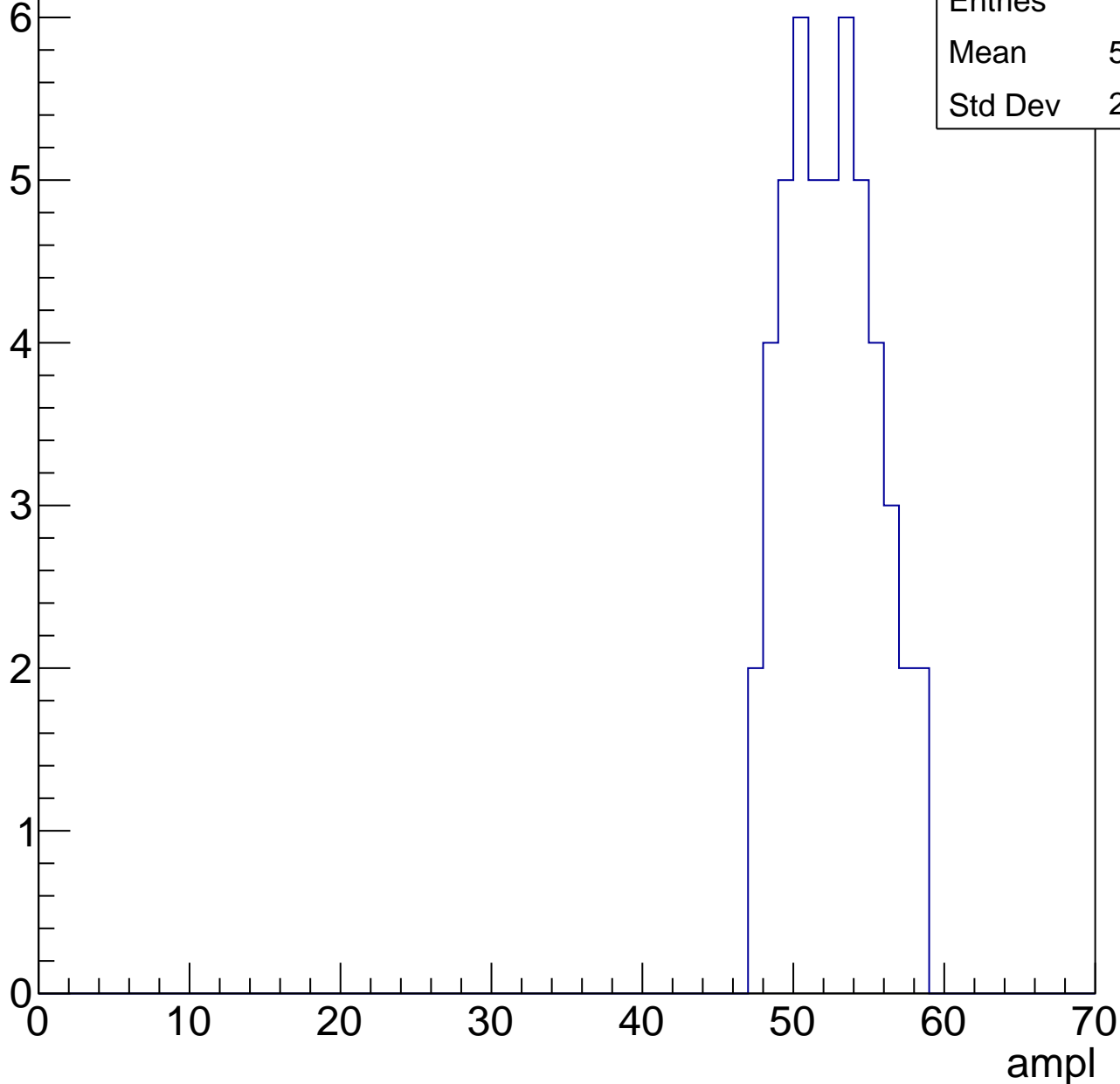


# B1L002S, U17-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

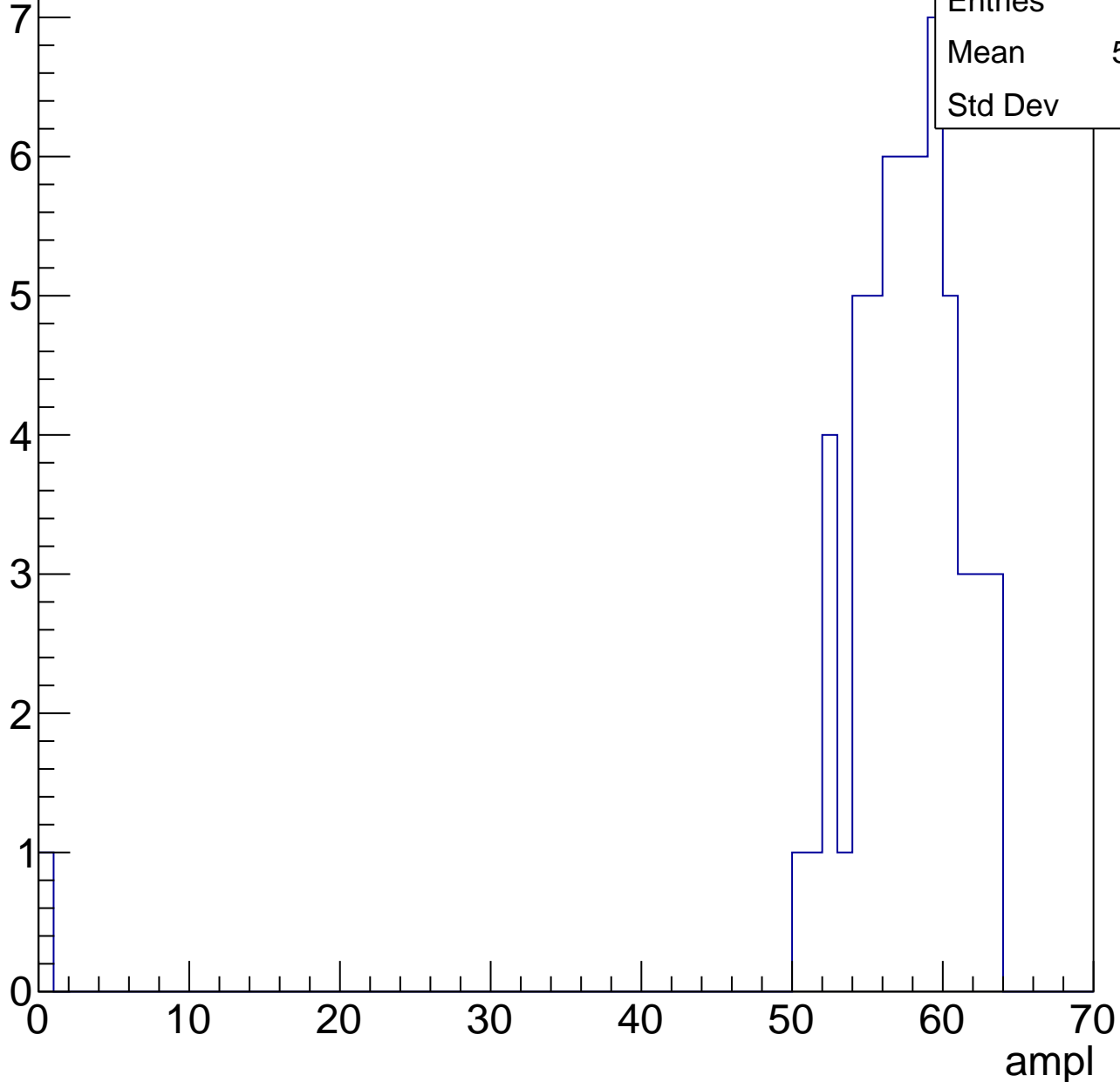
Entries	49
Mean	52.08
Std Dev	2.927



# B1L002S, U17-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

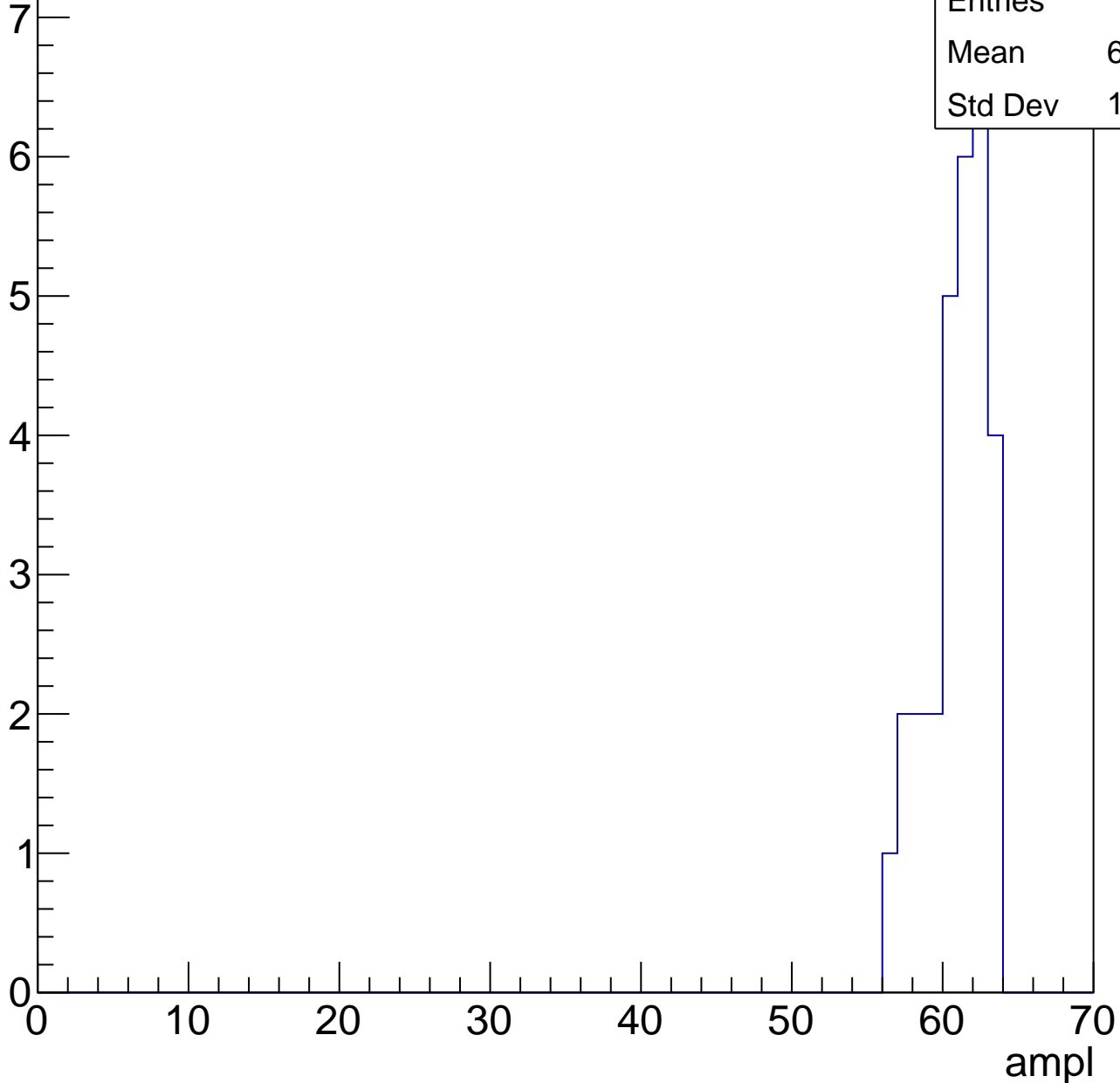


# B1L002S, U17-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	60.55
Std Dev	1.904

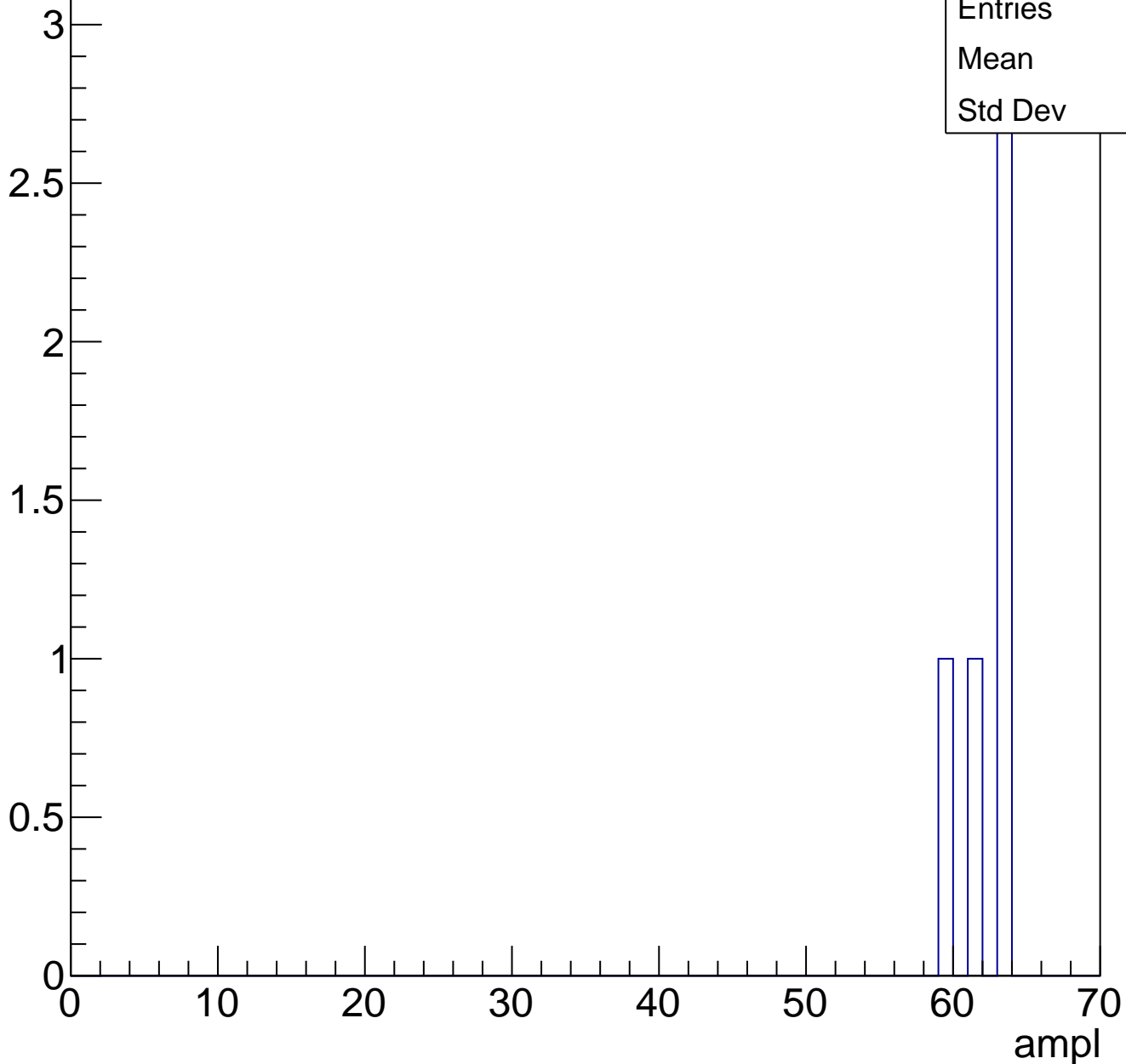




# B1L002S, U17-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	61.8
Std Dev	1.6

# B1L002S, U17-ch19, adc0

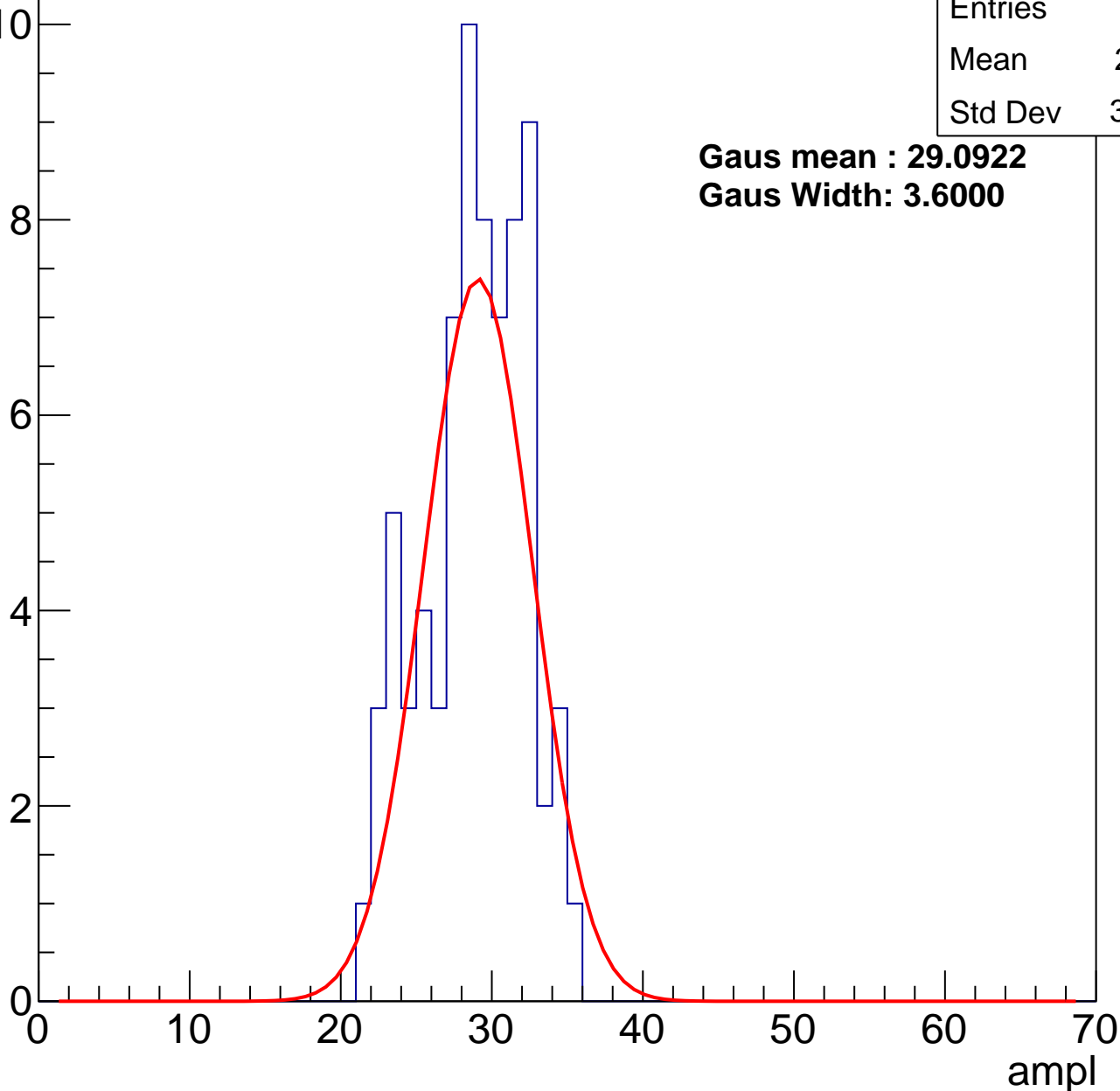
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.41
Std Dev	3.353

**Gaus mean : 29.0922**

**Gaus Width: 3.6000**



# B1L002S, U17-ch19, adc1

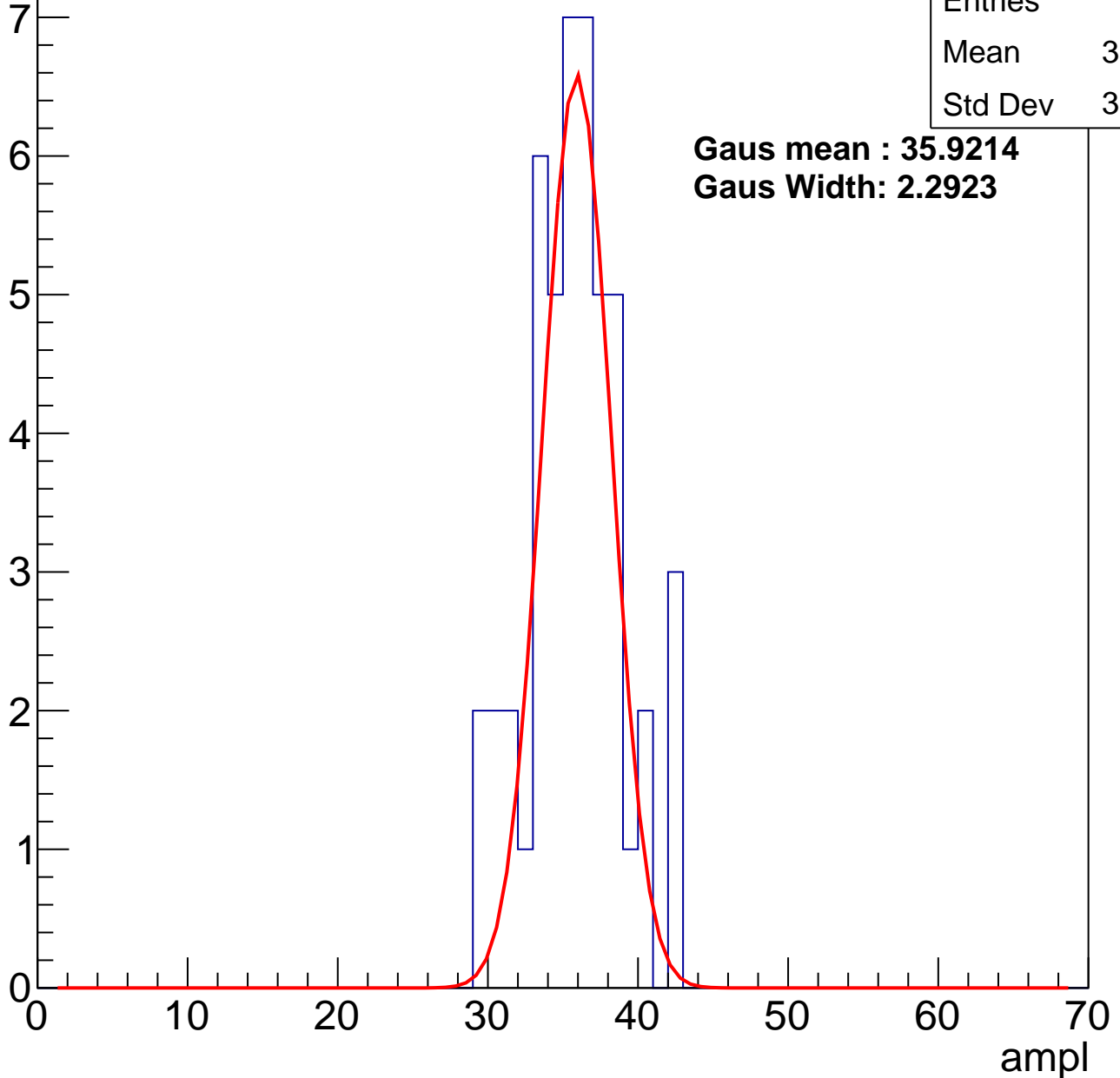
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	35.35
Std Dev	3.132

**Gaus mean : 35.9214**

**Gaus Width: 2.2923**



# B1L002S, U17-ch19, adc2

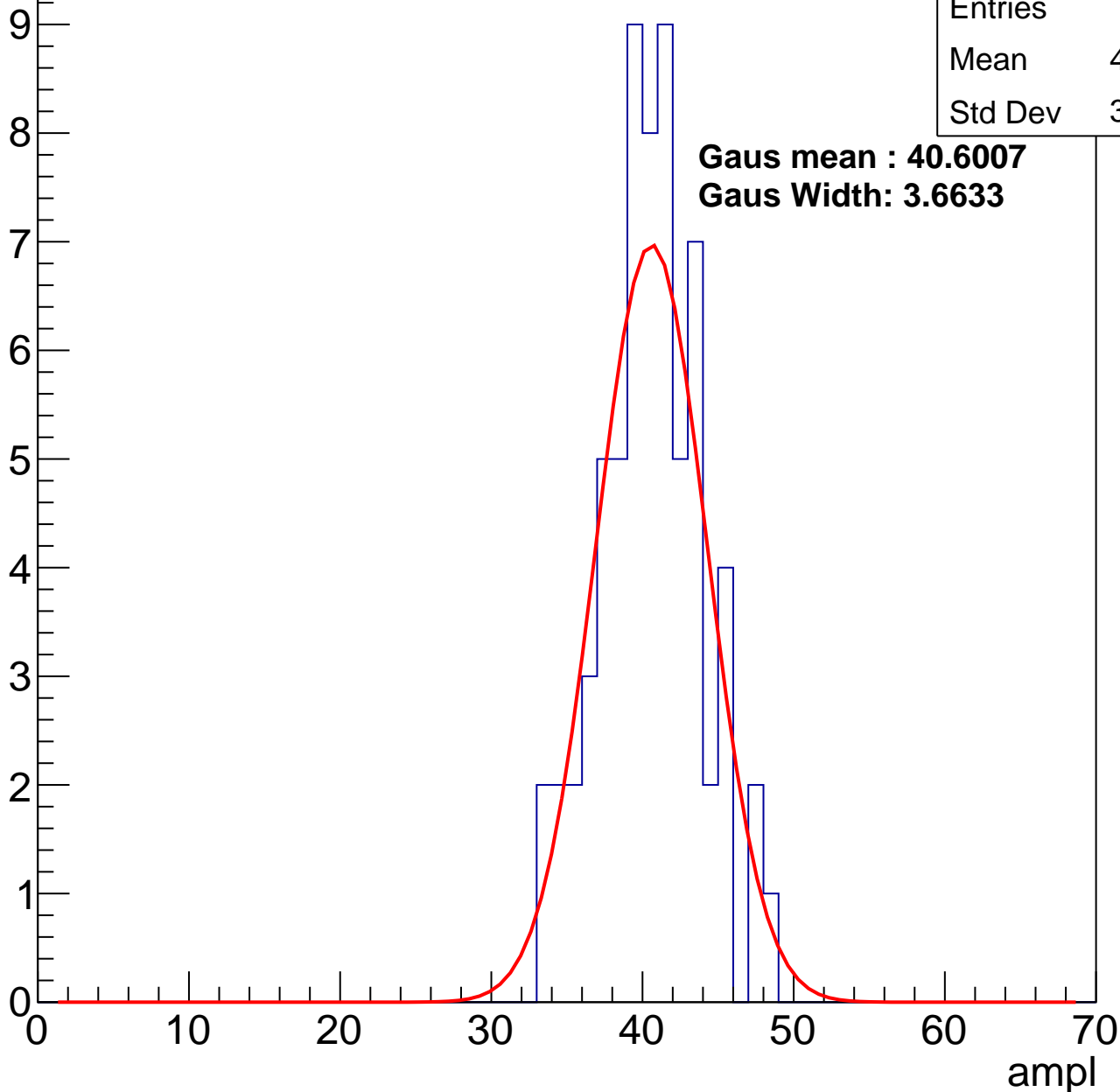
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.12
Std Dev	3.319

**Gaus mean : 40.6007**

**Gaus Width: 3.6633**

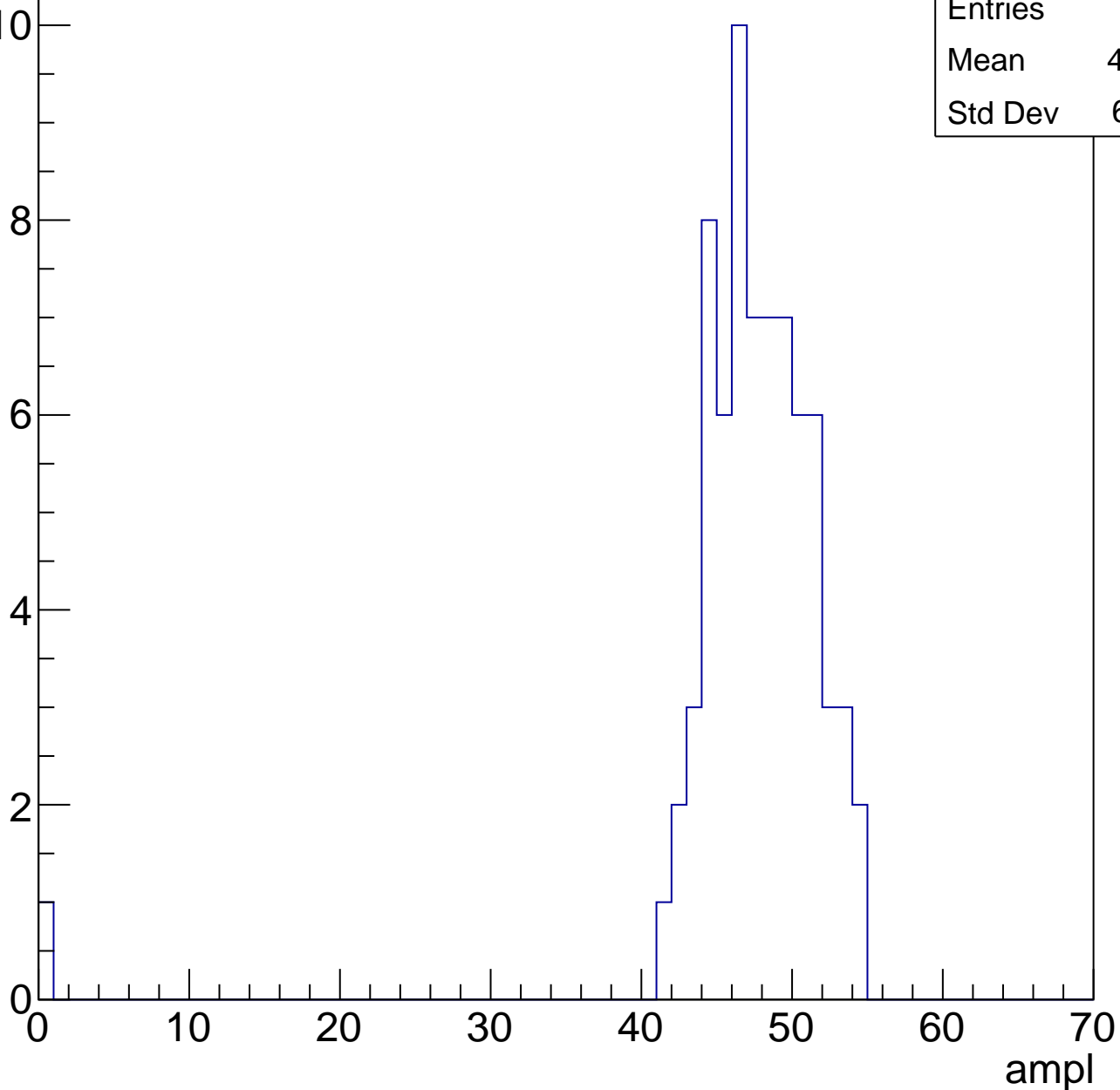


# B1L002S, U17-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	46.85
Std Dev	6.361

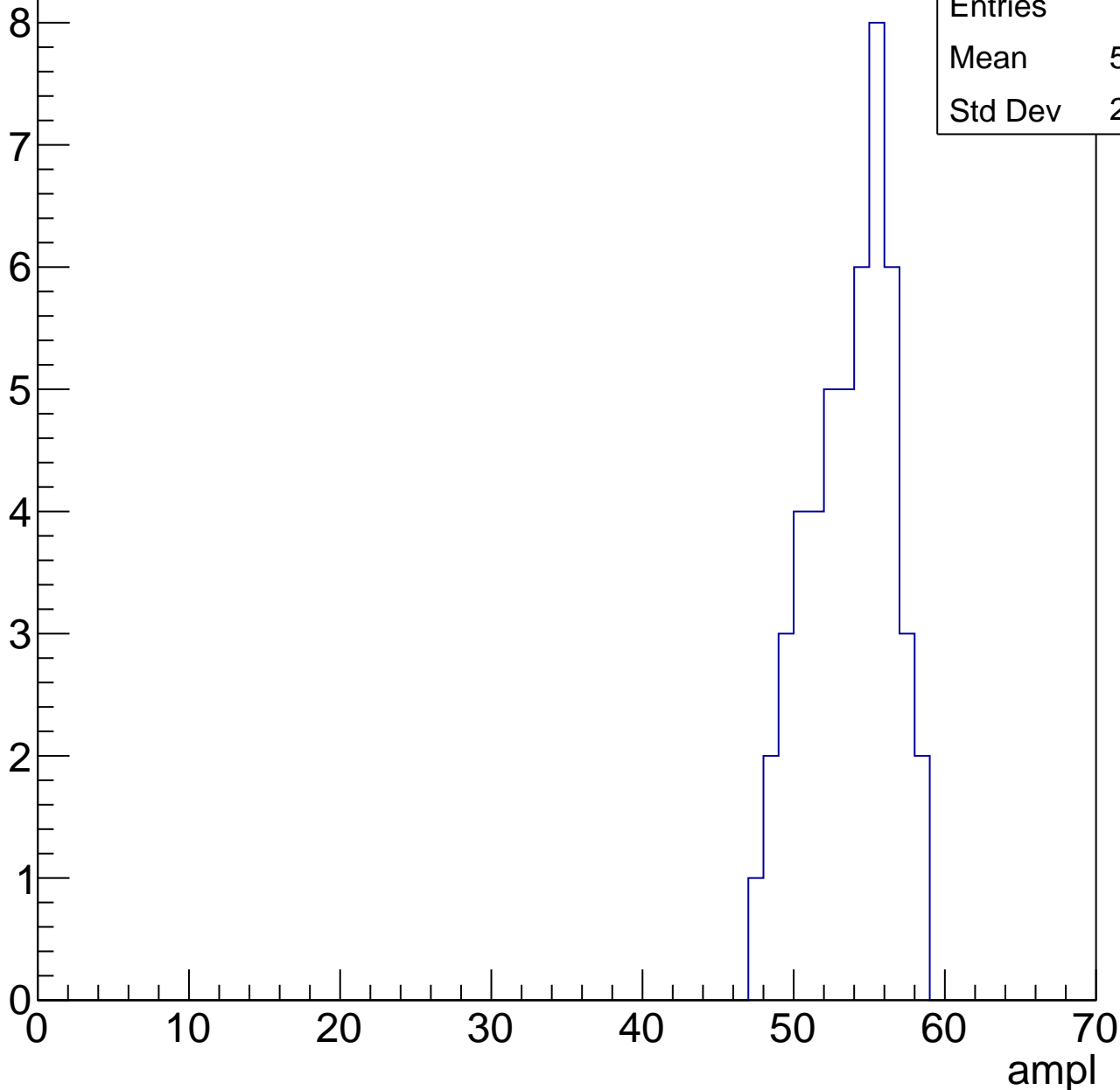


# B1L002S, U17-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

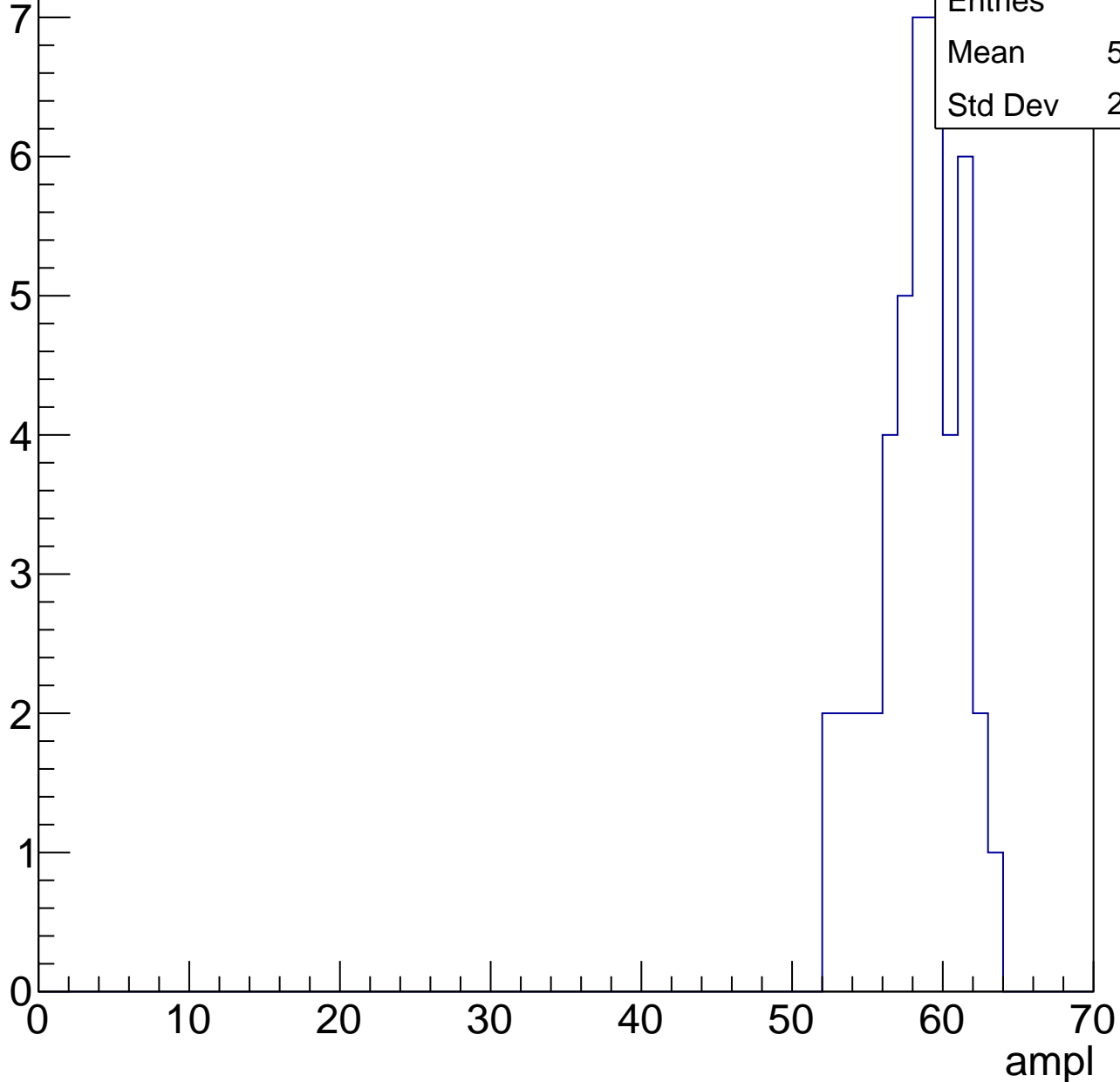
Entries	49
Mean	53.18
Std Dev	2.782



# B1L002S, U17-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

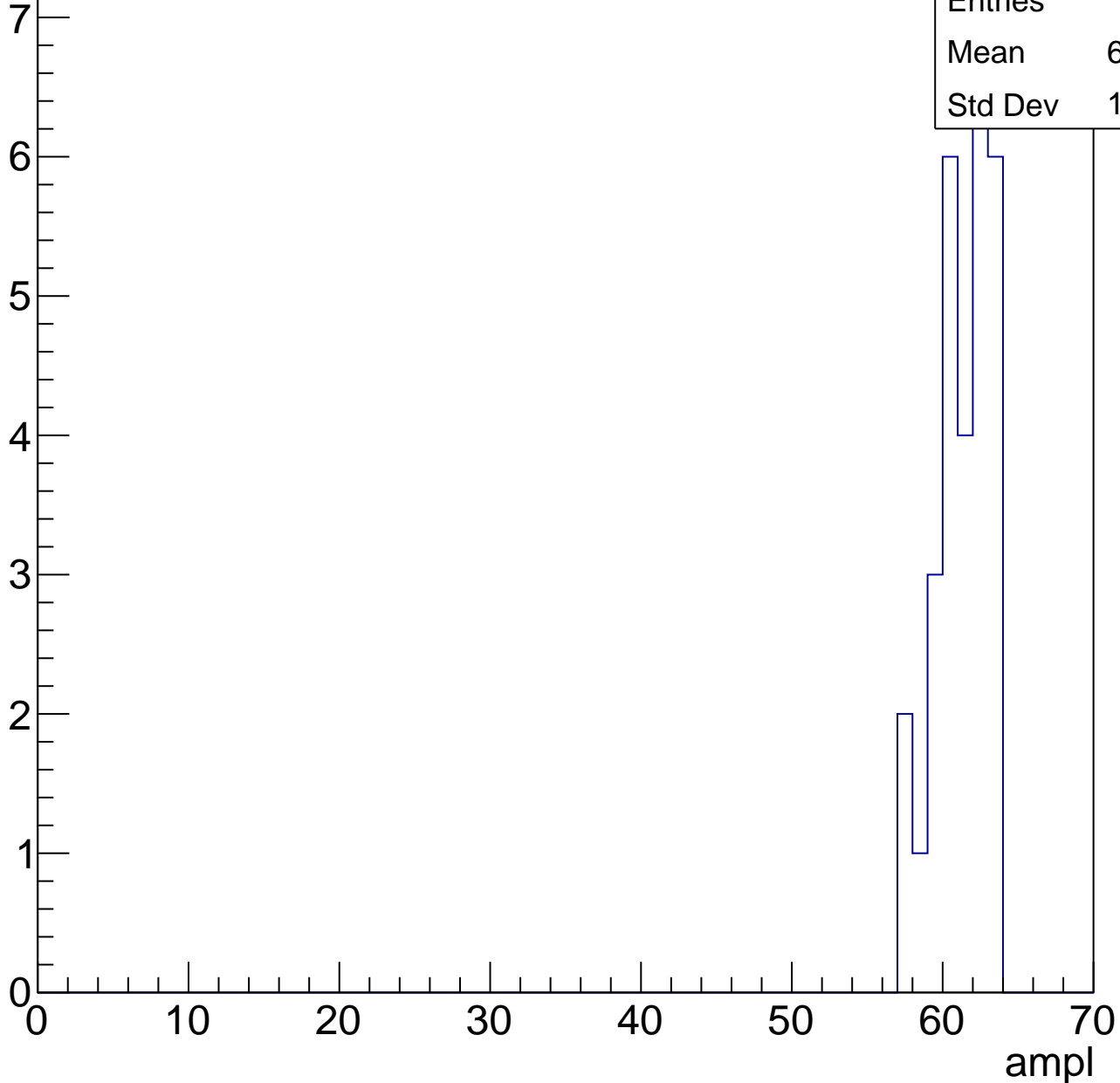


# B1L002S, U17-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	60.86
Std Dev	1.756

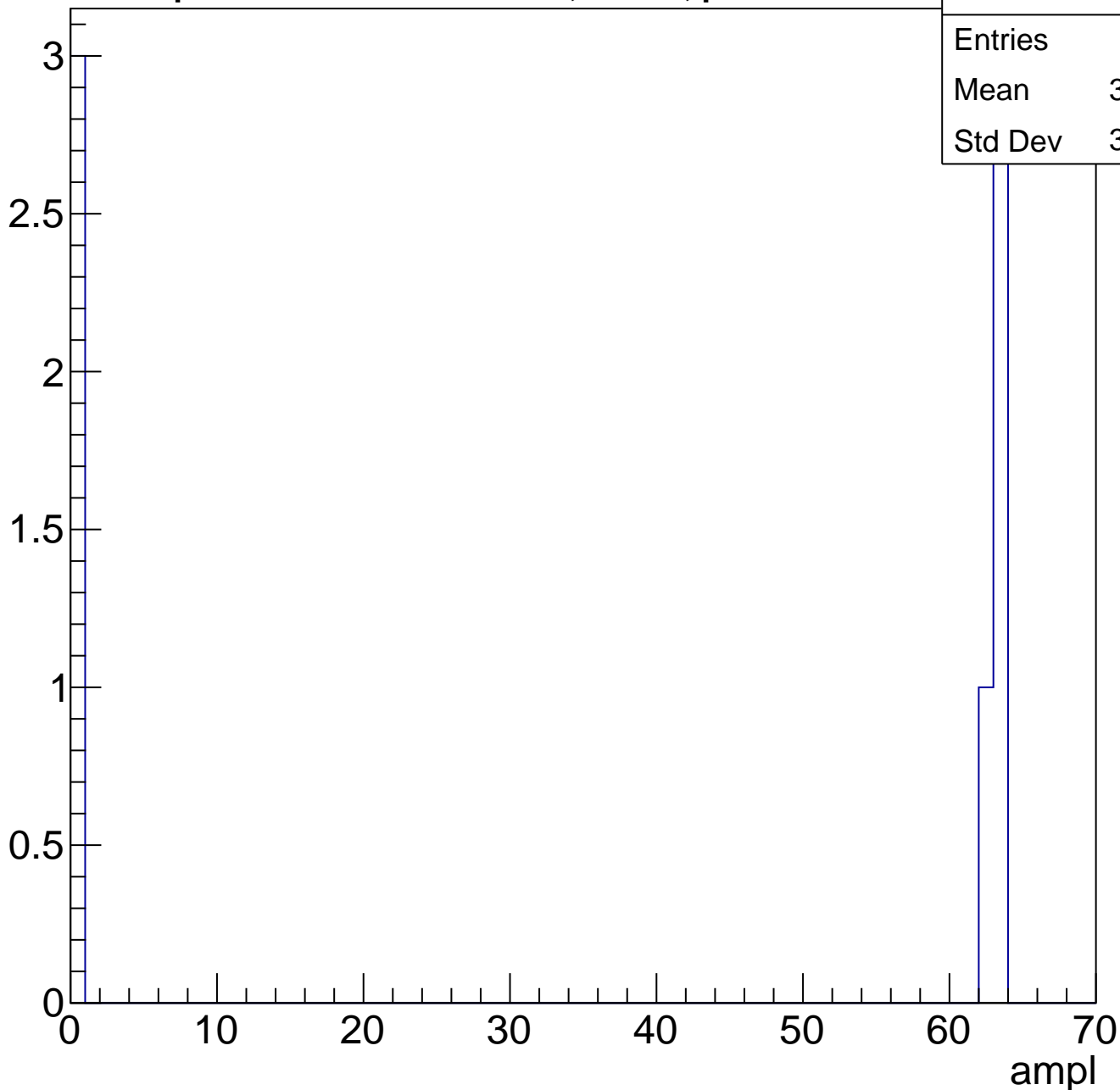




# B1L002S, U17-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch20, adc0

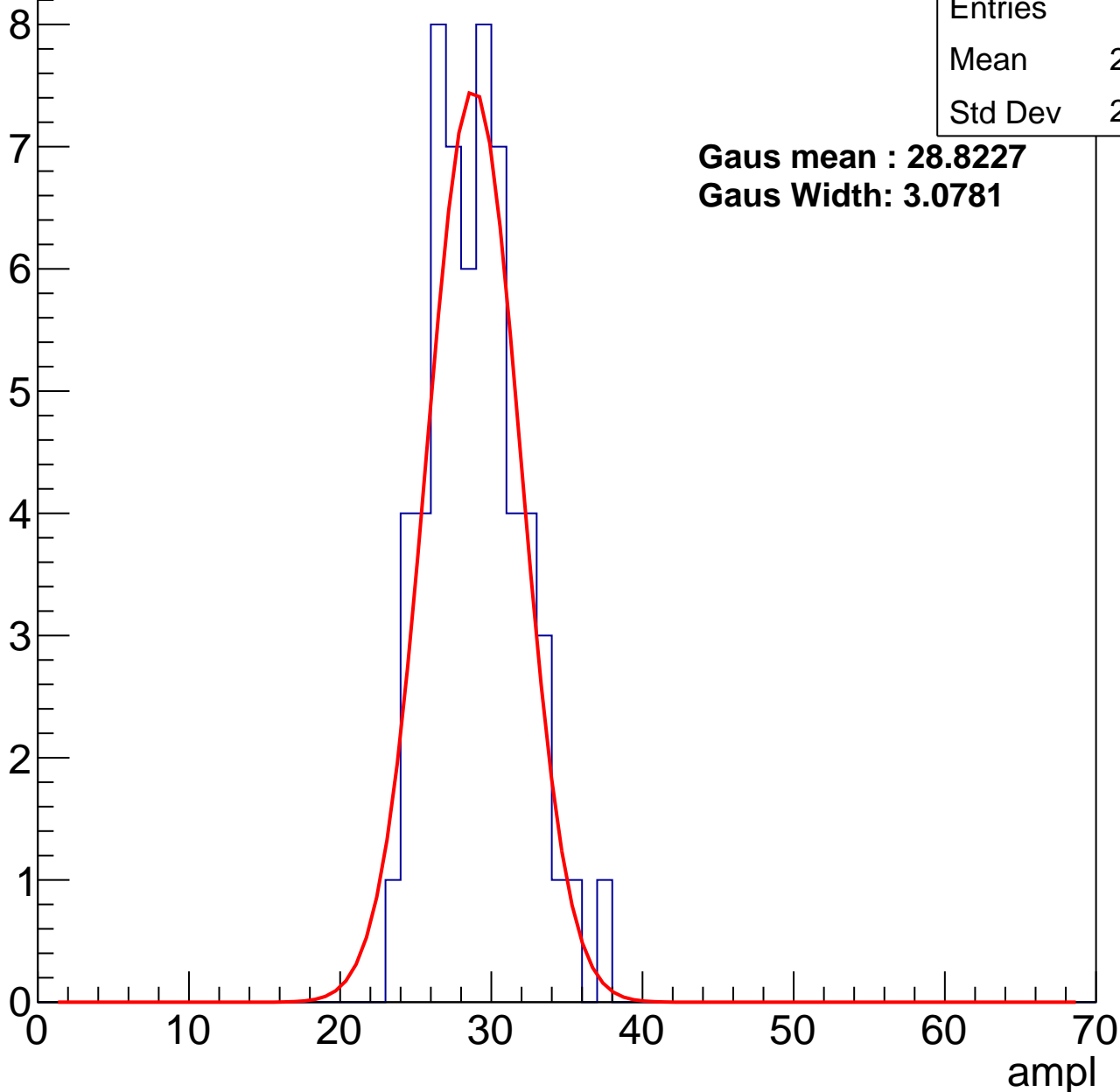
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.53
Std Dev	2.982

**Gaus mean : 28.8227**

**Gaus Width: 3.0781**



# B1L002S, U17-ch20, adc1

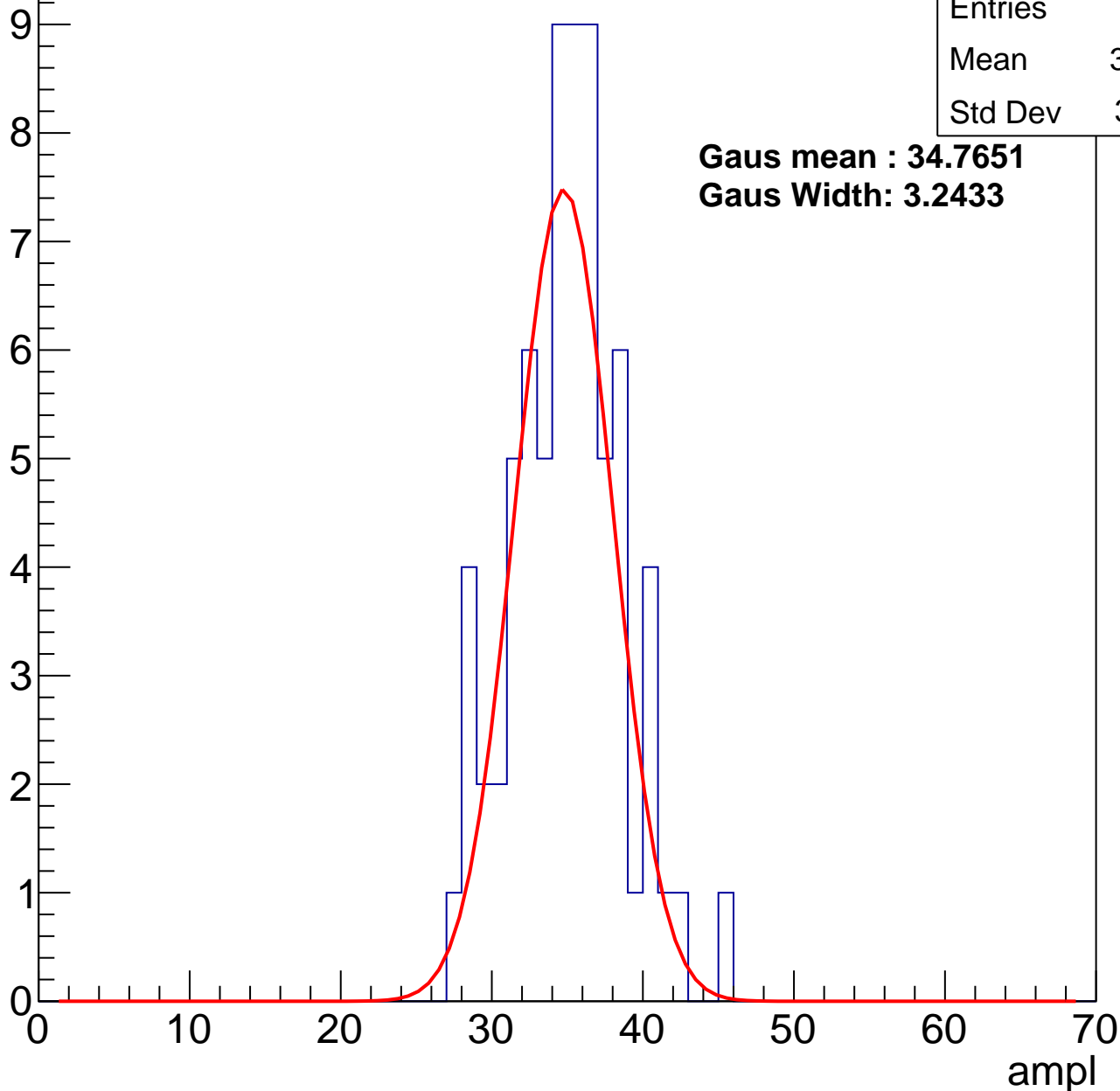
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.56
Std Dev	3.591

**Gaus mean : 34.7651**

**Gaus Width: 3.2433**



# B1L002S, U17-ch20, adc2

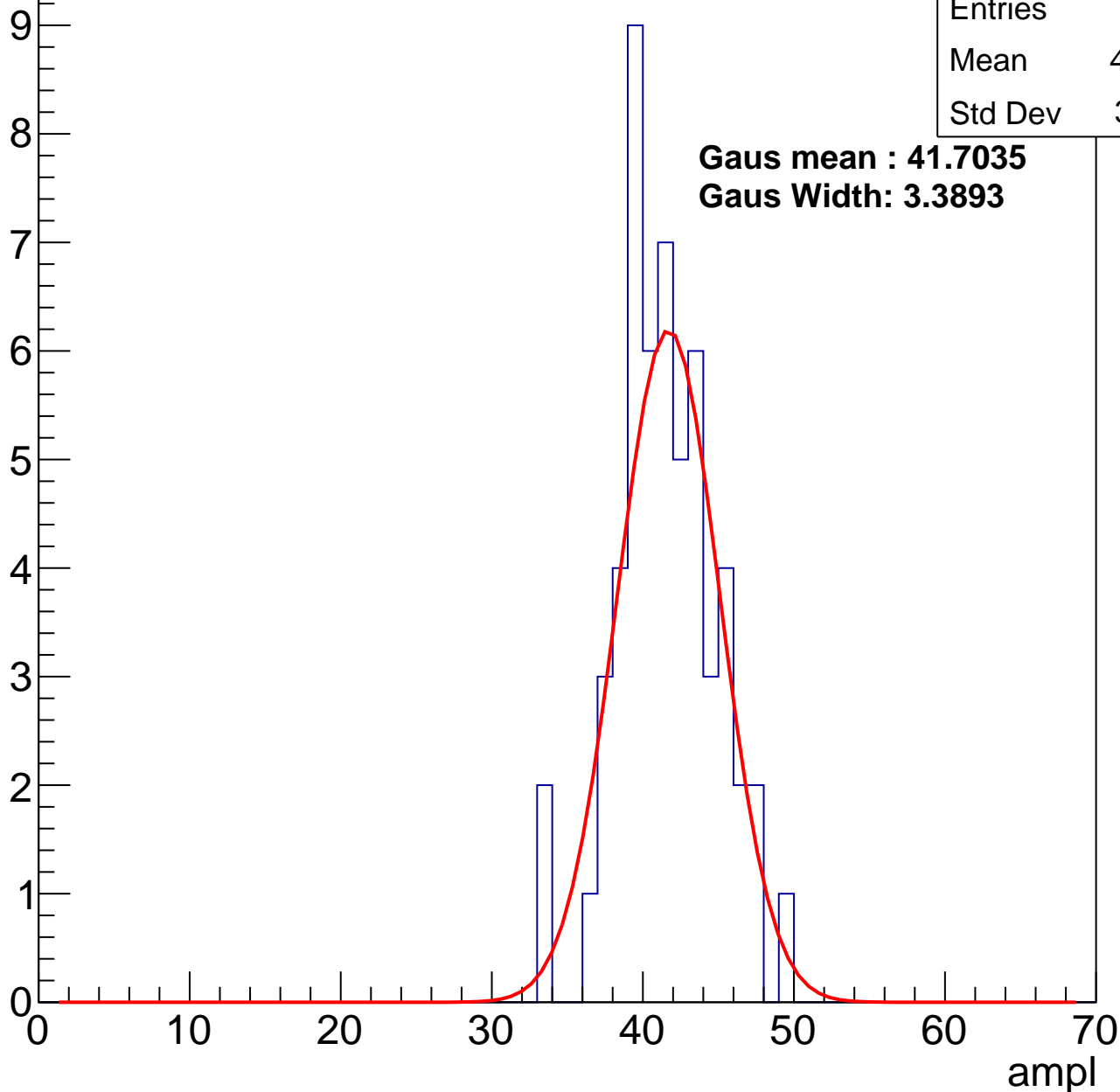
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.05
Std Dev	3.261

**Gaus mean : 41.7035**

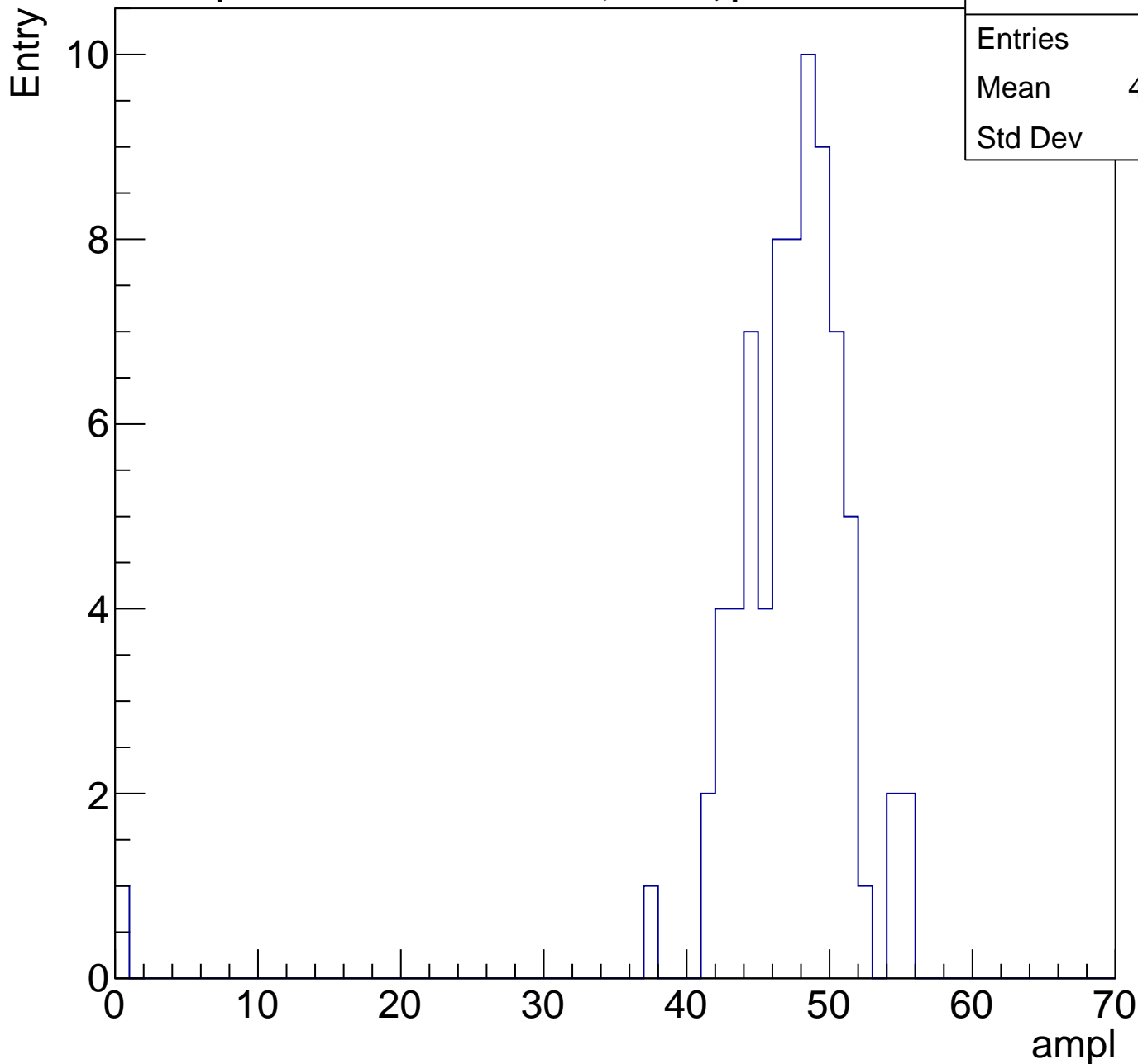
**Gaus Width: 3.3893**



# B1L002S, U17-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	46.49
Std Dev	6.38

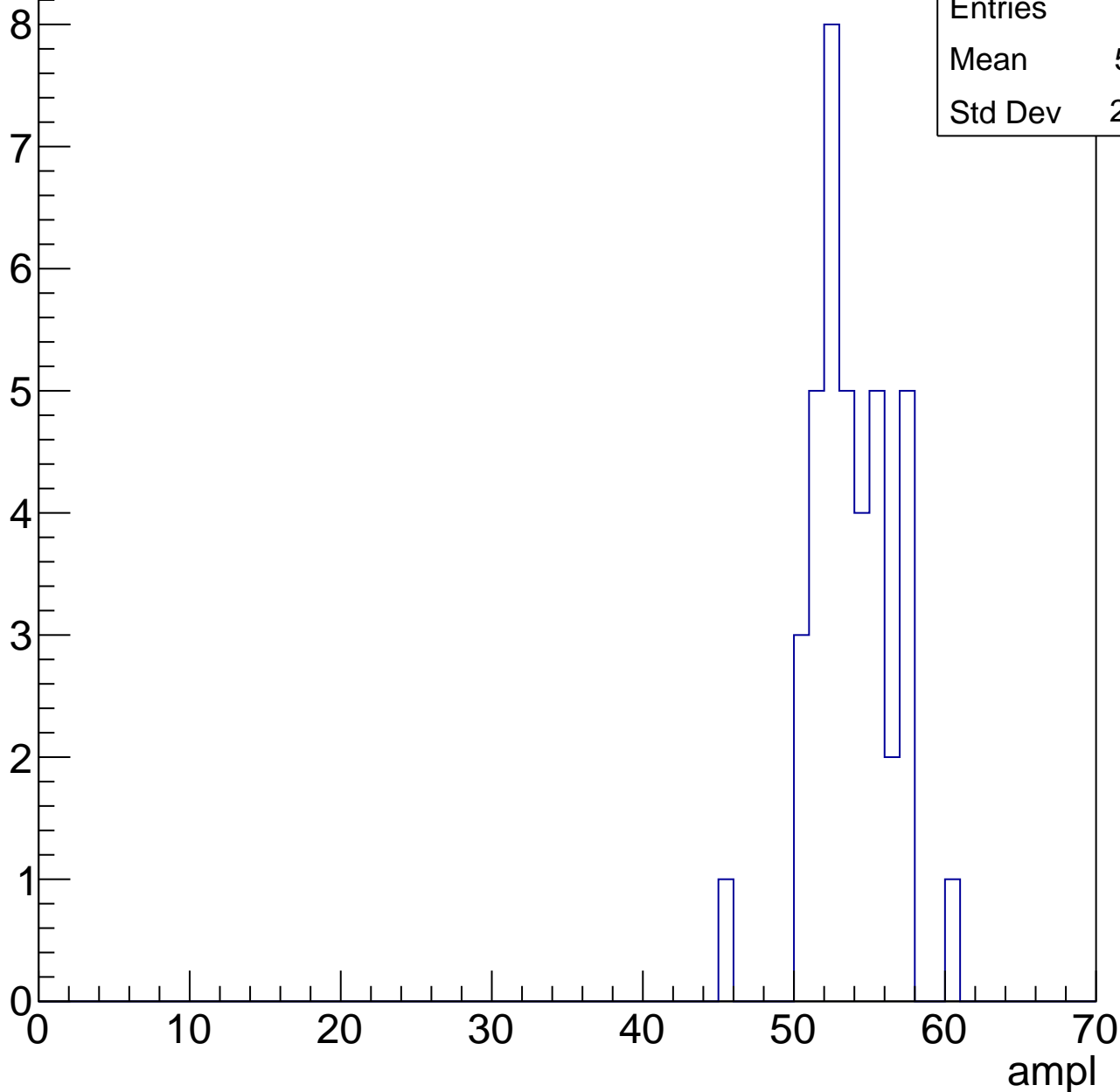


# B1L002S, U17-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	53.31
Std Dev	2.709

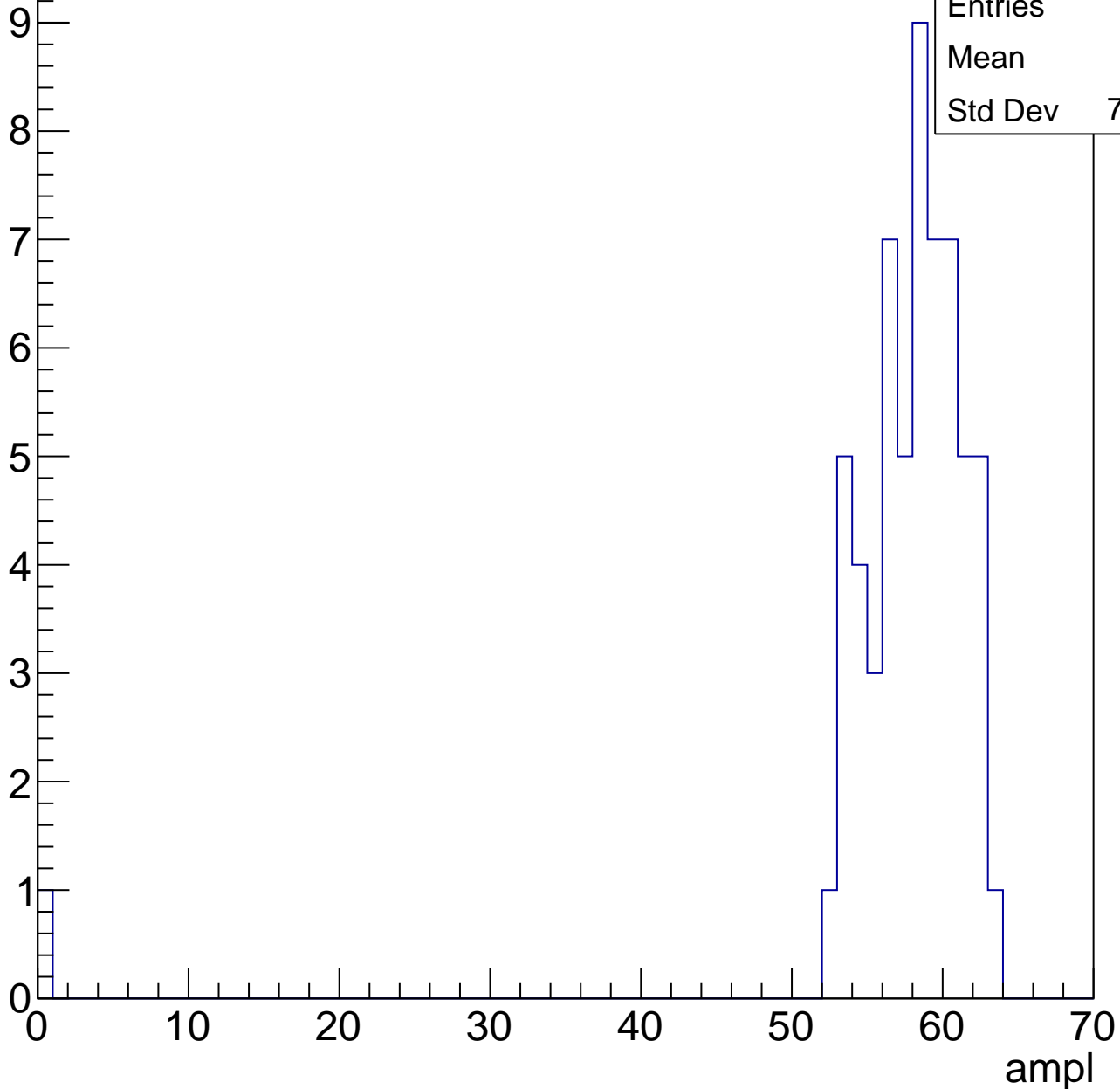


# B1L002S, U17-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.8
Std Dev	7.903

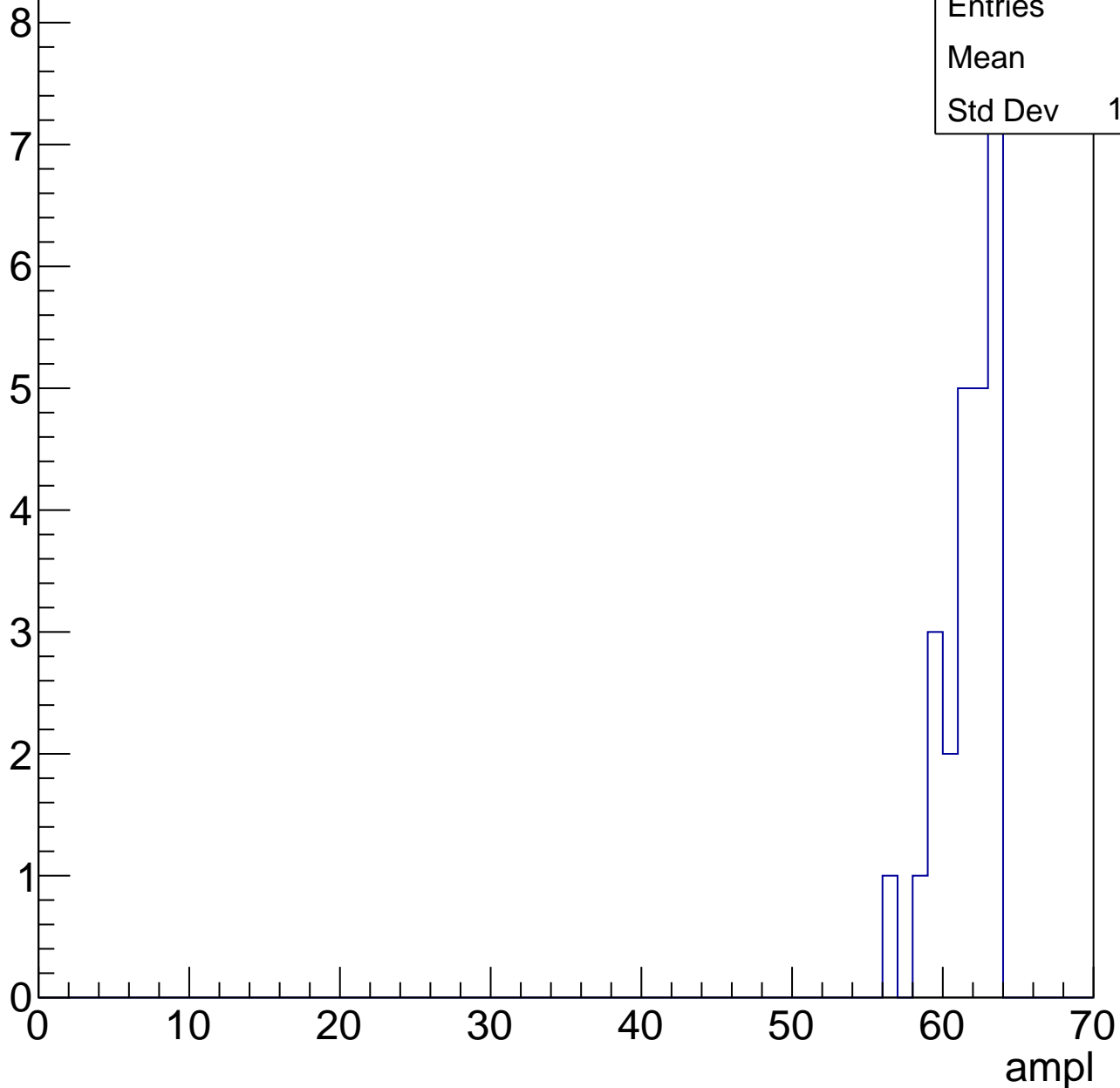


# B1L002S, U17-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.2
Std Dev	1.833

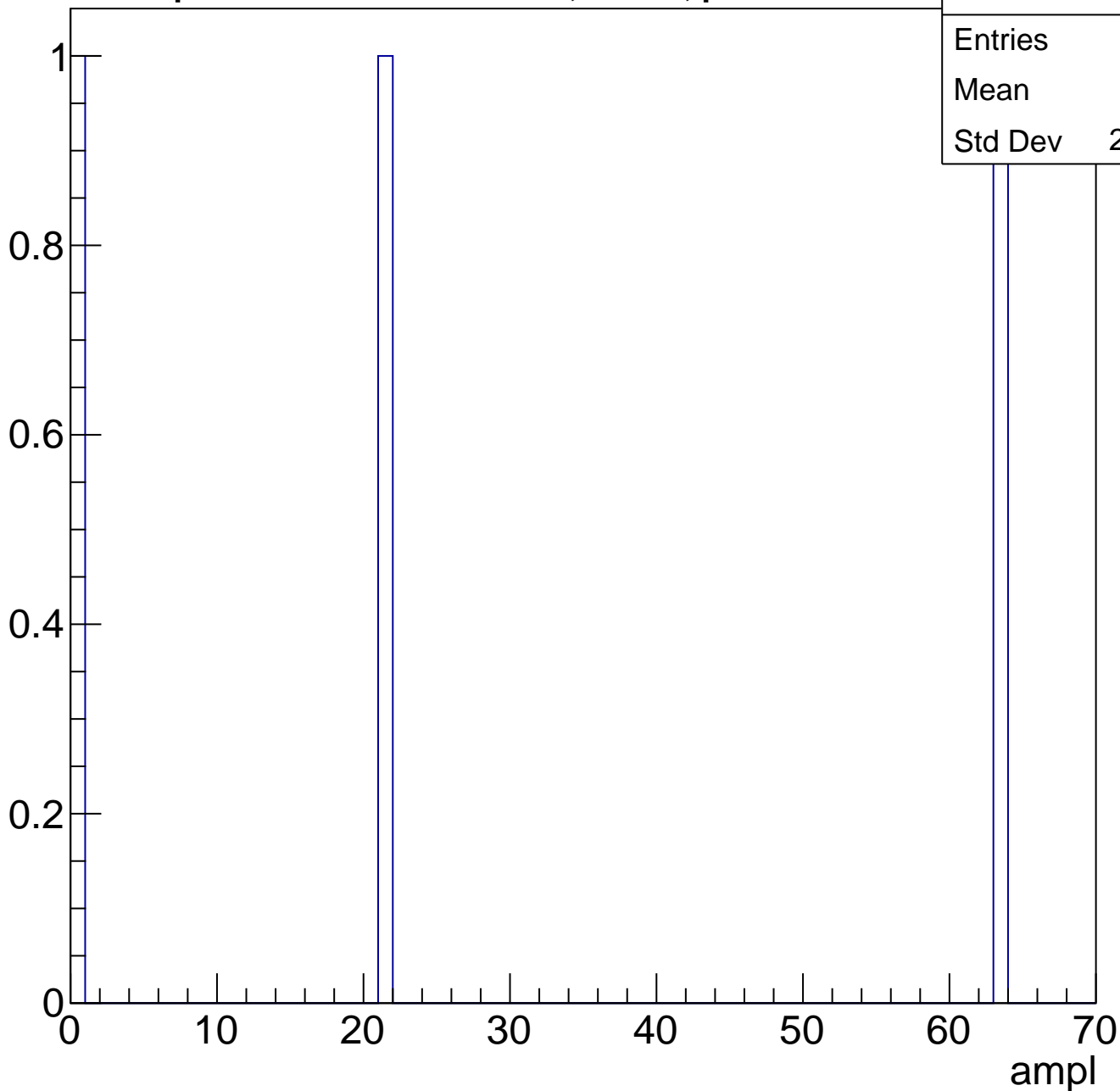




# B1L002S, U17-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L002S, U17-ch21, adc0

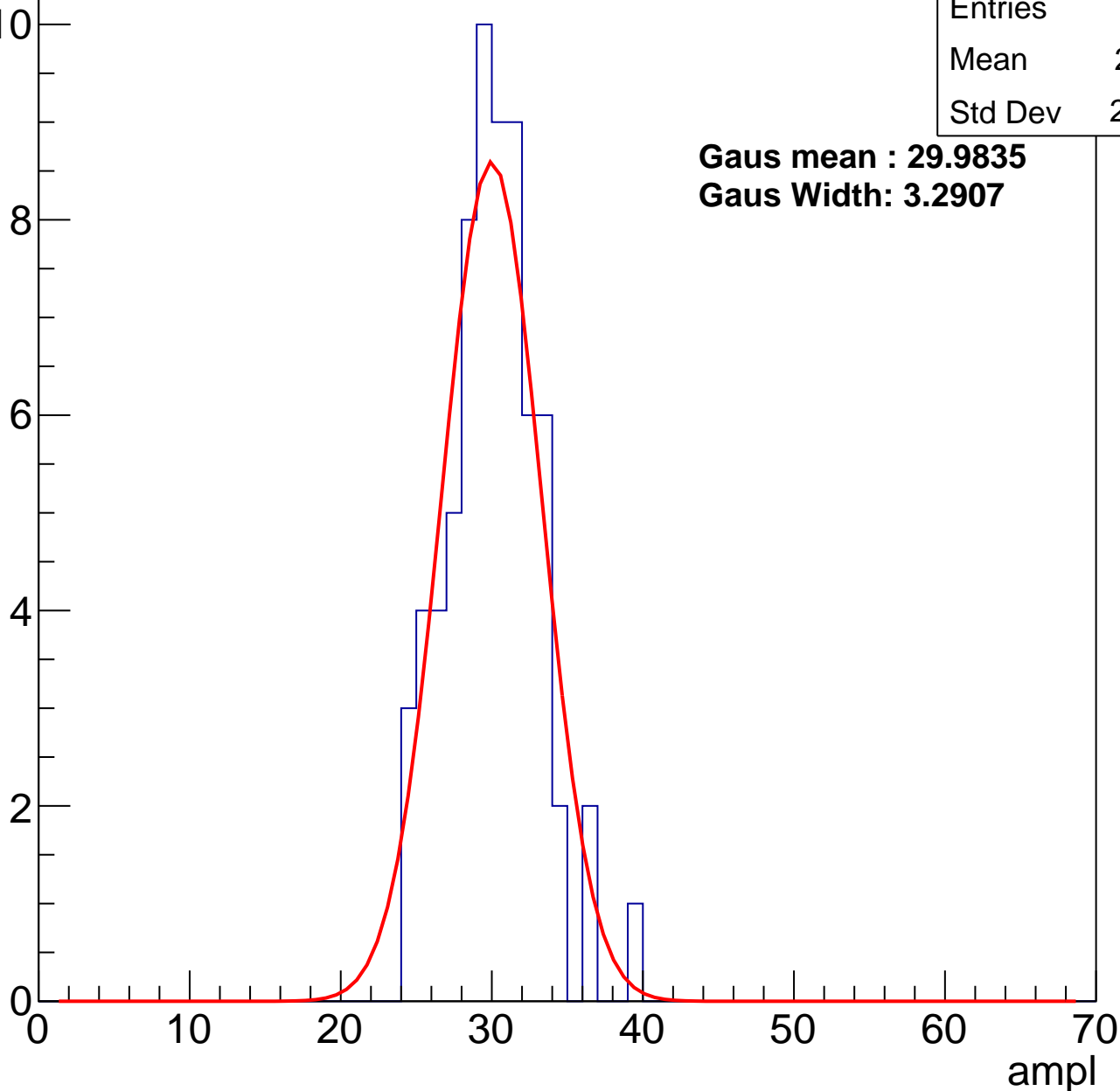
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	29.61
Std Dev	2.999

**Gaus mean : 29.9835**

**Gaus Width: 3.2907**



# B1L002S, U17-ch21, adc1

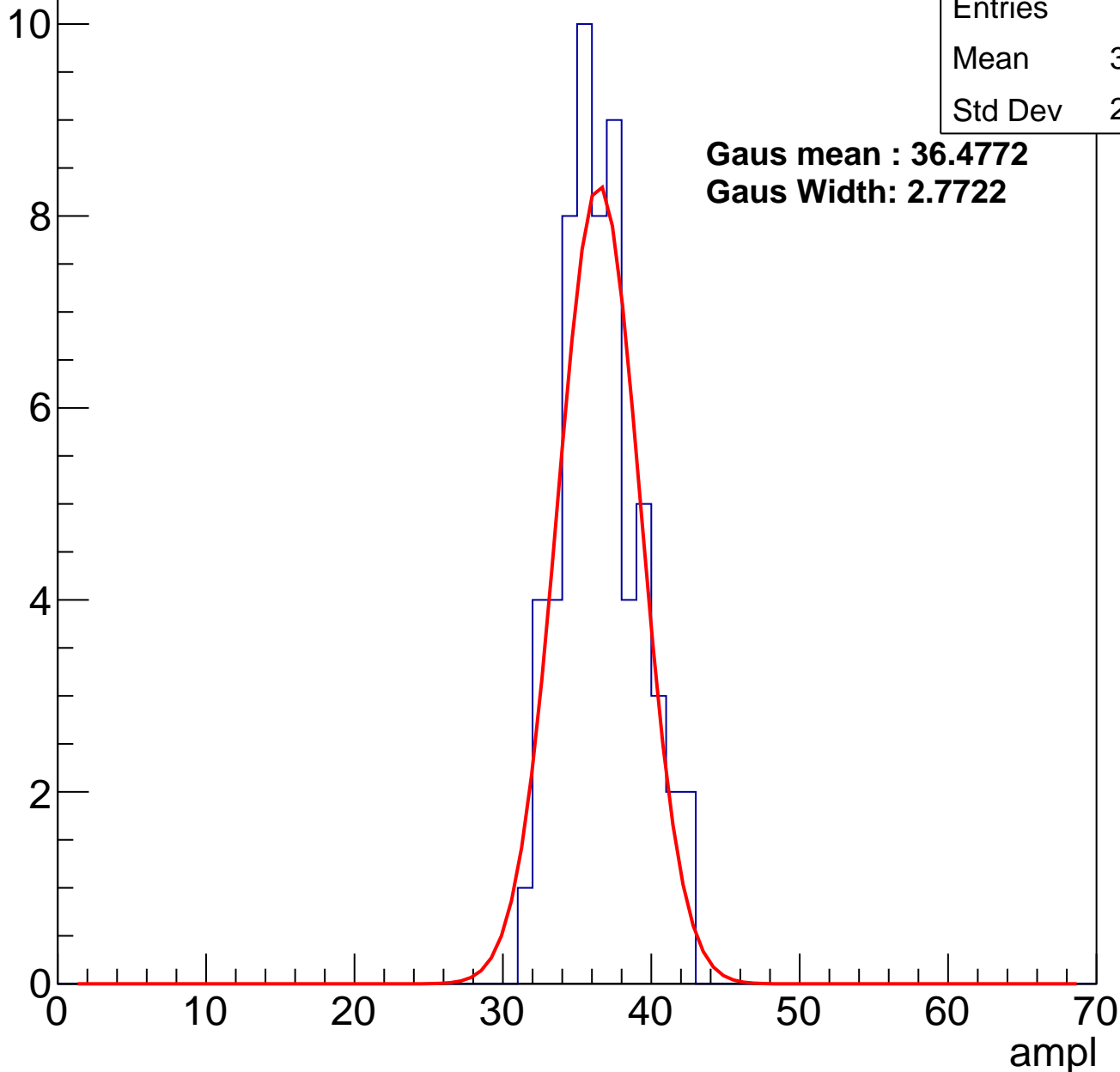
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	36.12
Std Dev	2.602

**Gaus mean : 36.4772**

**Gaus Width: 2.7722**

Entry



# B1L002S, U17-ch21, adc2

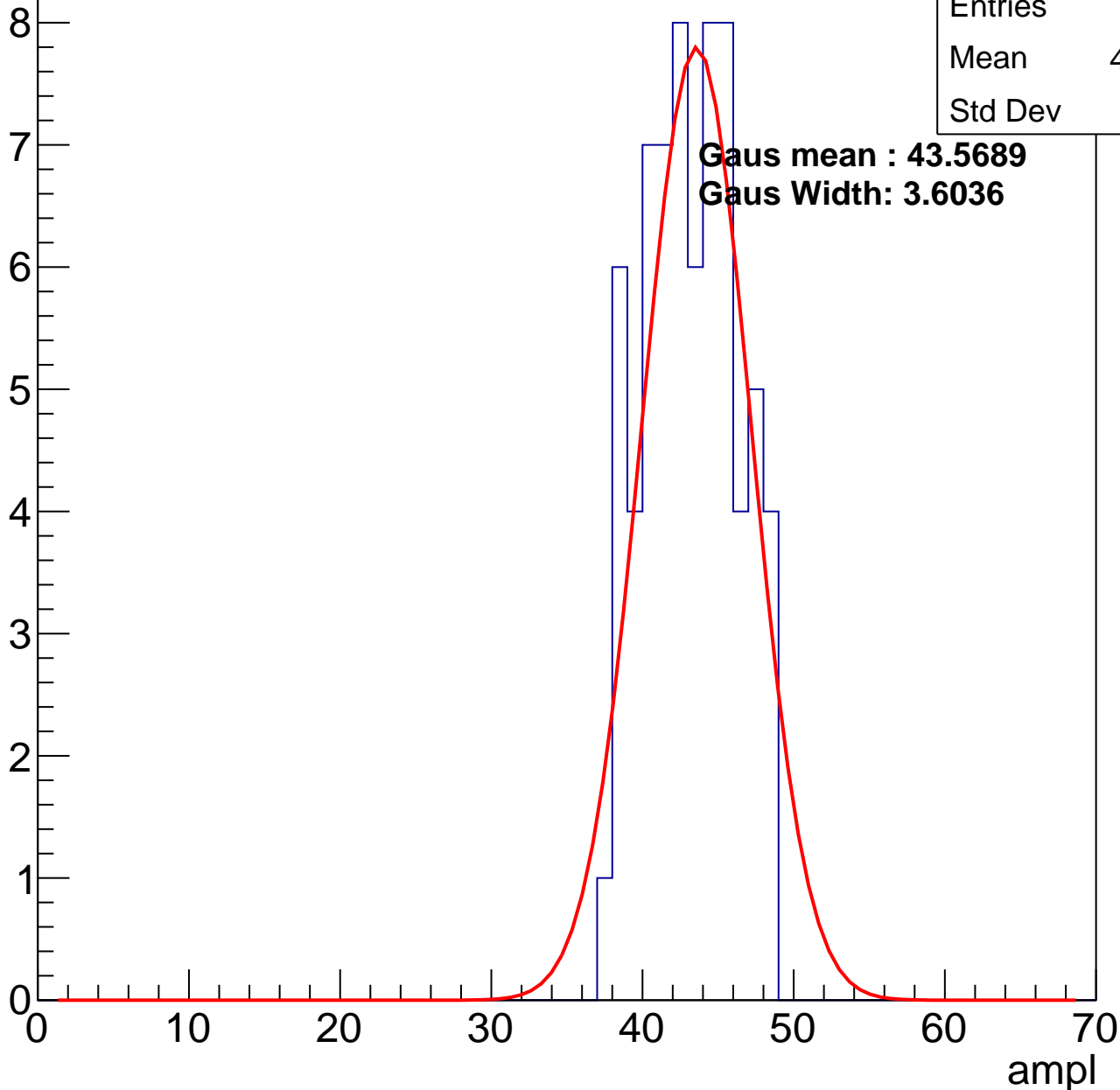
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	42.72
Std Dev	2.97

**Gaus mean : 43.5689**

**Gaus Width: 3.6036**

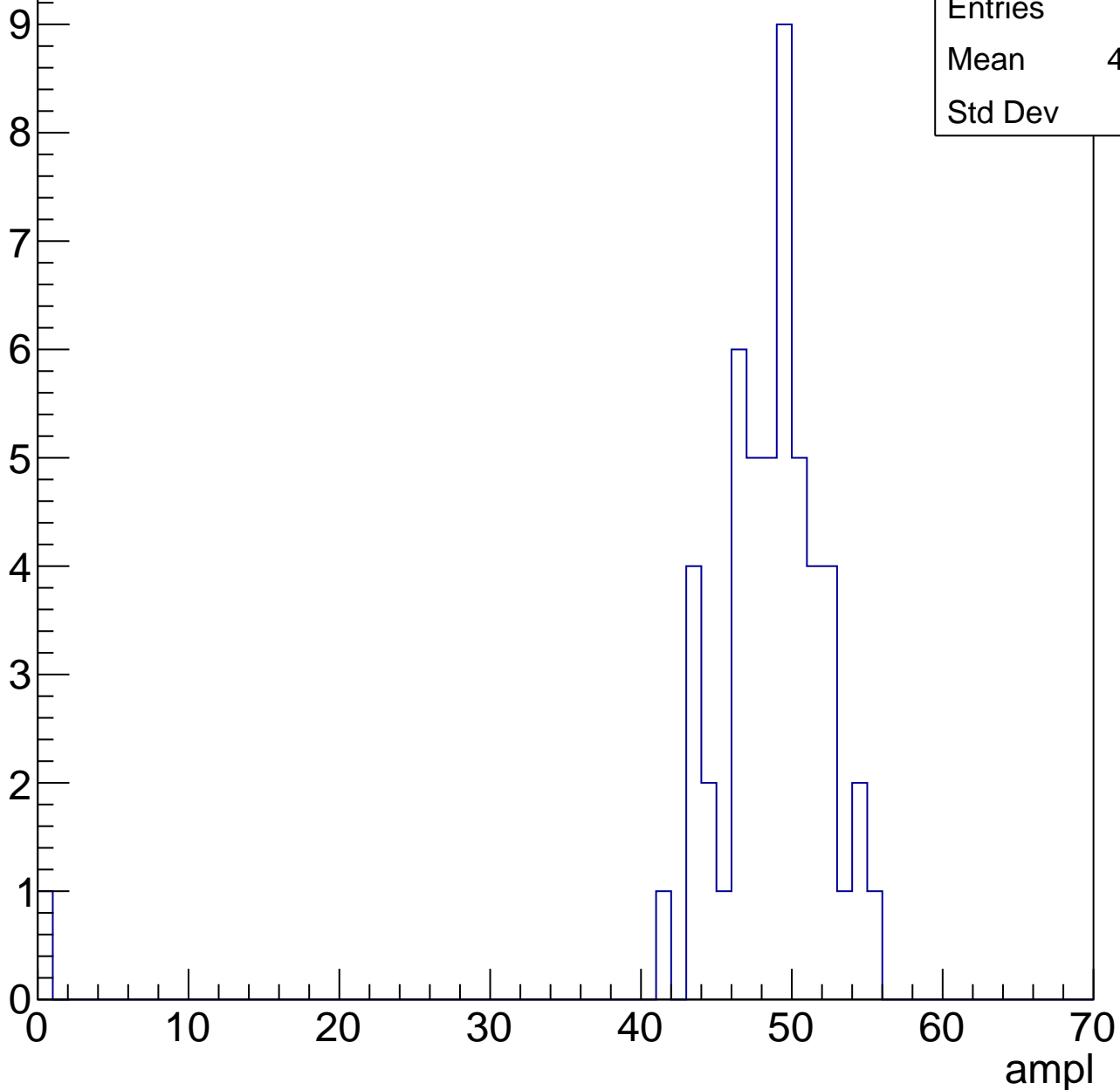


# B1L002S, U17-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	47.37
Std Dev	7.38

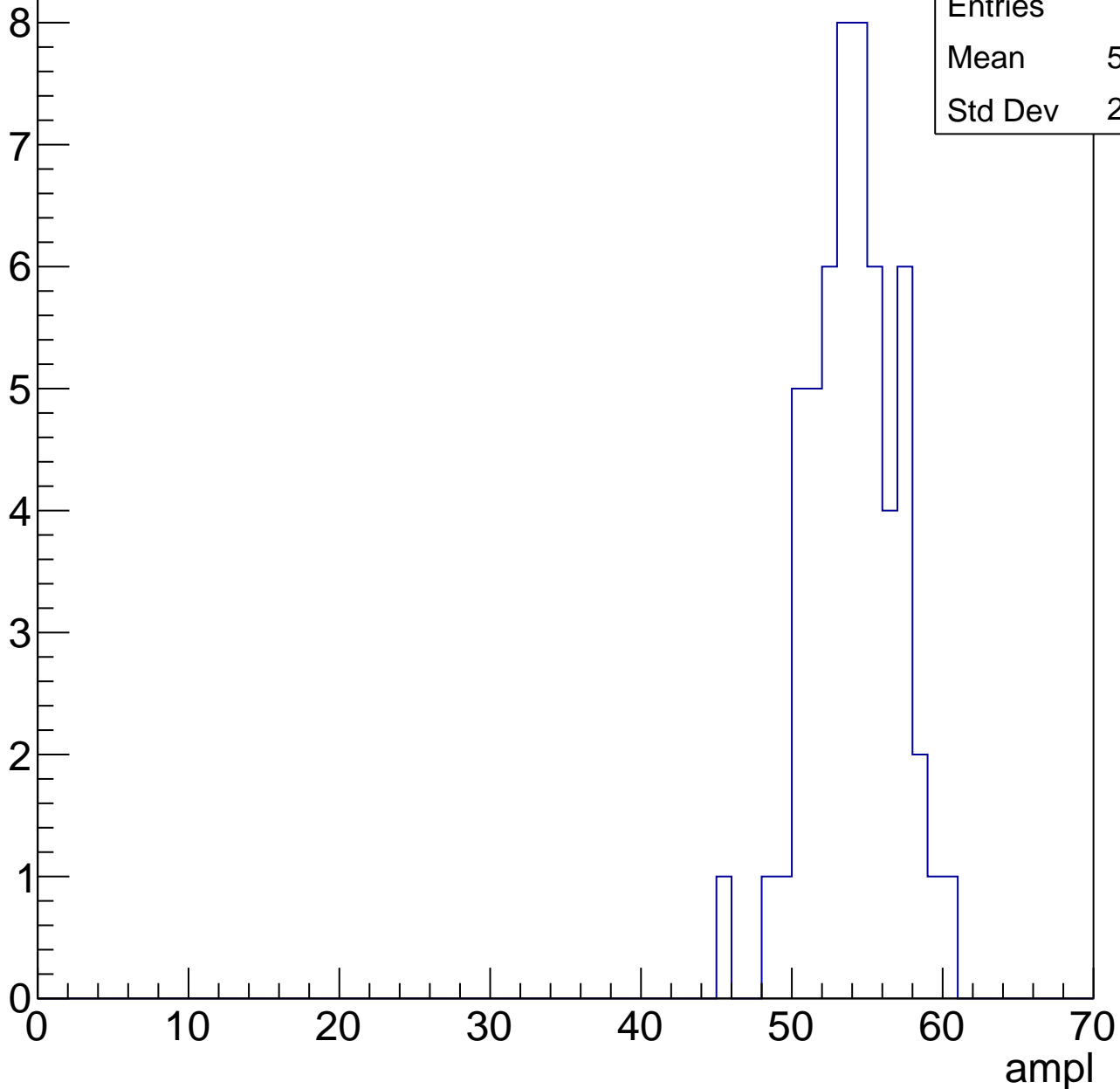


# B1L002S, U17-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.56
Std Dev	2.884

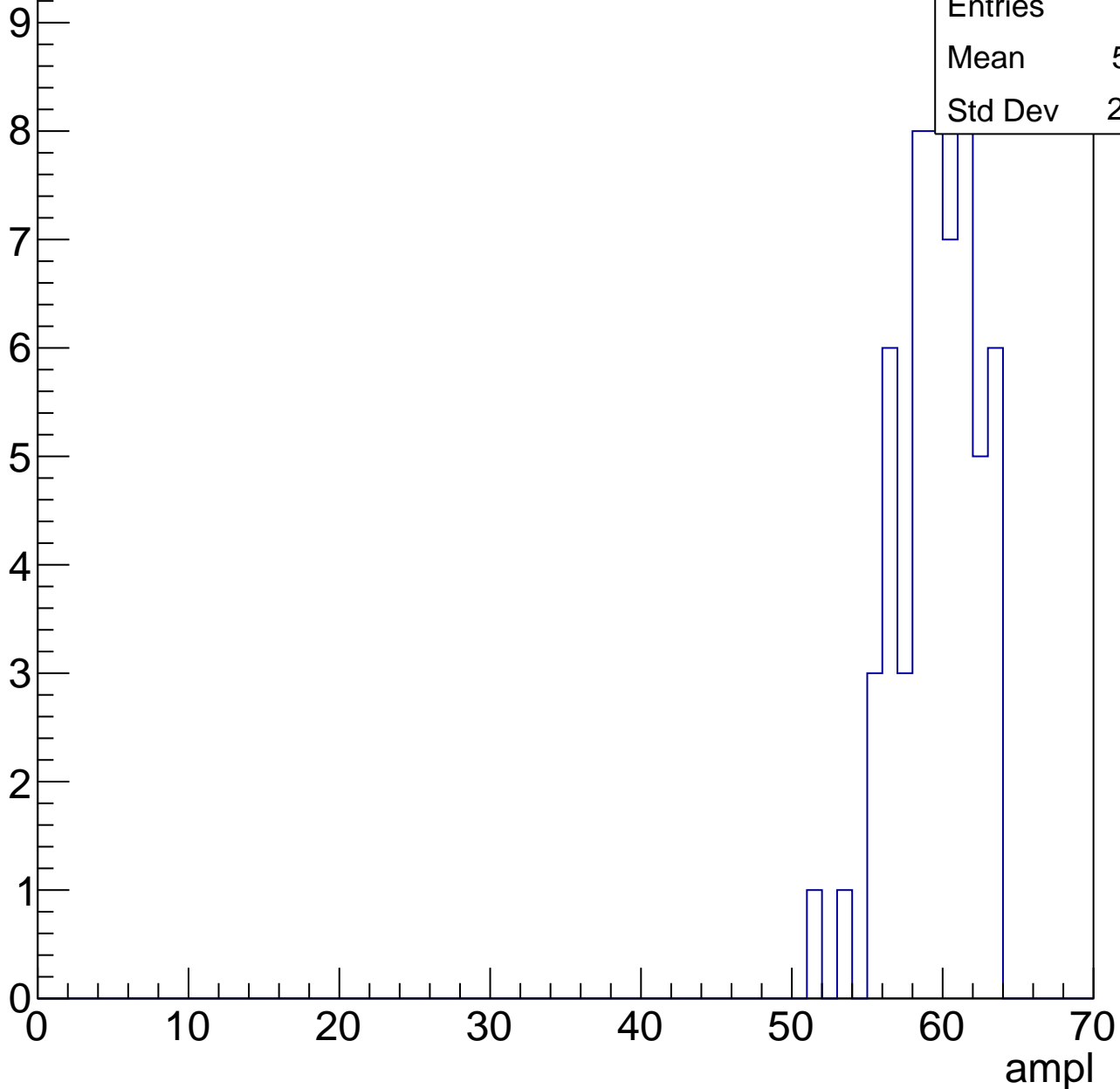


# B1L002S, U17-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	59.11
Std Dev	2.667

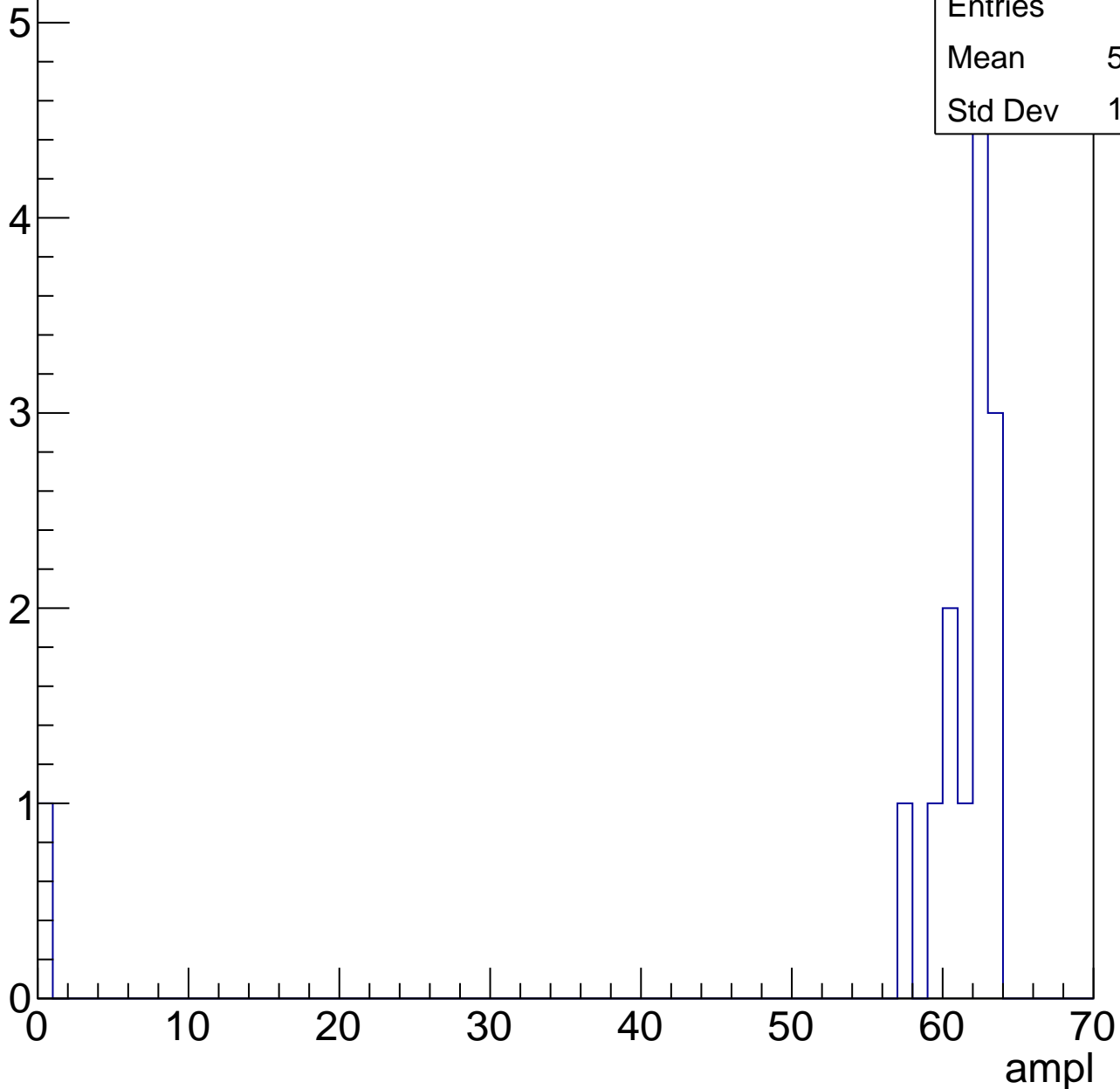


# B1L002S, U17-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	56.86
Std Dev	15.86

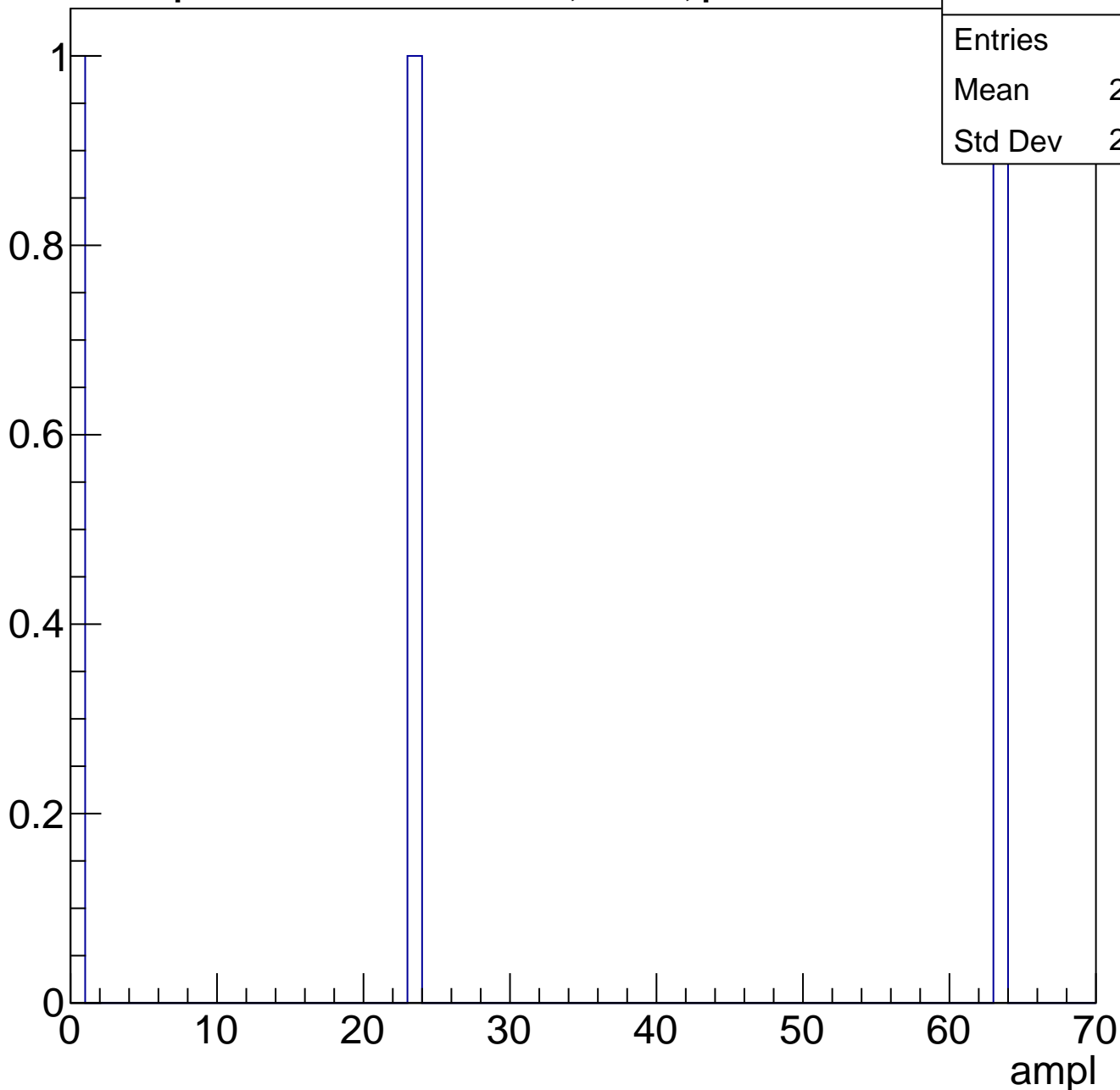




# B1L002S, U17-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B1L002S, U17-ch22, adc0

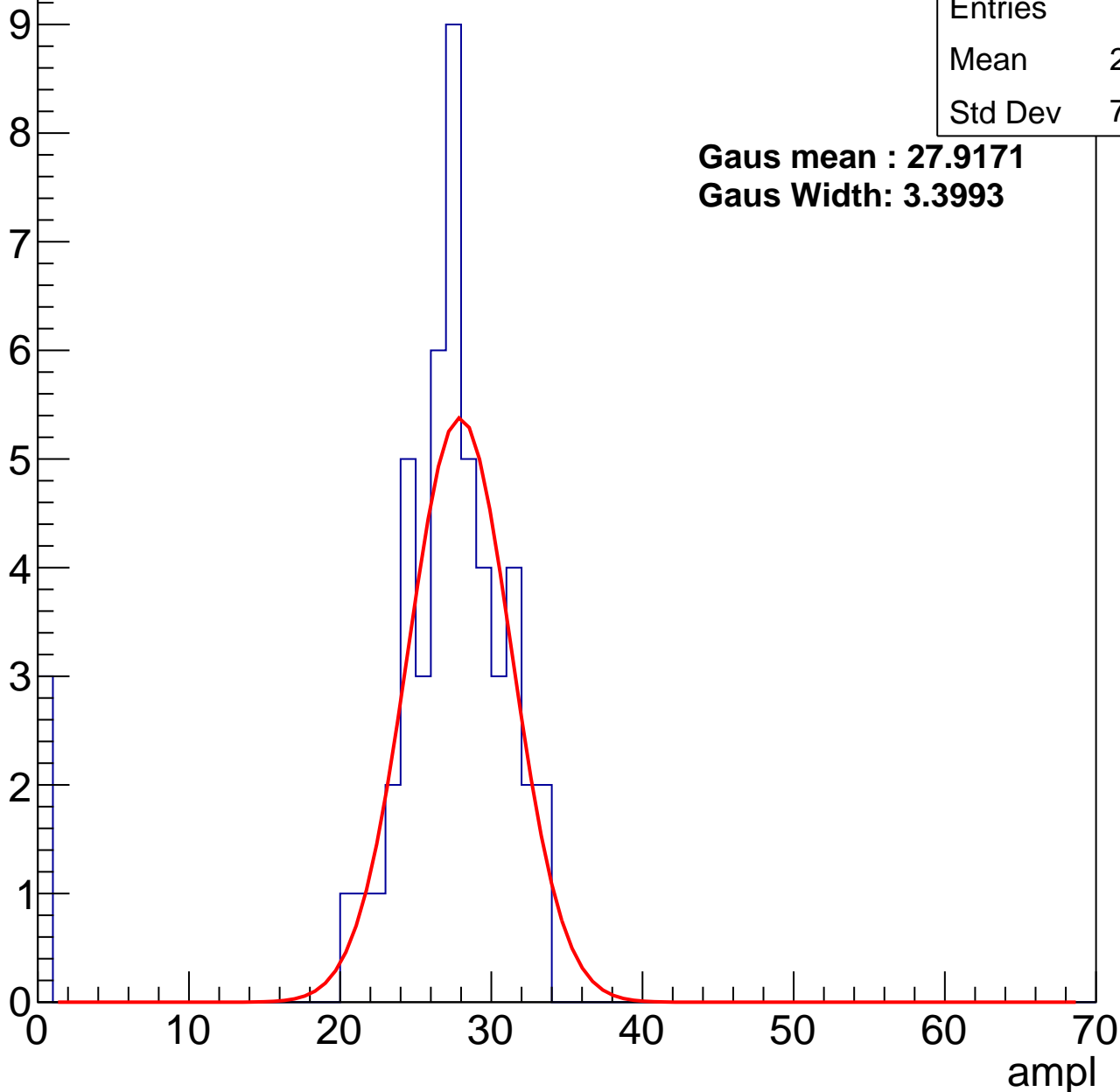
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	25.55
Std Dev	7.027

**Gaus mean : 27.9171**

**Gaus Width: 3.3993**



# B1L002S, U17-ch22, adc1

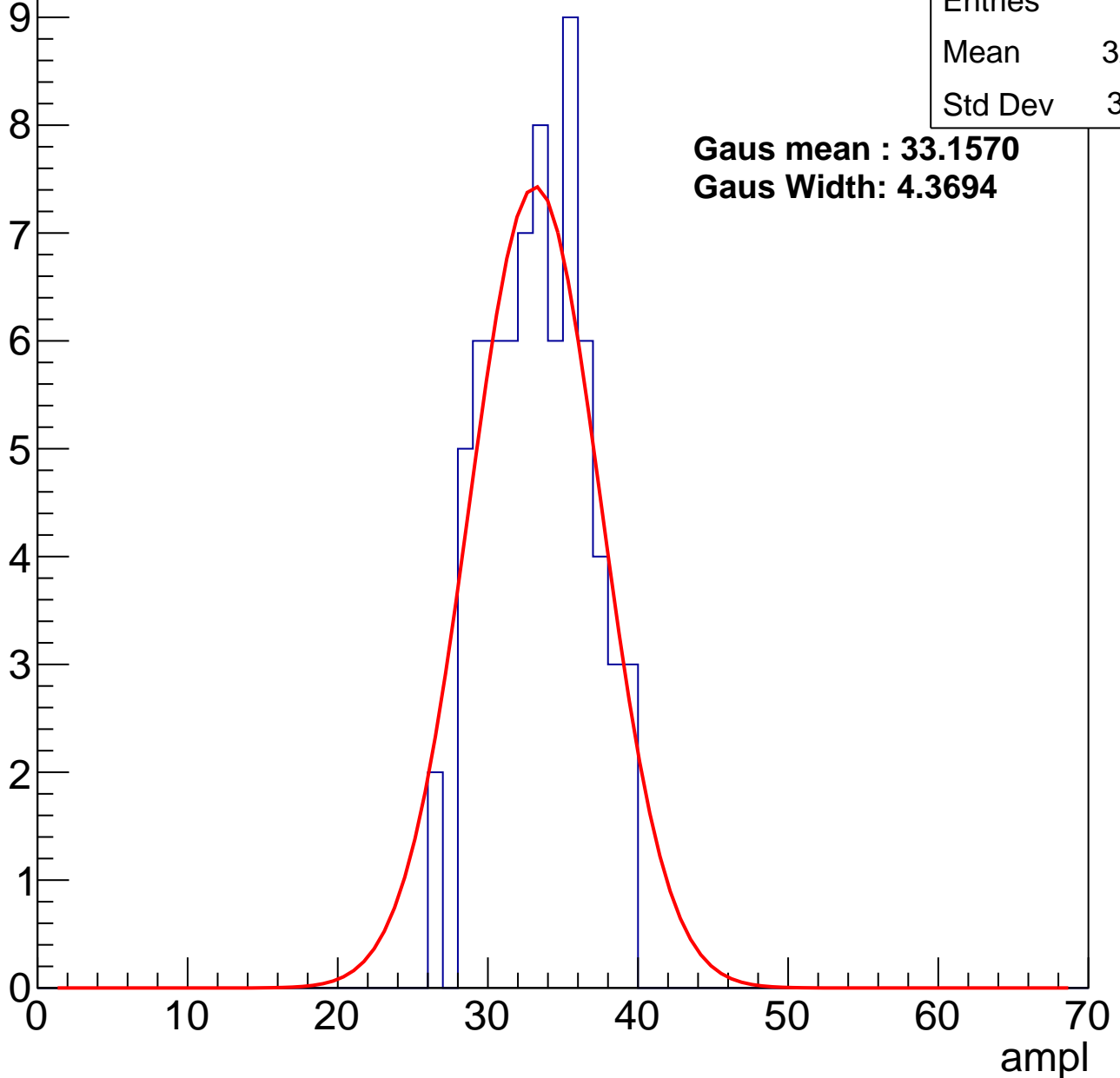
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	32.87
Std Dev	3.241

**Gaus mean : 33.1570**

**Gaus Width: 4.3694**



# B1L002S, U17-ch22, adc2

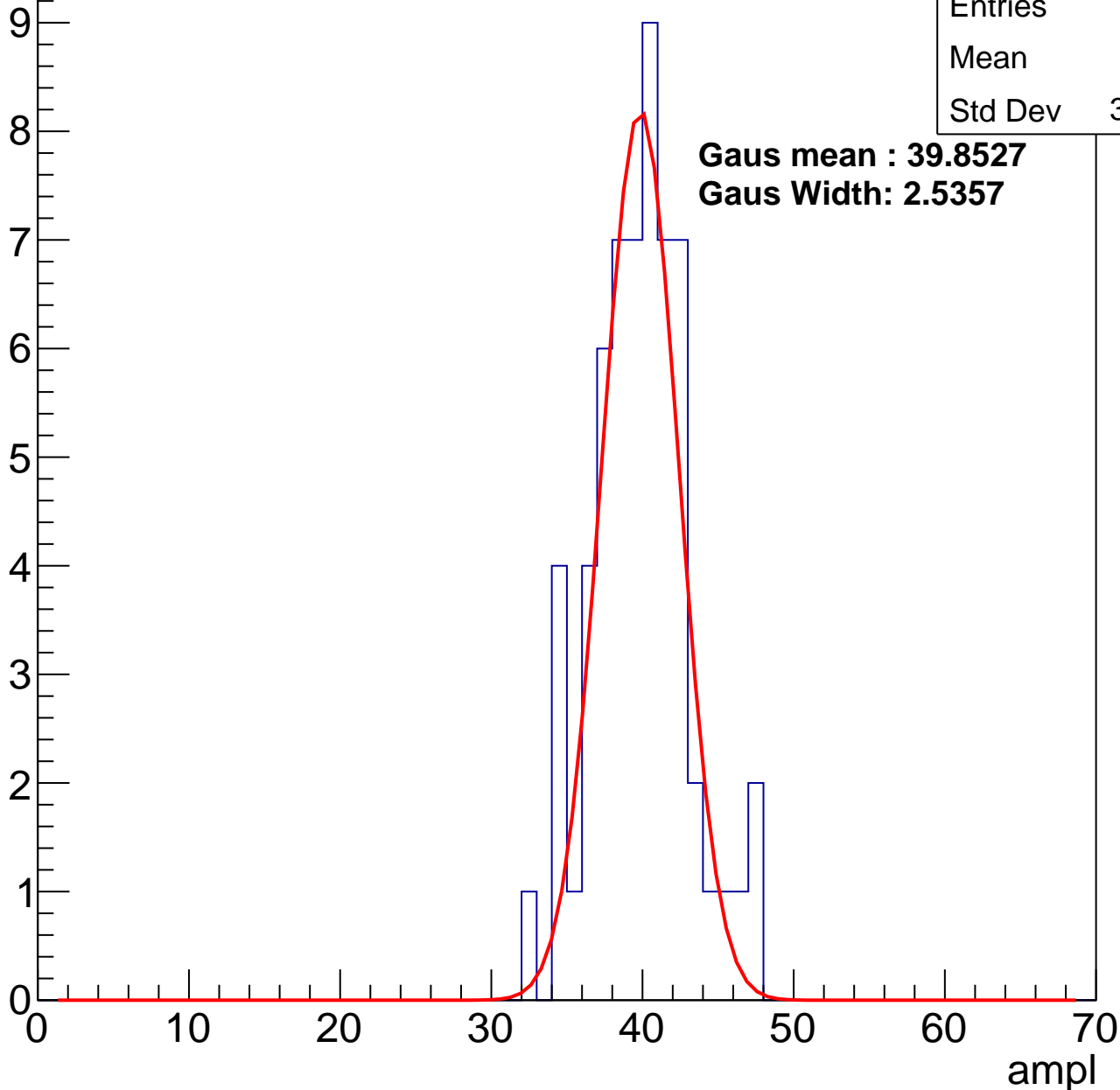
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	39.4
Std Dev	3.142

**Gaus mean : 39.8527**

**Gaus Width: 2.5357**



# B1L002S, U17-ch22, adc3

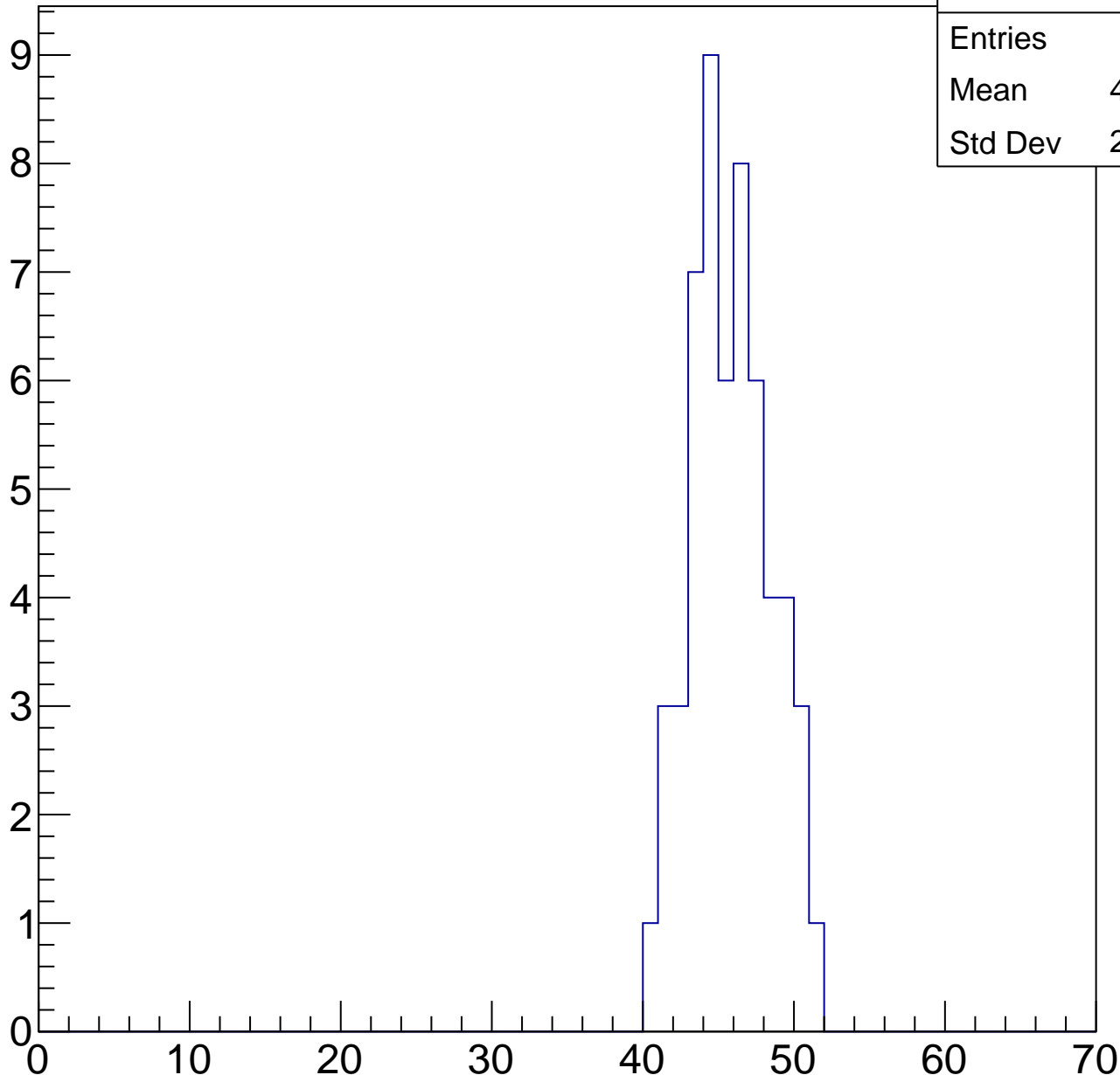
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	45.36
Std Dev	2.603

ampl



# B1L002S, U17-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	51.66
Std Dev	3.453

Entry

10

8

6

4

2

0

0

10

20

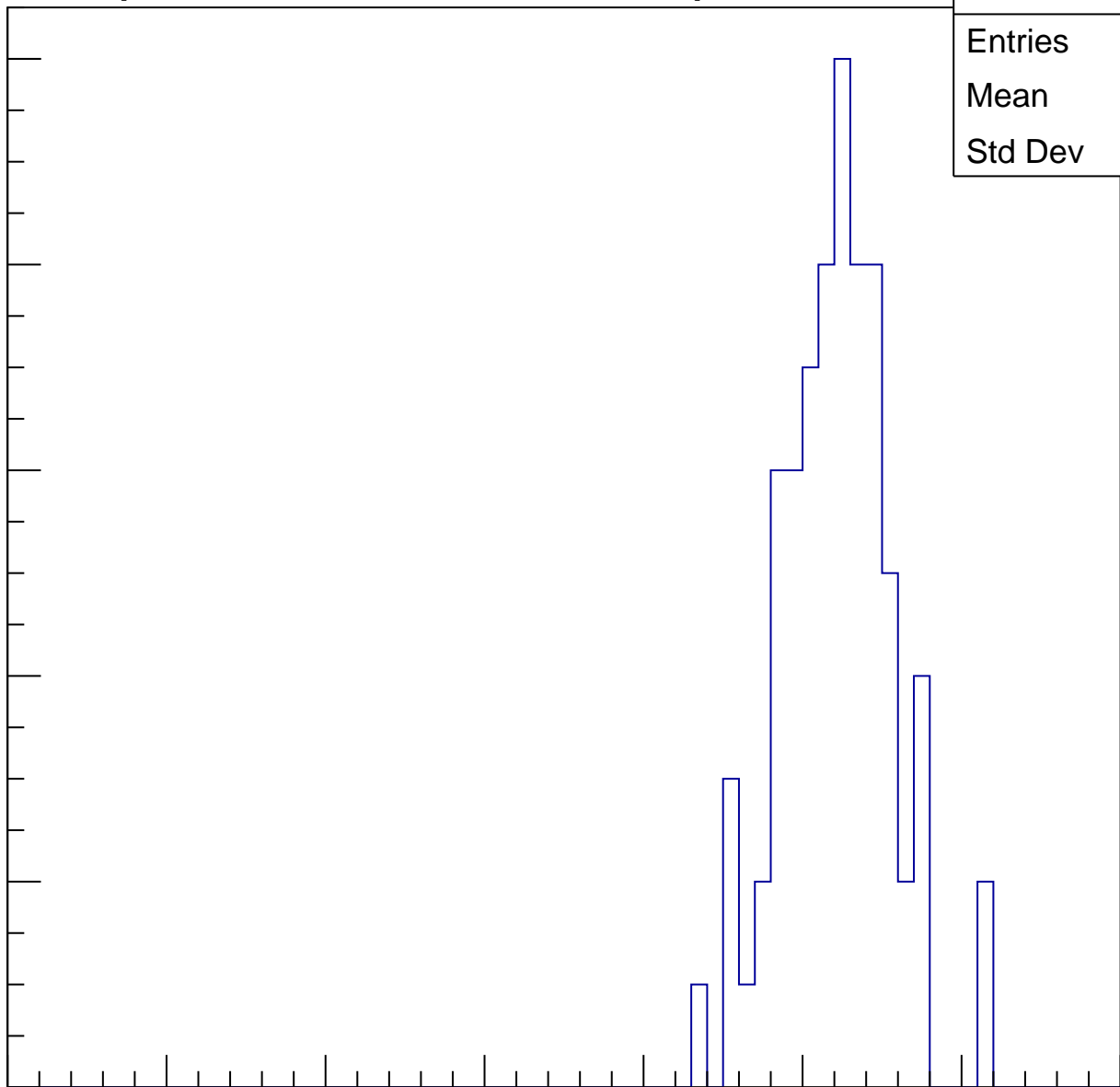
30

40

50

60

ampl

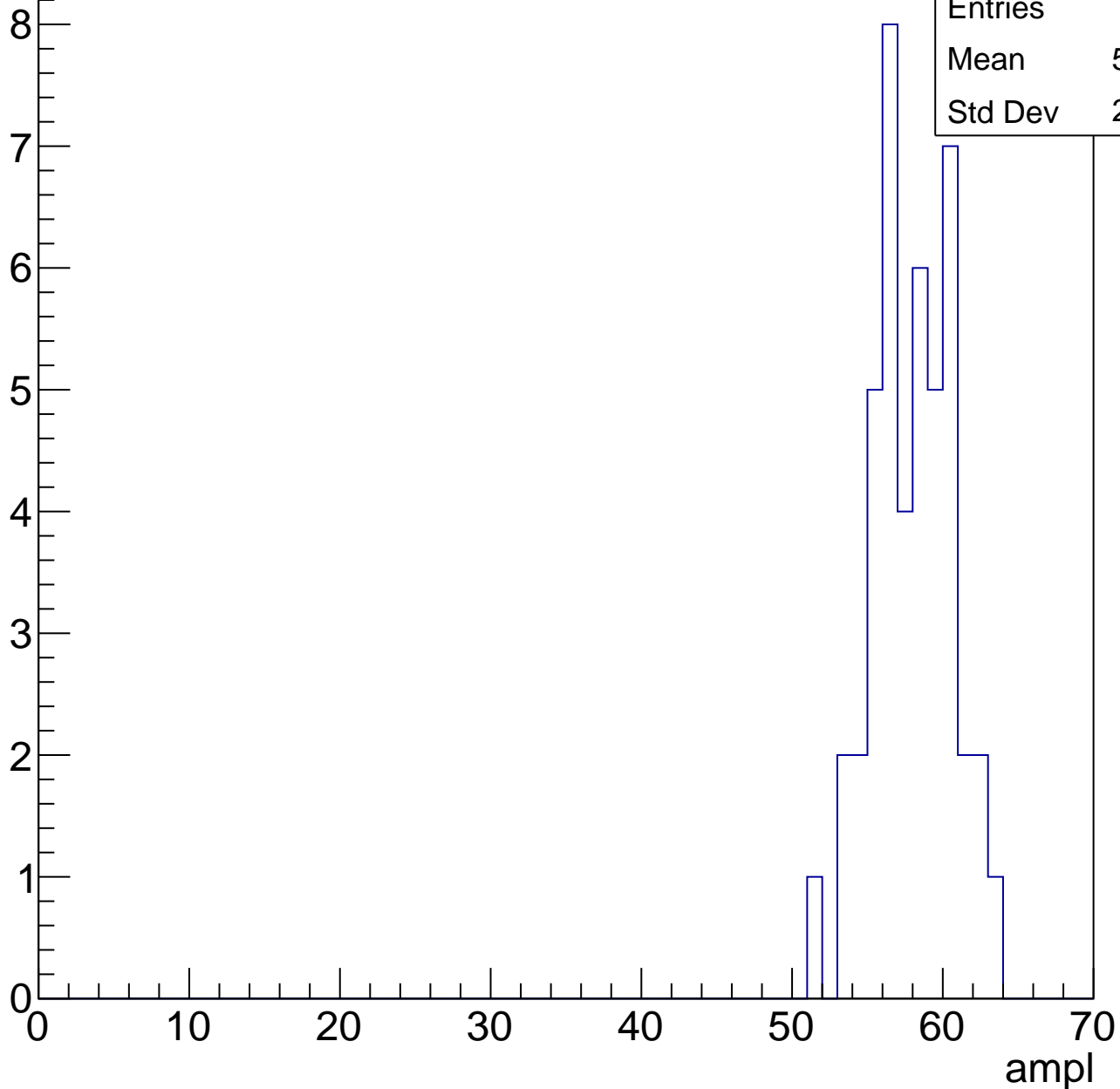


# B1L002S, U17-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	57.51
Std Dev	2.621

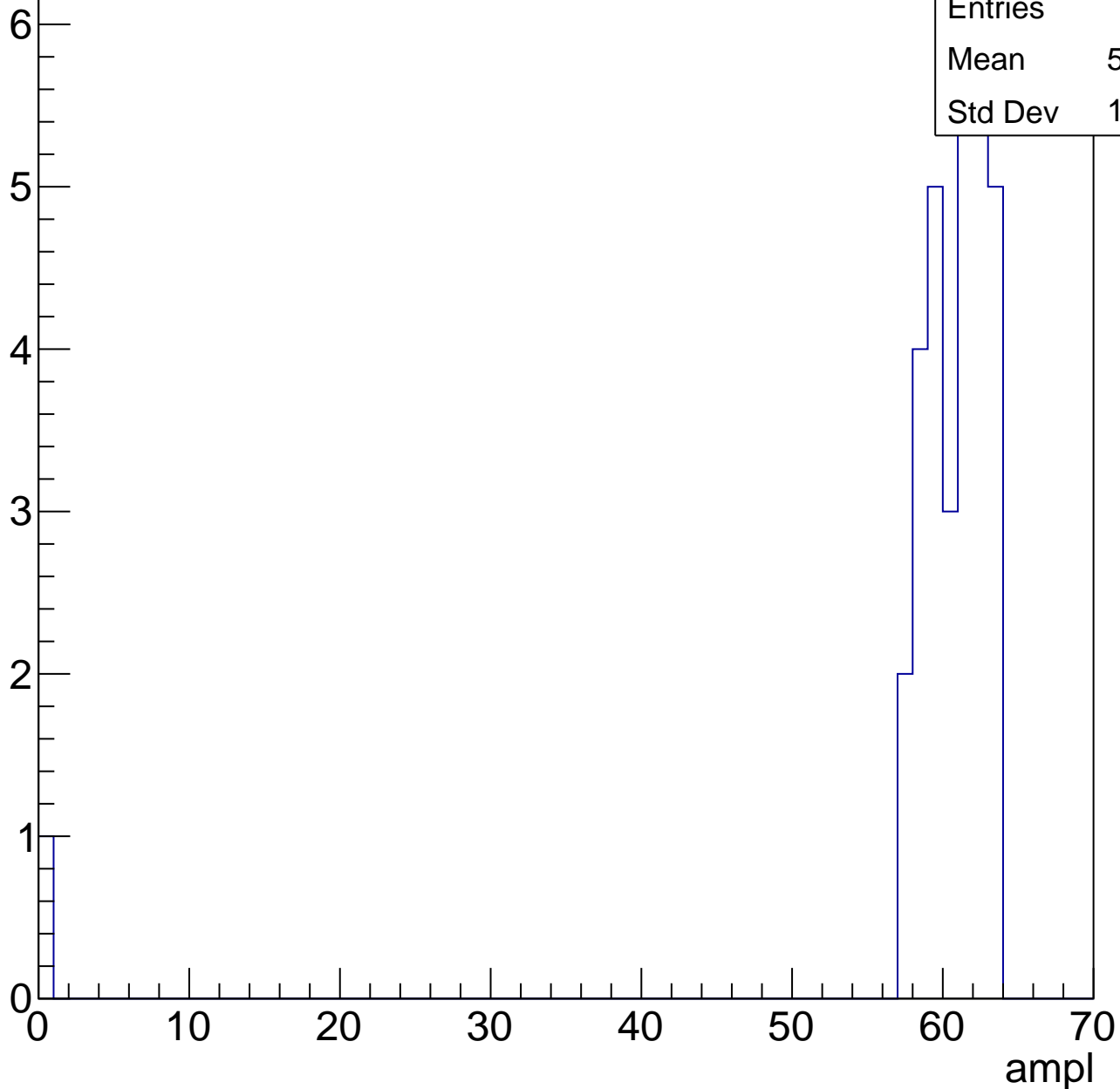


# B1L002S, U17-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	58.56
Std Dev	10.68

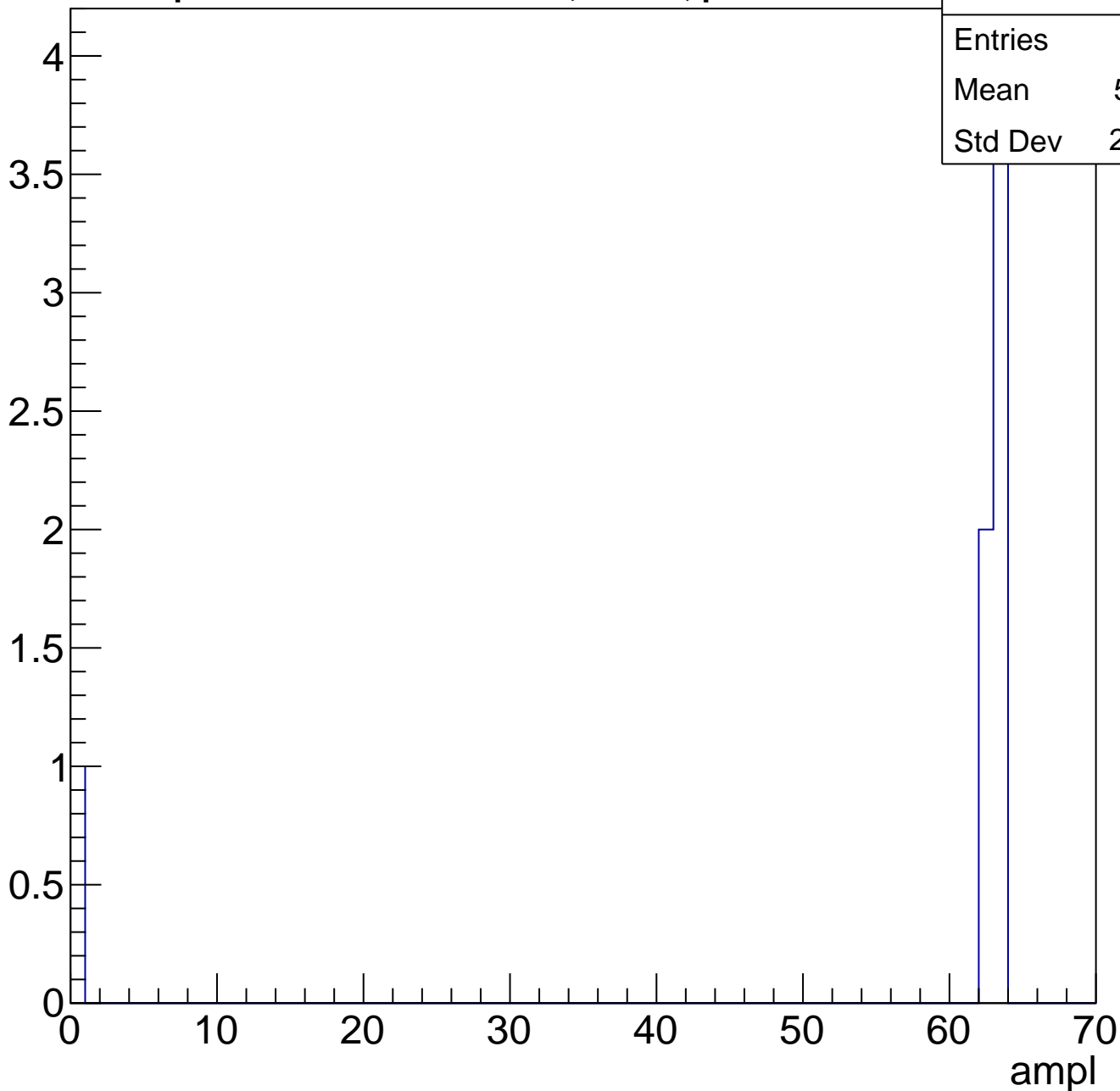




# B1L002S, U17-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch23, adc0

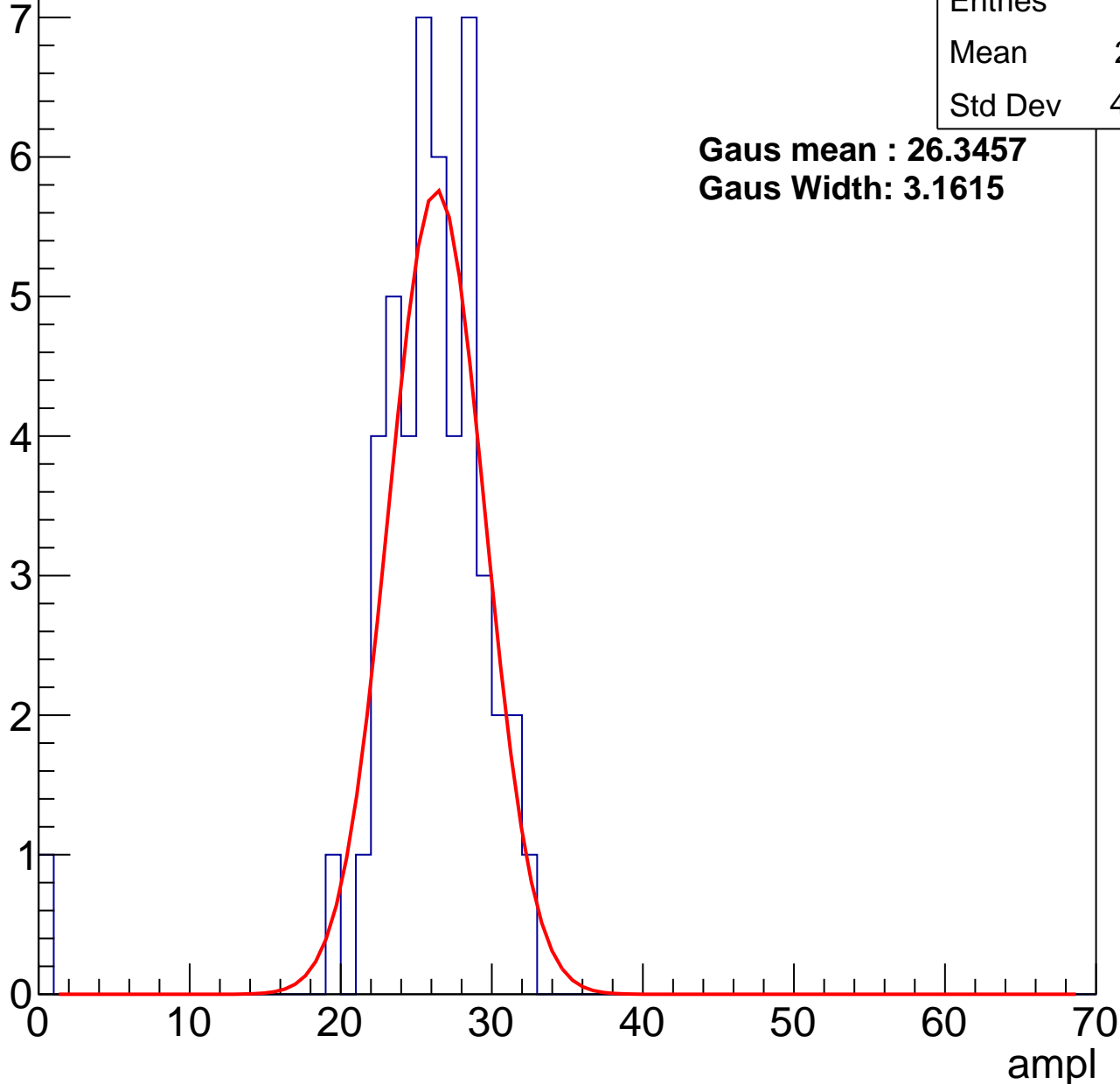
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	25.31
Std Dev	4.646

**Gaus mean : 26.3457**

**Gaus Width: 3.1615**



# B1L002S, U17-ch23, adc1

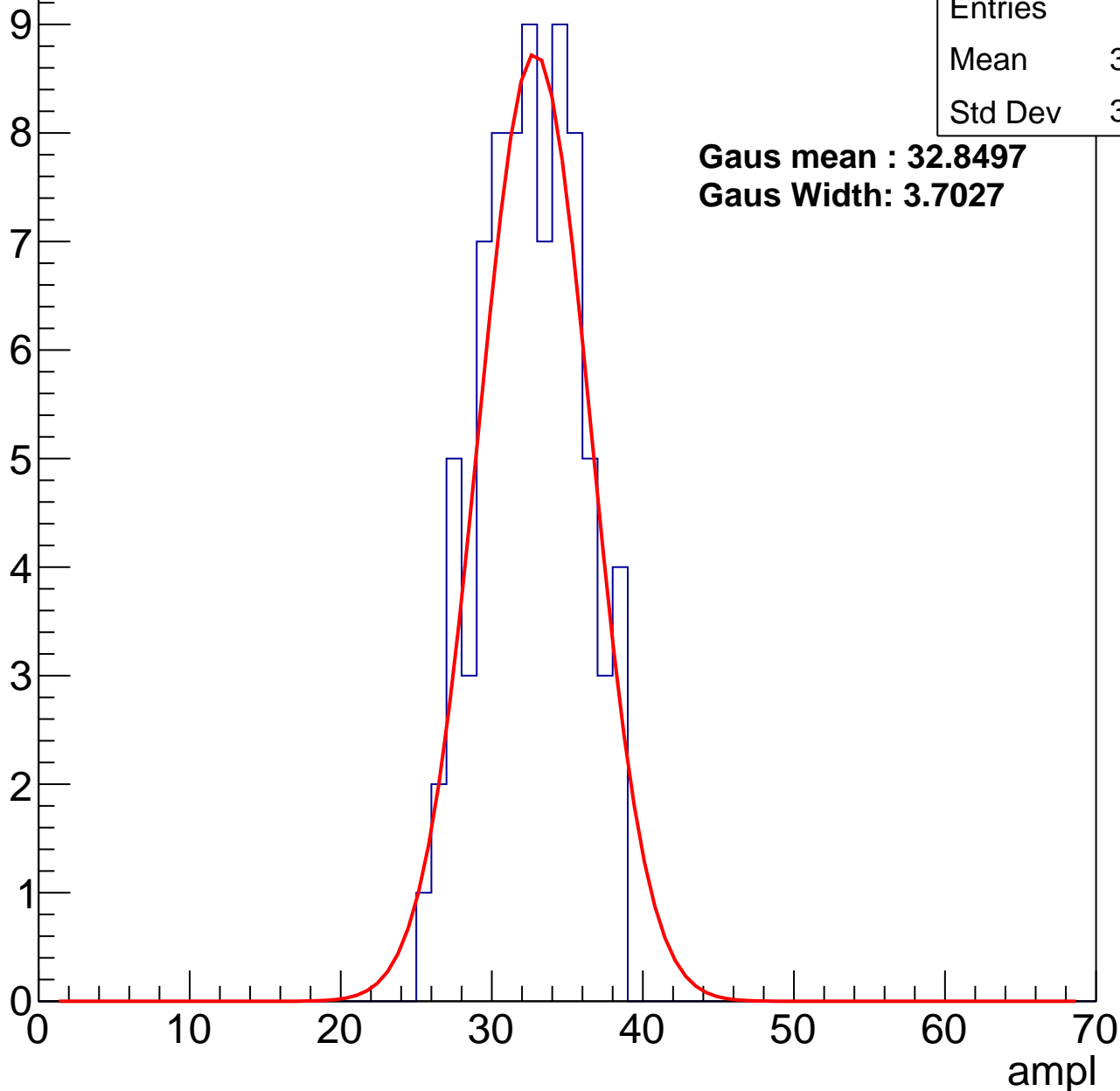
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	32.09
Std Dev	3.203

**Gaus mean : 32.8497**

**Gaus Width: 3.7027**



# B1L002S, U17-ch23, adc2

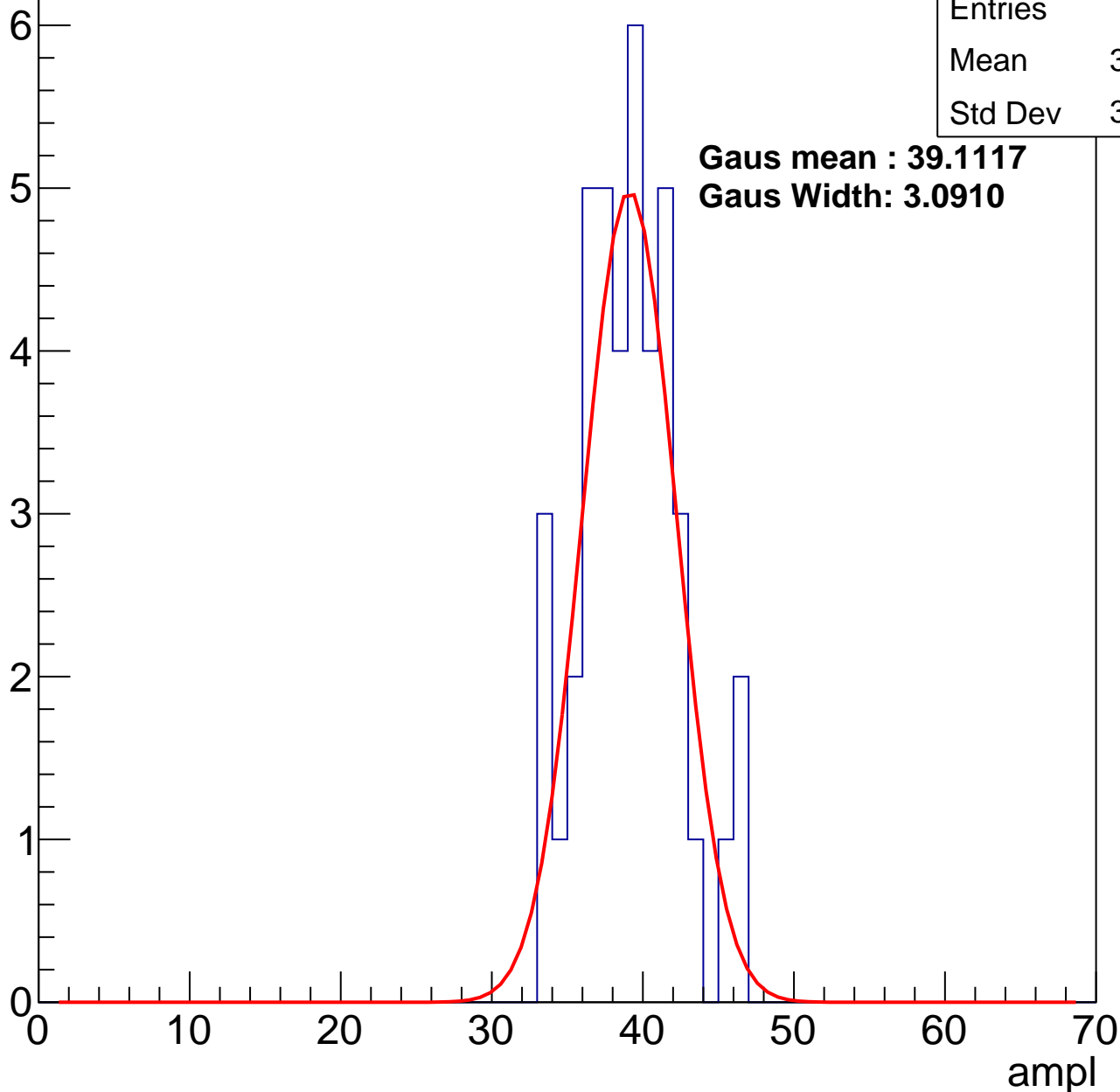
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	38.69
Std Dev	3.203

**Gaus mean : 39.1117**

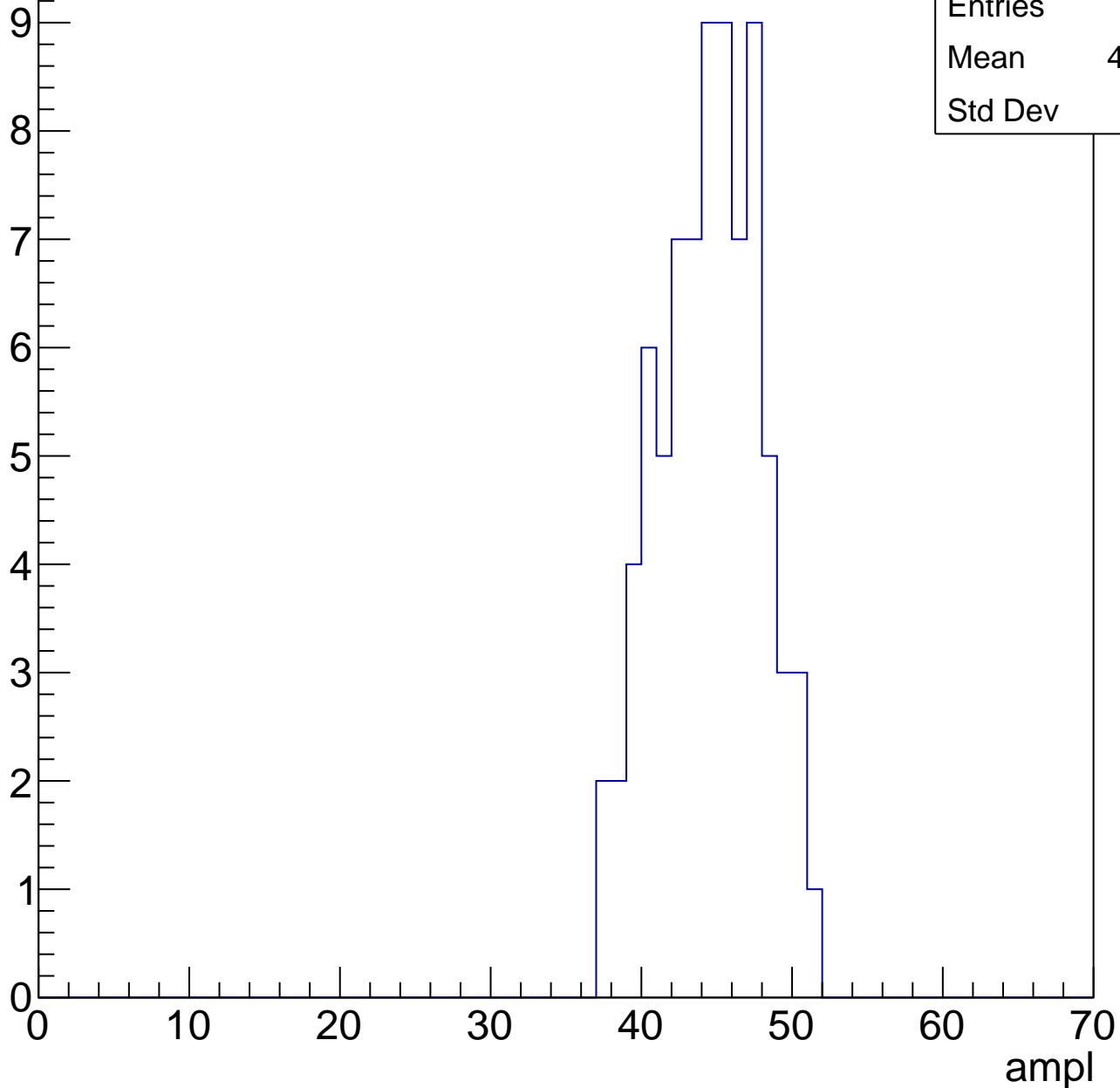
**Gaus Width: 3.0910**



# B1L002S, U17-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

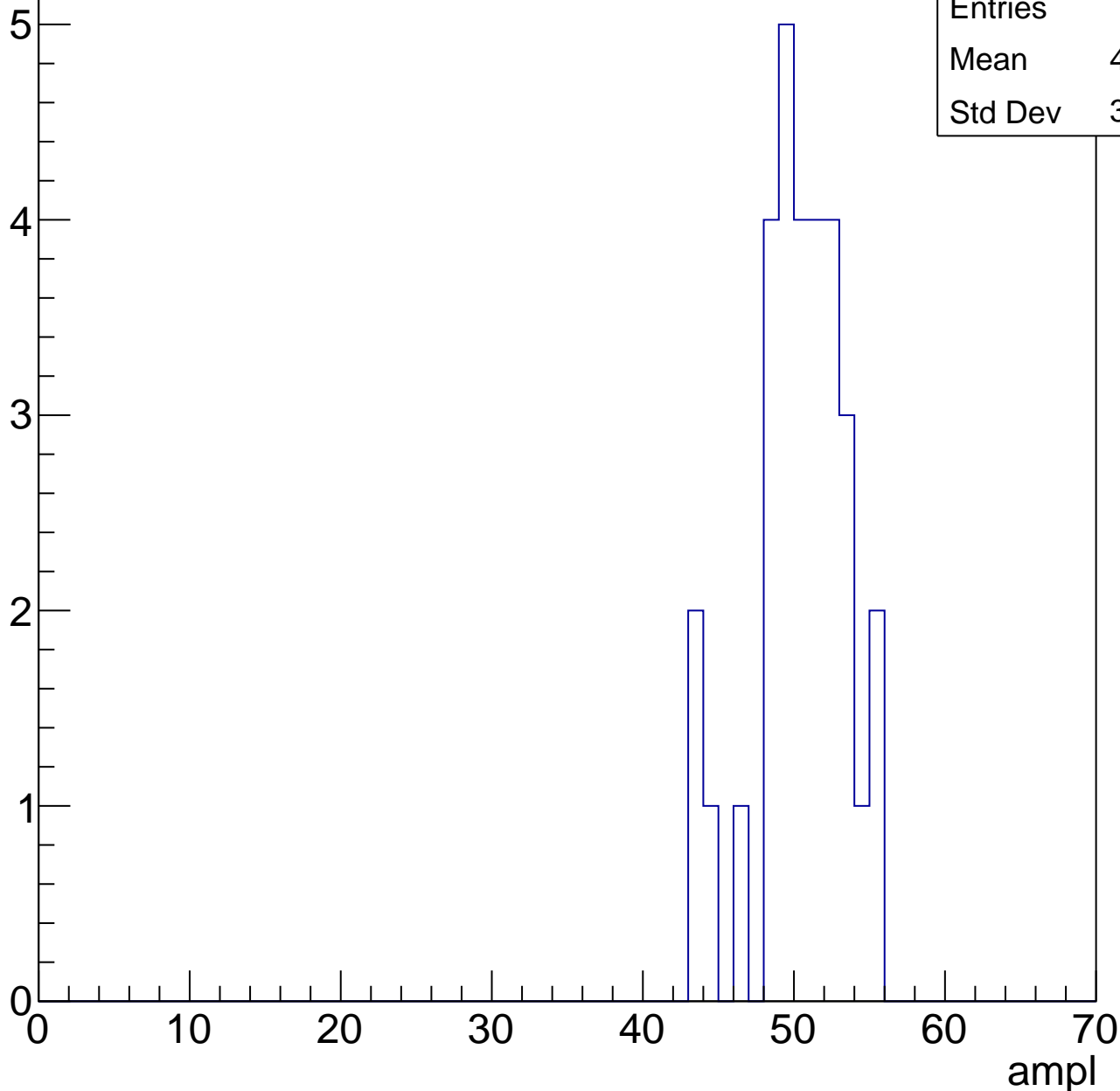


# B1L002S, U17-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	49.94
Std Dev	3.026



# B1L002S, U17-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

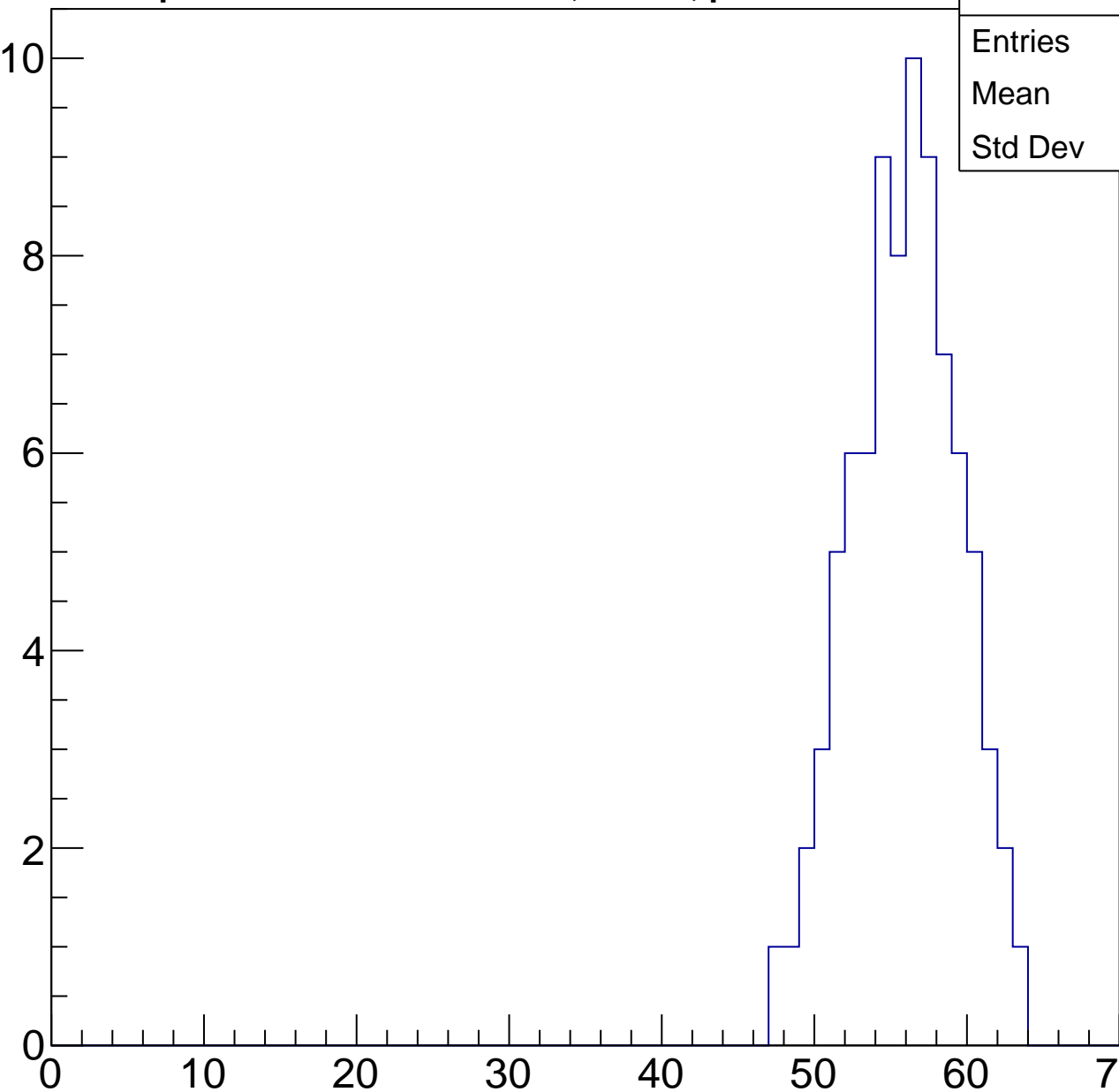
Entries	84
Mean	55.44
Std Dev	3.469

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

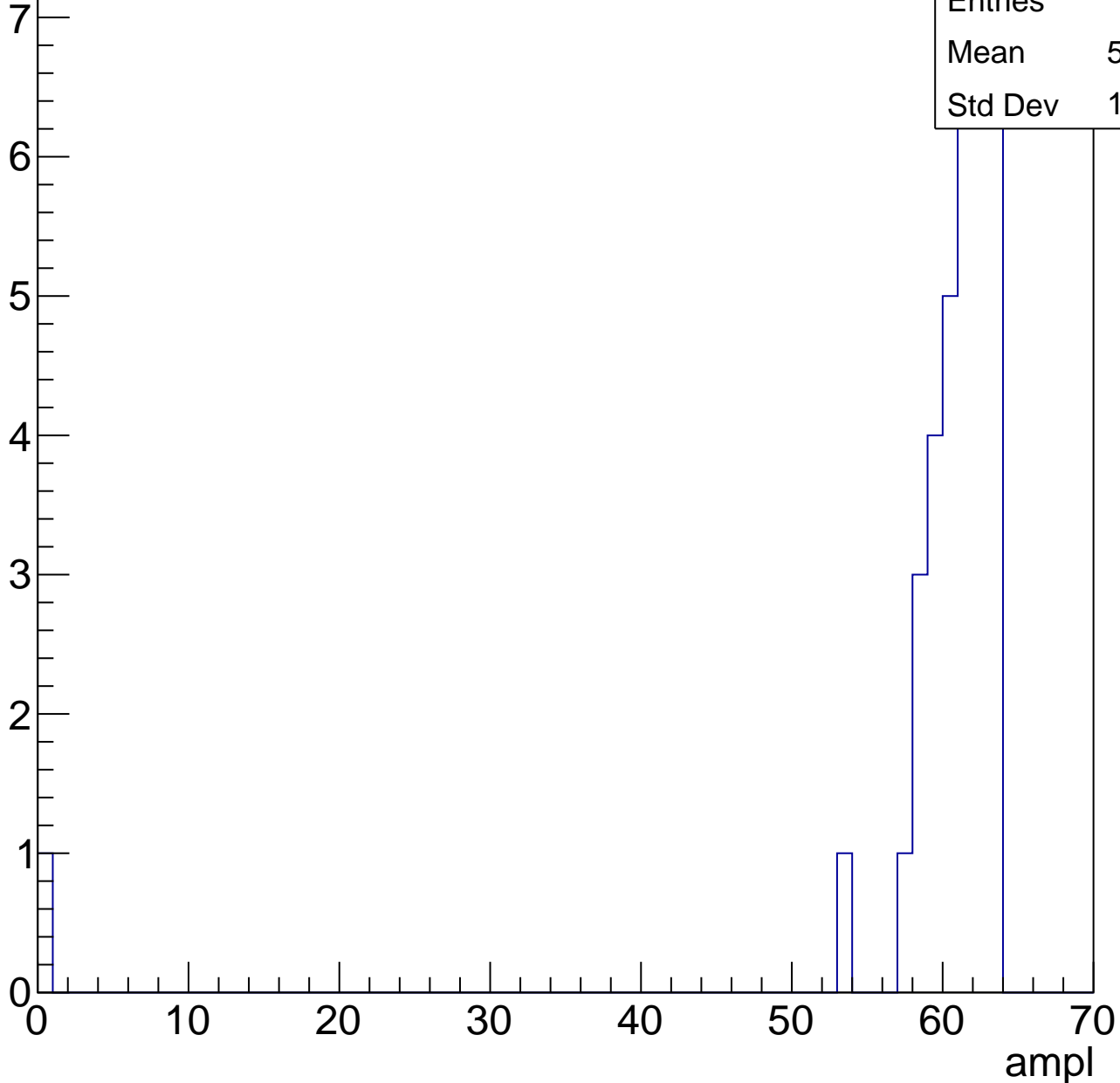


# B1L002S, U17-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.94
Std Dev	10.18

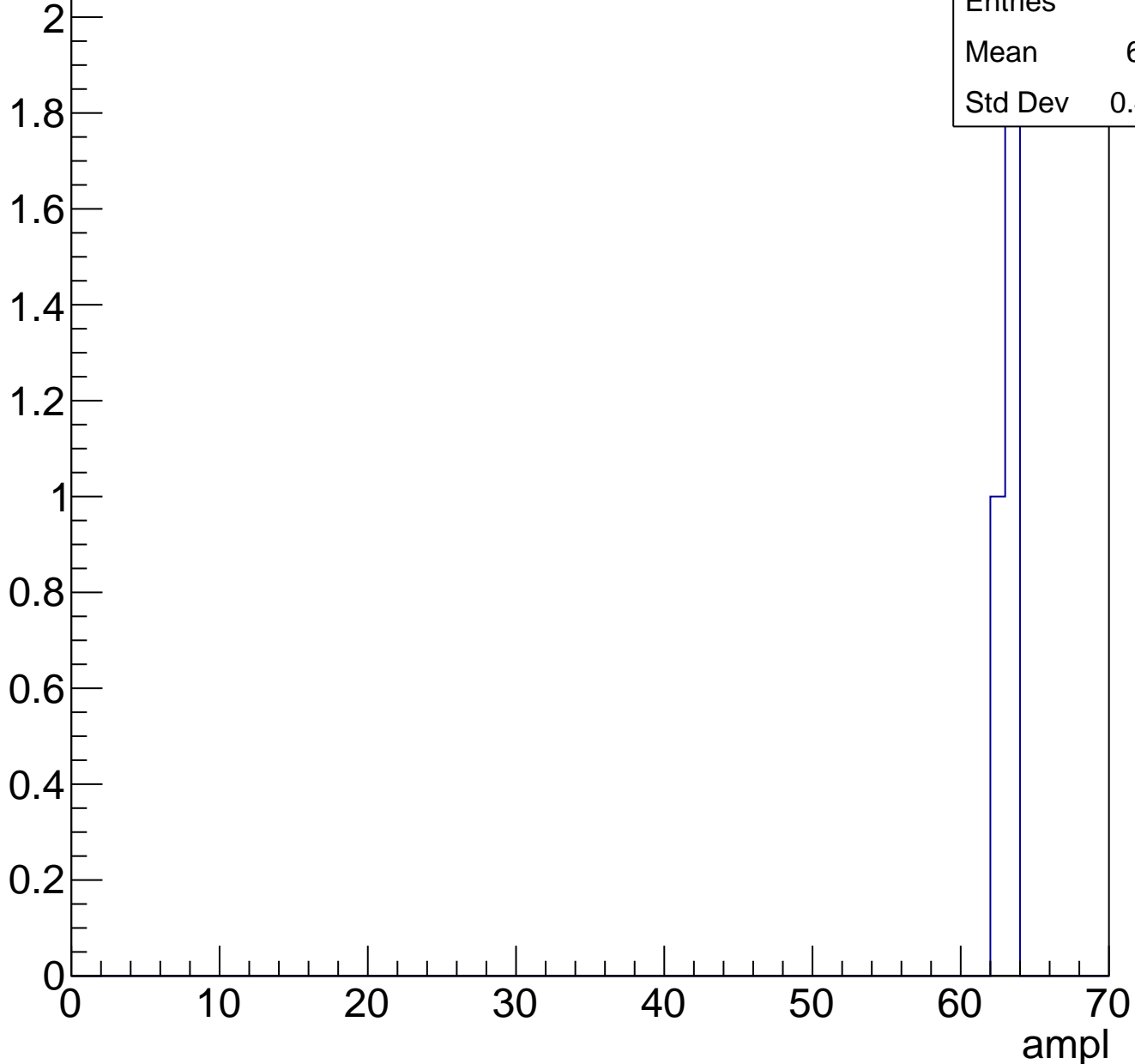




# B1L002S, U17-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

# B1L002S, U17-ch24, adc0

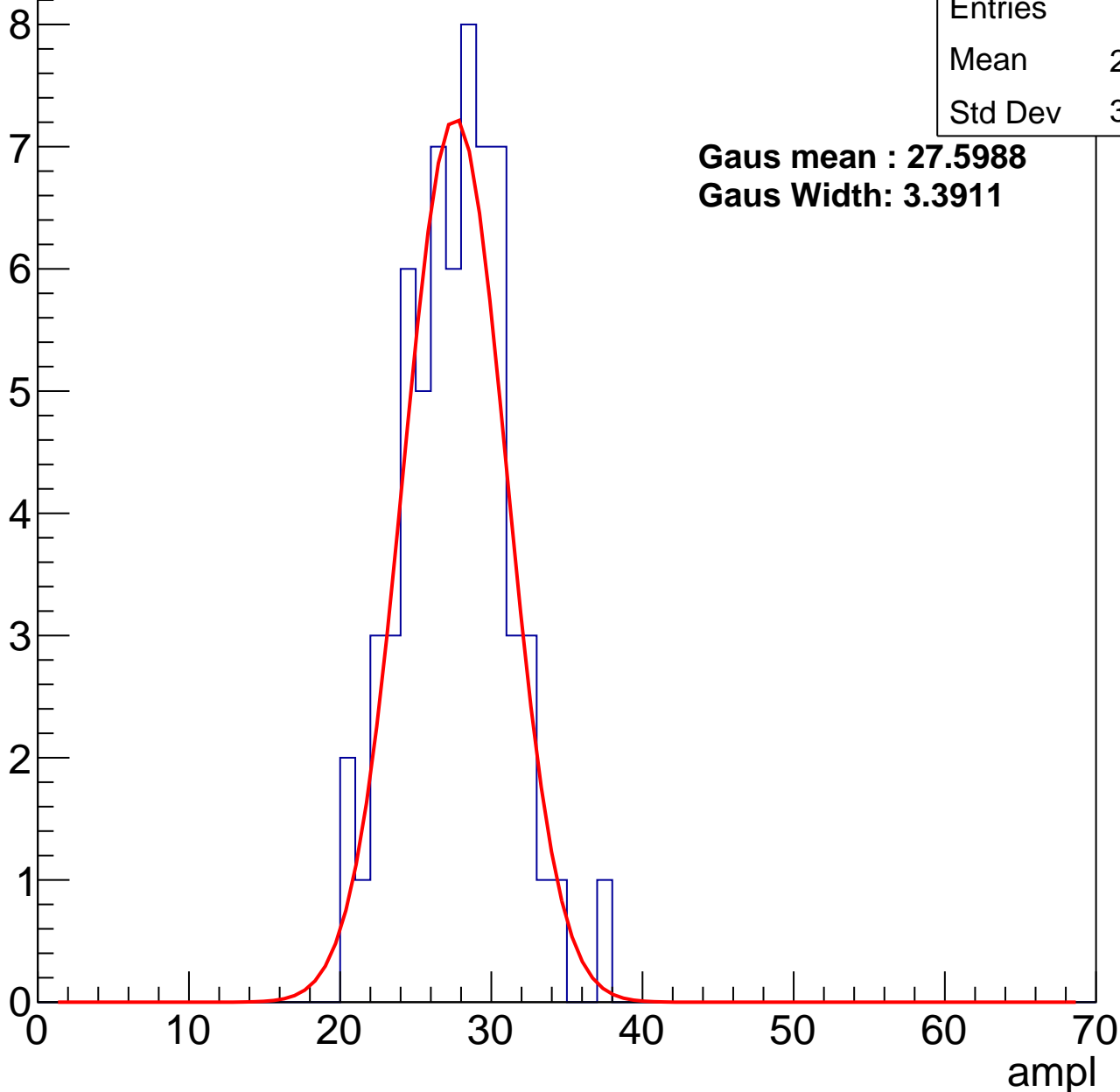
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.17
Std Dev	3.408

**Gaus mean : 27.5988**

**Gaus Width: 3.3911**



# B1L002S, U17-ch24, adc1

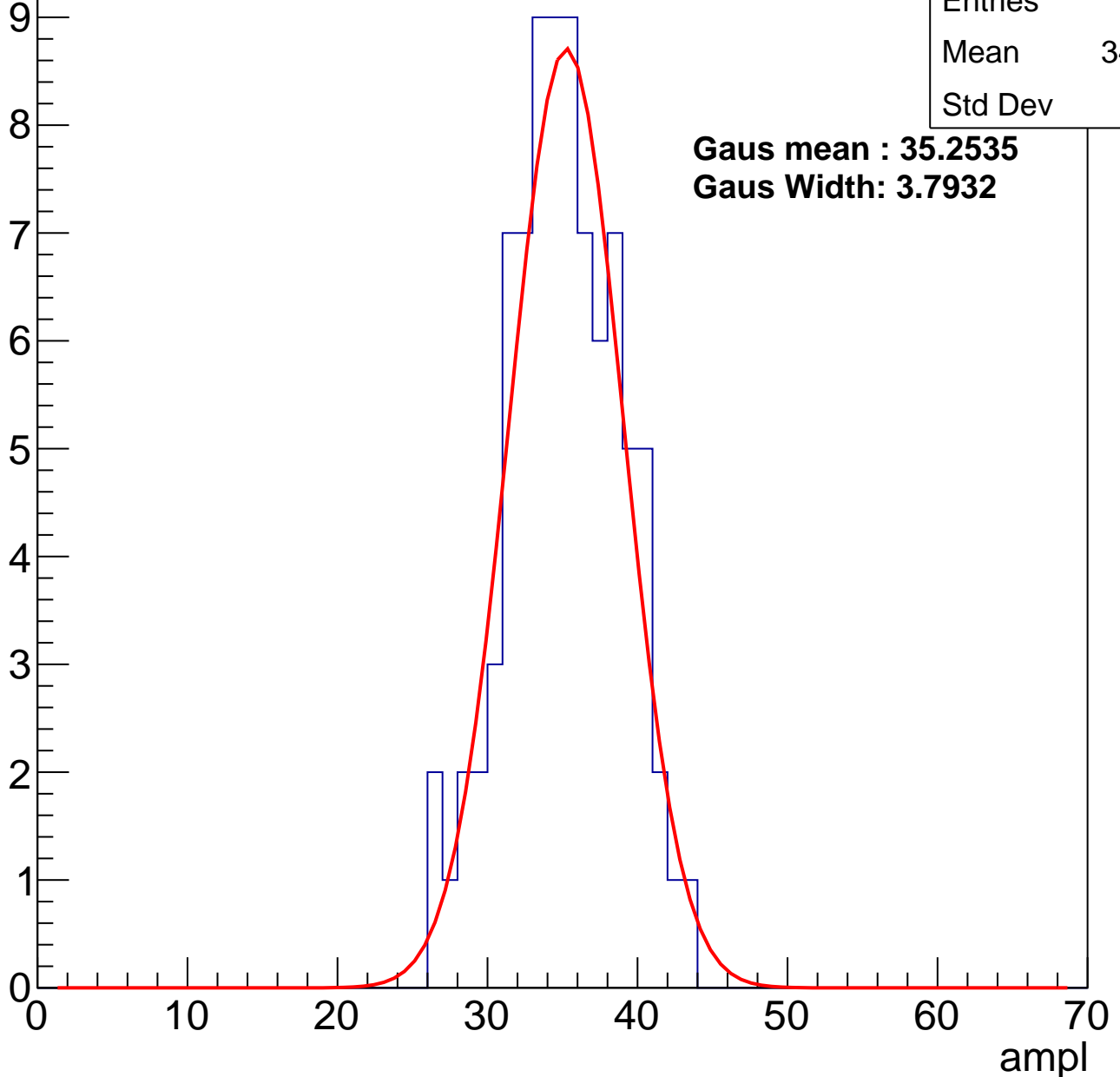
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	85
Mean	34.64
Std Dev	3.7

**Gaus mean : 35.2535**

**Gaus Width: 3.7932**



# B1L002S, U17-ch24, adc2

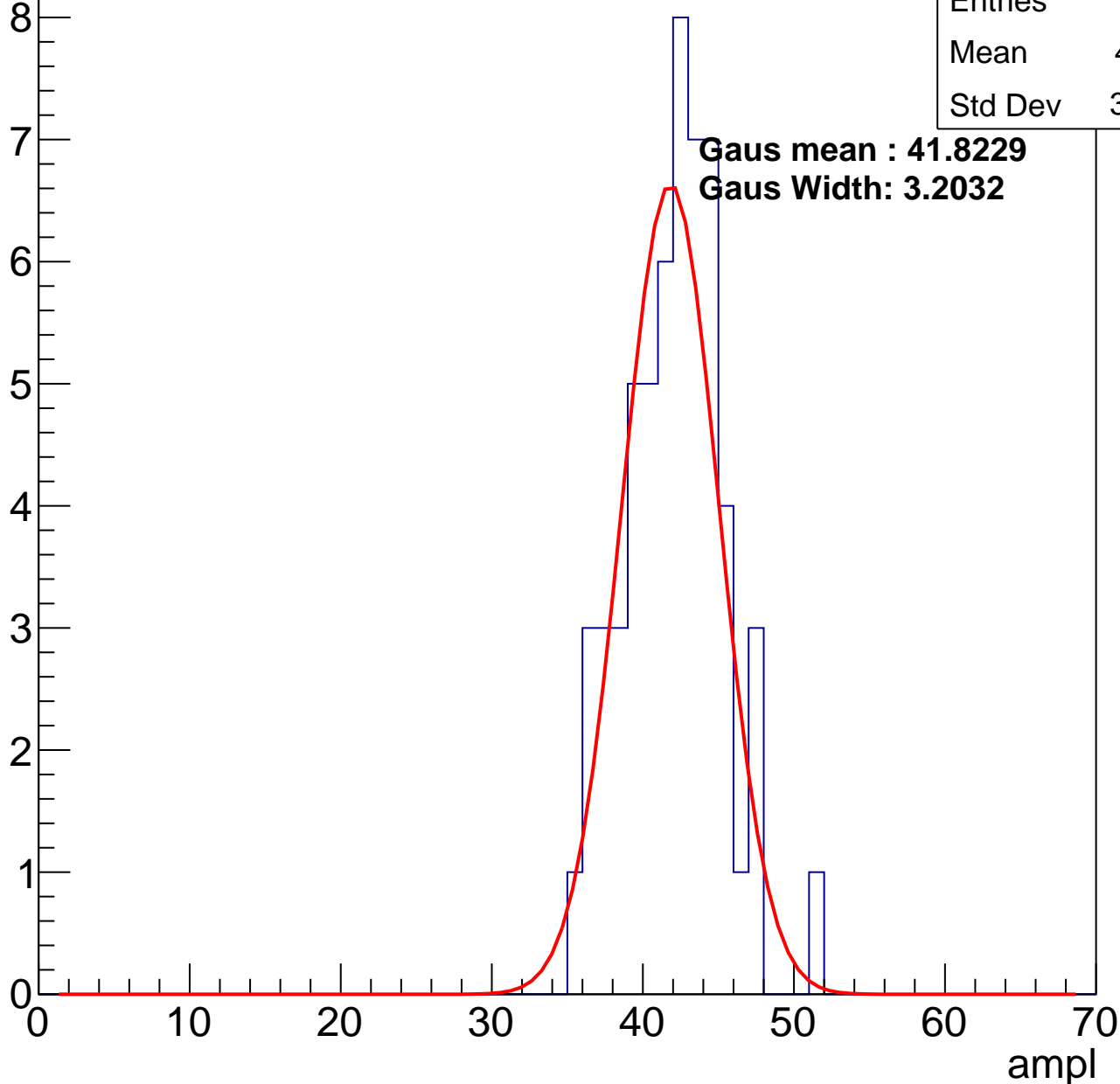
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.61
Std Dev	3.205

**Gaus mean : 41.8229**

**Gaus Width: 3.2032**

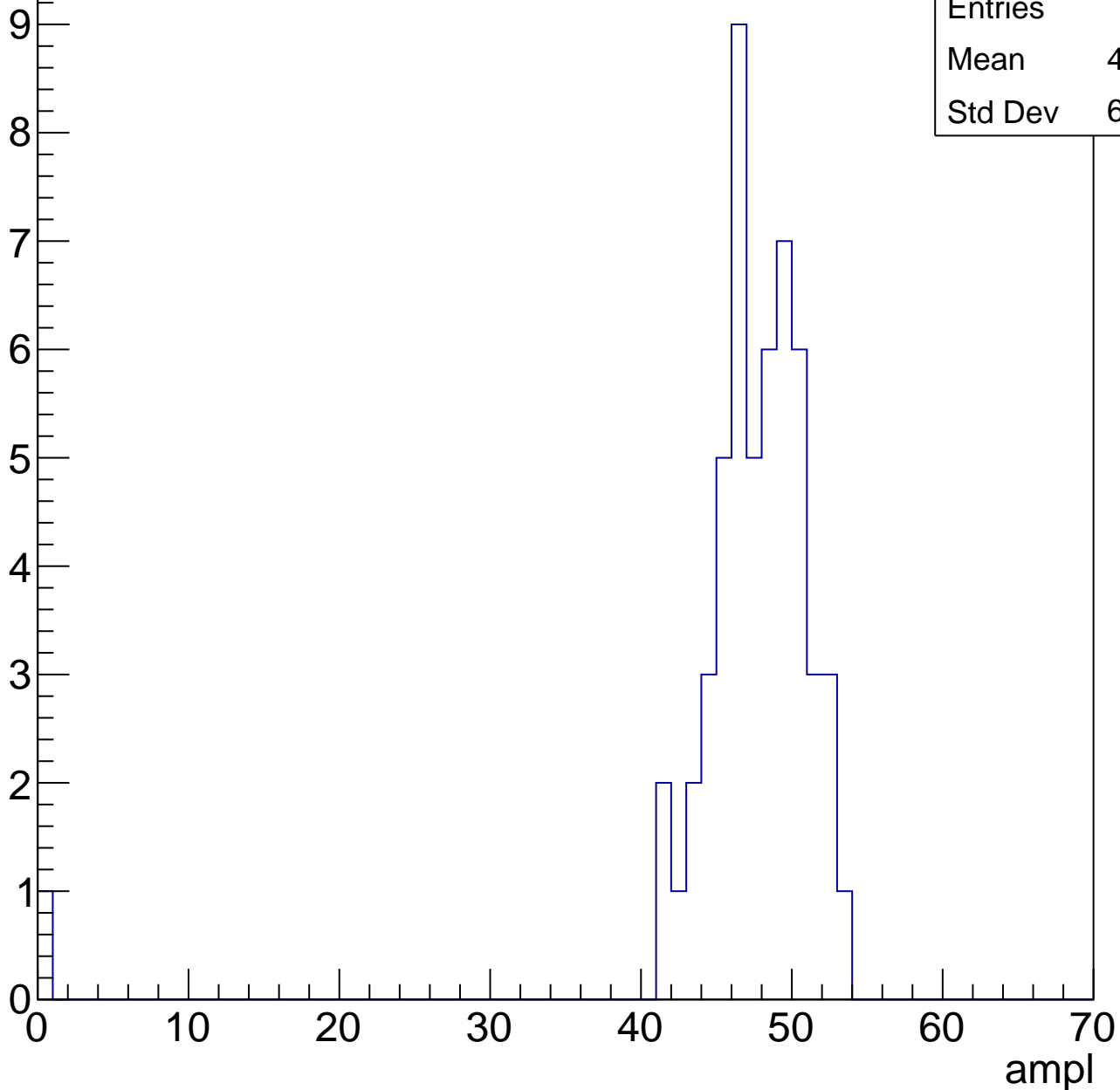


# B1L002S, U17-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	46.46
Std Dev	6.973

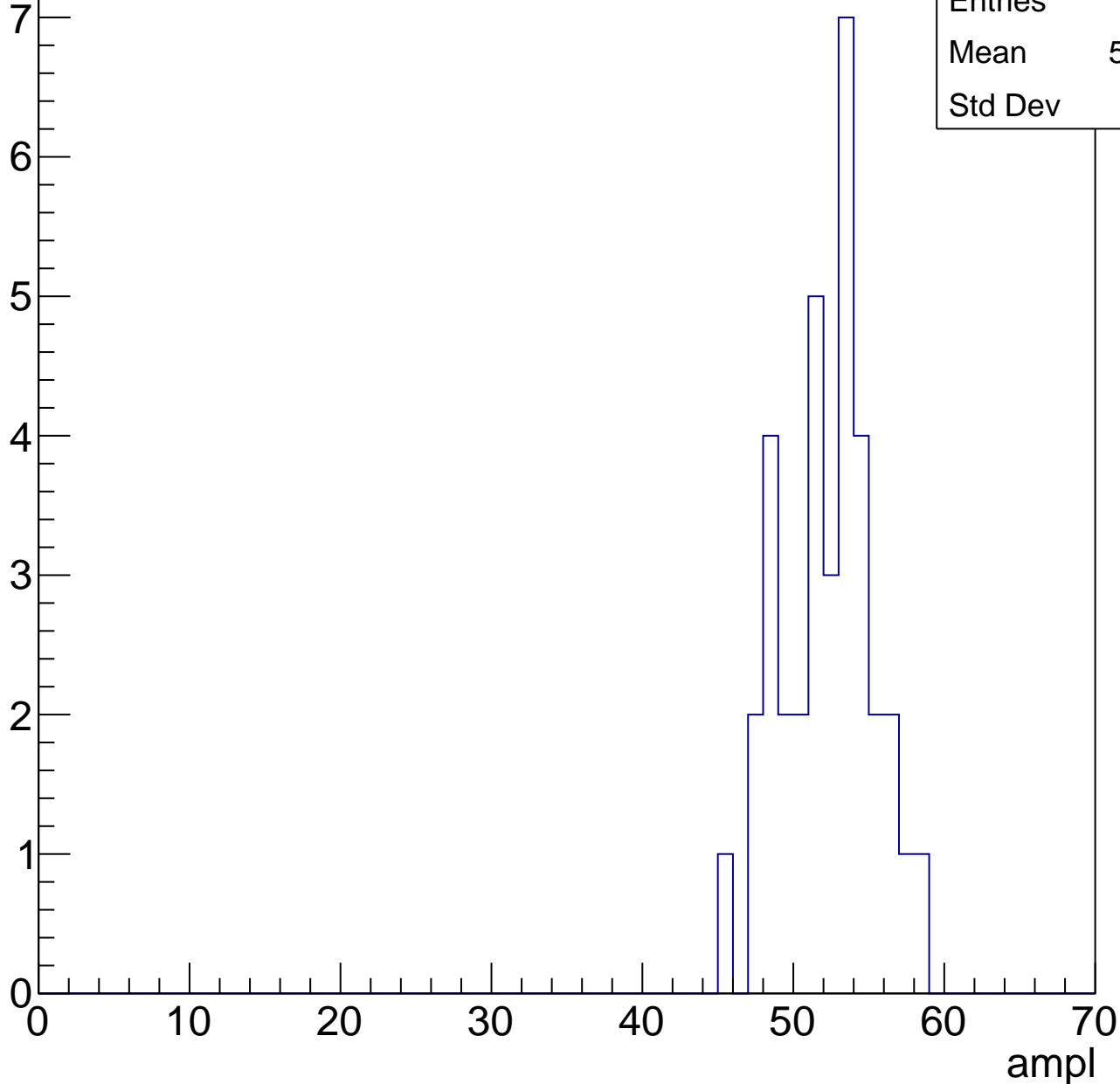


# B1L002S, U17-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	51.78
Std Dev	3.01

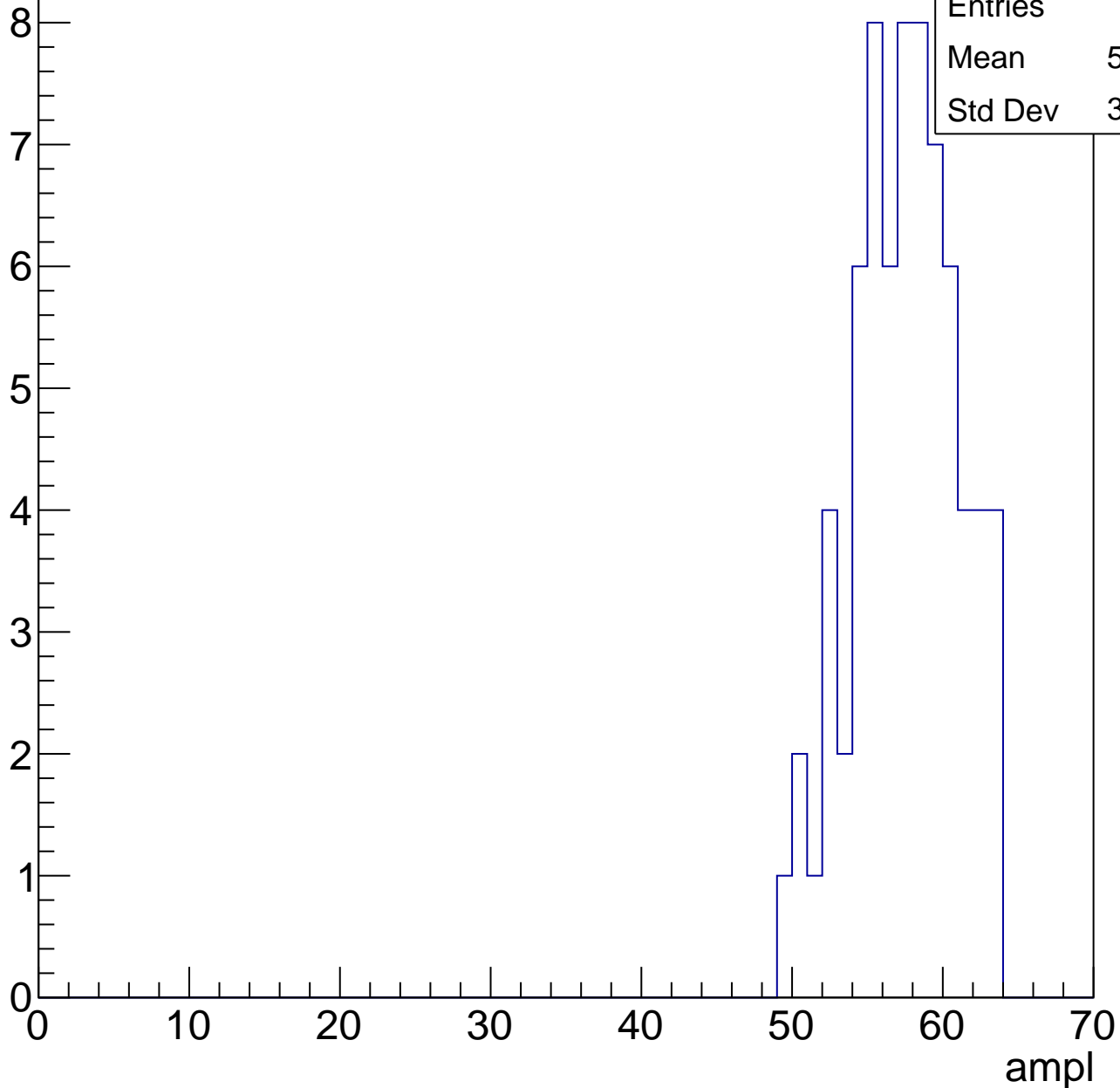


# B1L002S, U17-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	57.06
Std Dev	3.398

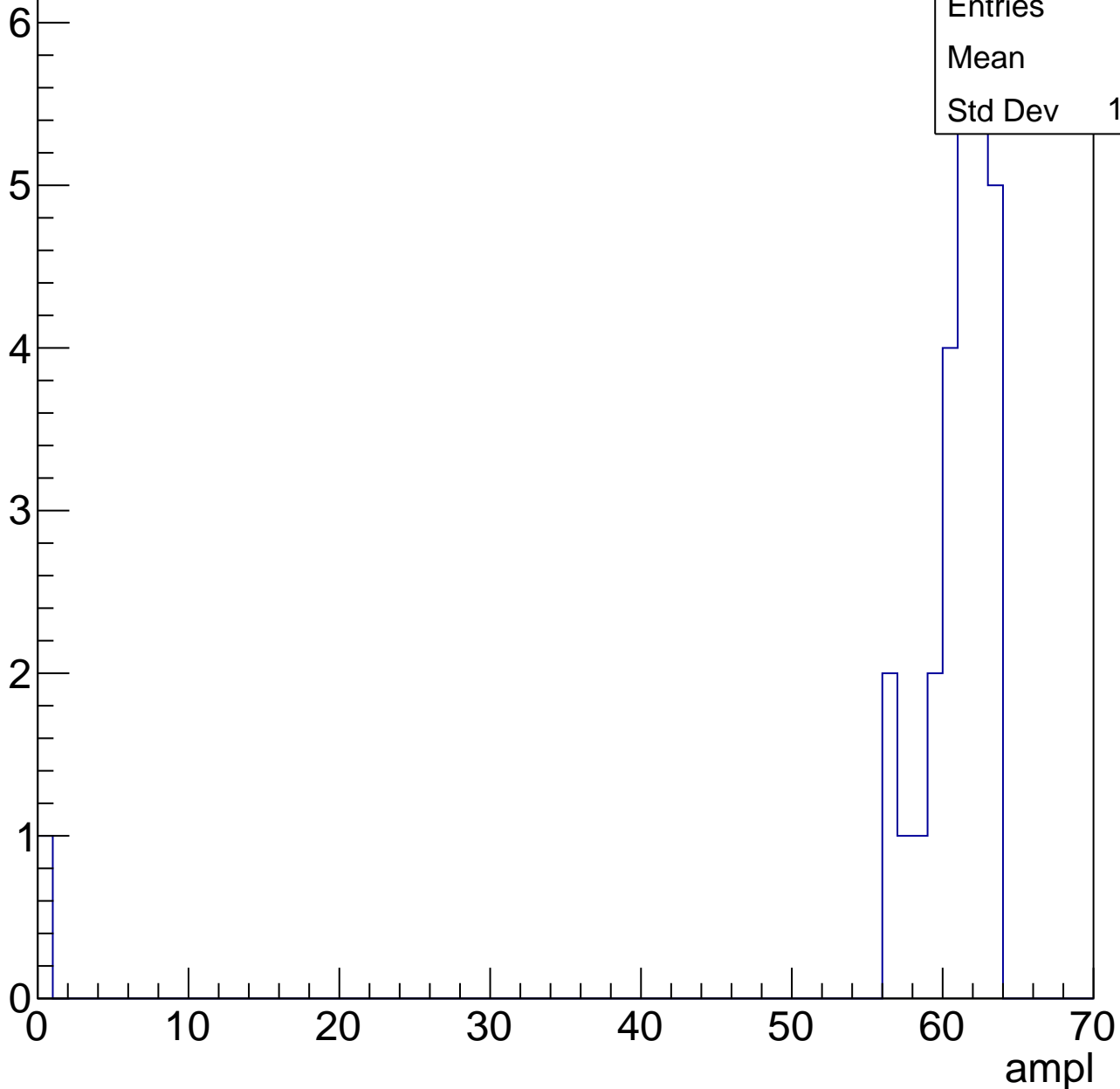


# B1L002S, U17-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.5
Std Dev	11.43

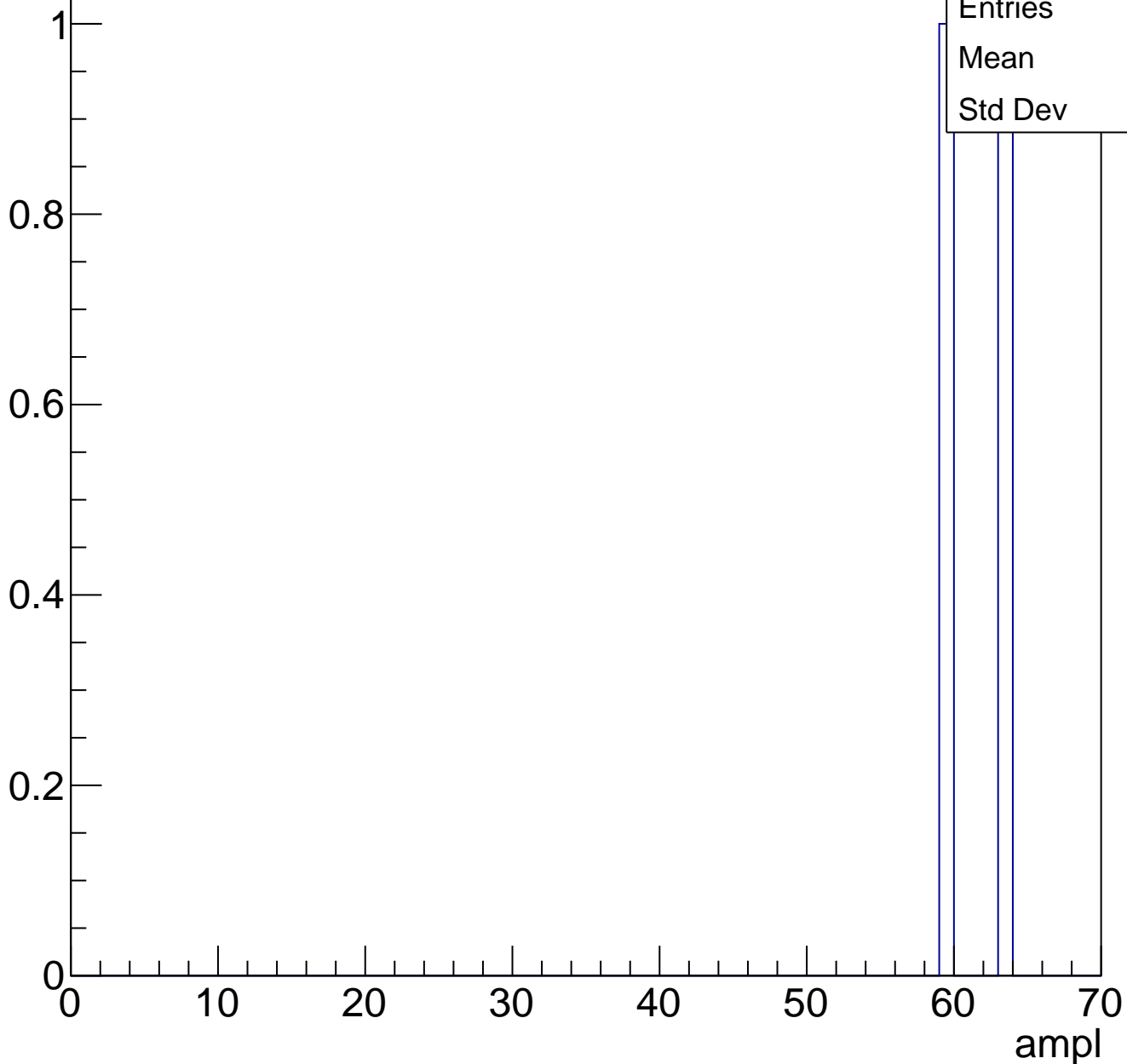




# B1L002S, U17-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch25, adc0

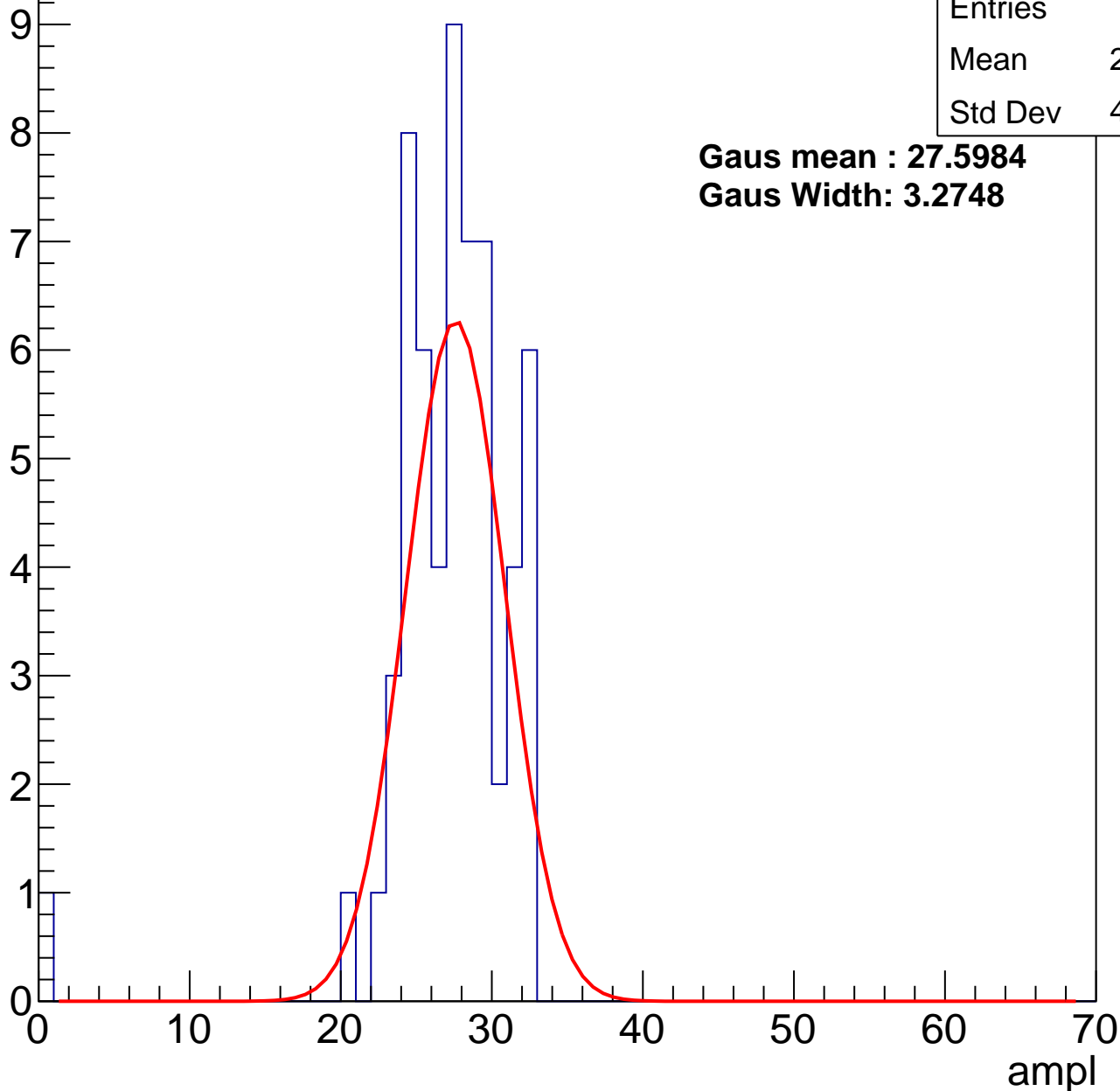
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	26.69
Std Dev	4.533

**Gaus mean : 27.5984**

**Gaus Width: 3.2748**



# B1L002S, U17-ch25, adc1

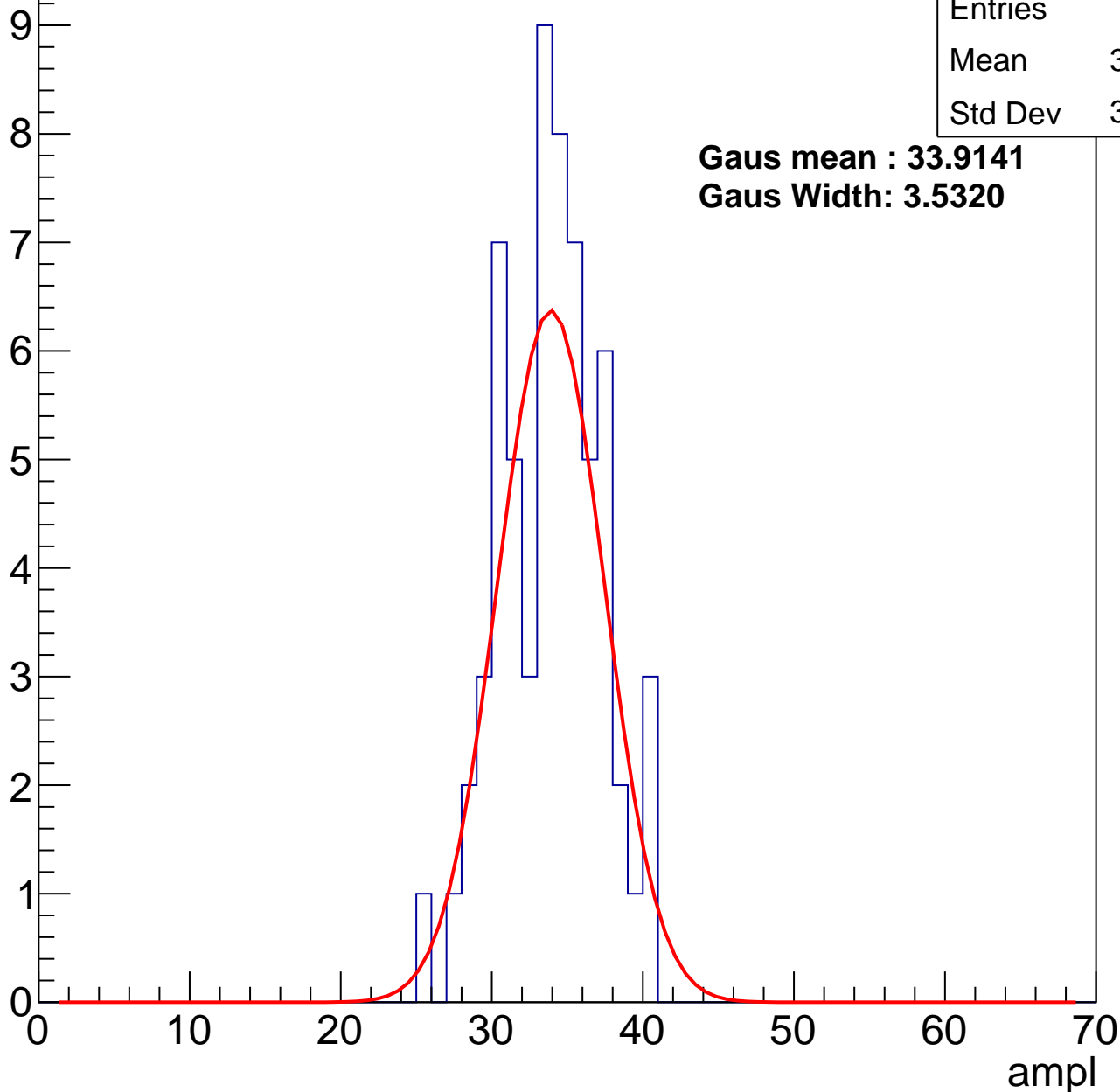
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.44
Std Dev	3.289

**Gaus mean : 33.9141**

**Gaus Width: 3.5320**



# B1L002S, U17-ch25, adc2

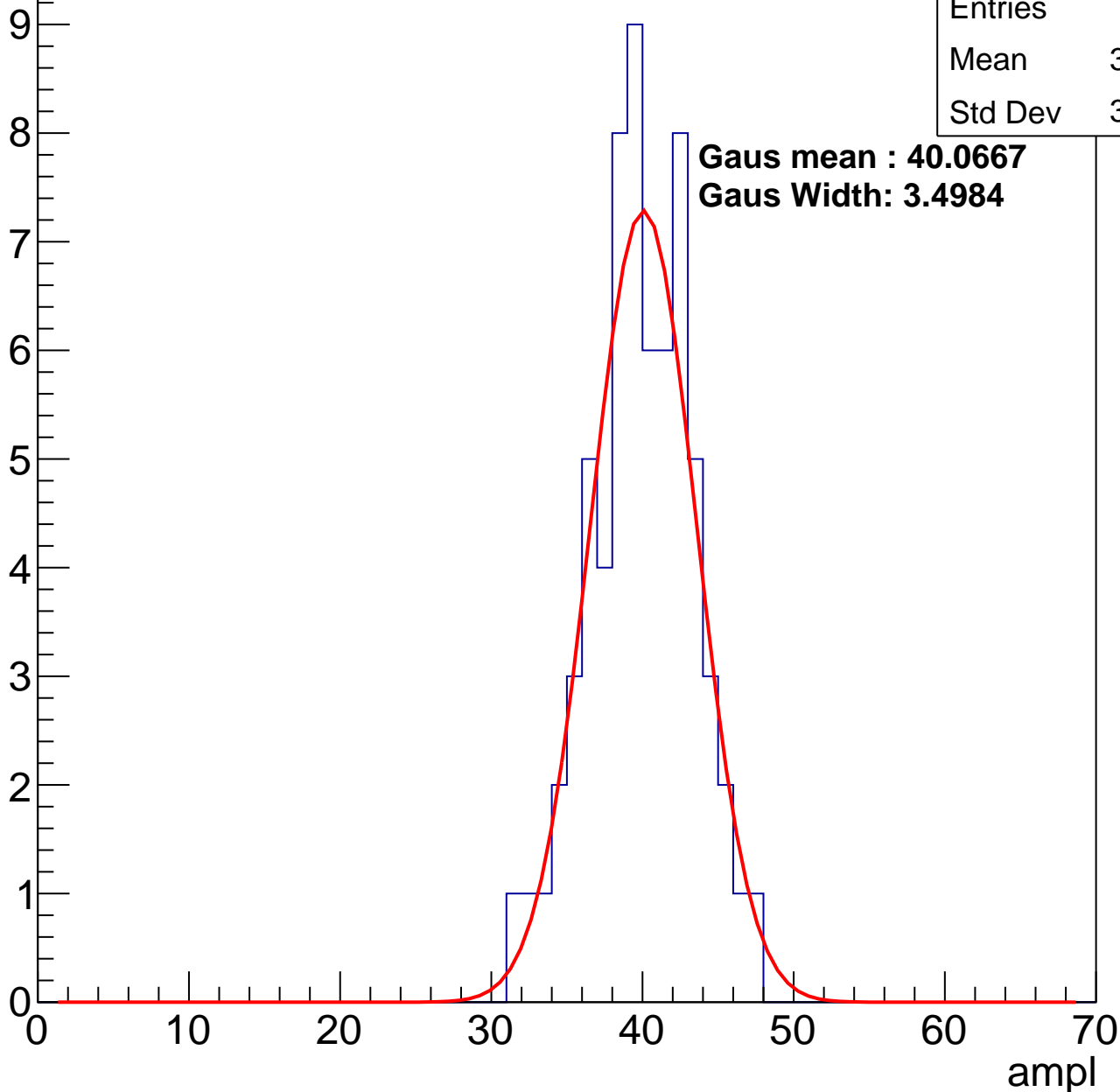
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	39.45
Std Dev	3.358

**Gaus mean : 40.0667**

**Gaus Width: 3.4984**

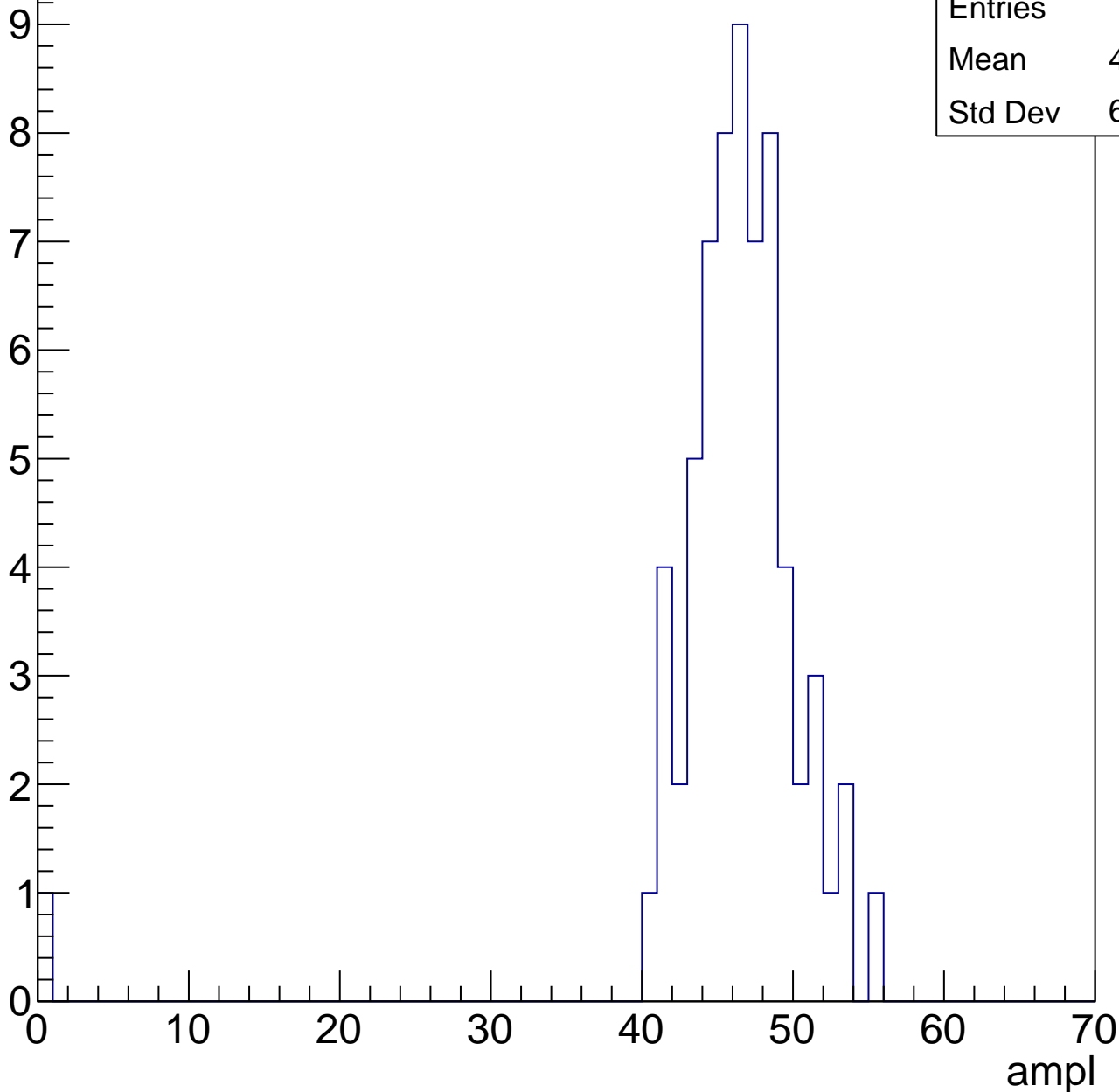


# B1L002S, U17-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

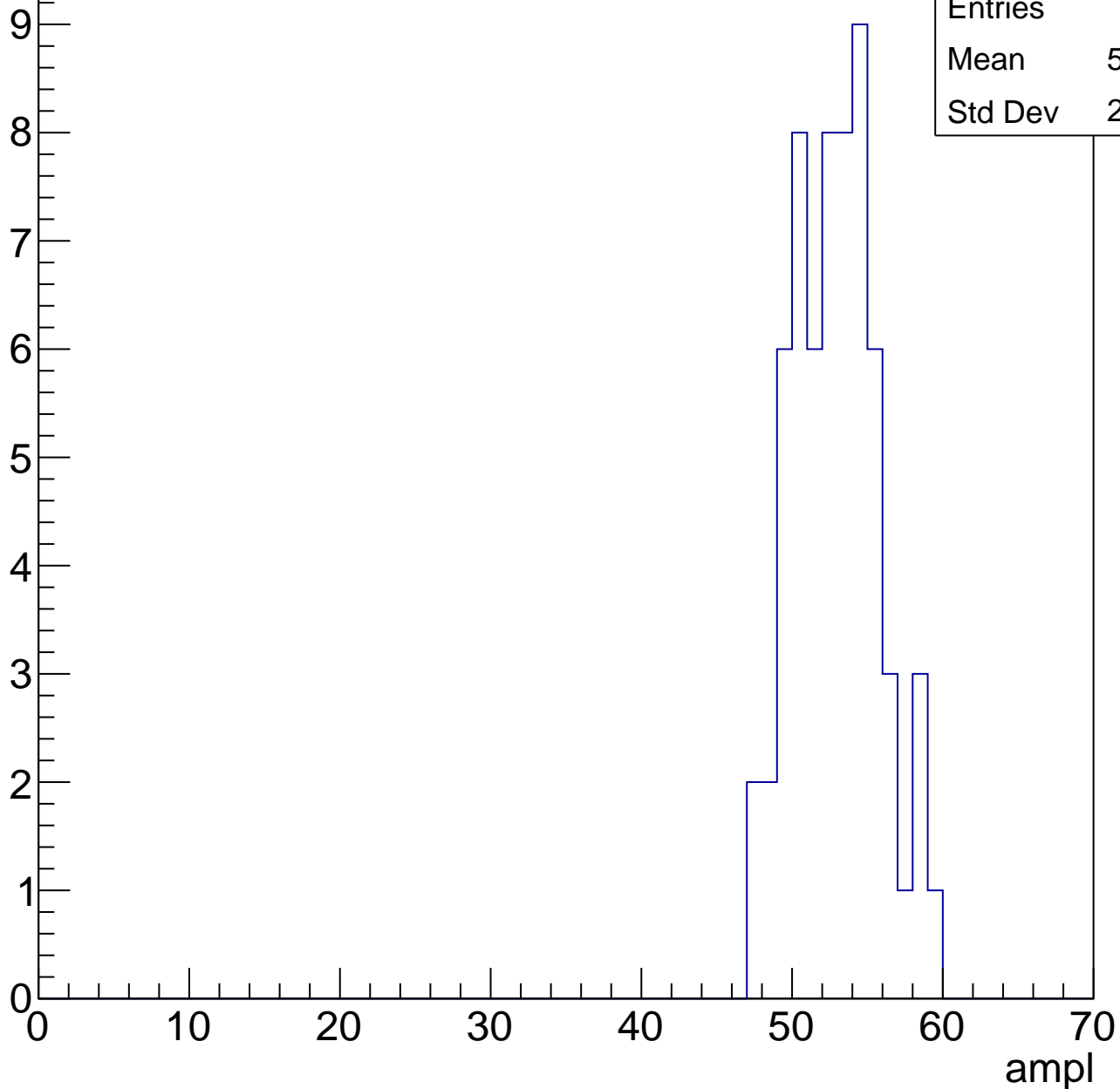
Entries	65
Mean	45.54
Std Dev	6.502



# B1L002S, U17-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

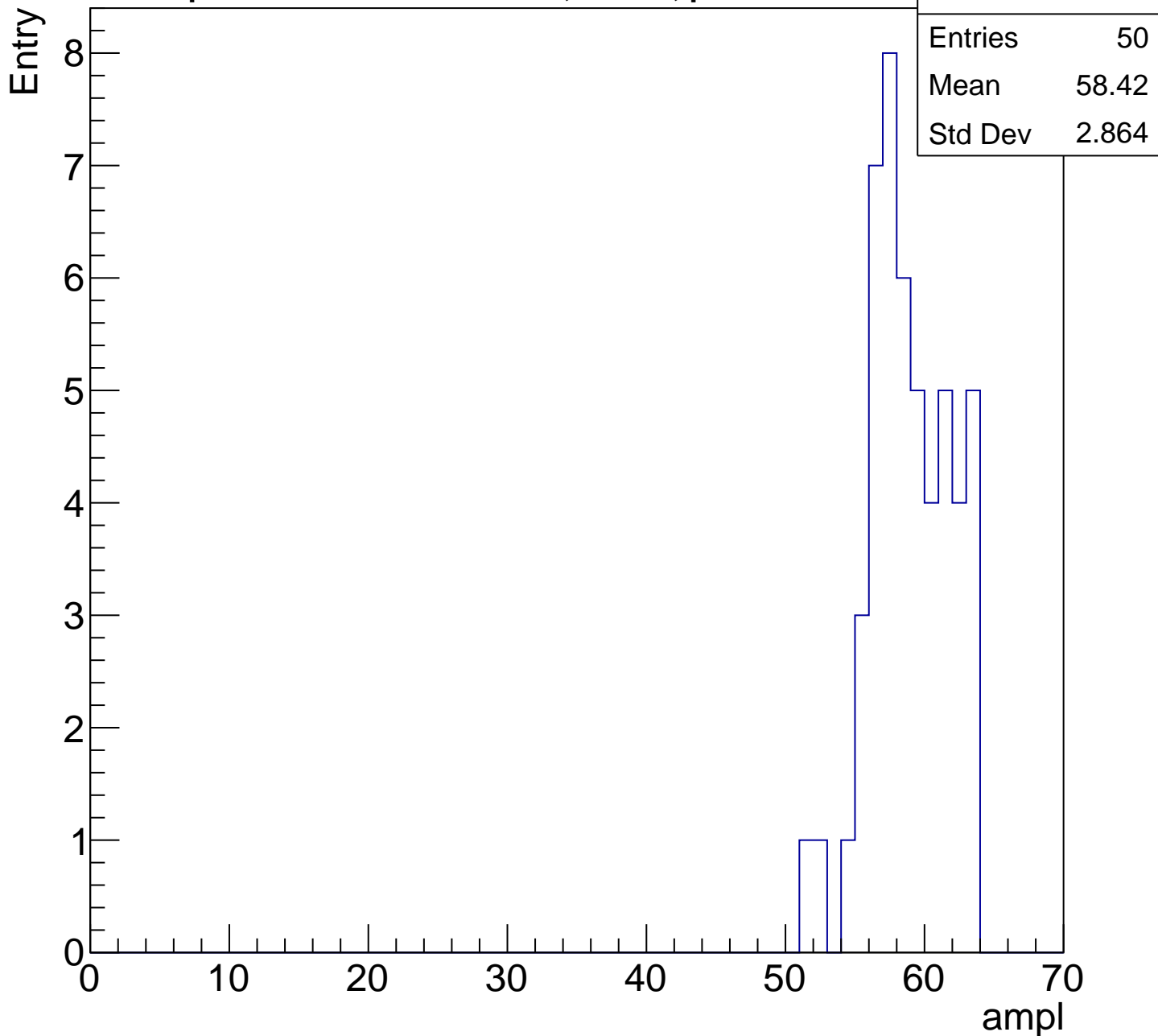
Entry



Entries	63
Mean	52.44
Std Dev	2.788

# B1L002S, U17-ch25, adc5

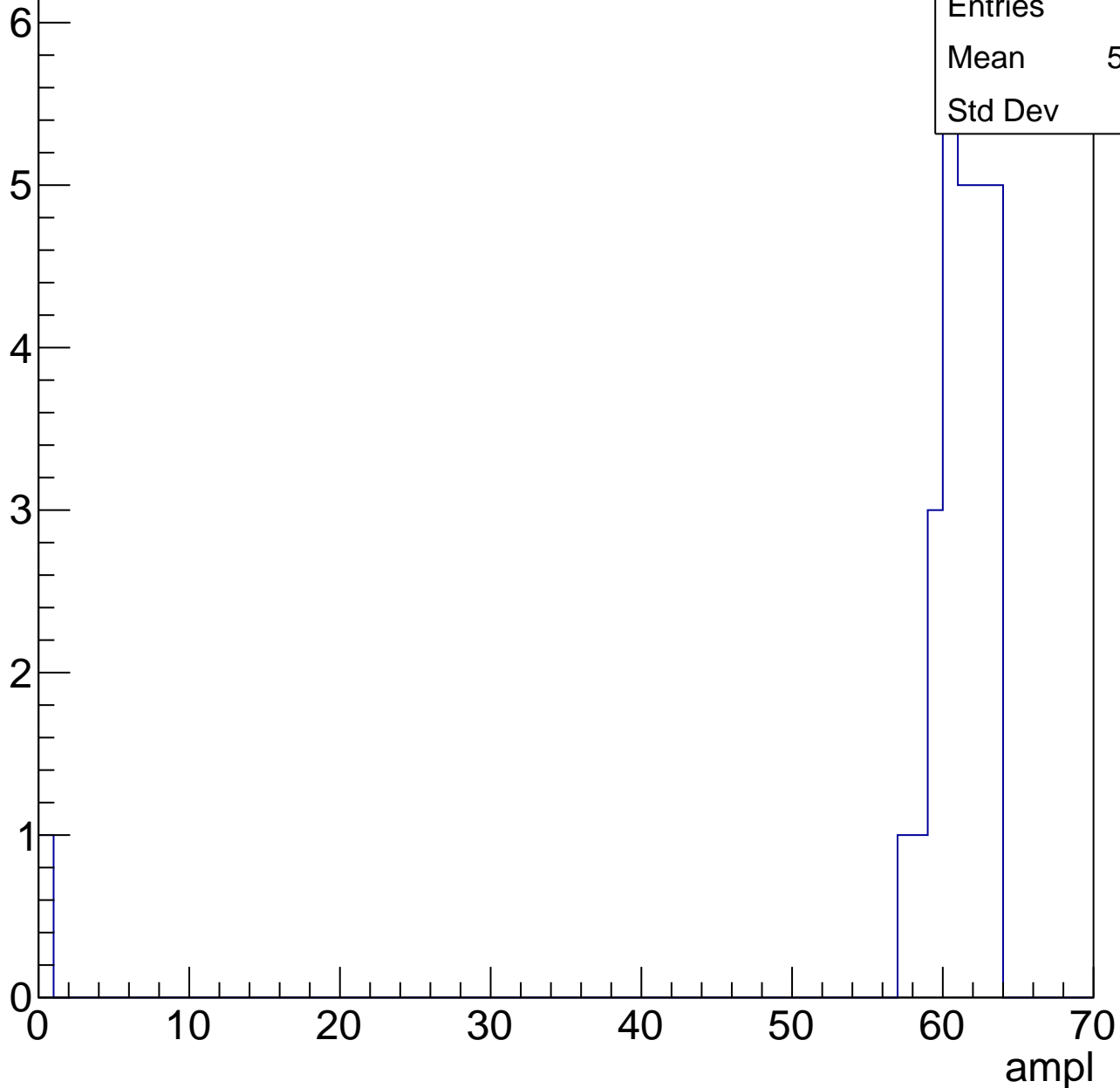
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U17-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



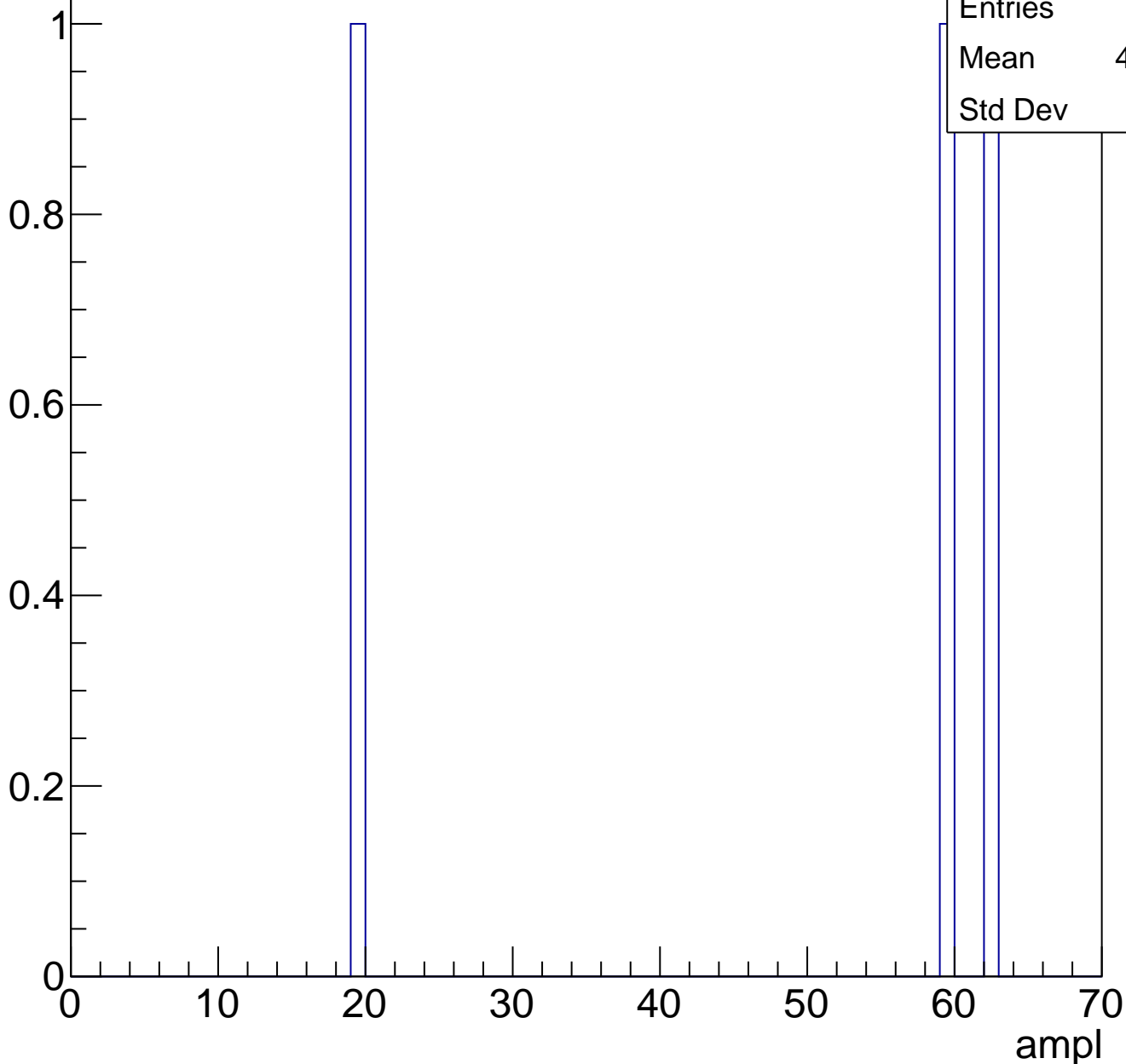
Entries	27
Mean	58.59
Std Dev	11.6



# B1L002S, U17-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch26, adc0

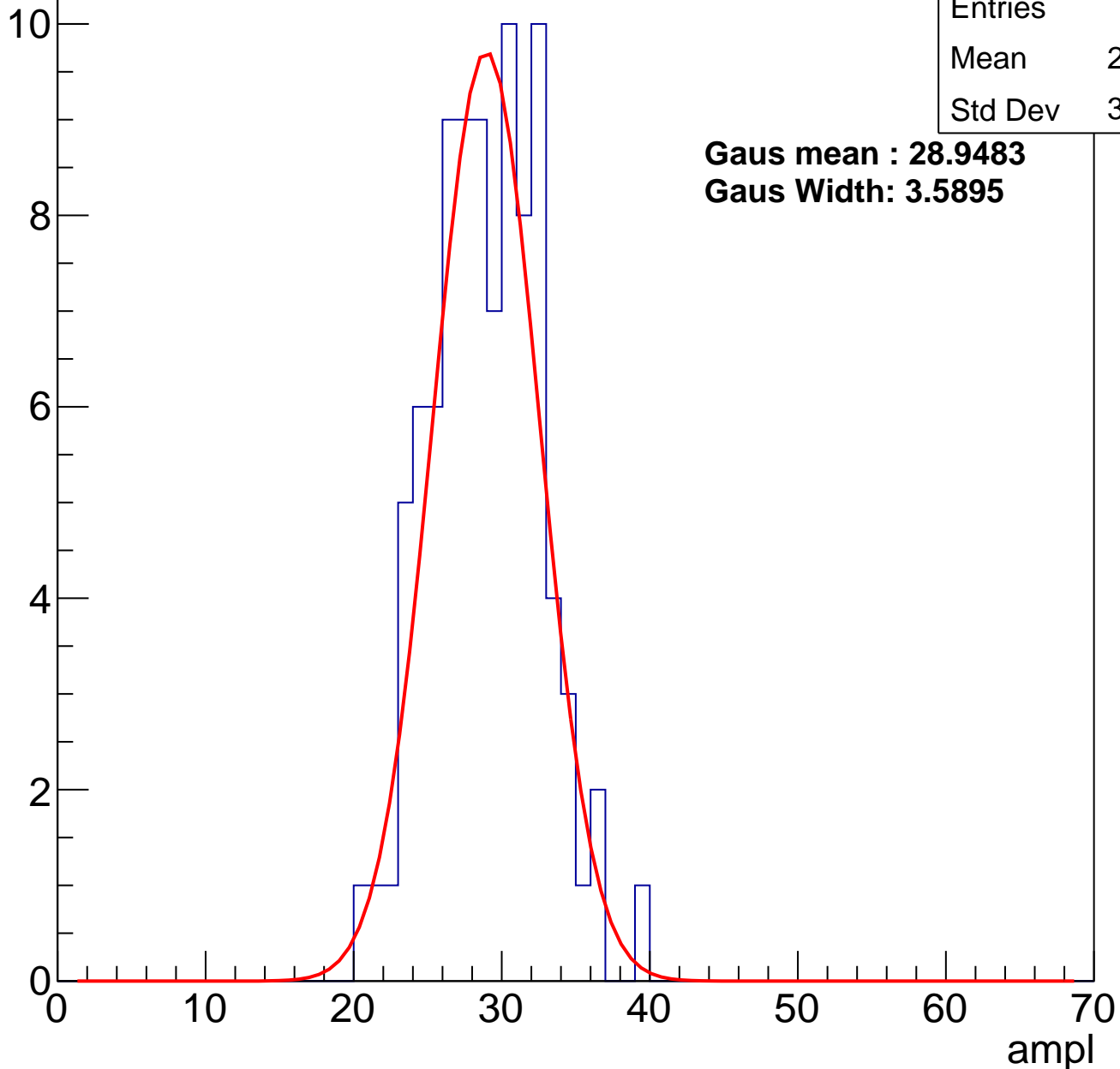
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	93
Mean	28.52
Std Dev	3.636

**Gaus mean : 28.9483**

**Gaus Width: 3.5895**

Entry



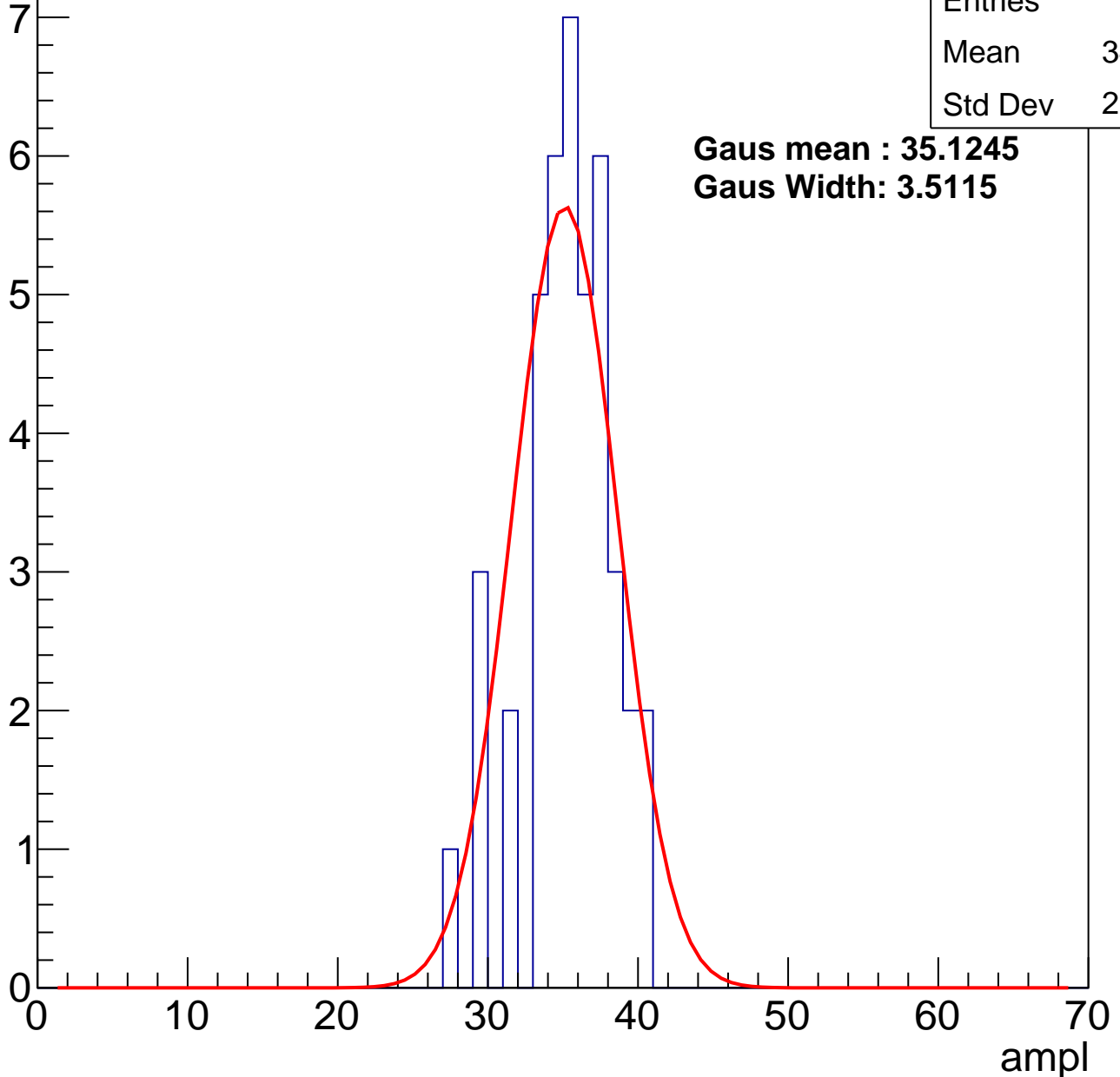
# B1L002S, U17-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	34.86
Std Dev	2.957

**Gaus mean : 35.1245**  
**Gaus Width: 3.5115**



# B1L002S, U17-ch26, adc2

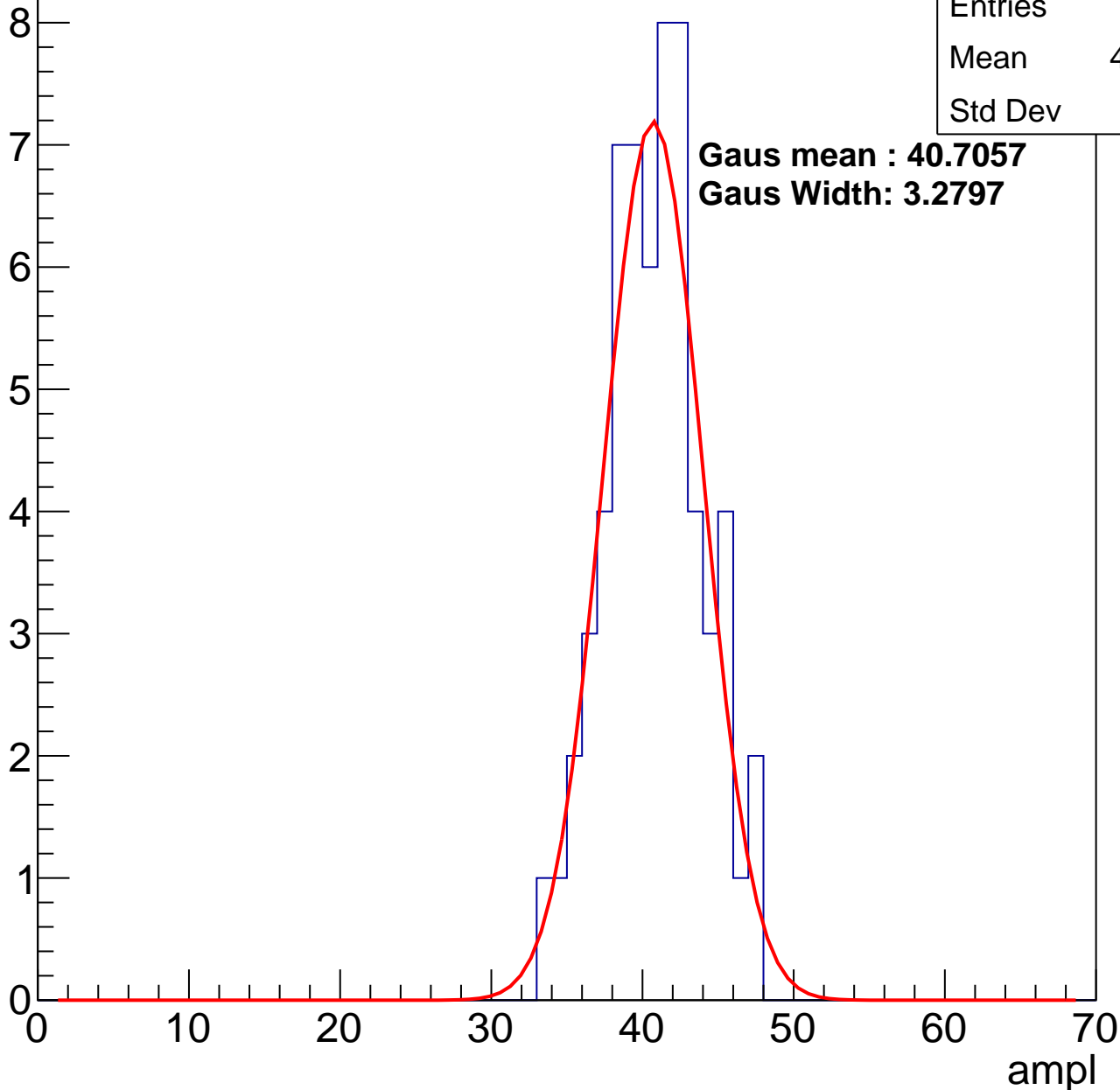
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.33
Std Dev	3.15

**Gaus mean : 40.7057**

**Gaus Width: 3.2797**

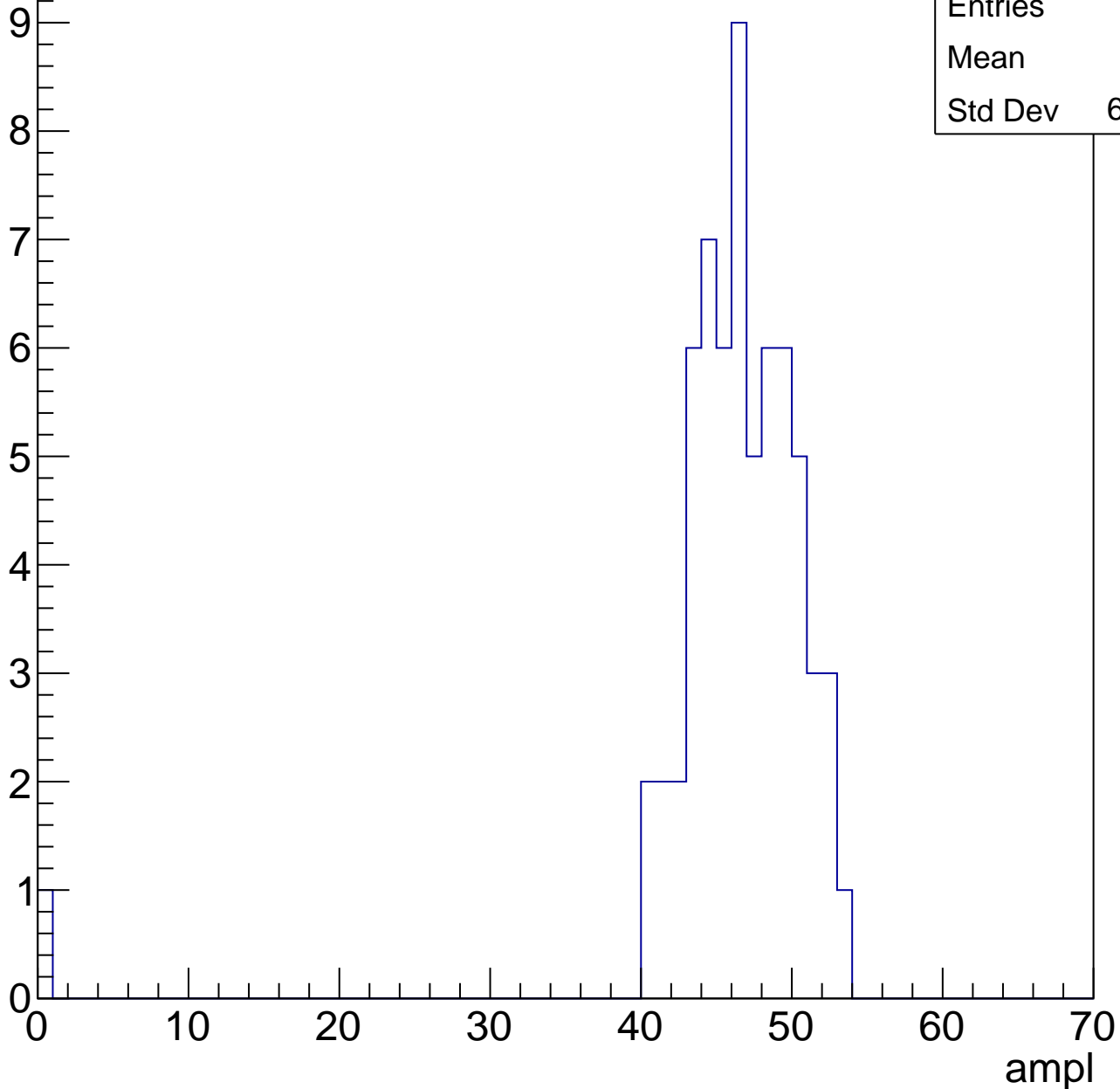


# B1L002S, U17-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

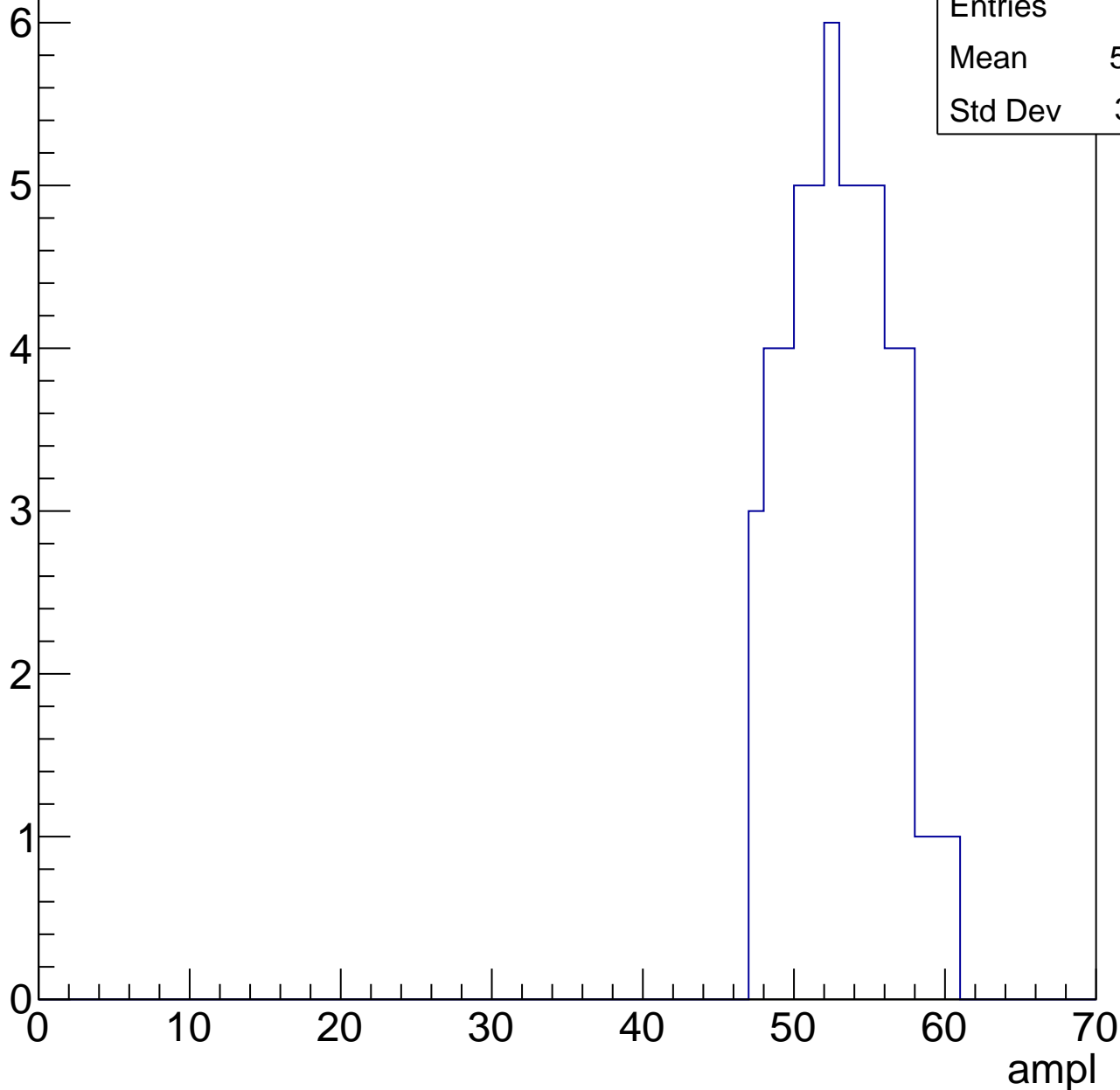
Entries	64
Mean	45.7
Std Dev	6.552



# B1L002S, U17-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

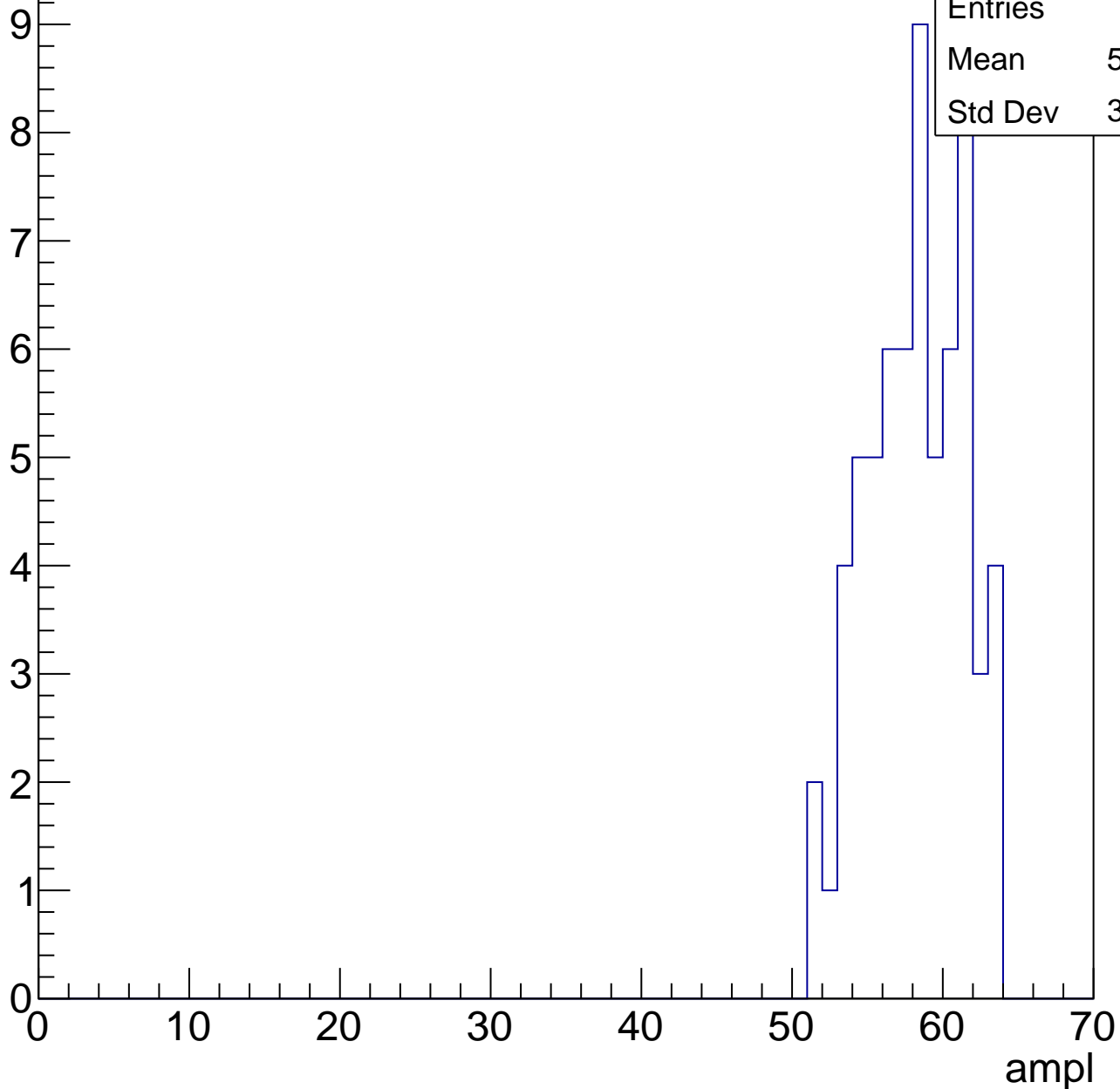


Entries	53
Mean	52.55
Std Dev	3.271

# B1L002S, U17-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

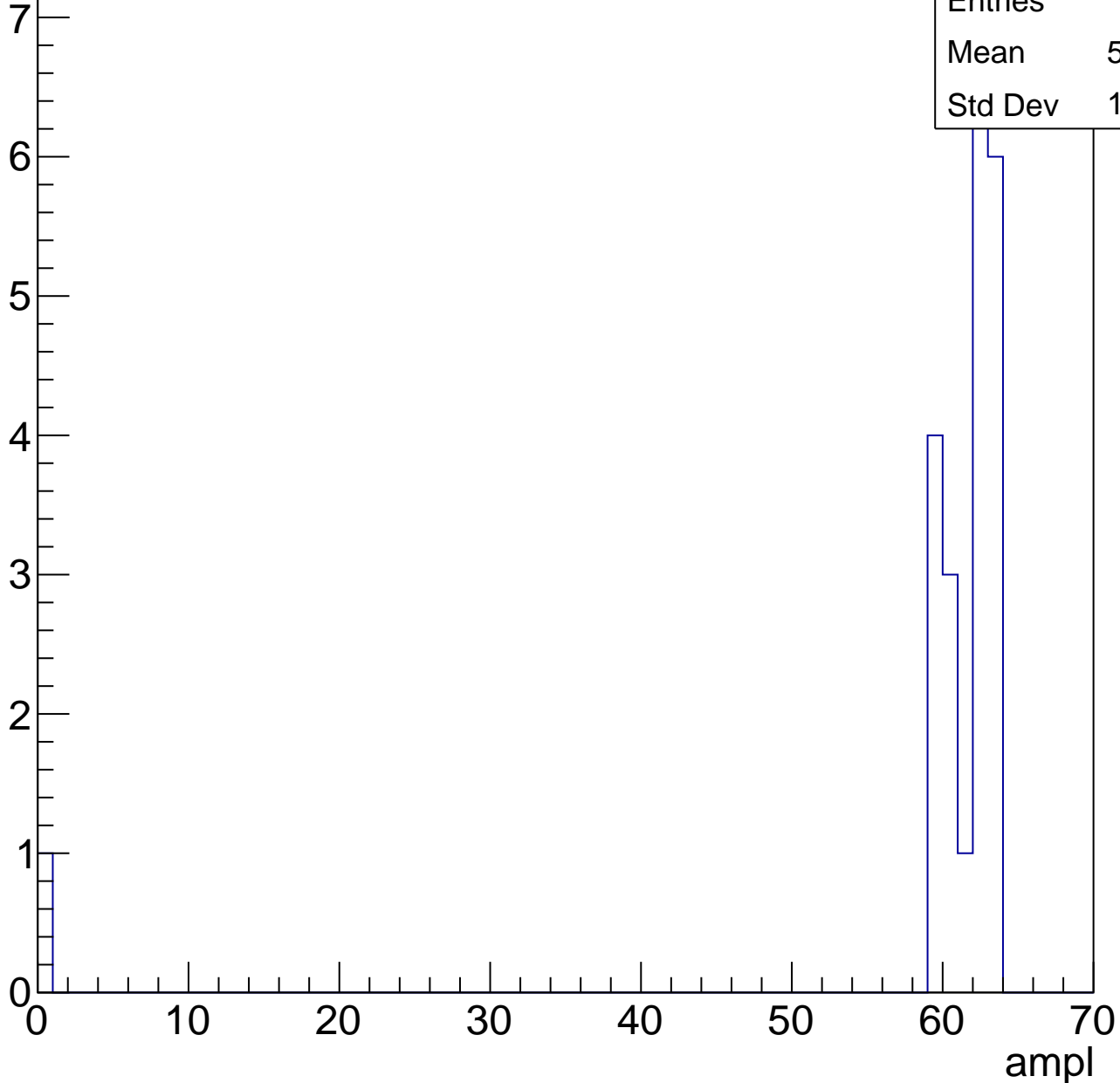


# B1L002S, U17-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.59
Std Dev	12.87





# B1L002S, U17-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch27, adc0

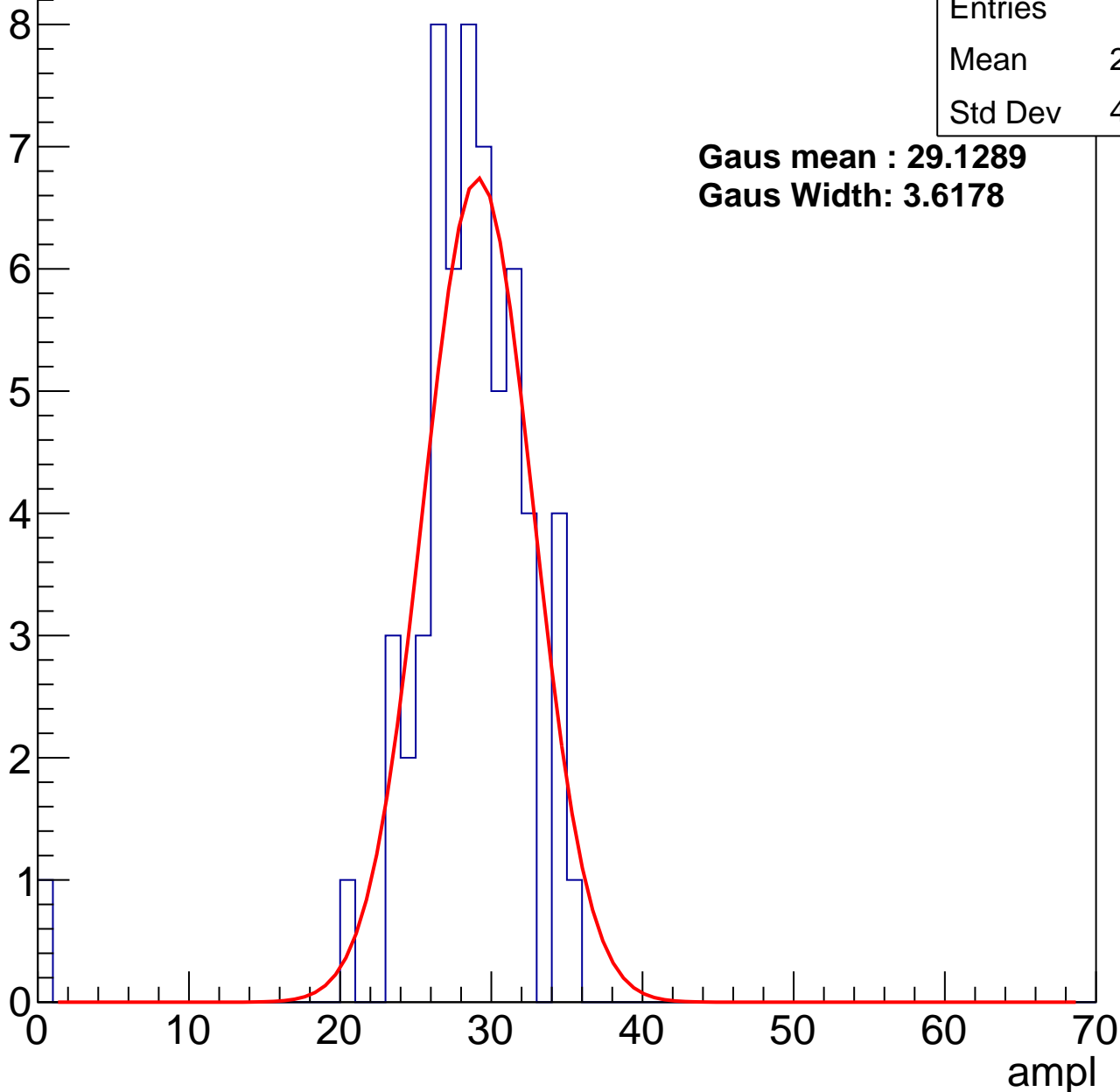
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.86
Std Dev	4.796

**Gaus mean : 29.1289**

**Gaus Width: 3.6178**



# B1L002S, U17-ch27, adc1

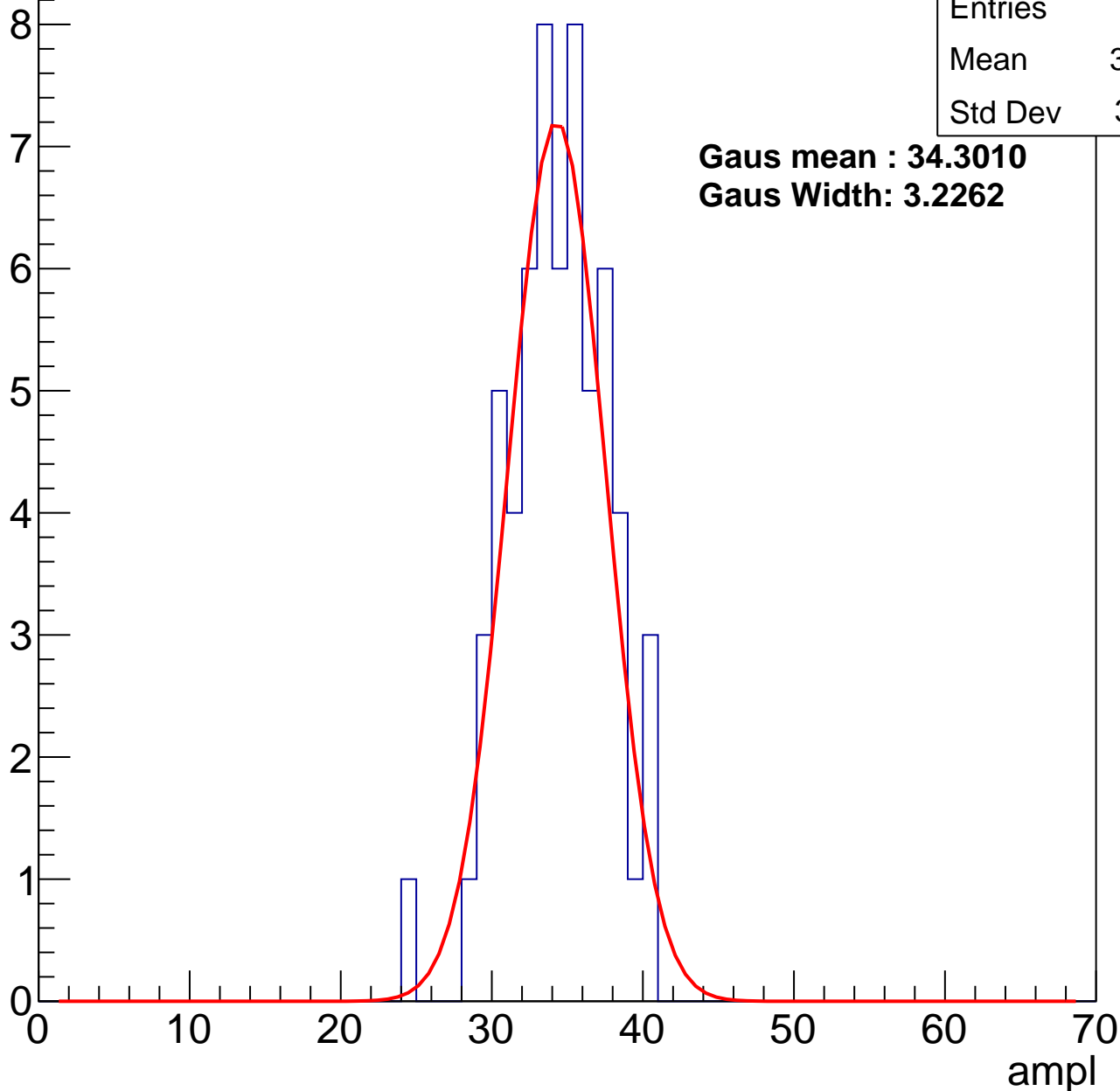
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	33.87
Std Dev	3.231

**Gaus mean : 34.3010**

**Gaus Width: 3.2262**



# B1L002S, U17-ch27, adc2

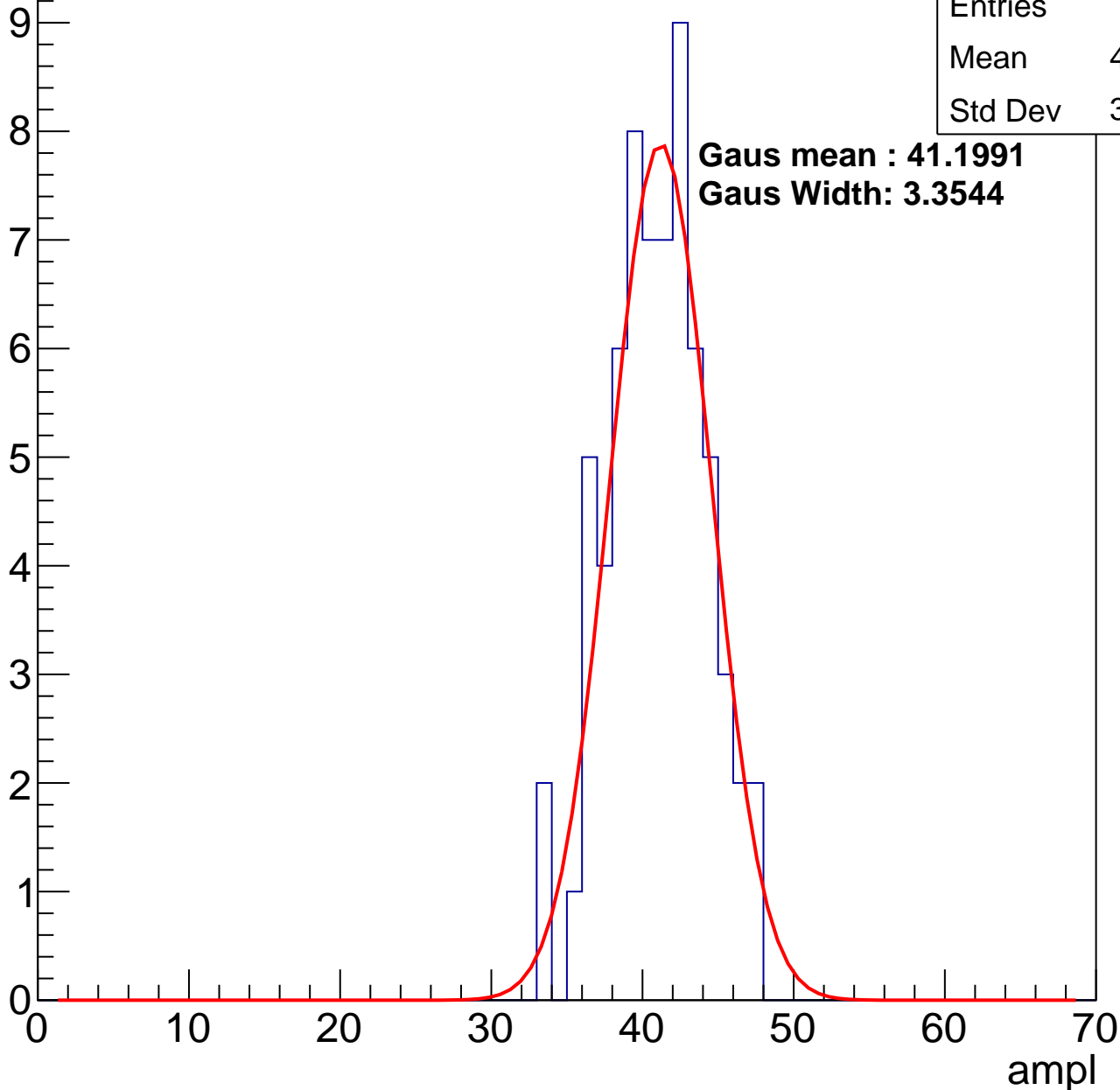
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.49
Std Dev	3.178

**Gaus mean : 41.1991**

**Gaus Width: 3.3544**

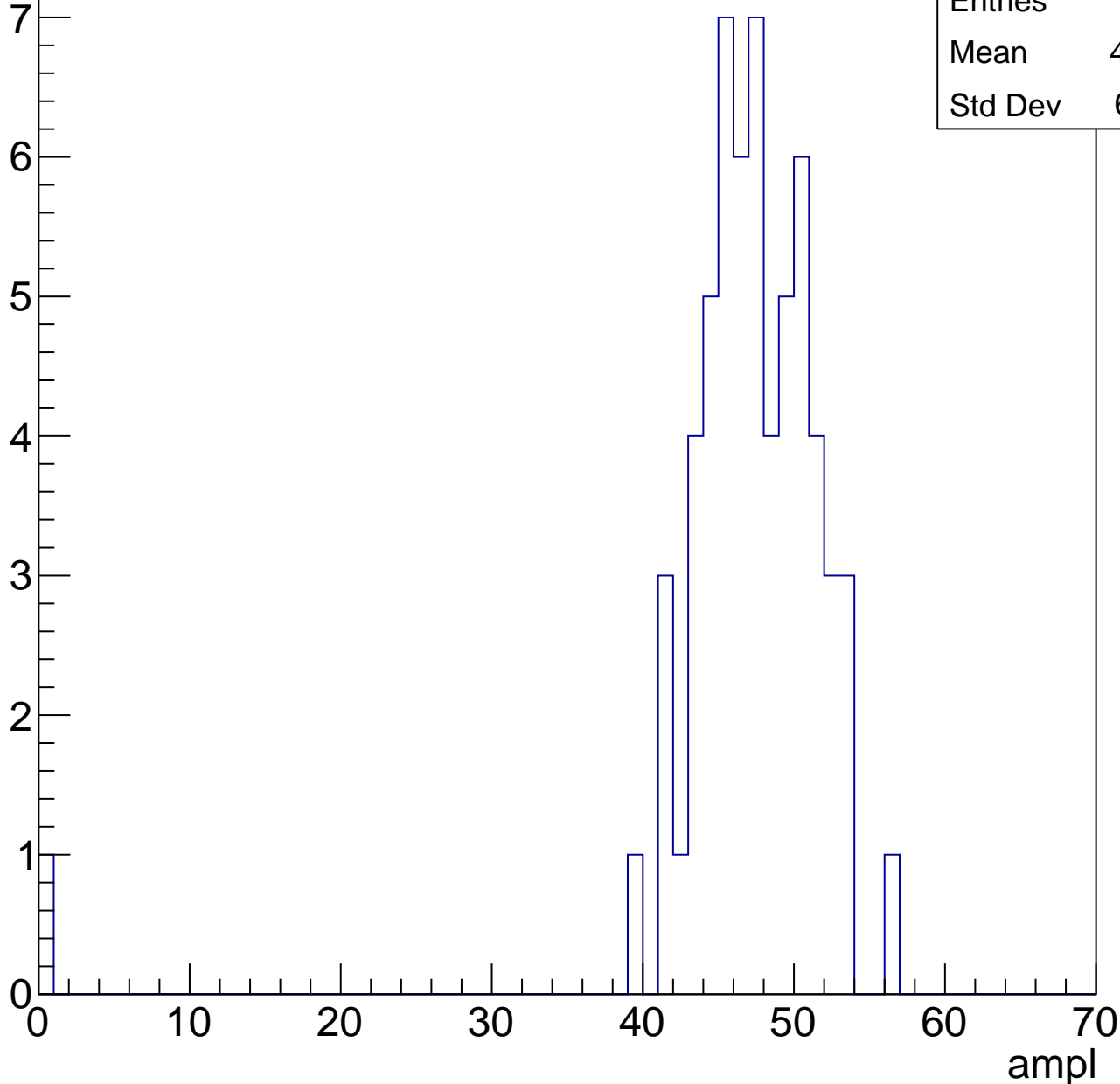


# B1L002S, U17-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.36
Std Dev	6.931

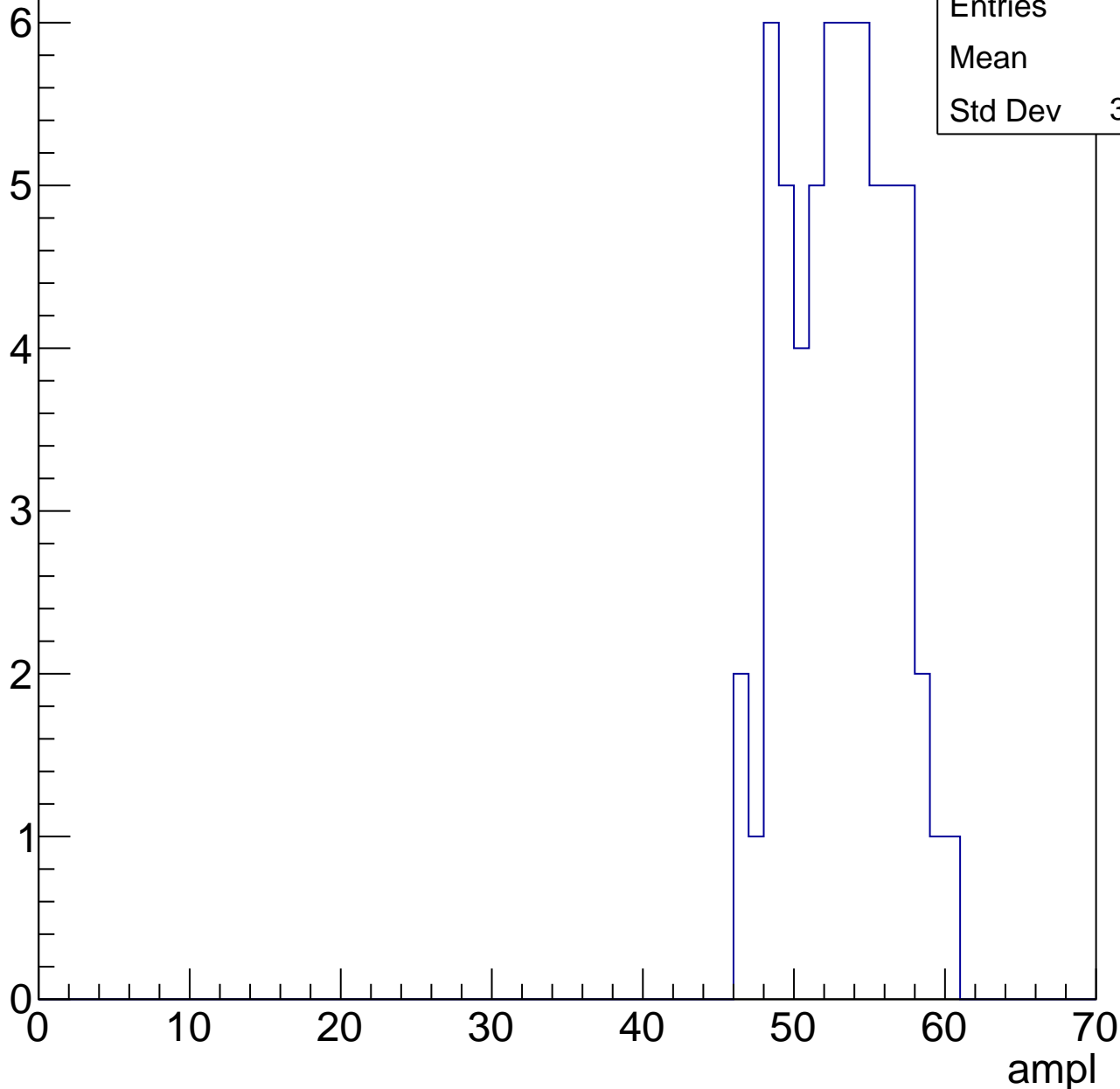


# B1L002S, U17-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

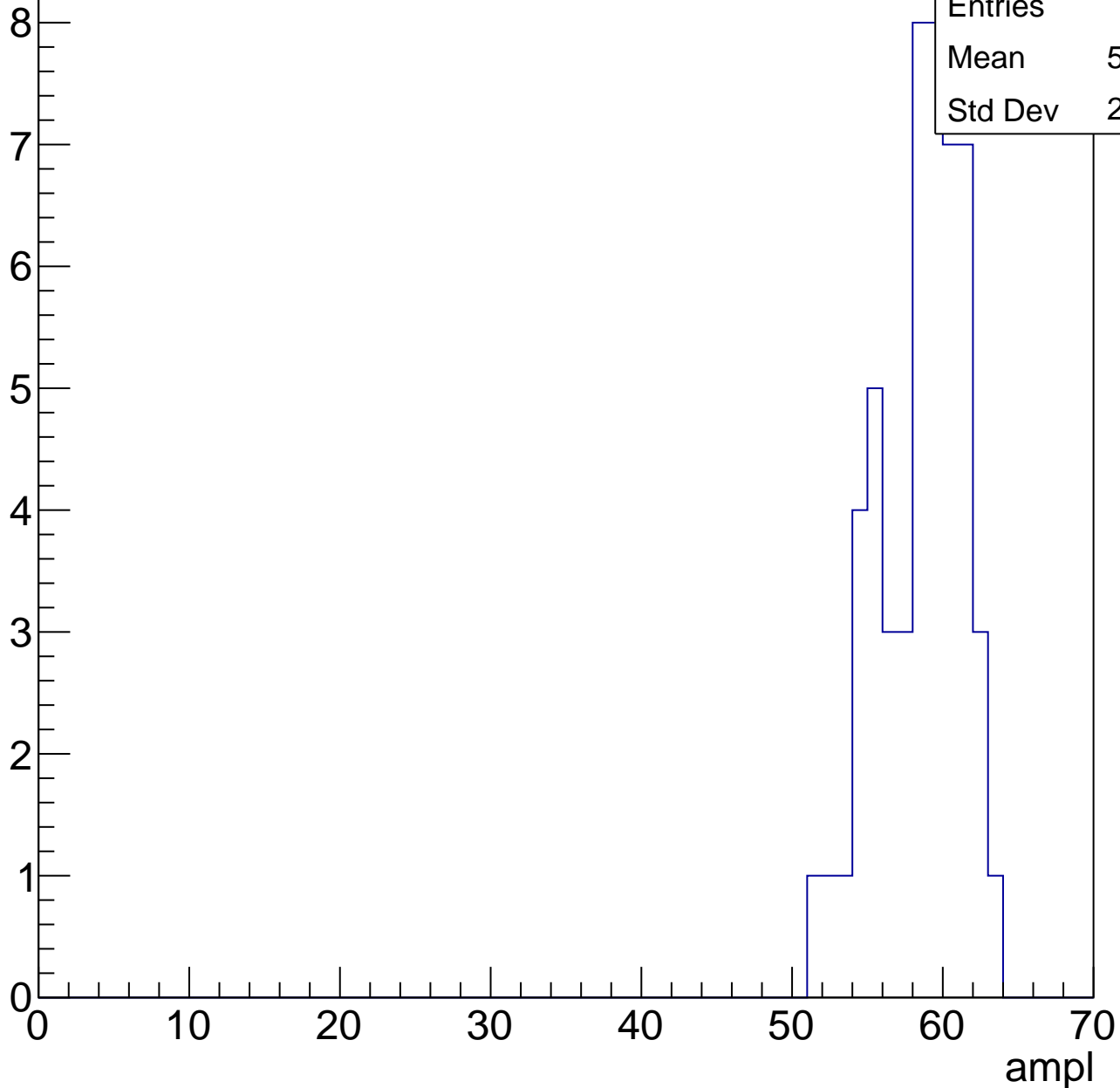
Entries	60
Mean	52.6
Std Dev	3.422



# B1L002S, U17-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



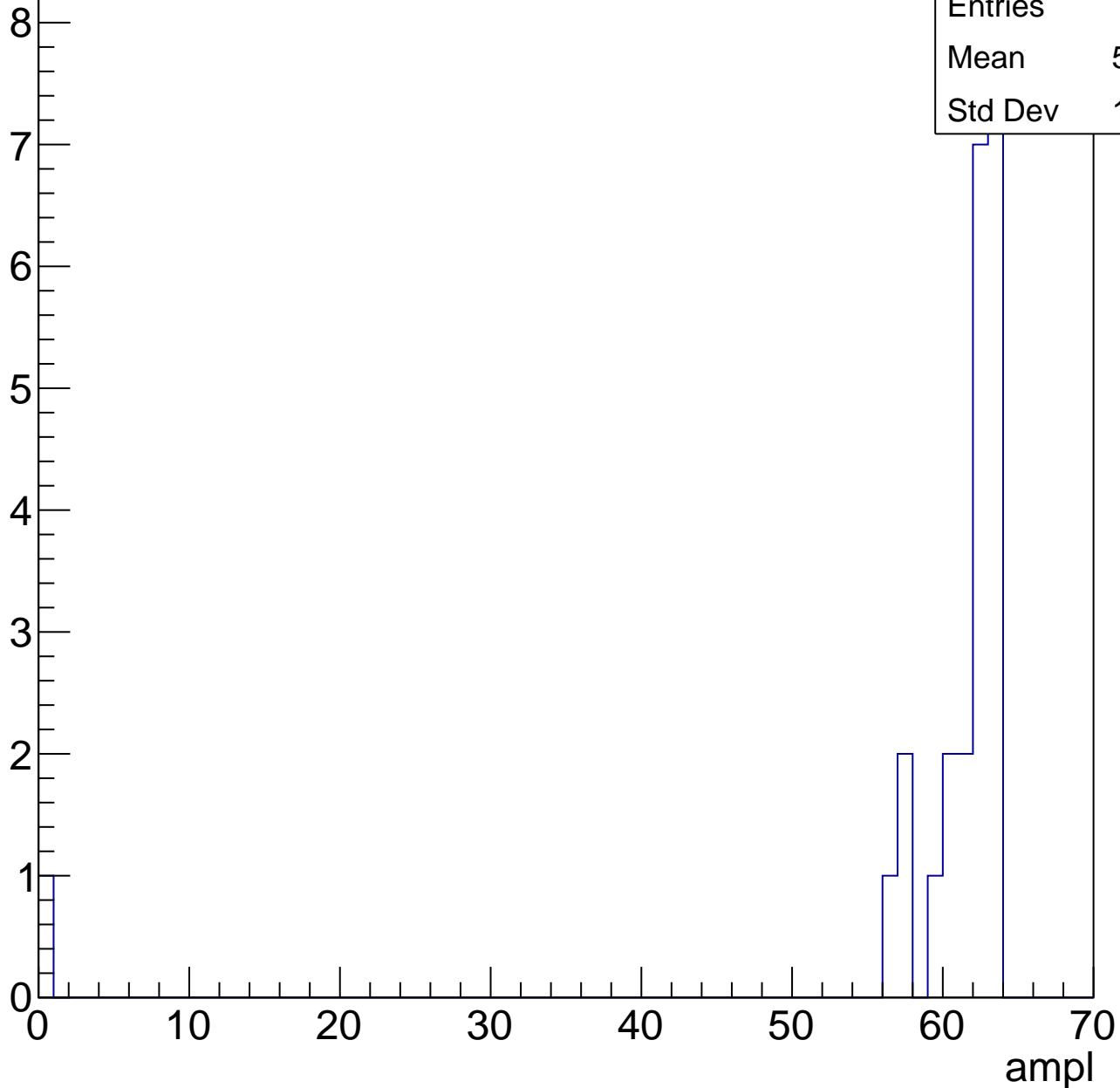
Entries	52
Mean	58.04
Std Dev	2.794

# B1L002S, U17-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.71
Std Dev	12.41

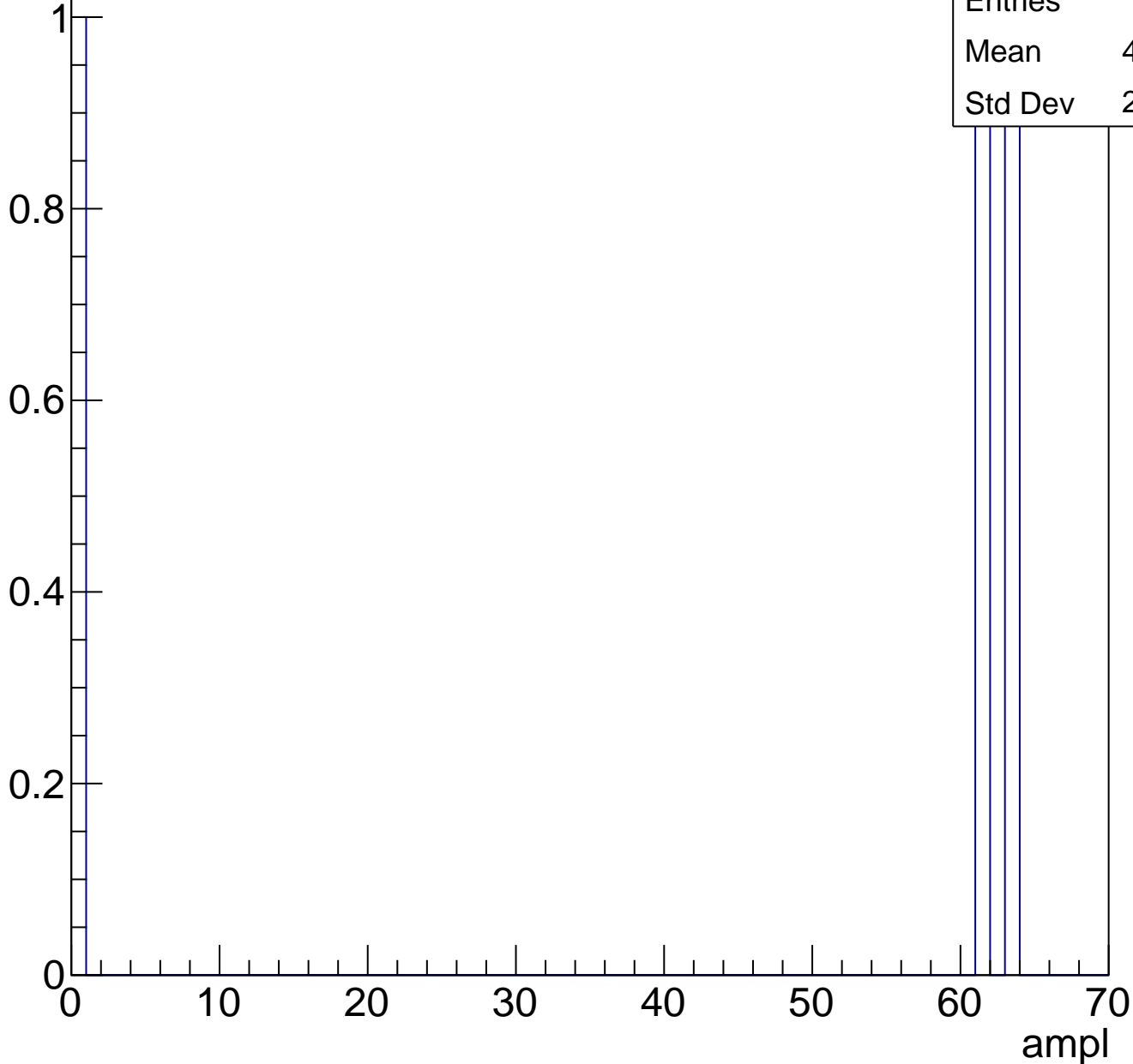




# B1L002S, U17-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch28, adc0

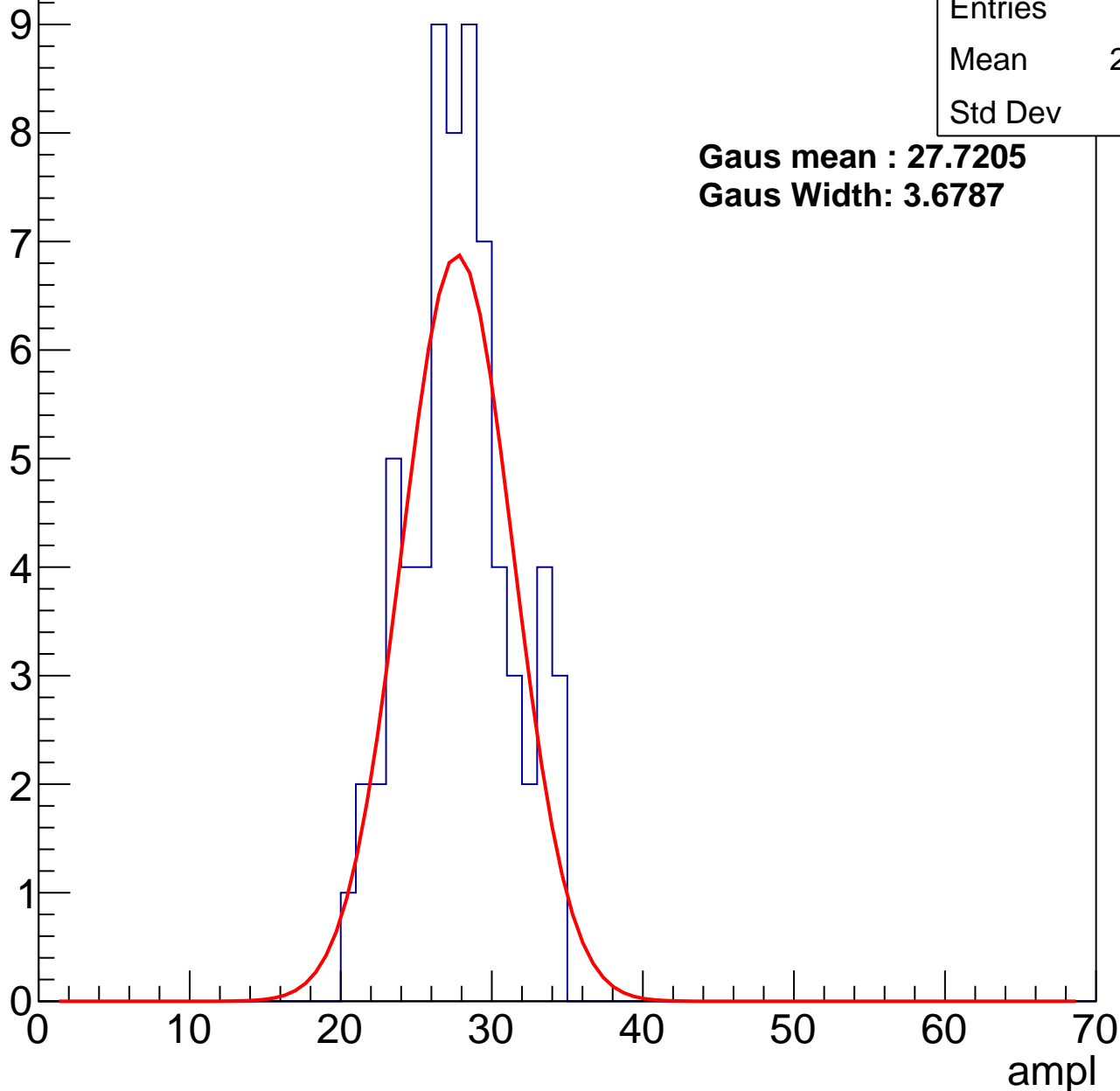
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.36
Std Dev	3.38

**Gaus mean : 27.7205**

**Gaus Width: 3.6787**



# B1L002S, U17-ch28, adc1

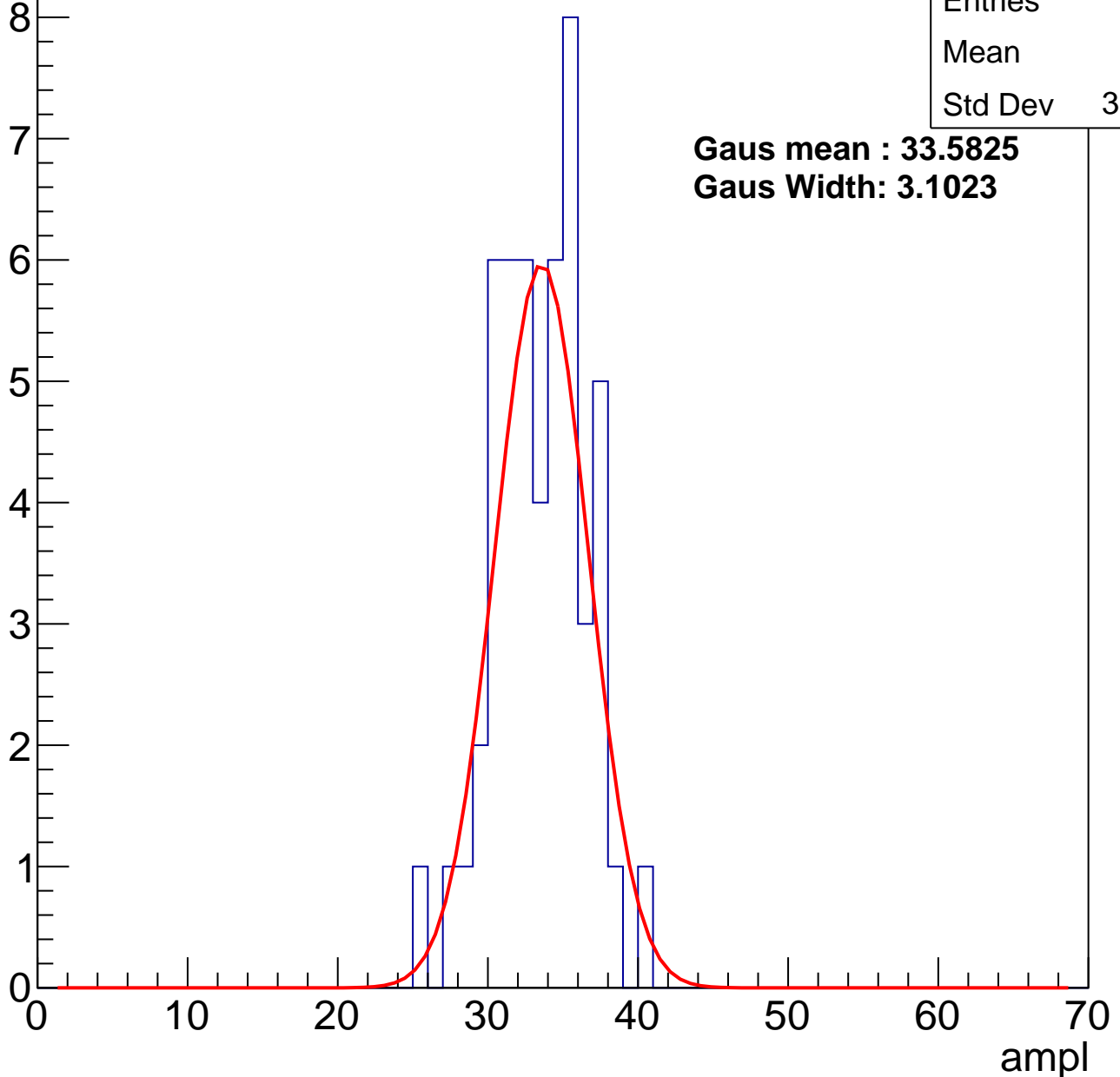
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	33
Std Dev	3.003

**Gaus mean : 33.5825**

**Gaus Width: 3.1023**



# B1L002S, U17-ch28, adc2

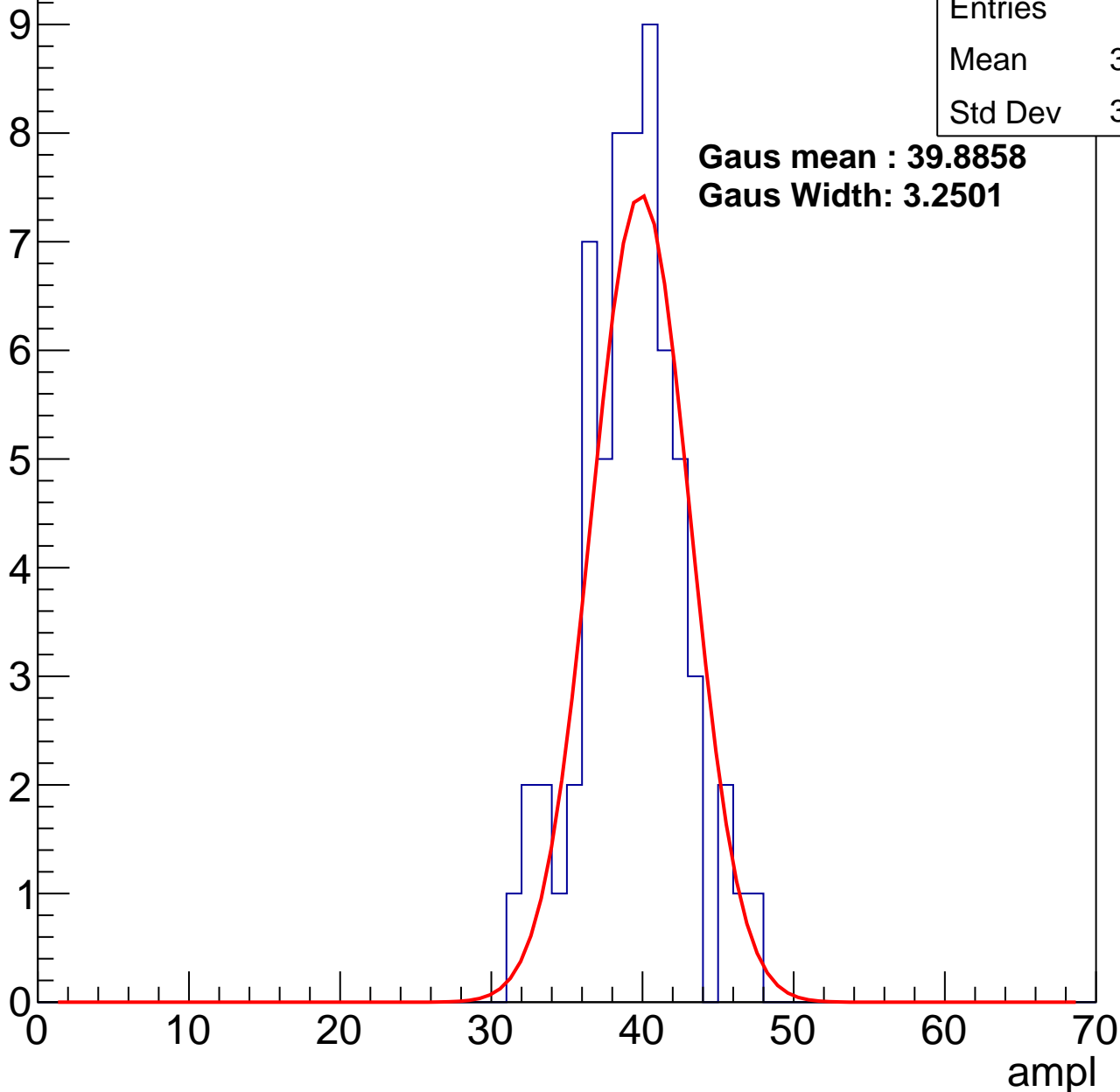
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	38.83
Std Dev	3.312

**Gaus mean : 39.8858**

**Gaus Width: 3.2501**

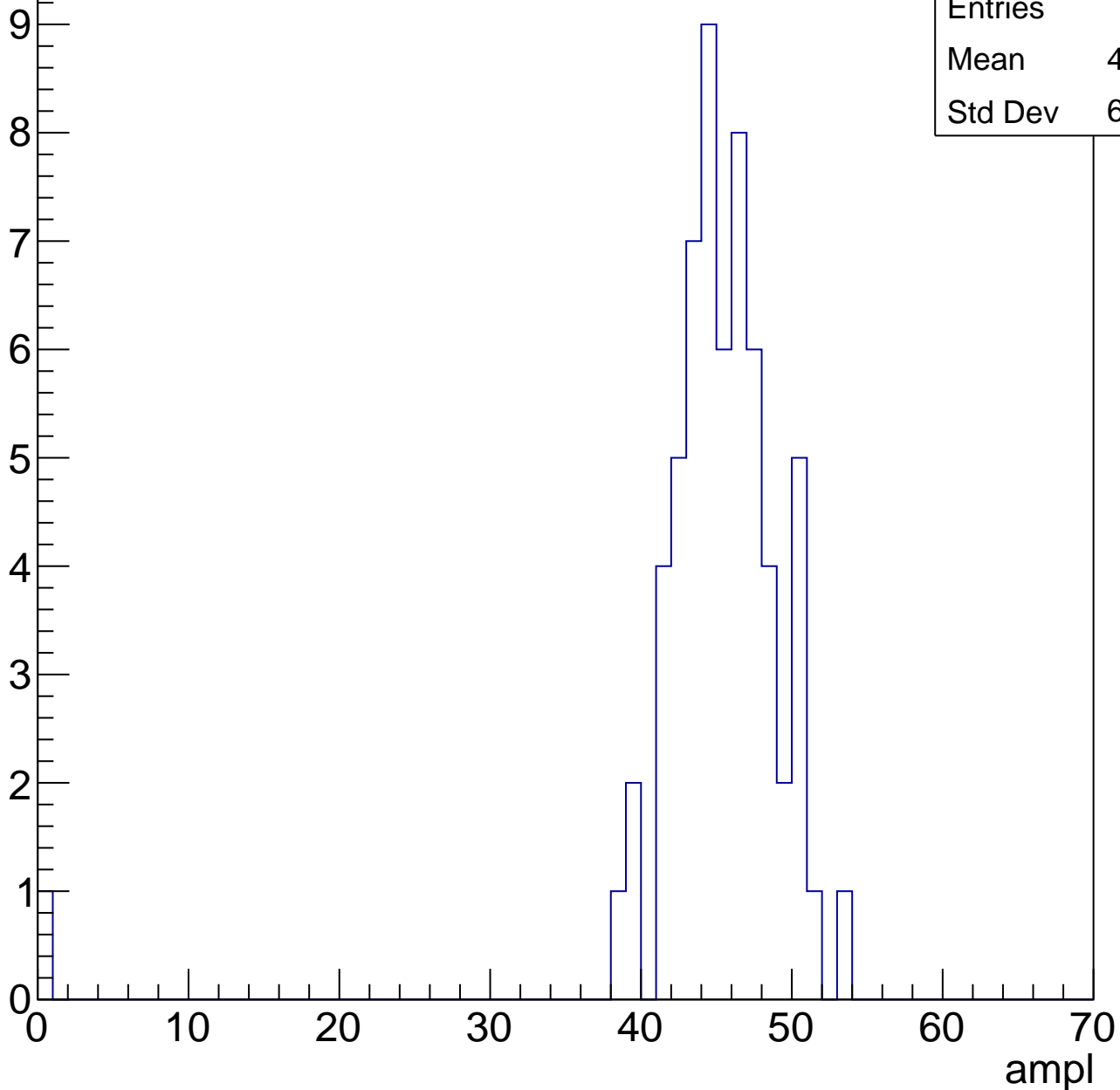


# B1L002S, U17-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	44.37
Std Dev	6.466

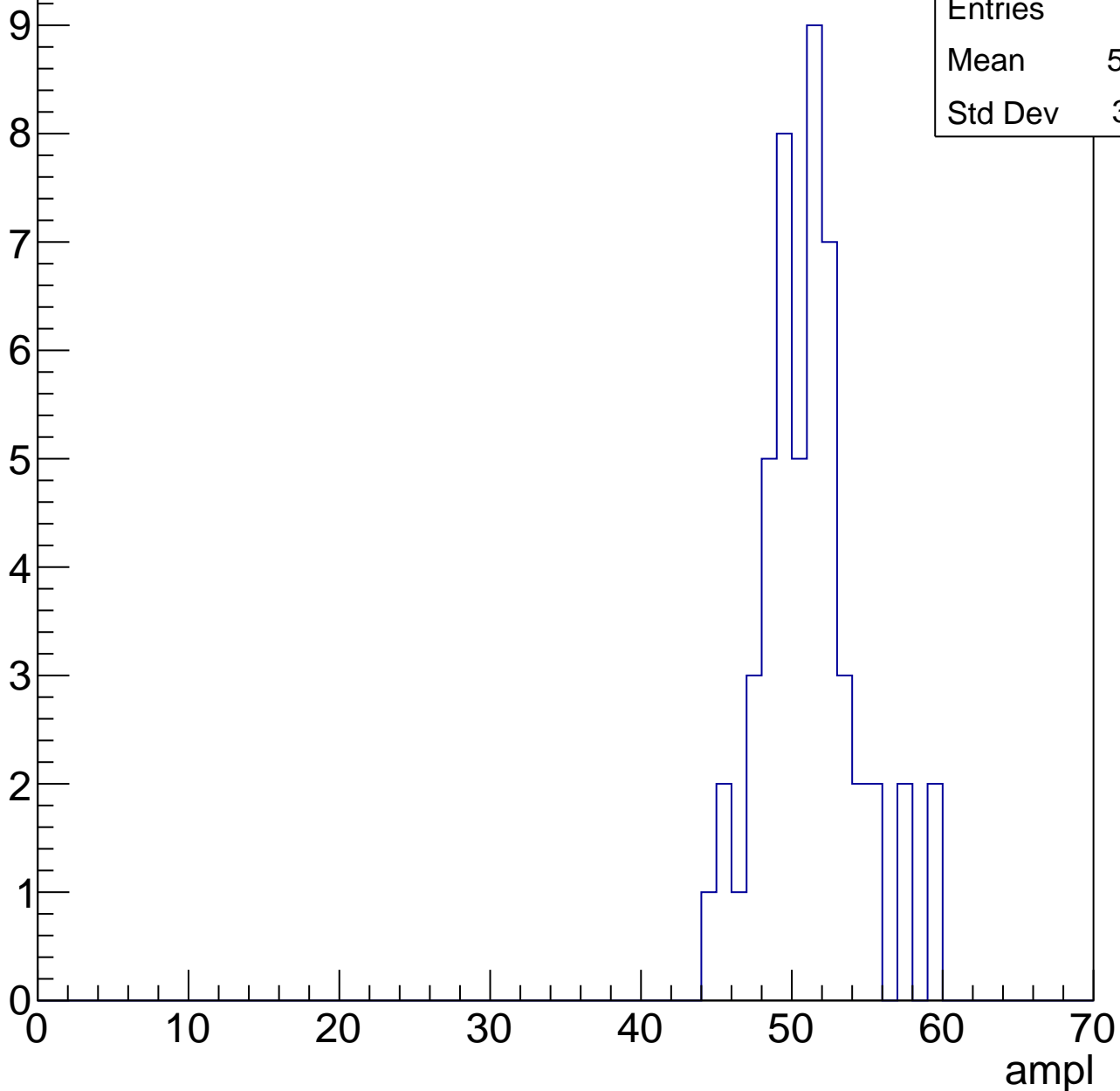


# B1L002S, U17-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	50.67
Std Dev	3.221

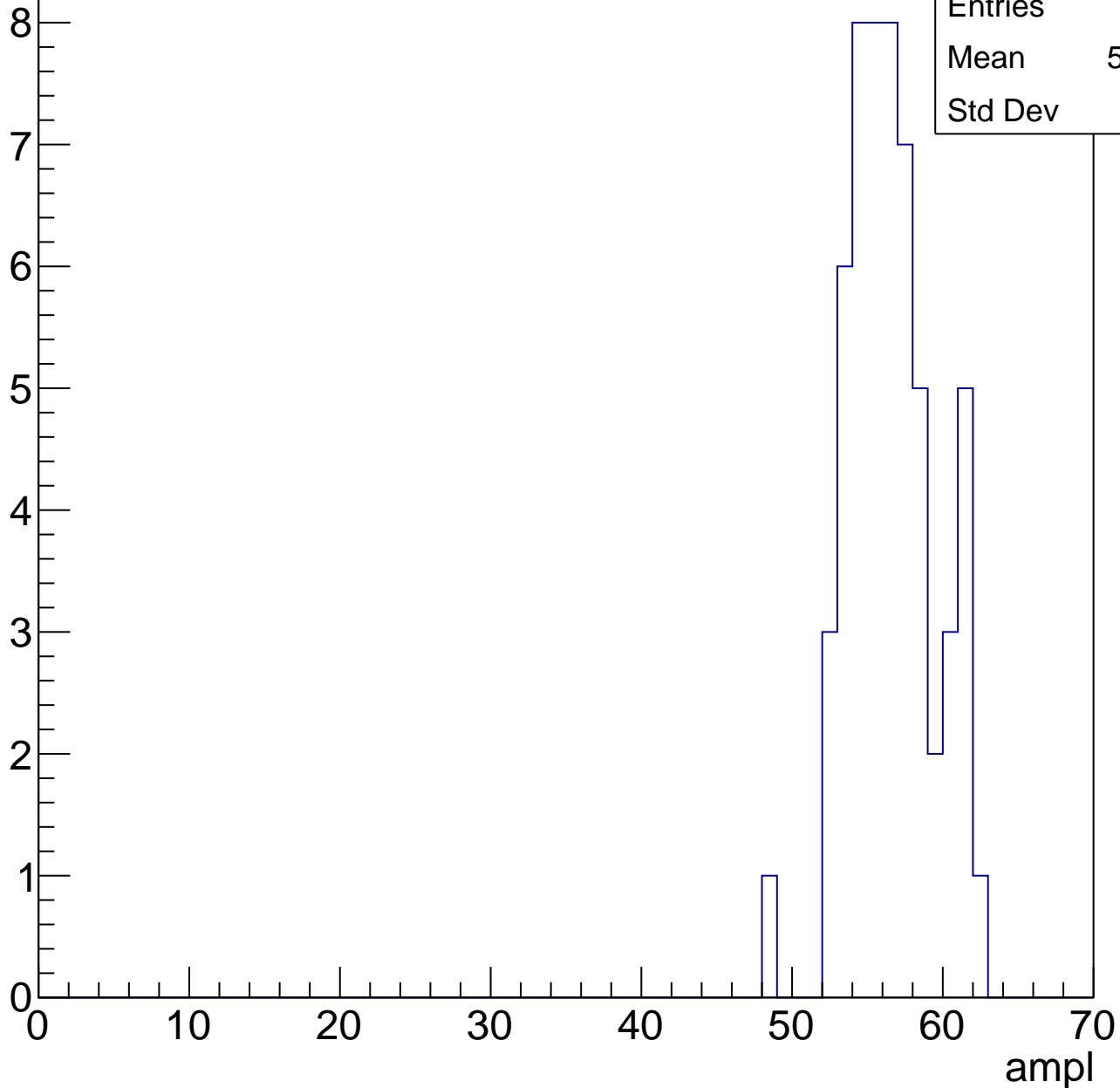


# B1L002S, U17-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	56.07
Std Dev	2.84

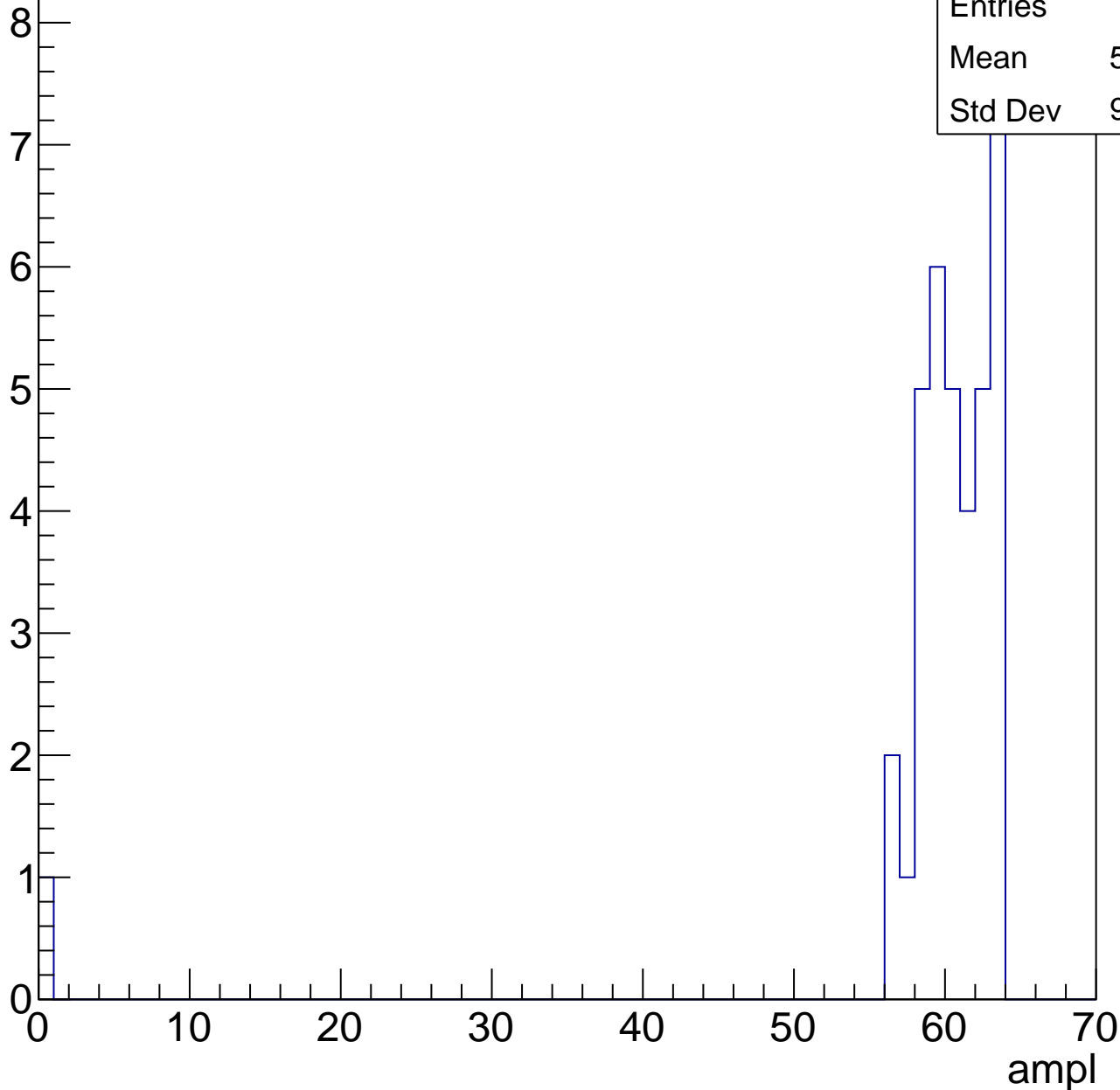


# B1L002S, U17-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	58.68
Std Dev	9.997

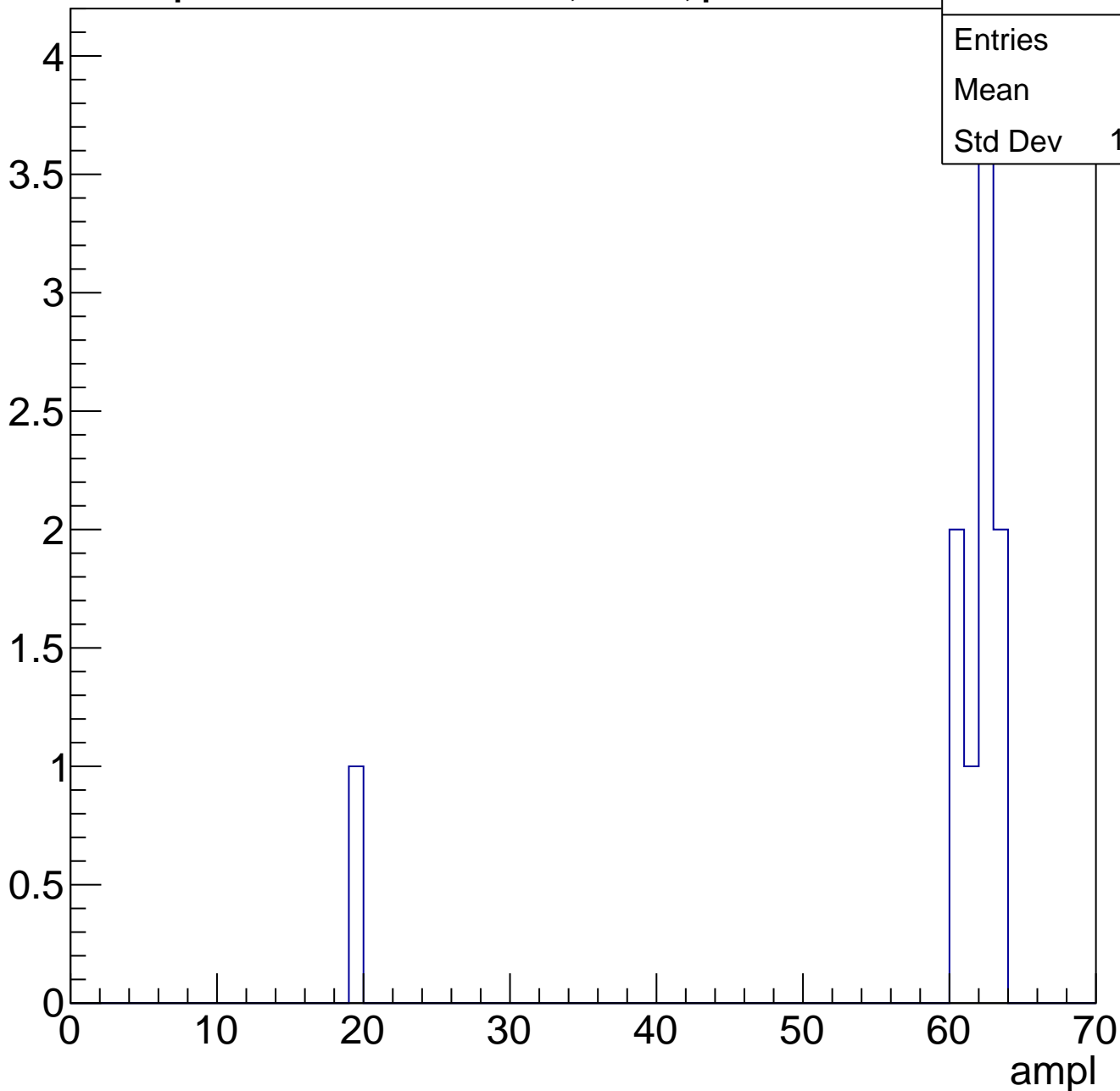




# B1L002S, U17-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	10
Mean	57.4
Std Dev	12.84

# B1L002S, U17-ch29, adc0

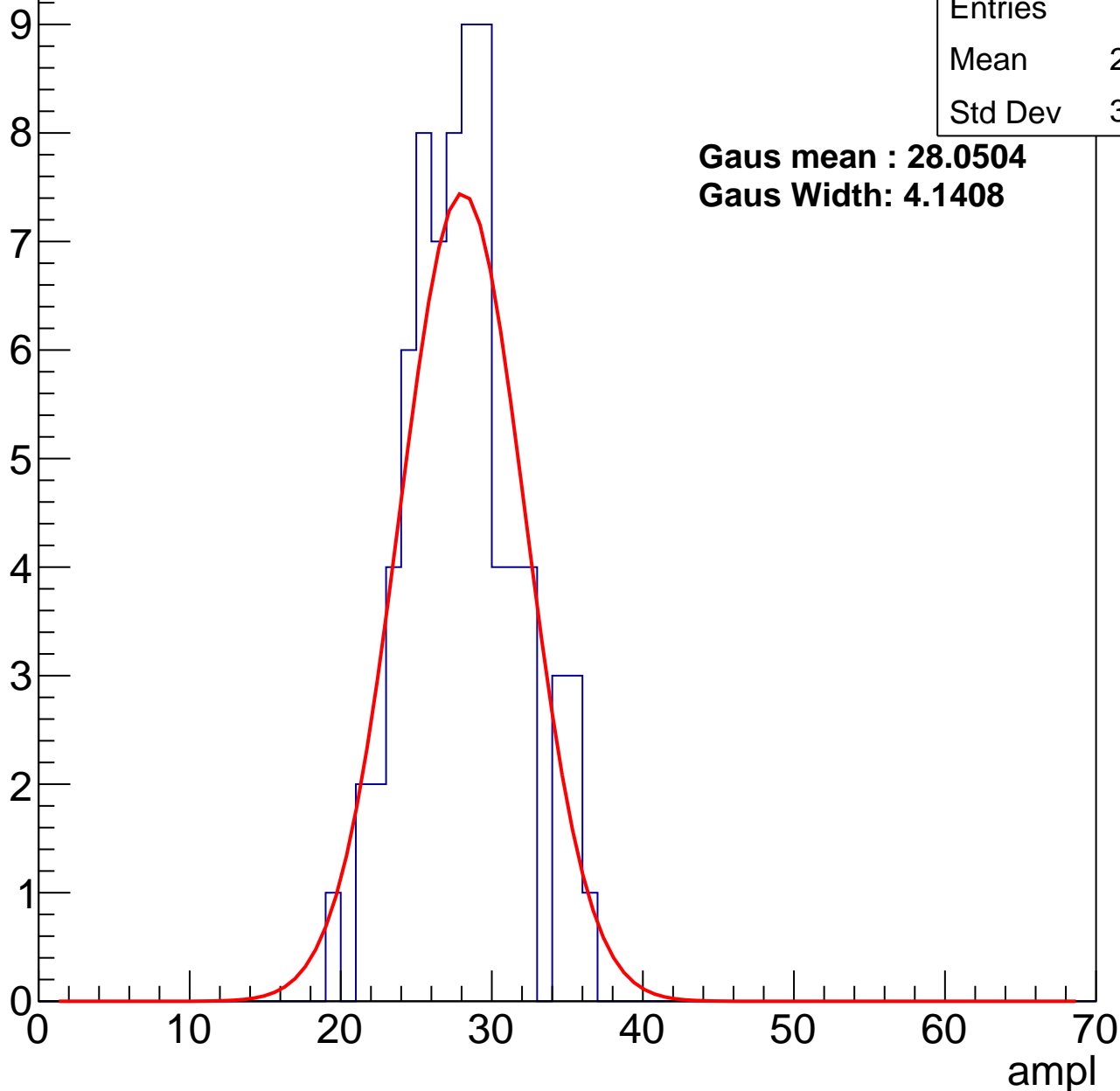
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	27.56
Std Dev	3.623

**Gaus mean : 28.0504**

**Gaus Width: 4.1408**



# B1L002S, U17-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	34.41
Std Dev	3.074

**Gaus mean : 35.2620**

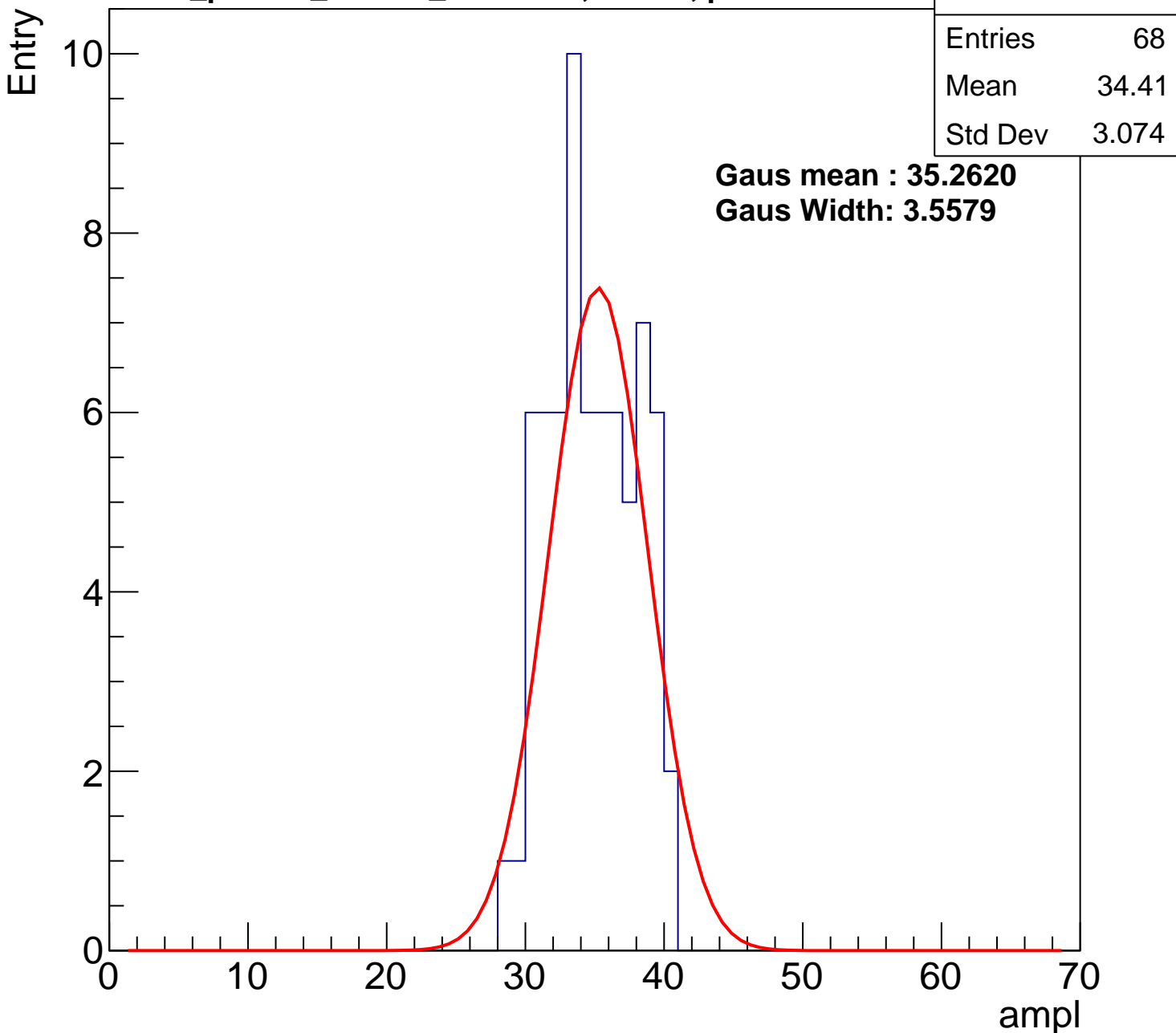
**Gaus Width: 3.5579**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch29, adc2

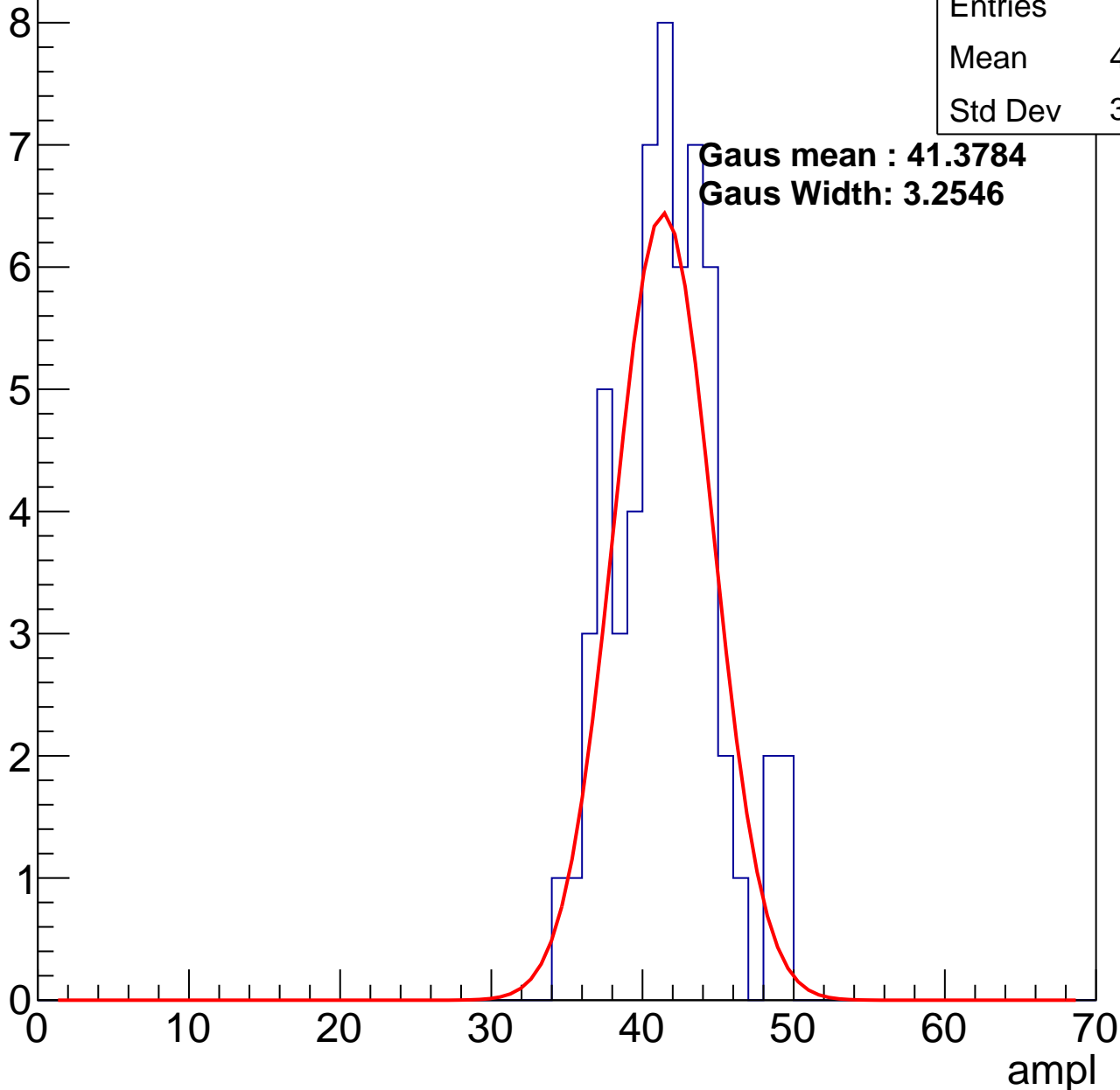
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.16
Std Dev	3.372

**Gaus mean : 41.3784**

**Gaus Width: 3.2546**

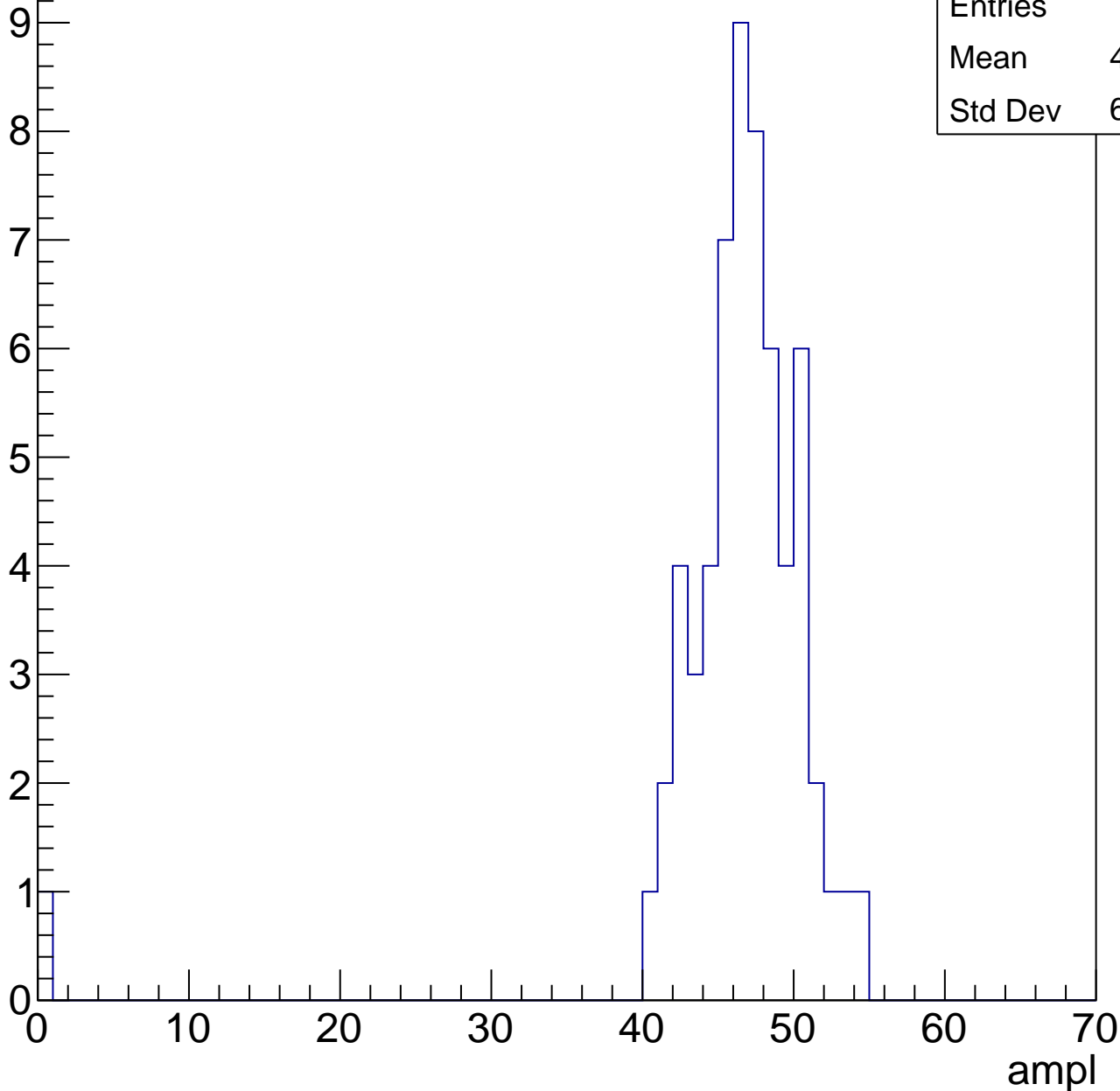


# B1L002S, U17-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.75
Std Dev	6.677

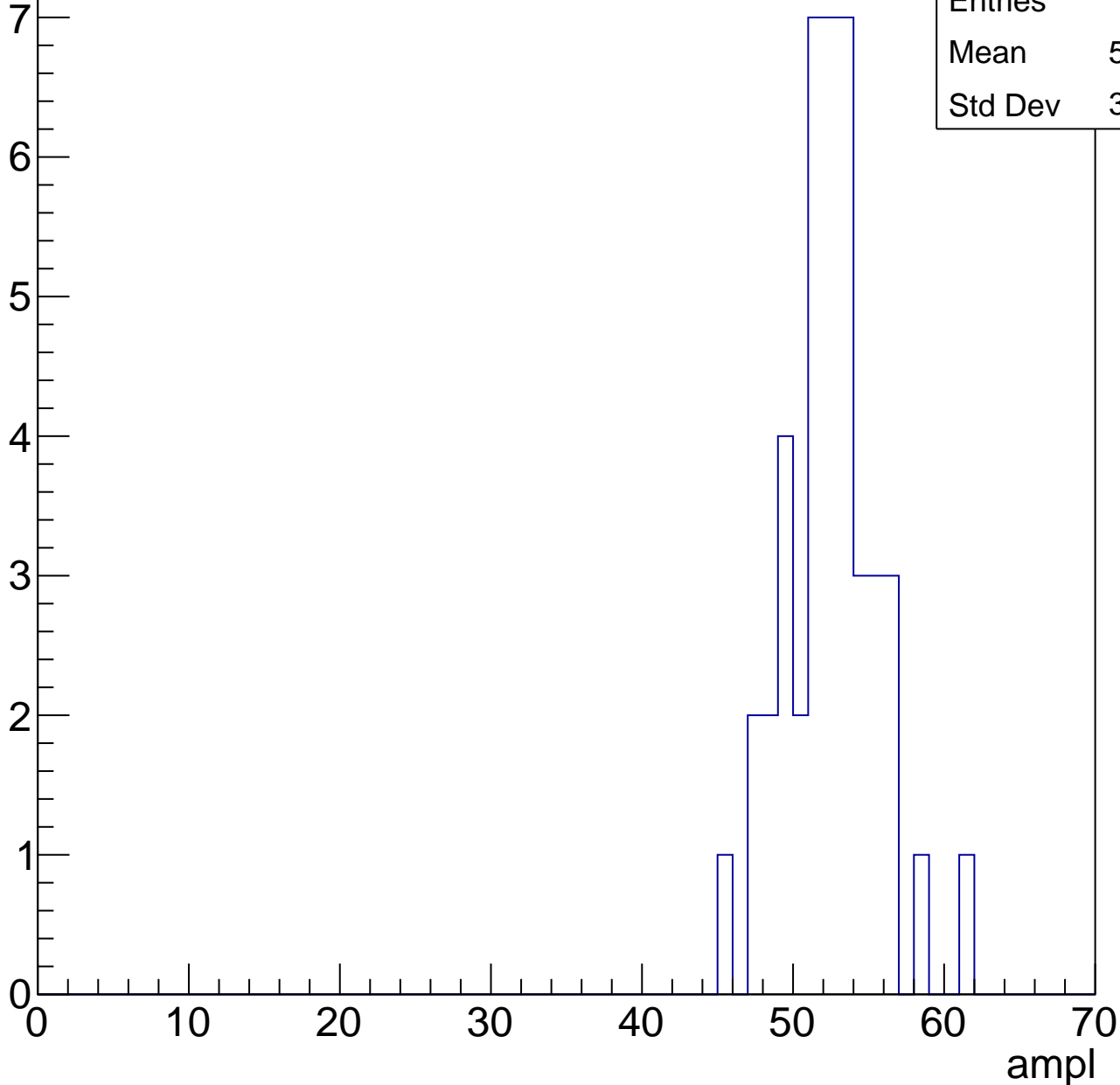


# B1L002S, U17-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	52.02
Std Dev	3.023

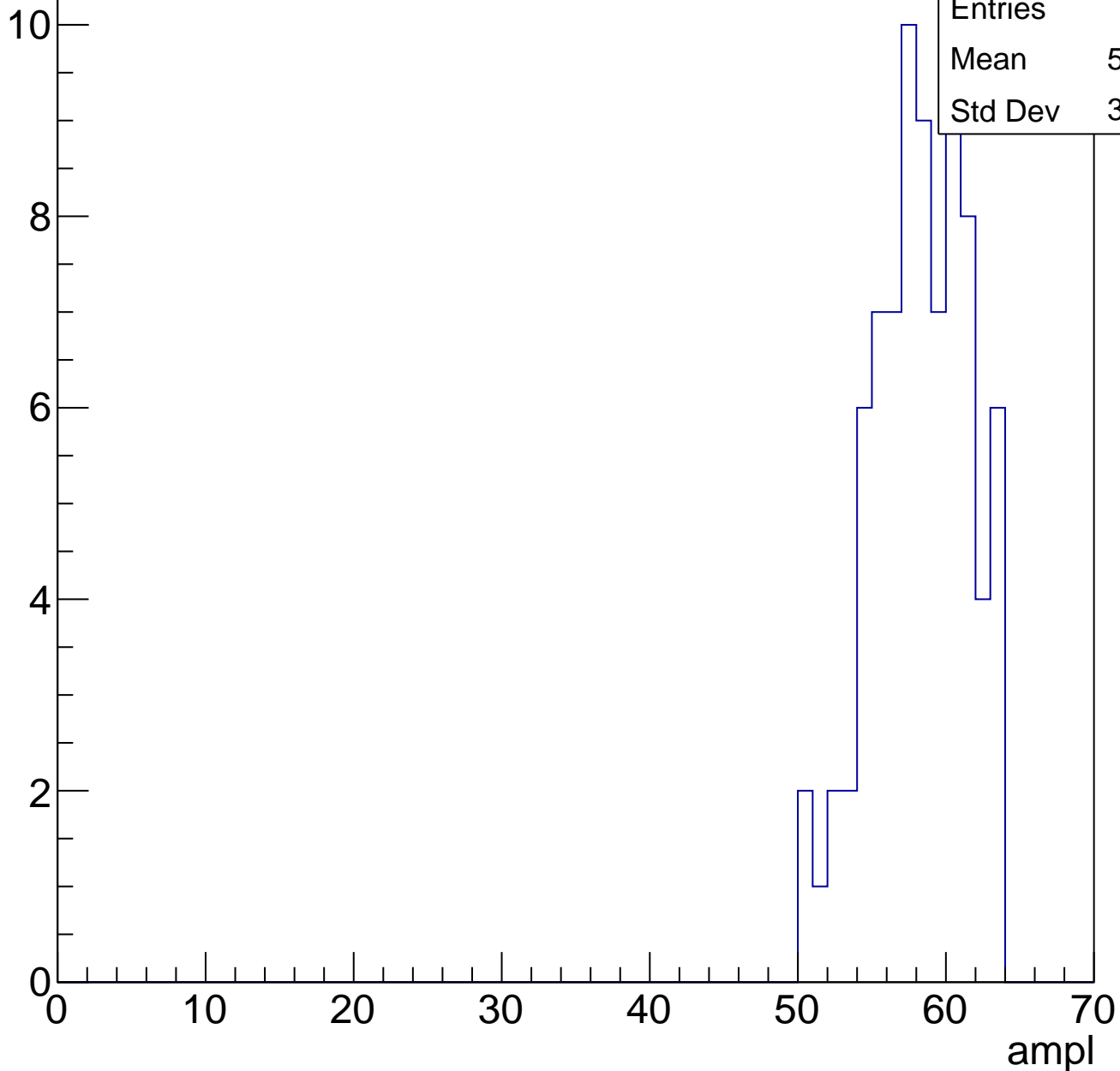


# B1L002S, U17-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	80
Mean	57.76
Std Dev	3.203

Entry

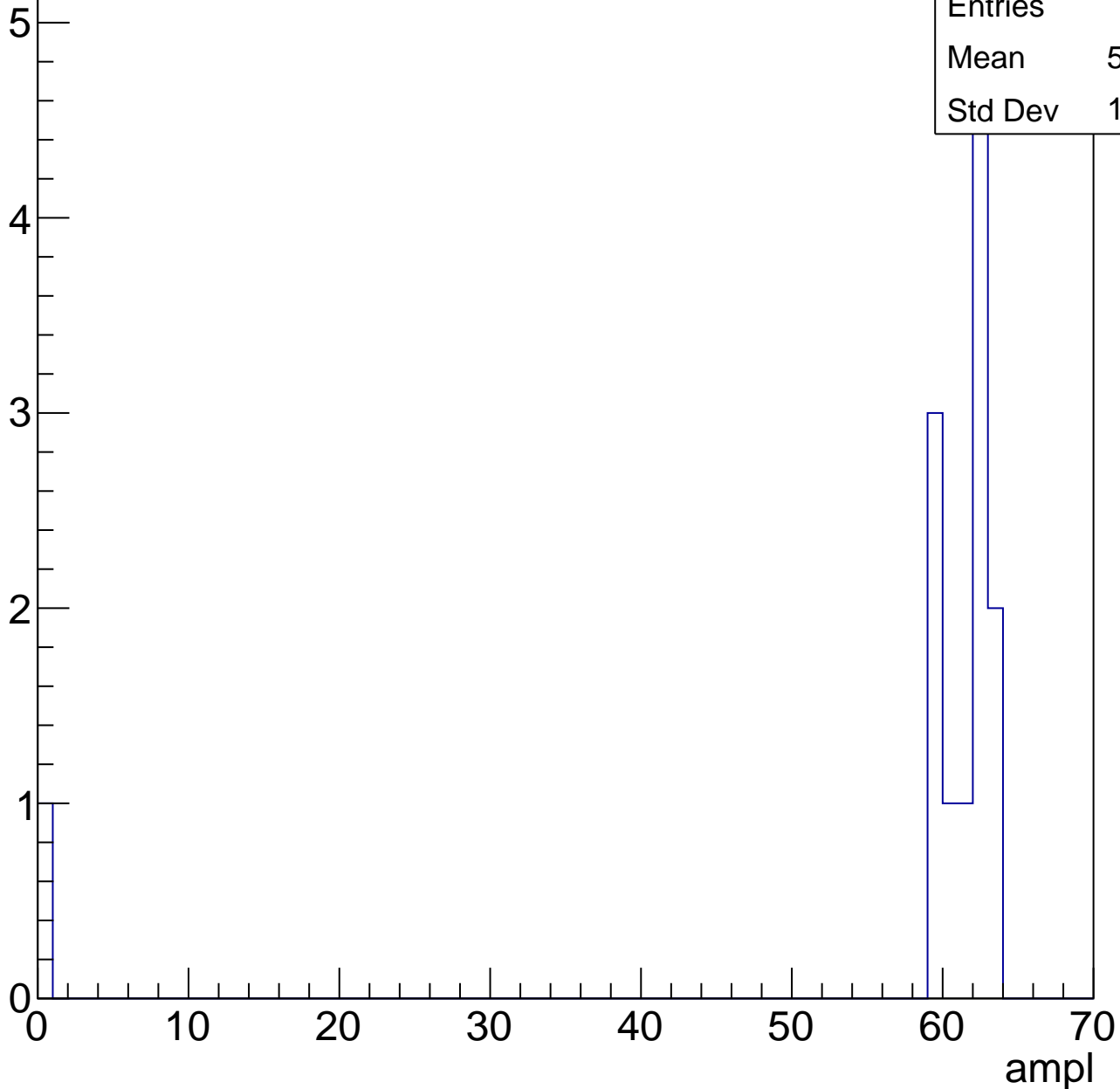


# B1L002S, U17-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	56.46
Std Dev	16.36

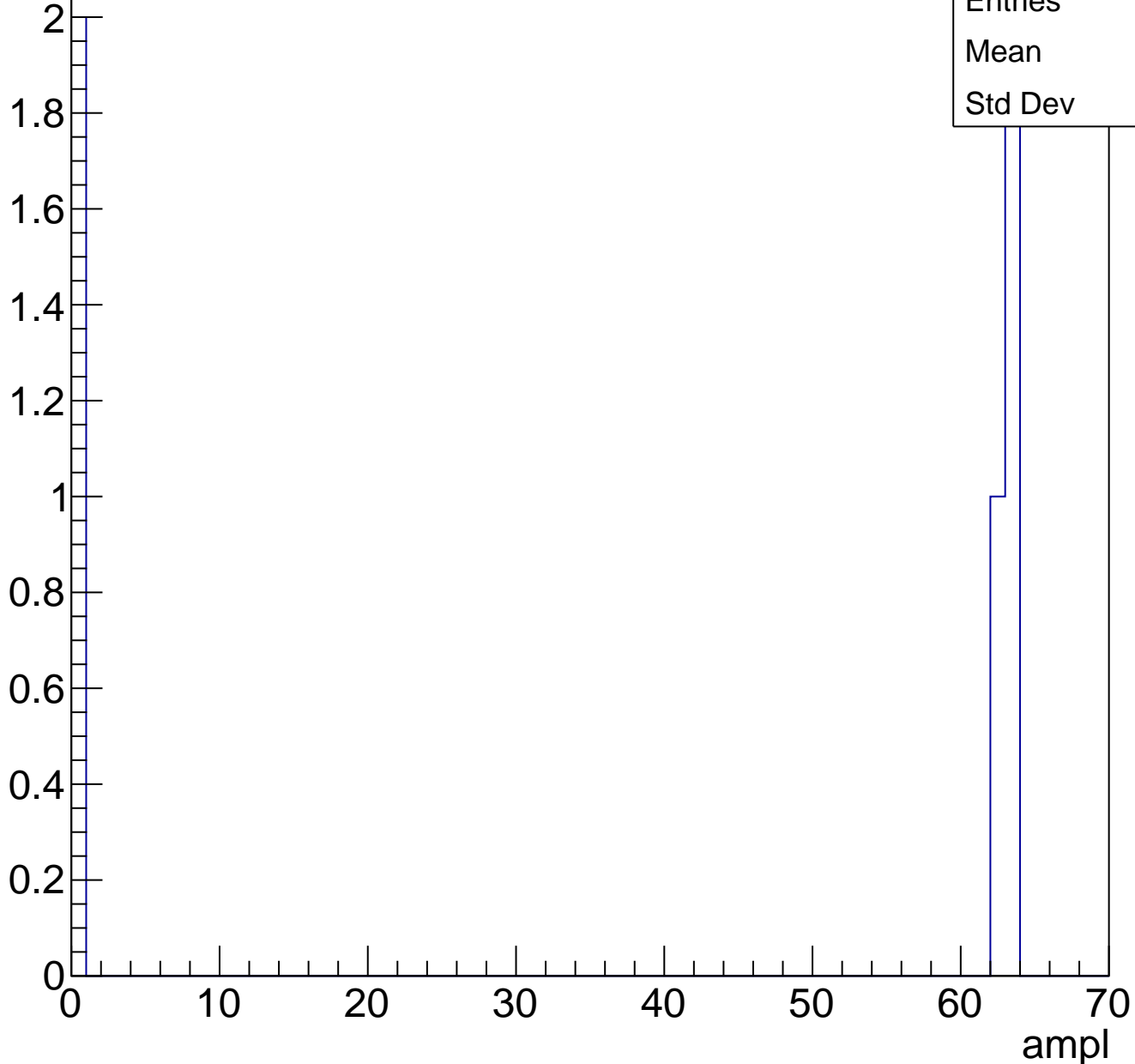




# B1L002S, U17-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch30, adc0

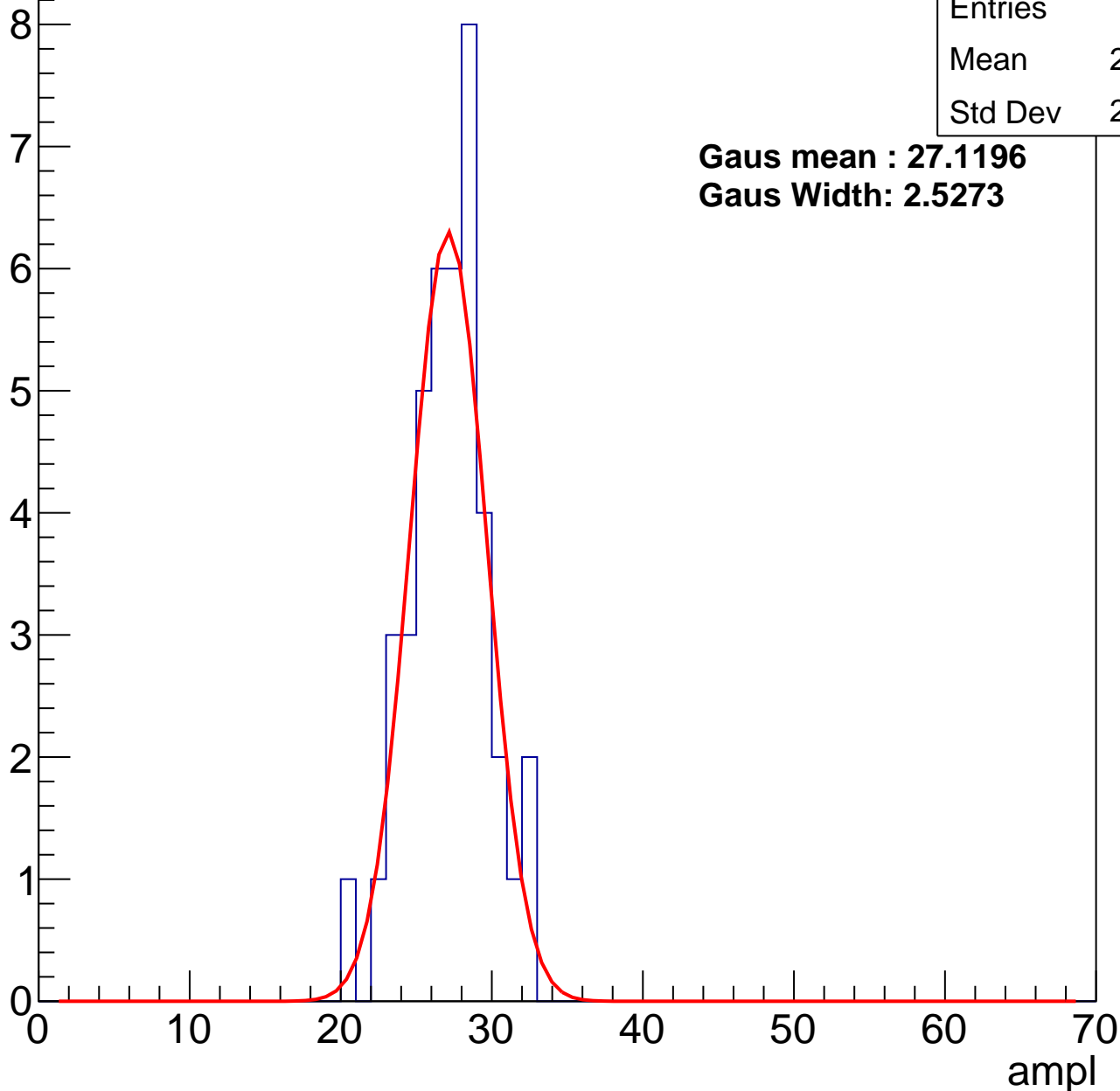
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	26.69
Std Dev	2.577

**Gaus mean : 27.1196**

**Gaus Width: 2.5273**



# B1L002S, U17-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	32.9
Std Dev	3.68

**Gaus mean : 33.3881**

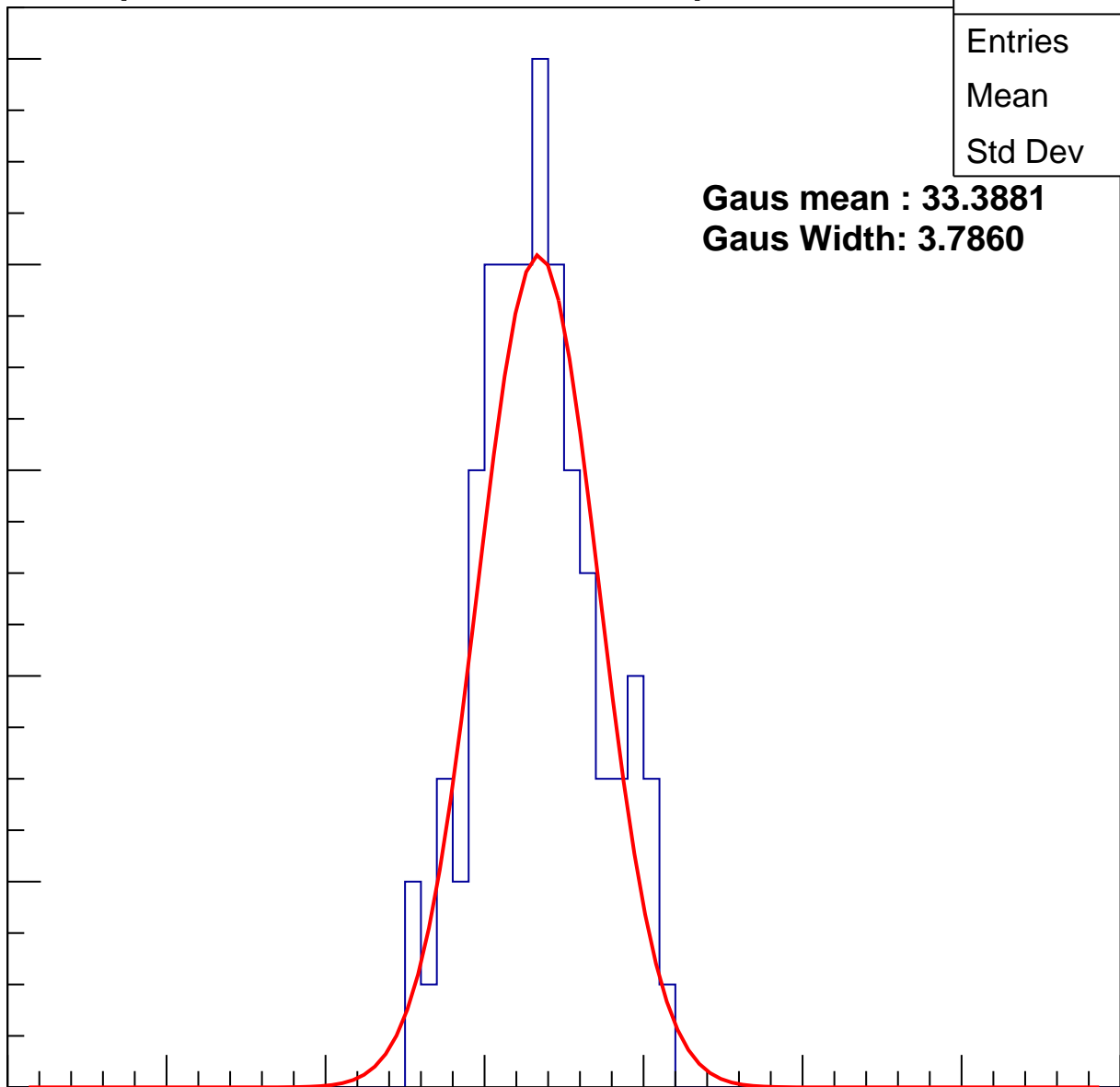
**Gaus Width: 3.7860**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch30, adc2

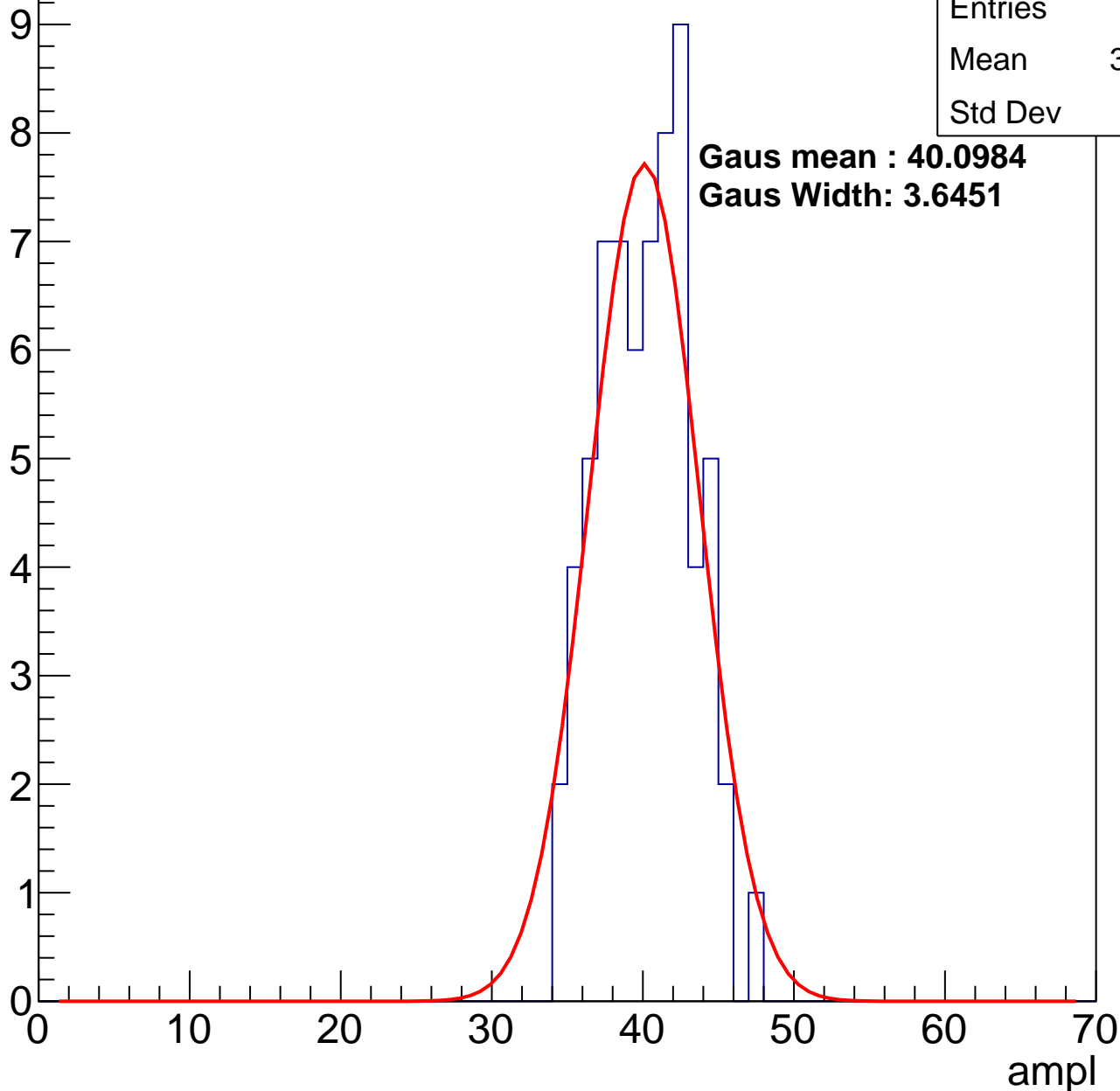
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.73
Std Dev	3

**Gaus mean : 40.0984**

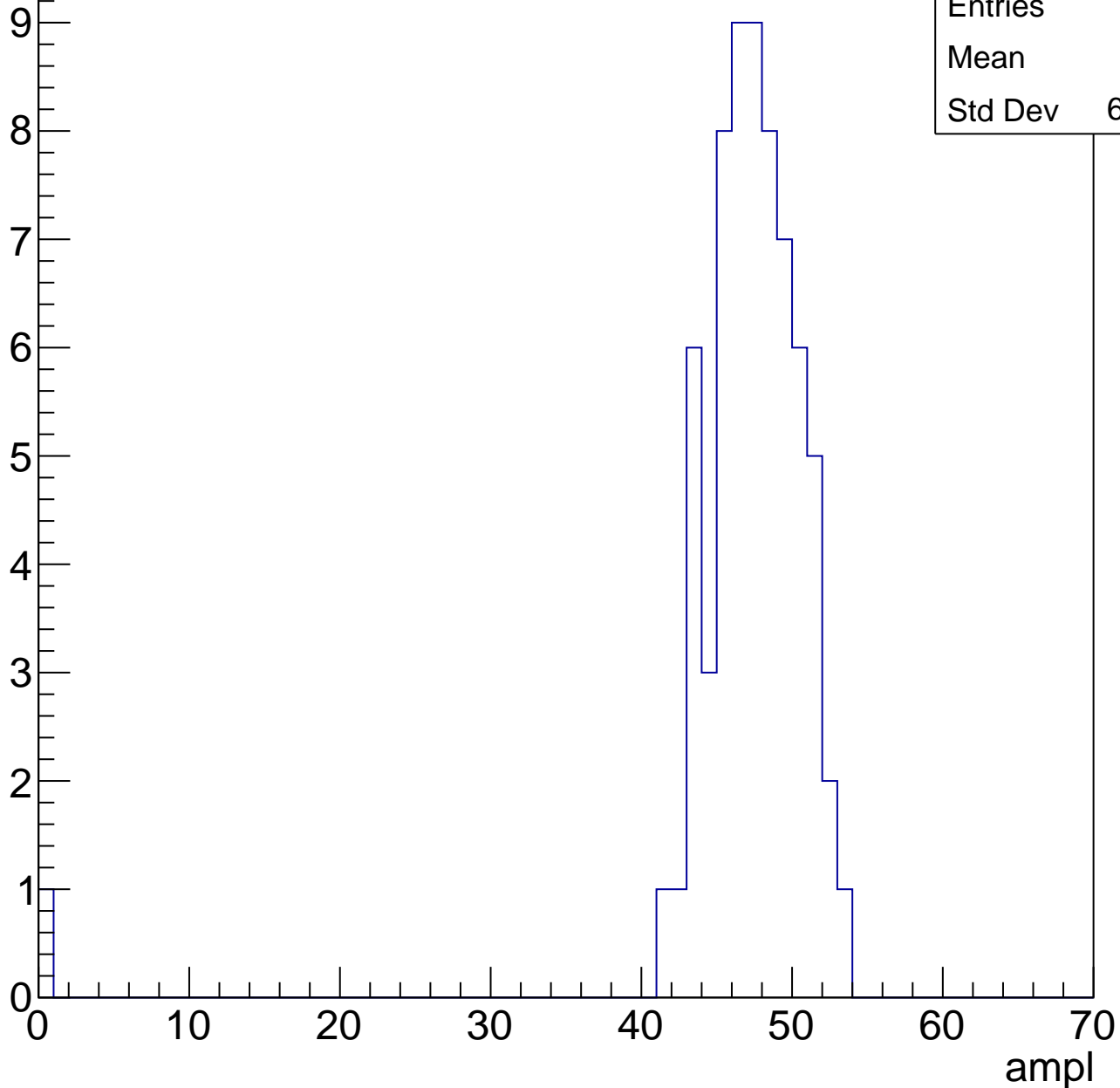
**Gaus Width: 3.6451**



# B1L002S, U17-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



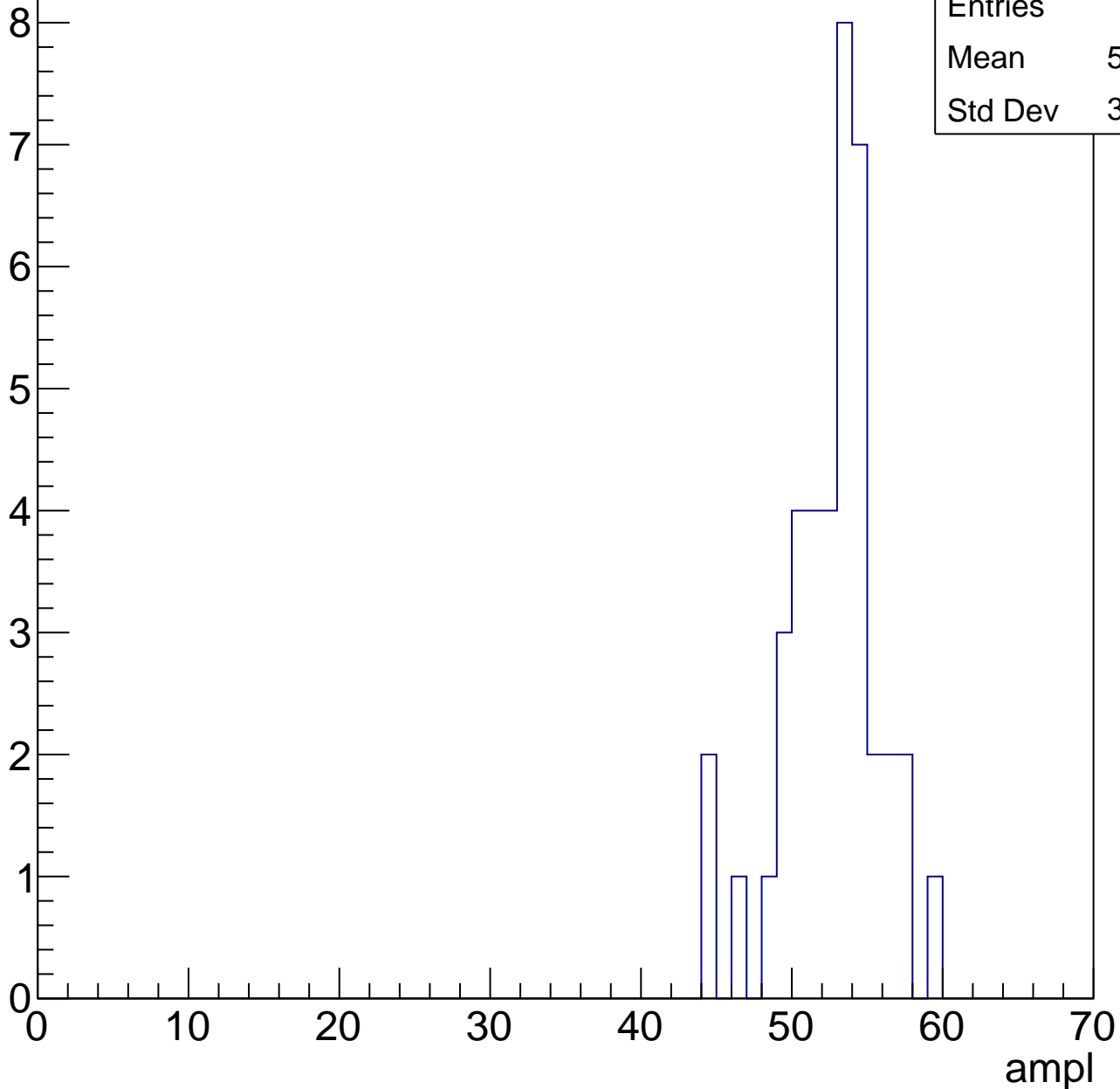
Entries	67
Mean	46.4
Std Dev	6.308

# B1L002S, U17-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	52.15
Std Dev	3.167

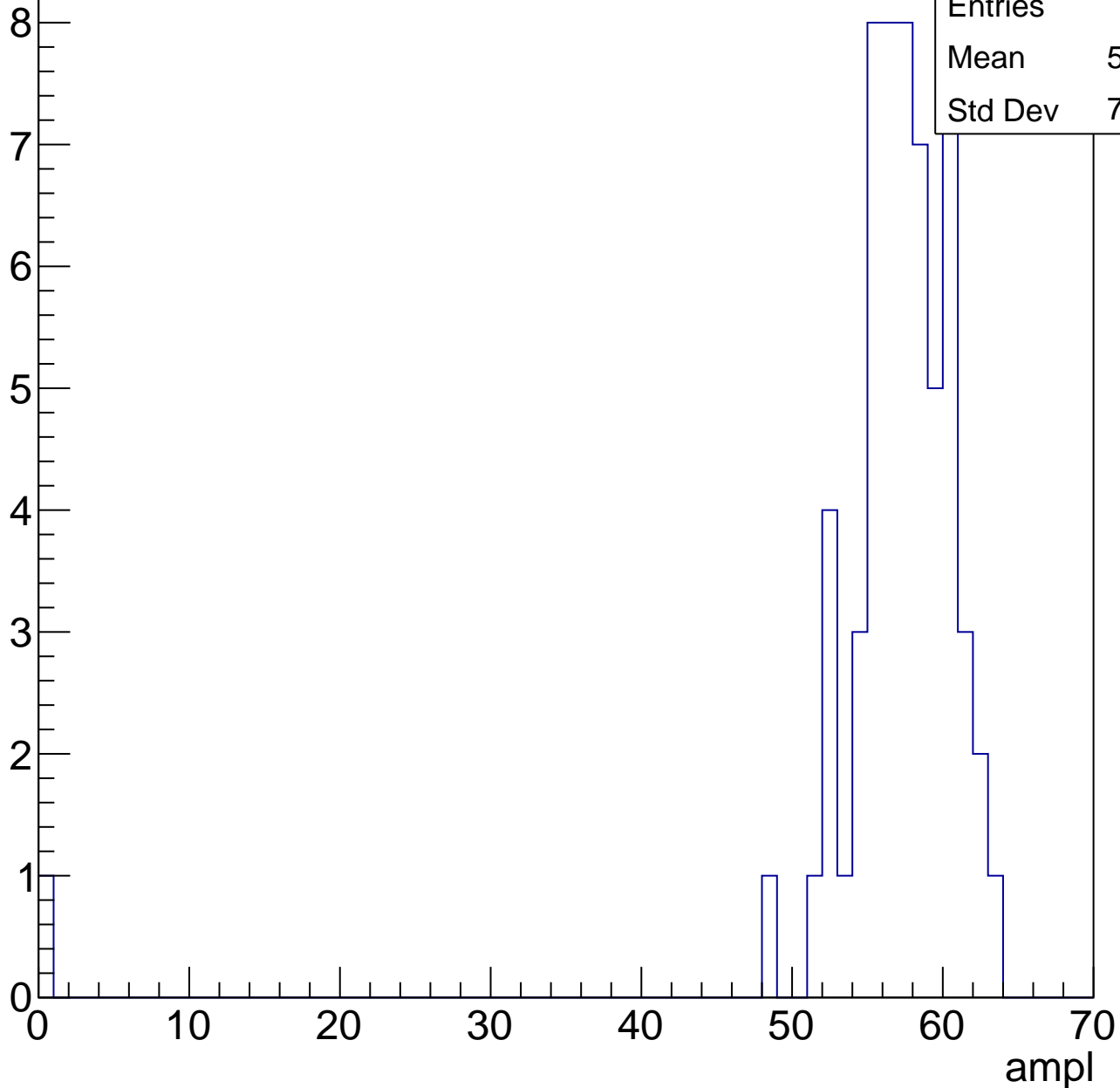


# B1L002S, U17-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

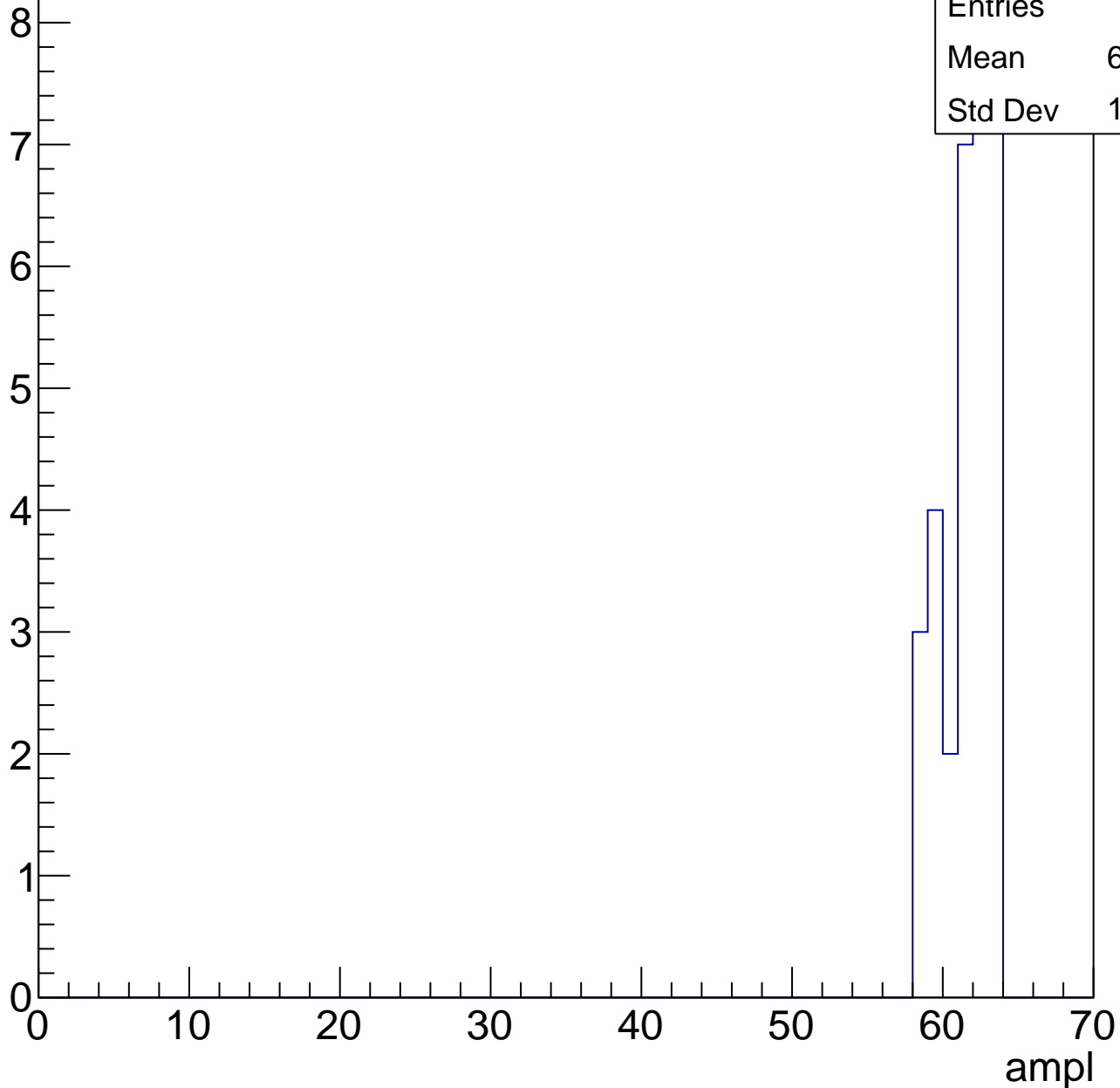
Entries	61
Mean	56.02
Std Dev	7.812



# B1L002S, U17-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U17-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L002S, U17-ch31, adc0

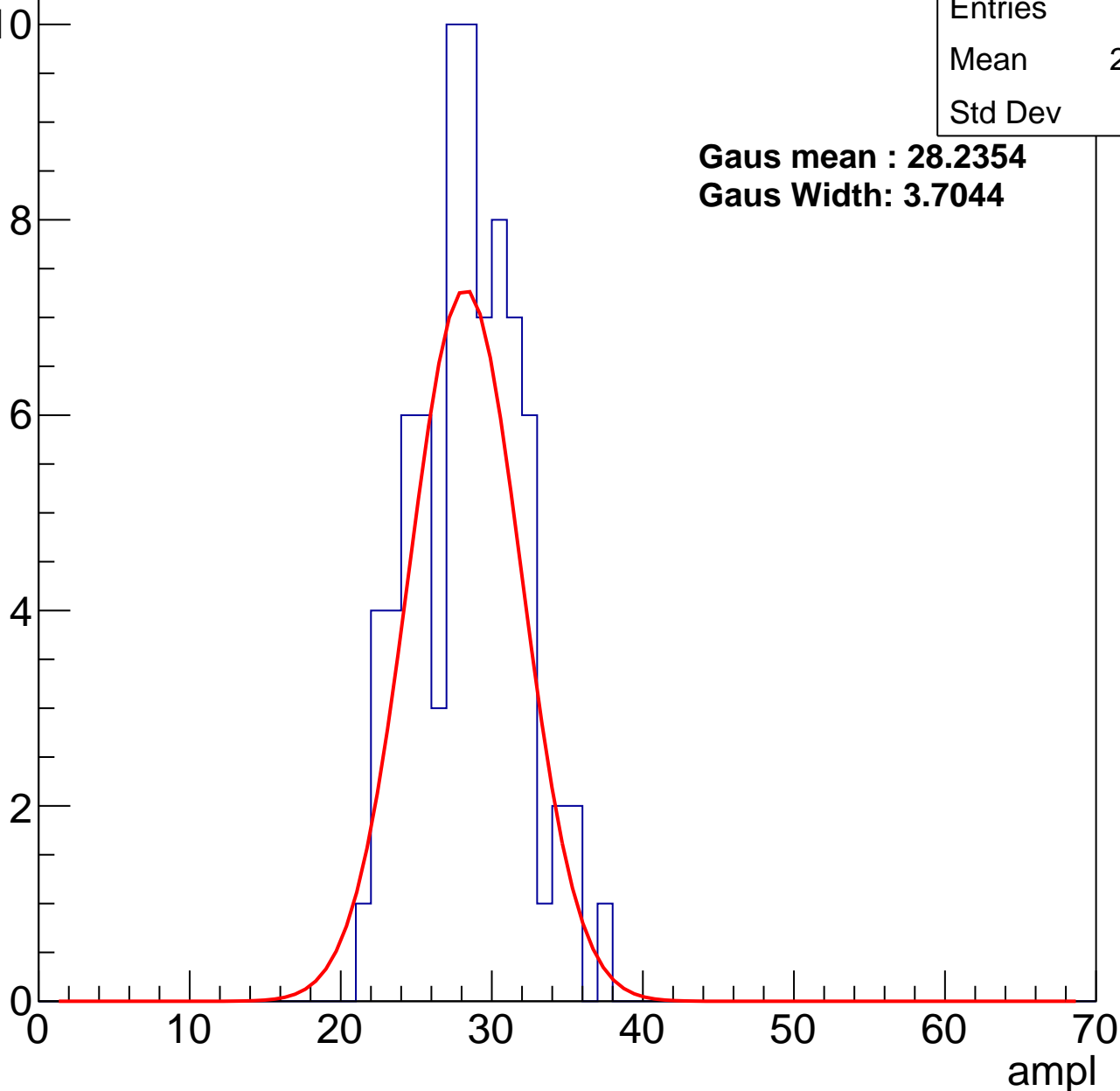
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	27.99
Std Dev	3.47

**Gaus mean : 28.2354**

**Gaus Width: 3.7044**



# B1L002S, U17-ch31, adc1

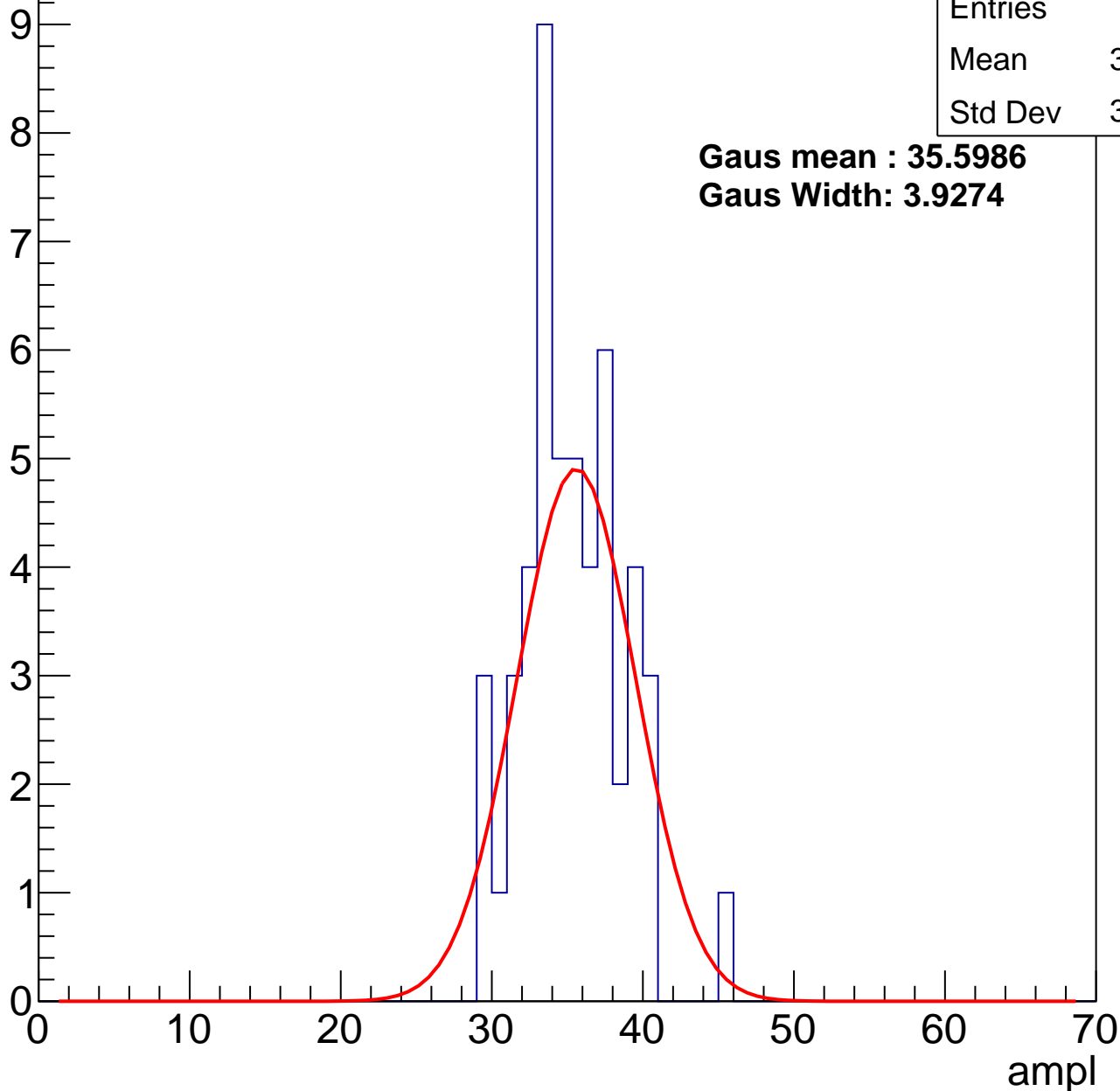
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	34.86
Std Dev	3.292

**Gaus mean : 35.5986**

**Gaus Width: 3.9274**



# B1L002S, U17-ch31, adc2

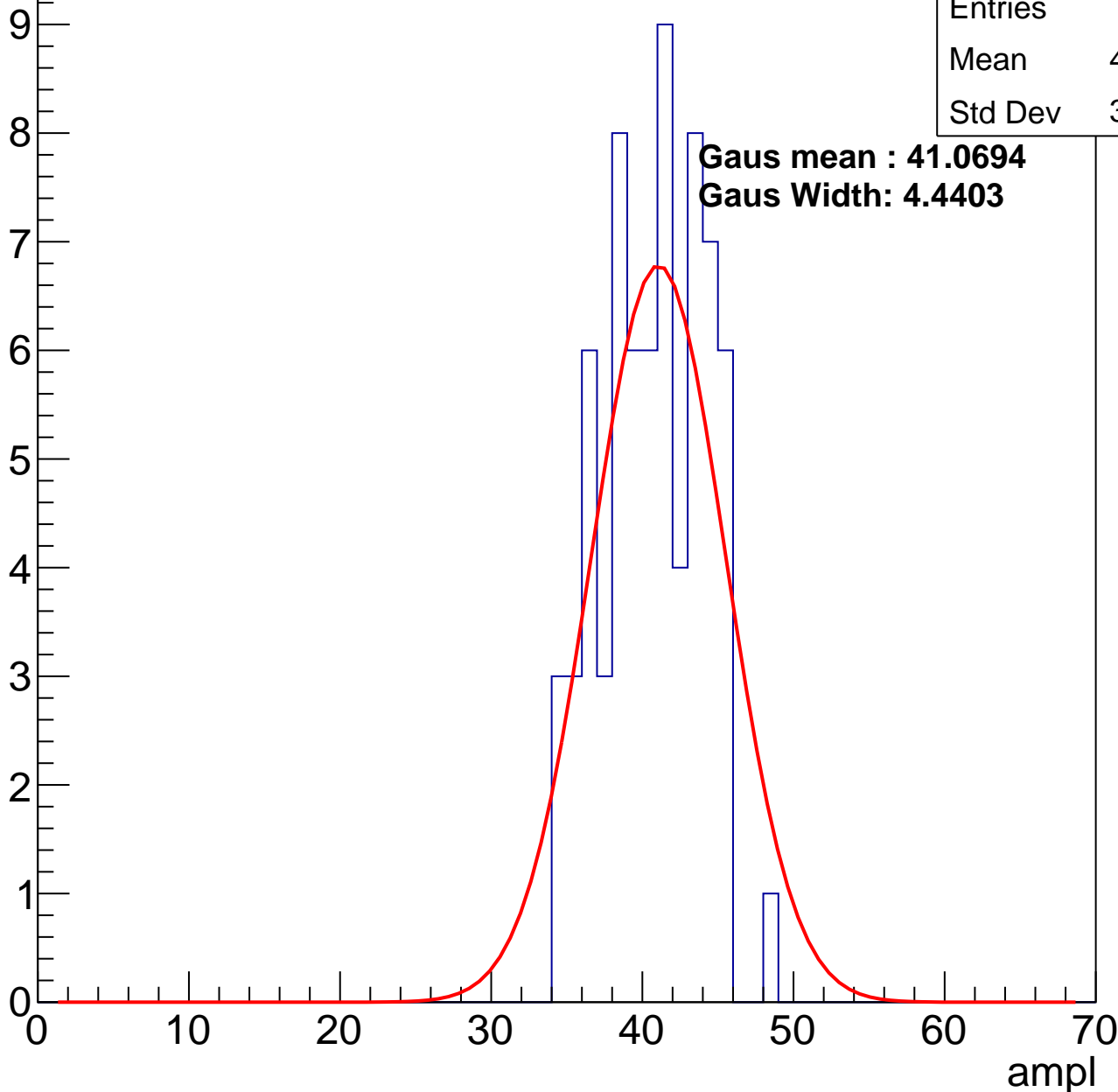
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.27
Std Dev	3.299

**Gaus mean : 41.0694**

**Gaus Width: 4.4403**

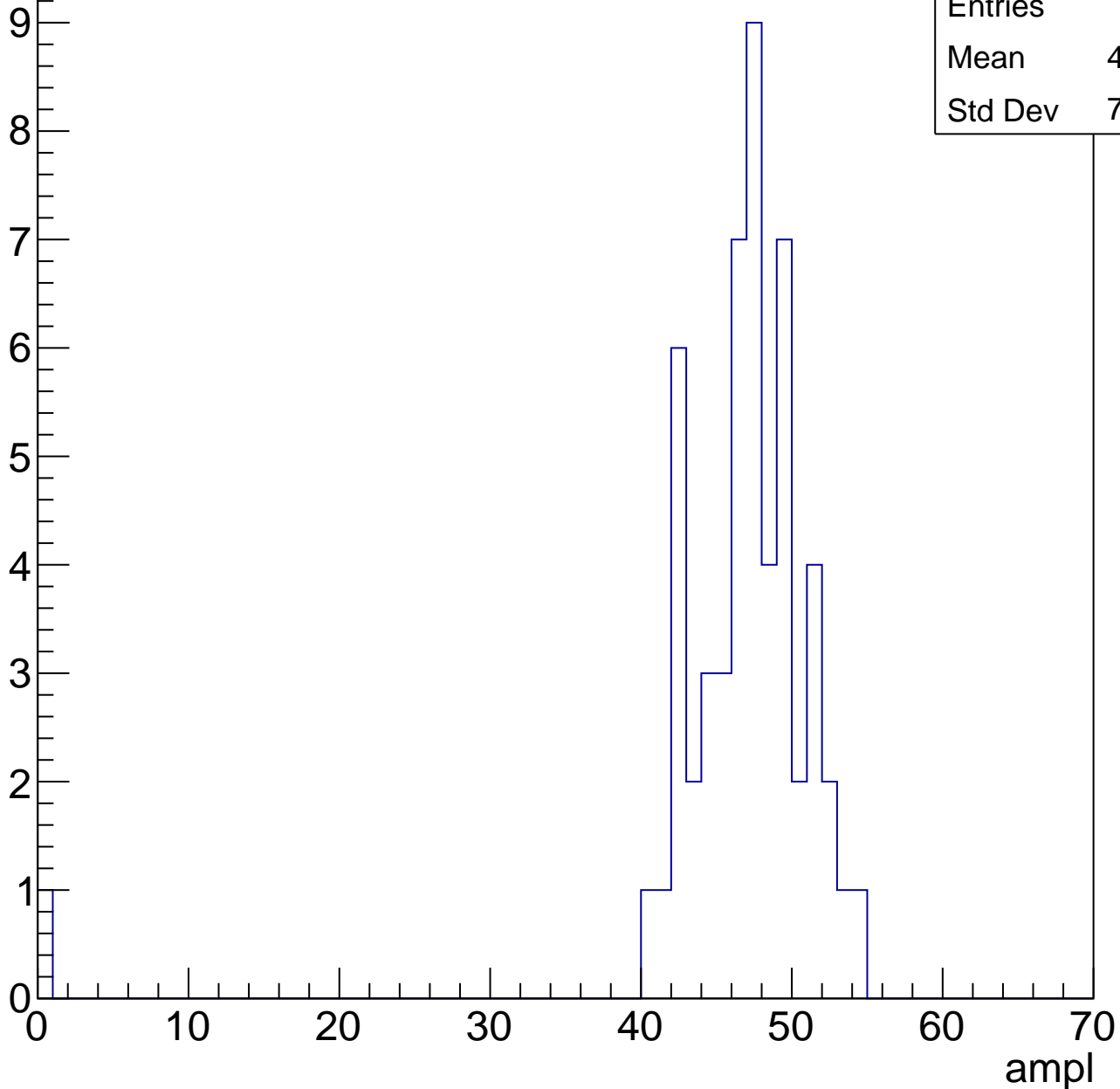


# B1L002S, U17-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

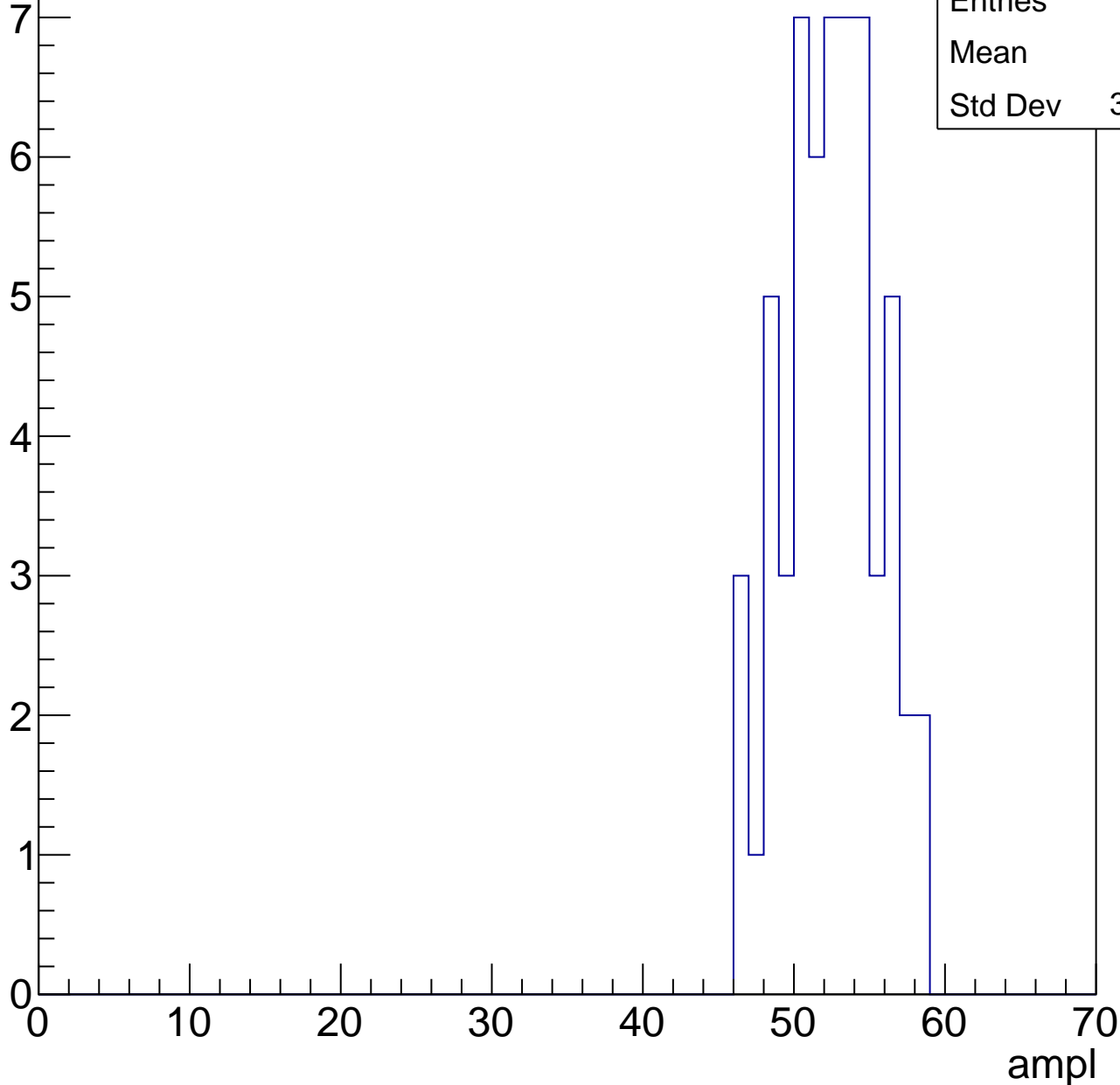
Entries	54
Mean	45.94
Std Dev	7.085



# B1L002S, U17-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



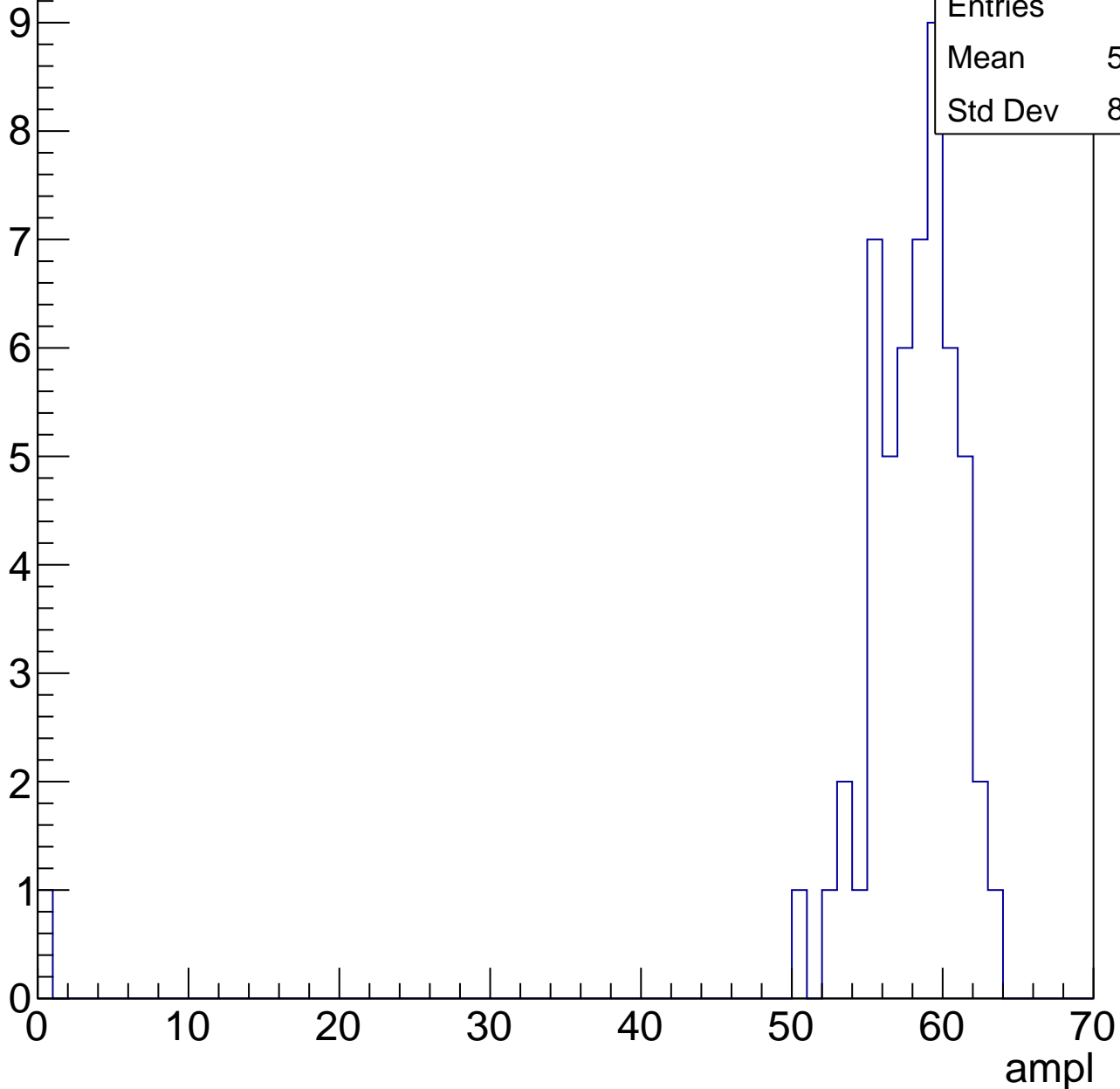
Entries	58
Mean	52
Std Dev	3.046

# B1L002S, U17-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	56.63
Std Dev	8.224

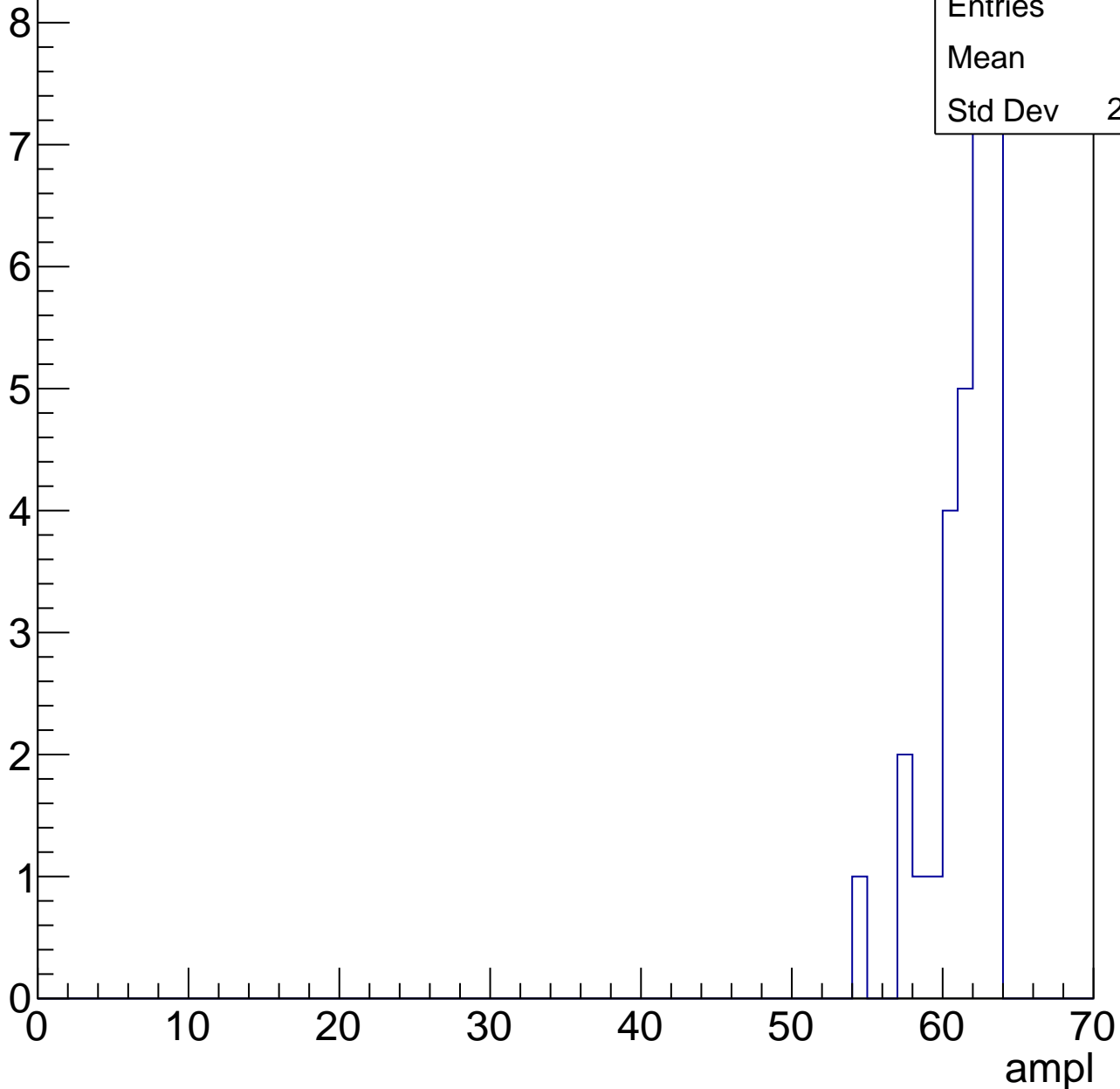


# B1L002S, U17-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	61
Std Dev	2.145

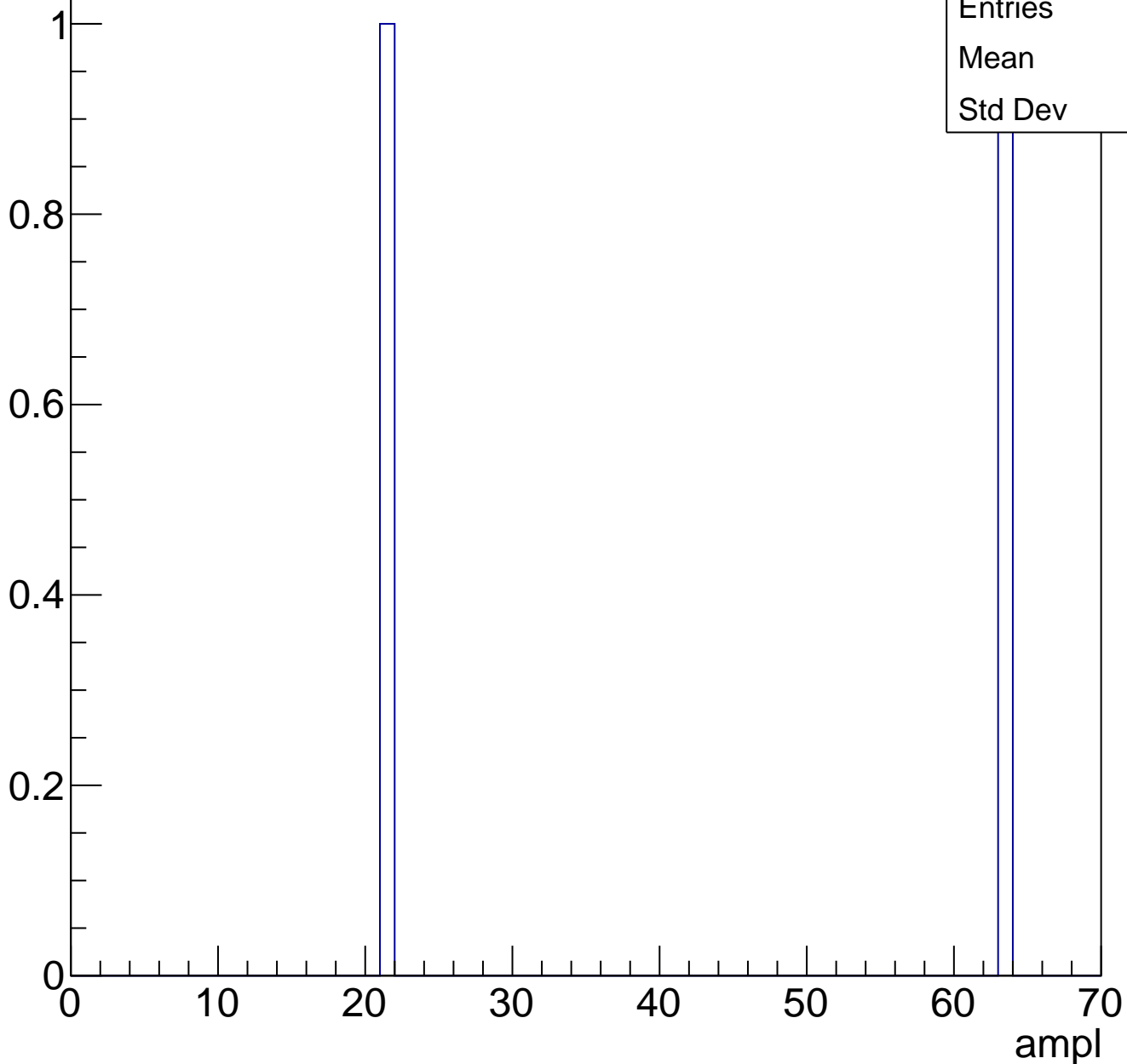




# B1L002S, U17-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch32, adc0

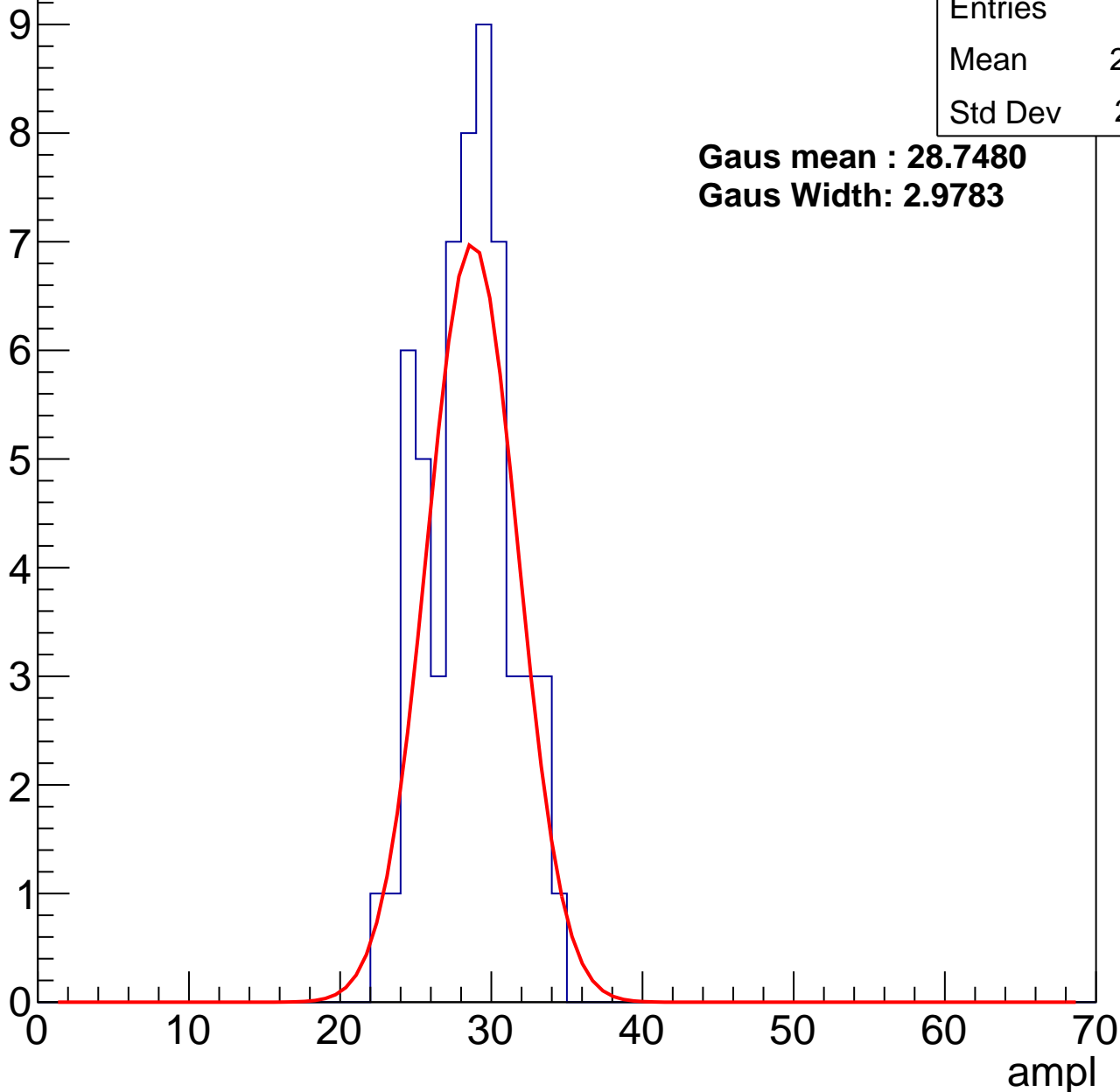
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.04
Std Dev	2.791

**Gaus mean : 28.7480**

**Gaus Width: 2.9783**



# B1L002S, U17-ch32, adc1

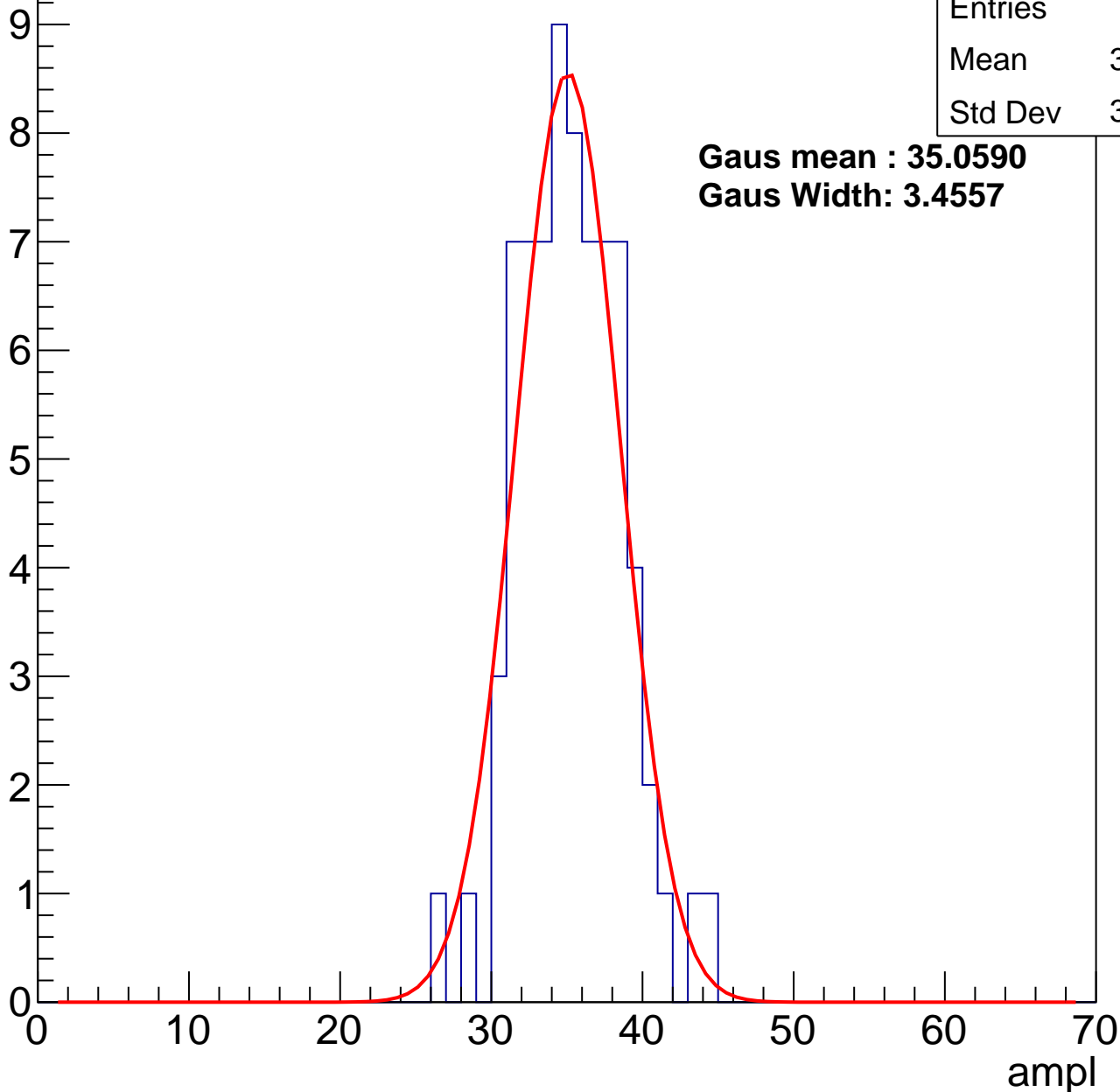
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34.84
Std Dev	3.327

**Gaus mean : 35.0590**

**Gaus Width: 3.4557**



# B1L002S, U17-ch32, adc2

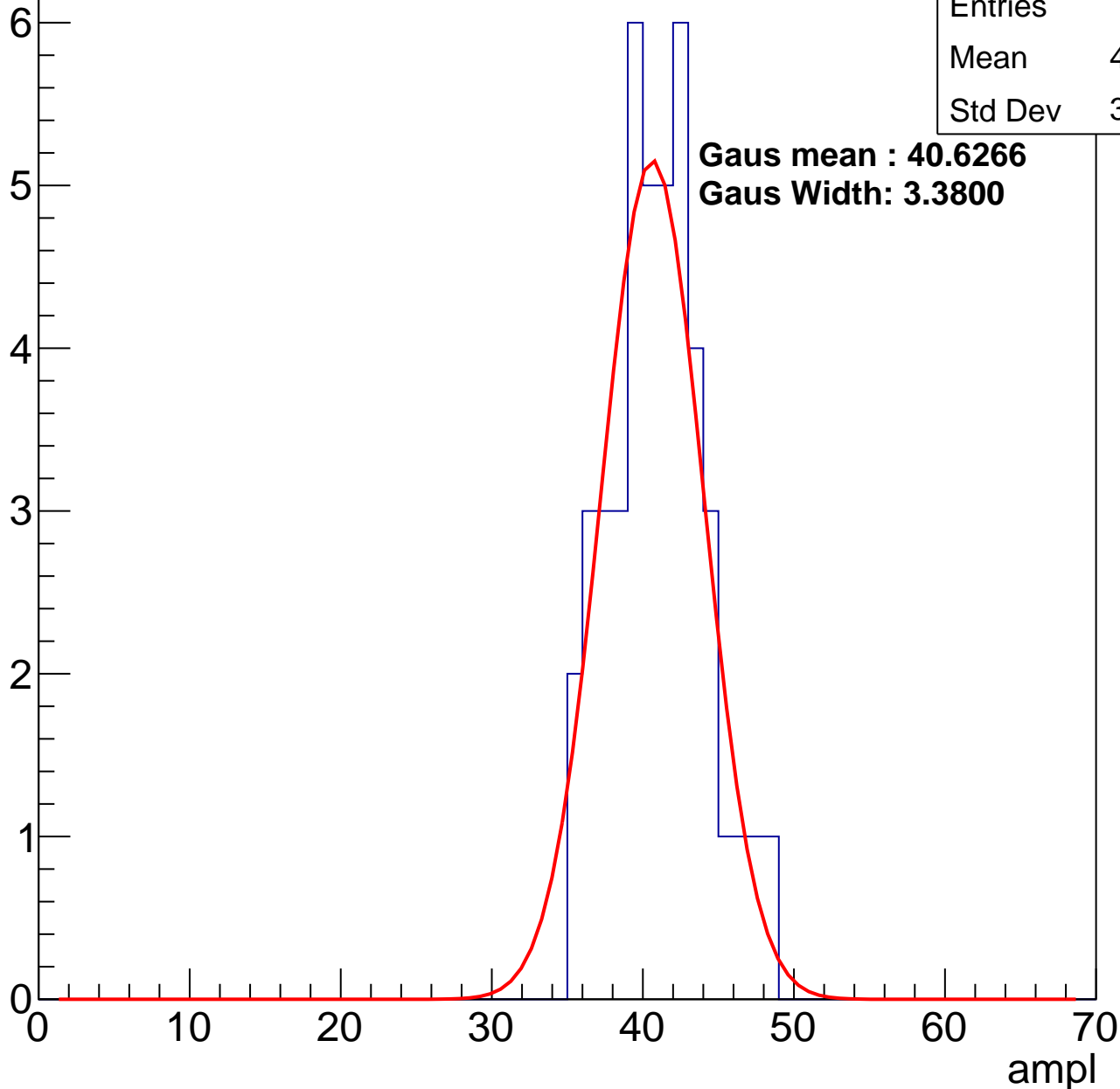
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	40.55
Std Dev	3.078

**Gaus mean : 40.6266**

**Gaus Width: 3.3800**

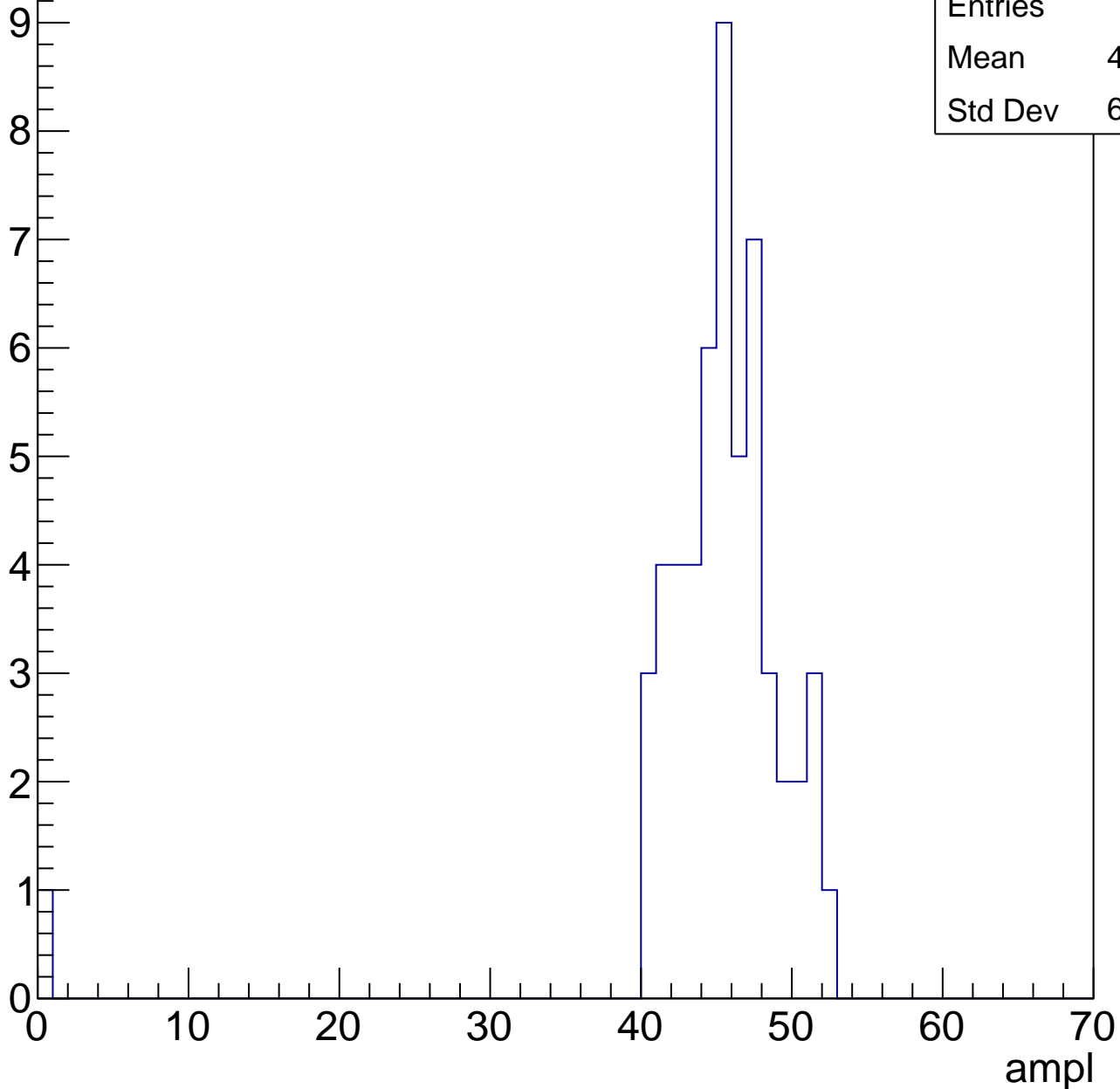


# B1L002S, U17-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	44.43
Std Dev	6.808

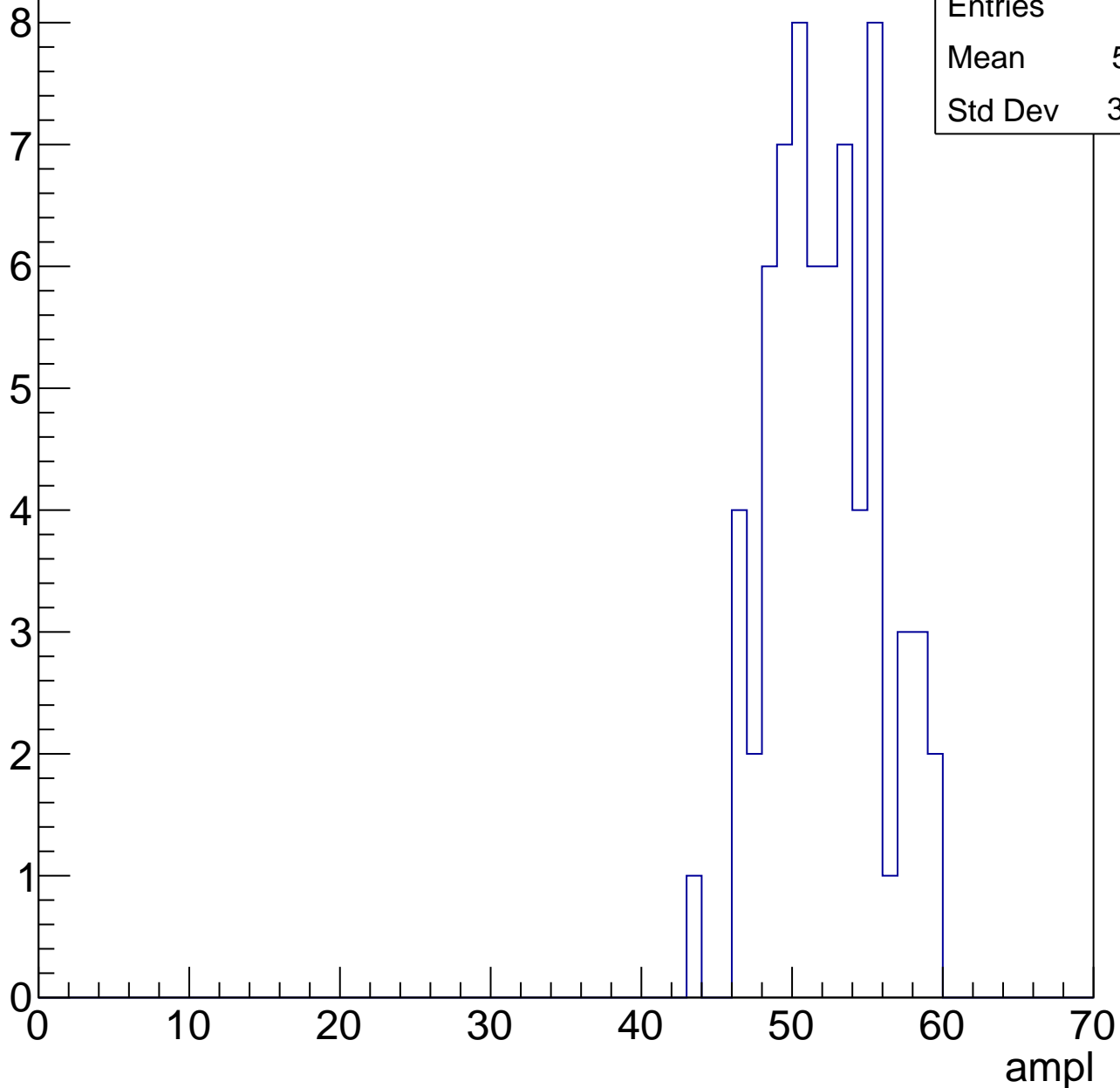


# B1L002S, U17-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	51.71
Std Dev	3.552

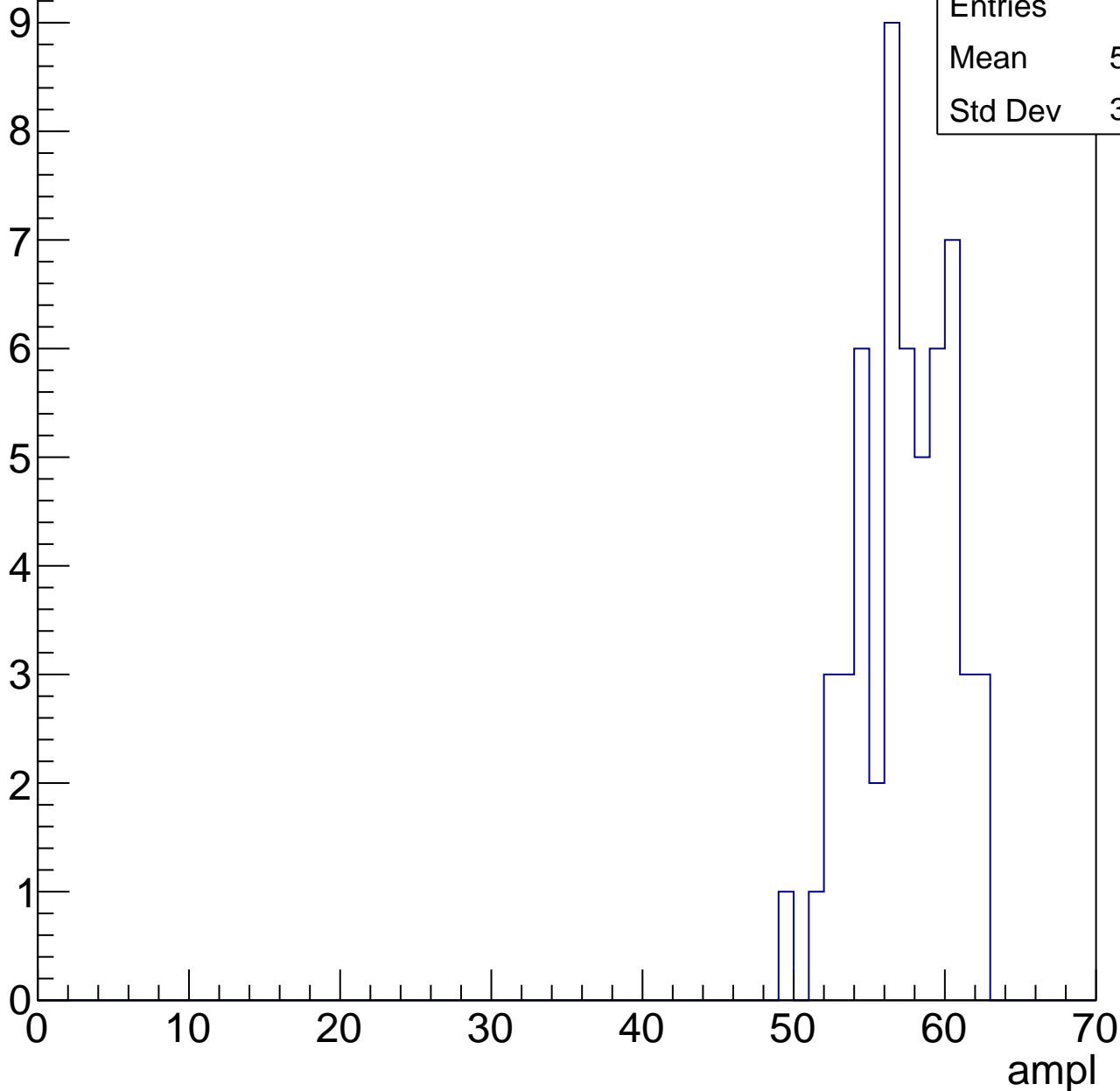


# B1L002S, U17-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	56.87
Std Dev	3.039

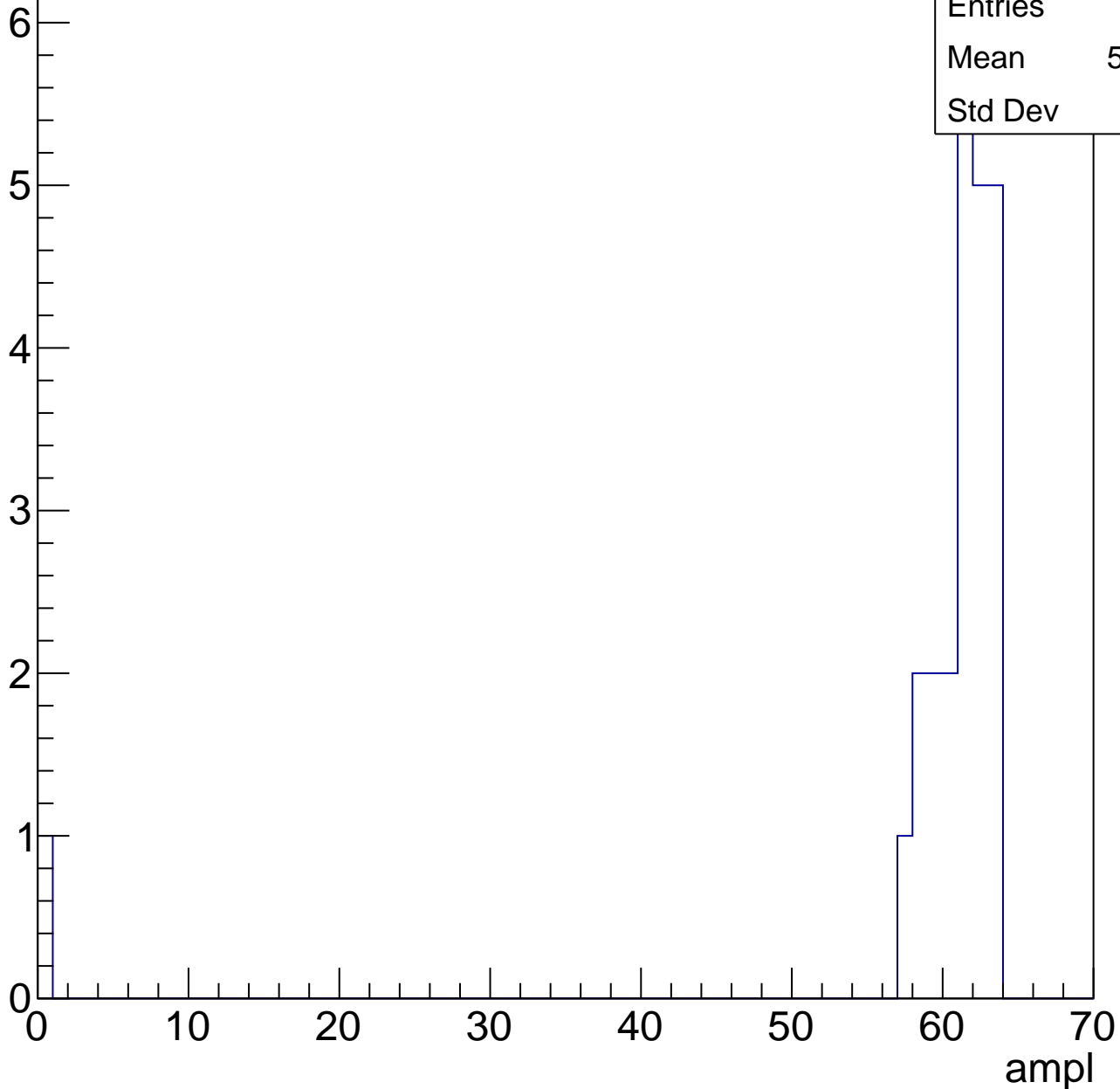


# B1L002S, U17-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.42
Std Dev	12.3



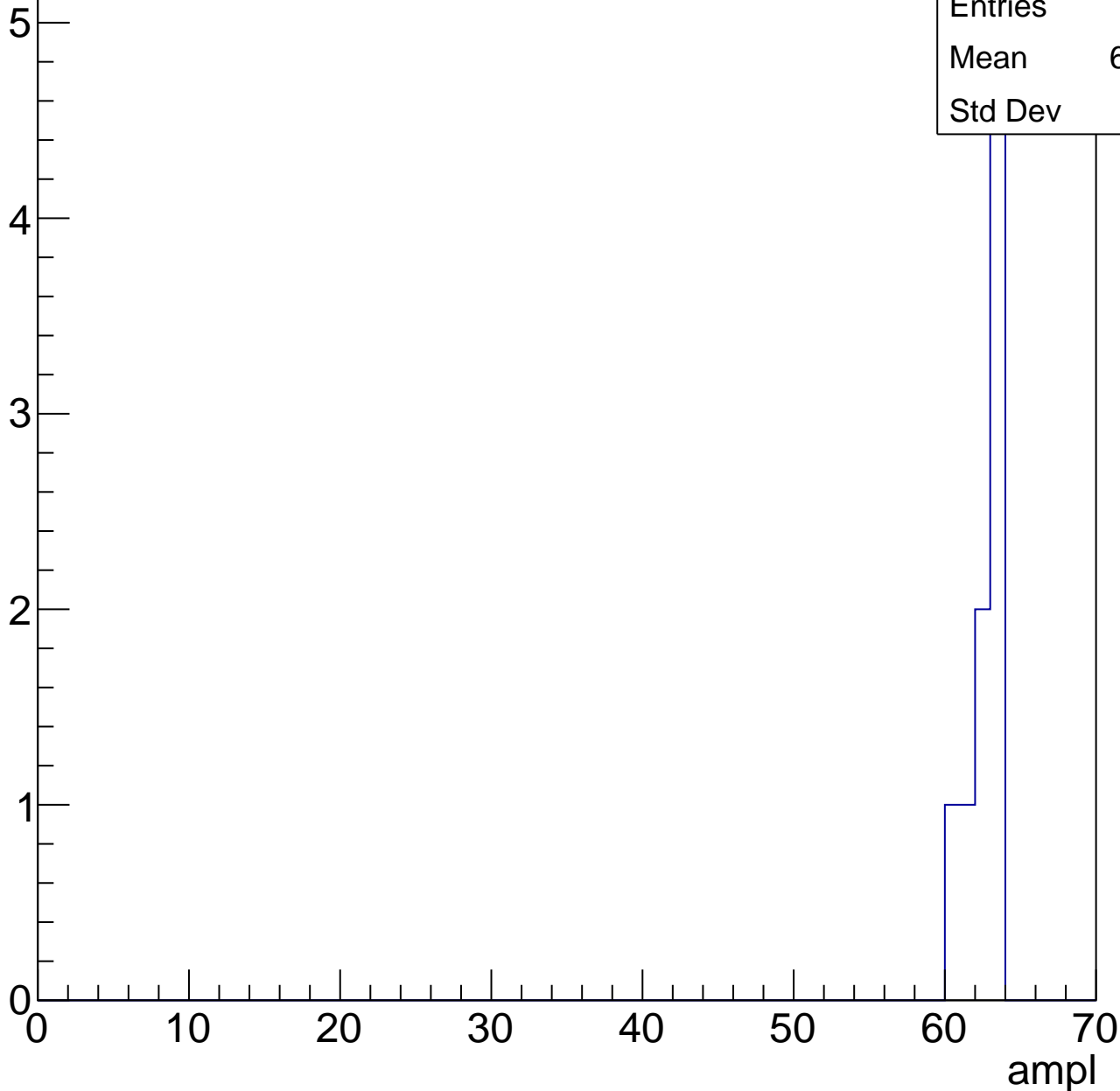


# B1L002S, U17-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	9
Mean	62.22
Std Dev	1.03



# B1L002S, U17-ch33, adc0

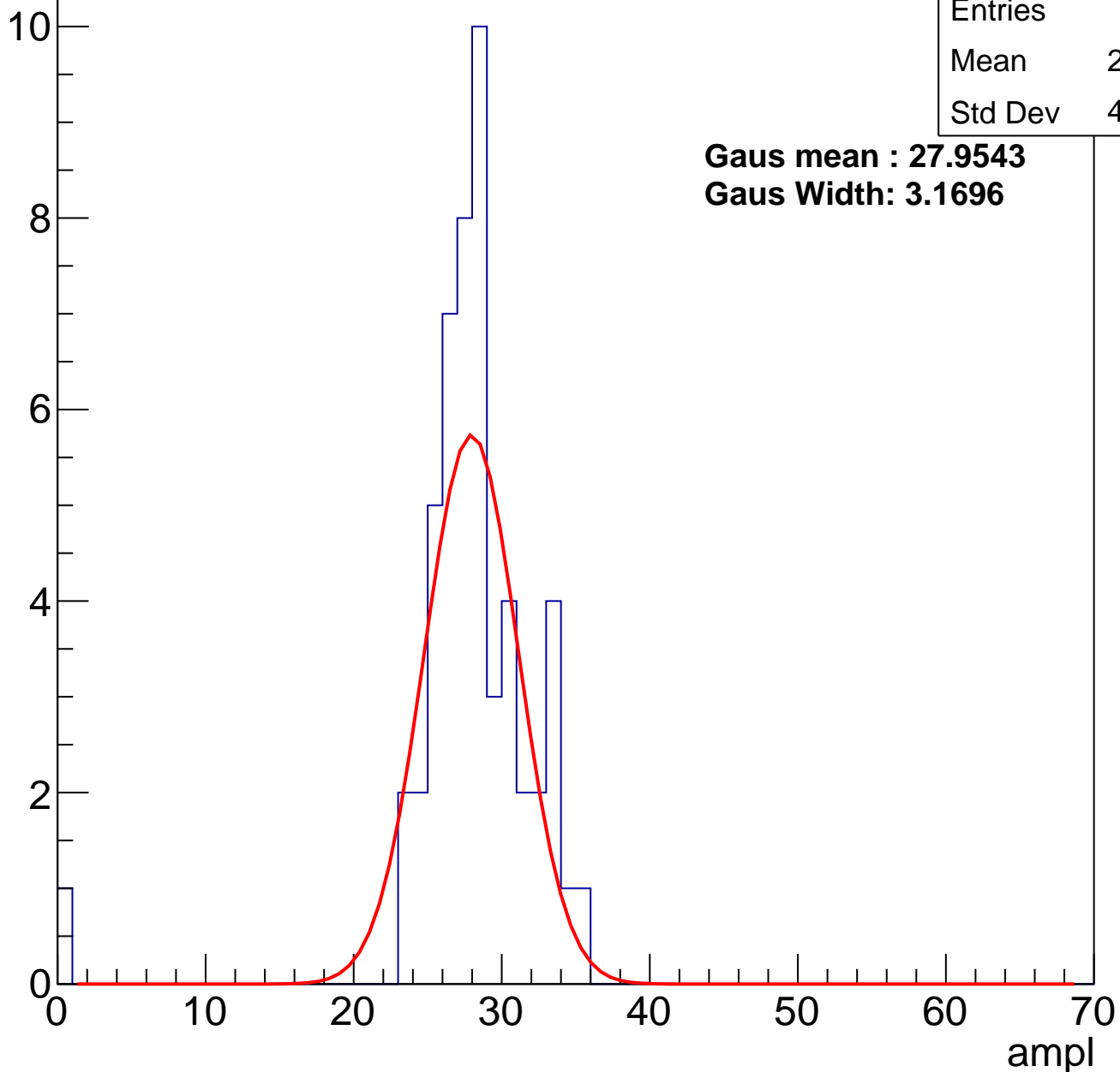
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	52
Mean	27.52
Std Dev	4.782

**Gaus mean : 27.9543**

**Gaus Width: 3.1696**

Entry



# B1L002S, U17-ch33, adc1

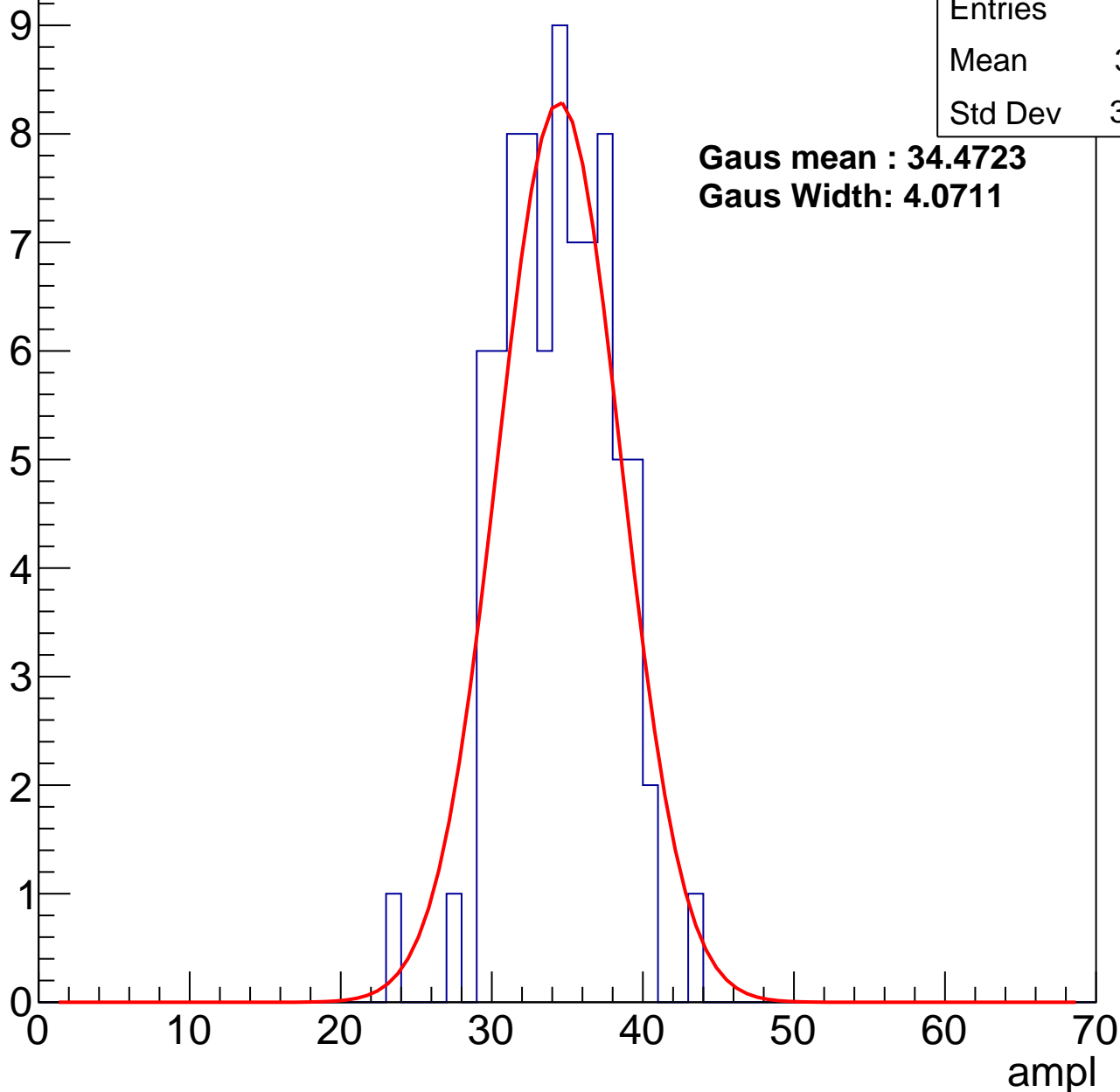
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	33.91
Std Dev	3.519

**Gaus mean : 34.4723**

**Gaus Width: 4.0711**



# B1L002S, U17-ch33, adc2

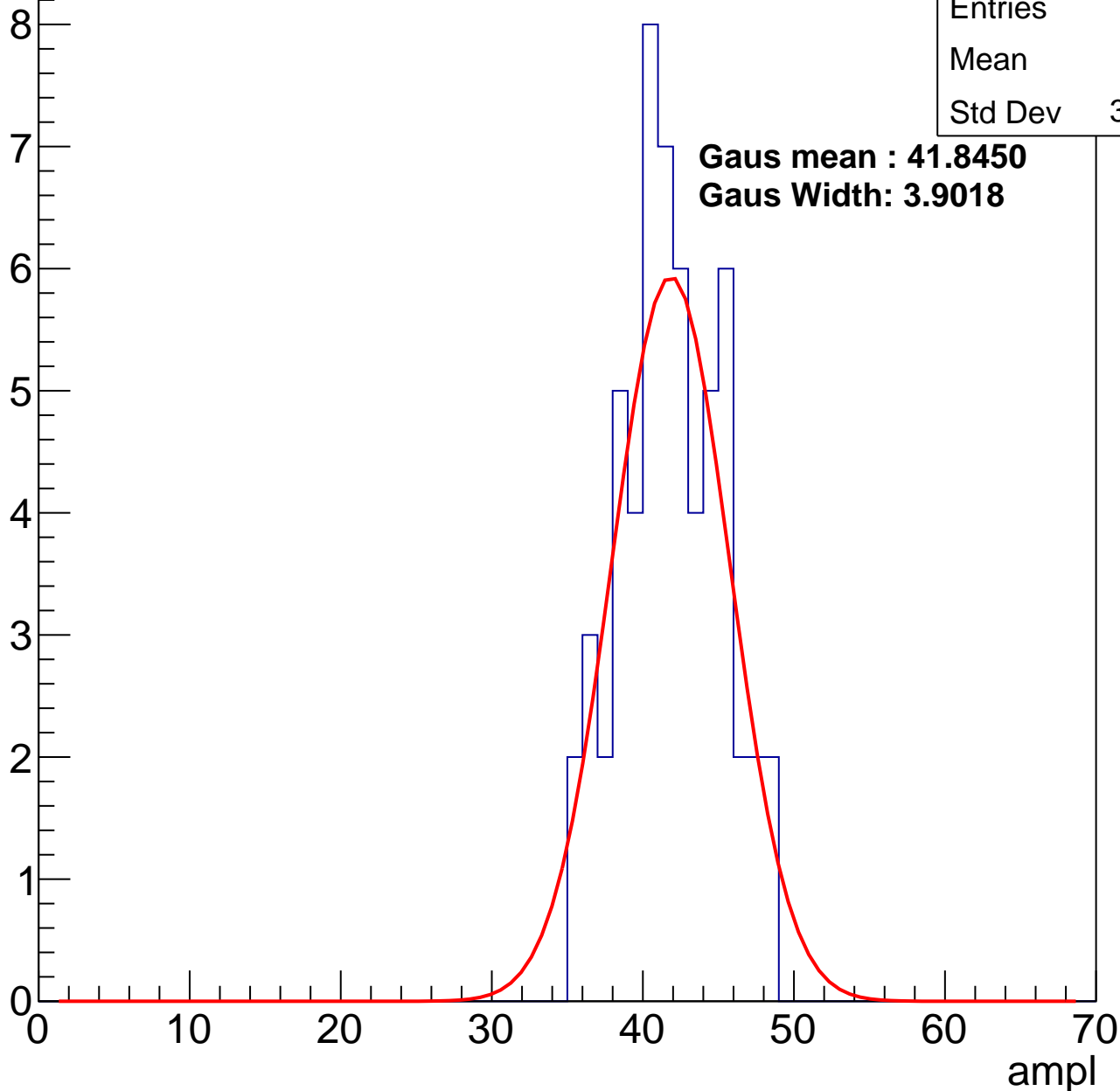
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.4
Std Dev	3.274

**Gaus mean : 41.8450**

**Gaus Width: 3.9018**

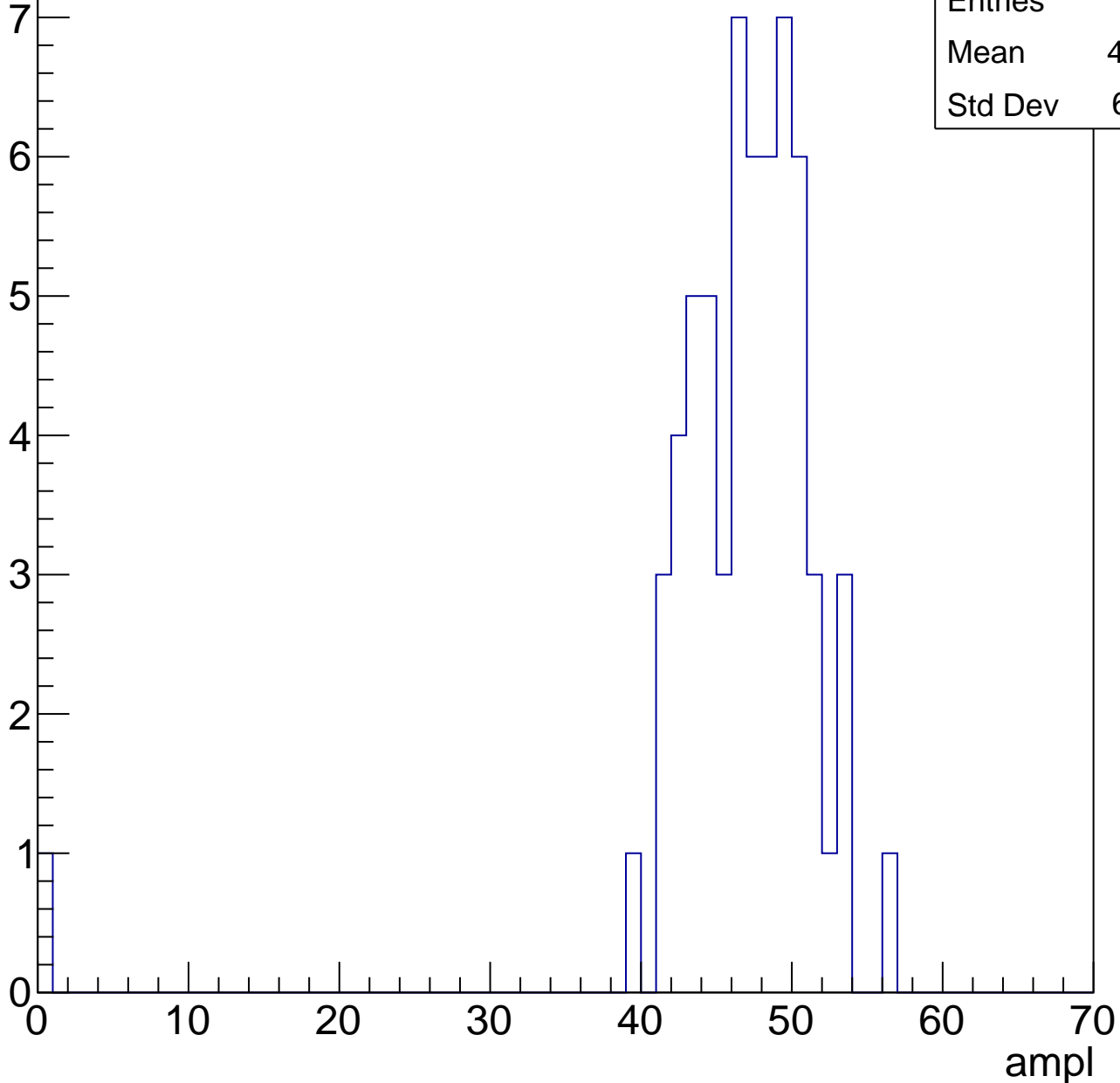


# B1L002S, U17-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

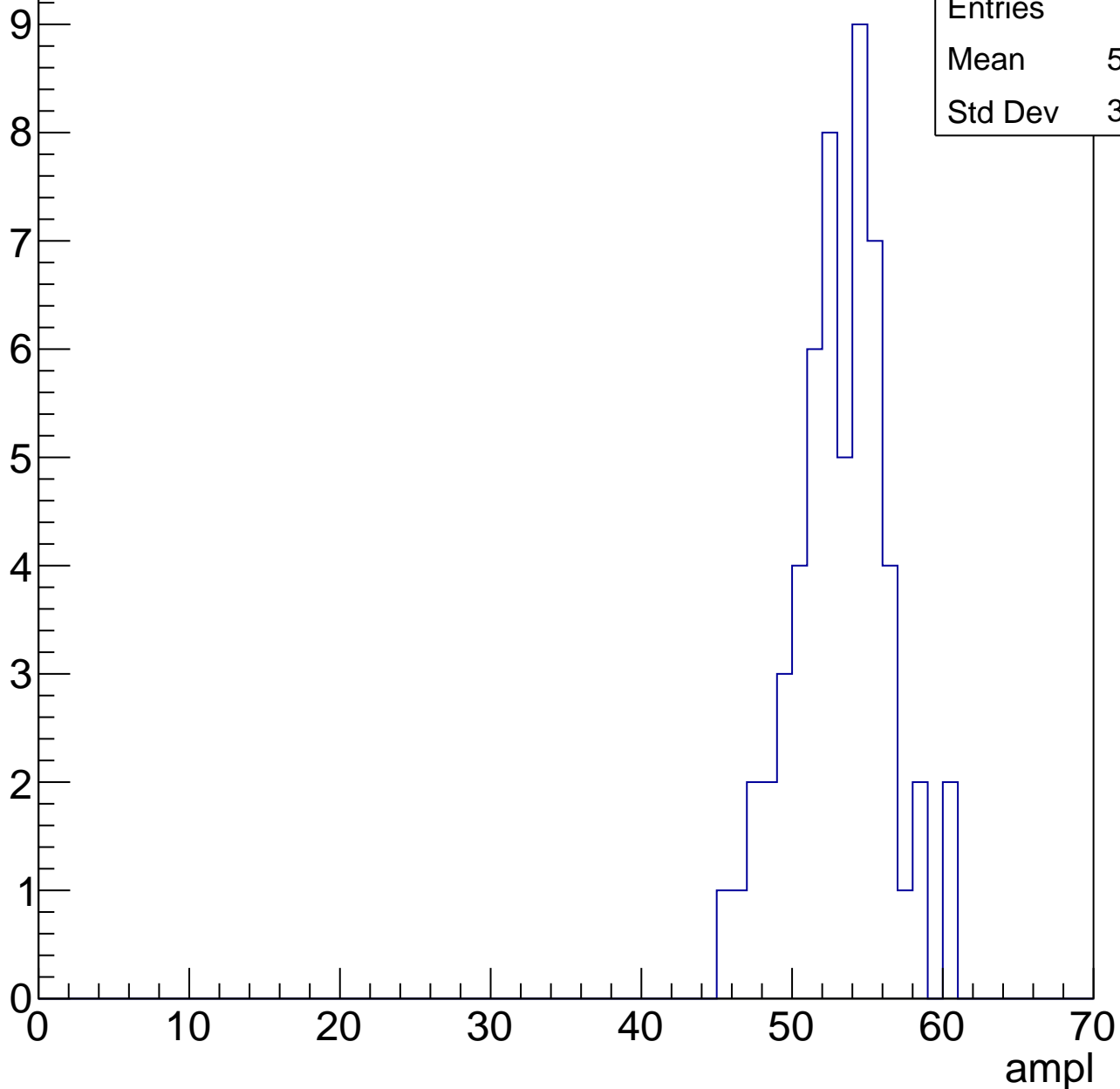
Entries	62
Mean	46.05
Std Dev	6.861



# B1L002S, U17-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

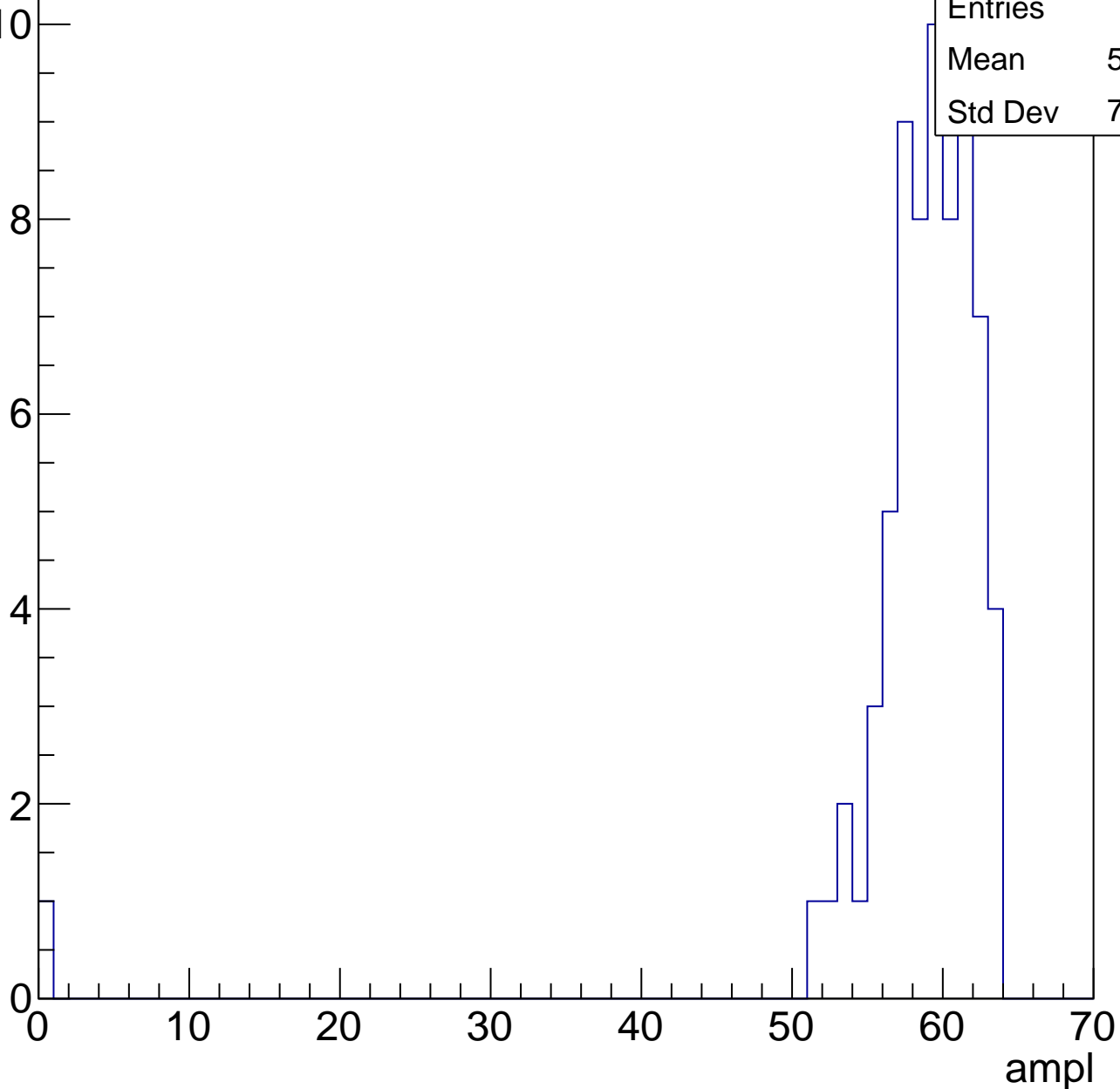


# B1L002S, U17-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	57.83
Std Dev	7.522

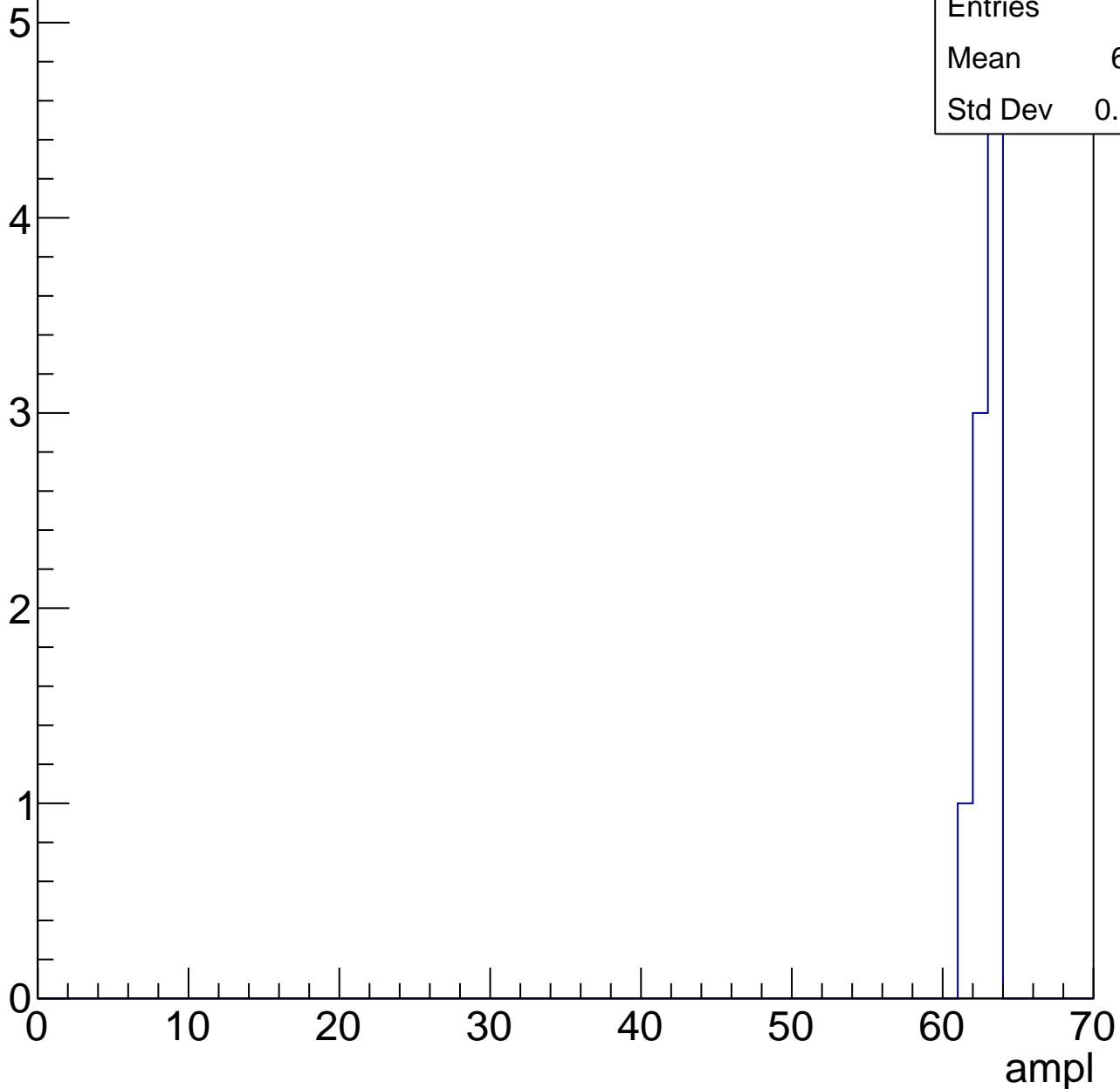


# B1L002S, U17-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	9
Mean	62.44
Std Dev	0.6849





# B1L002S, U17-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L002S, U17-ch34, adc0

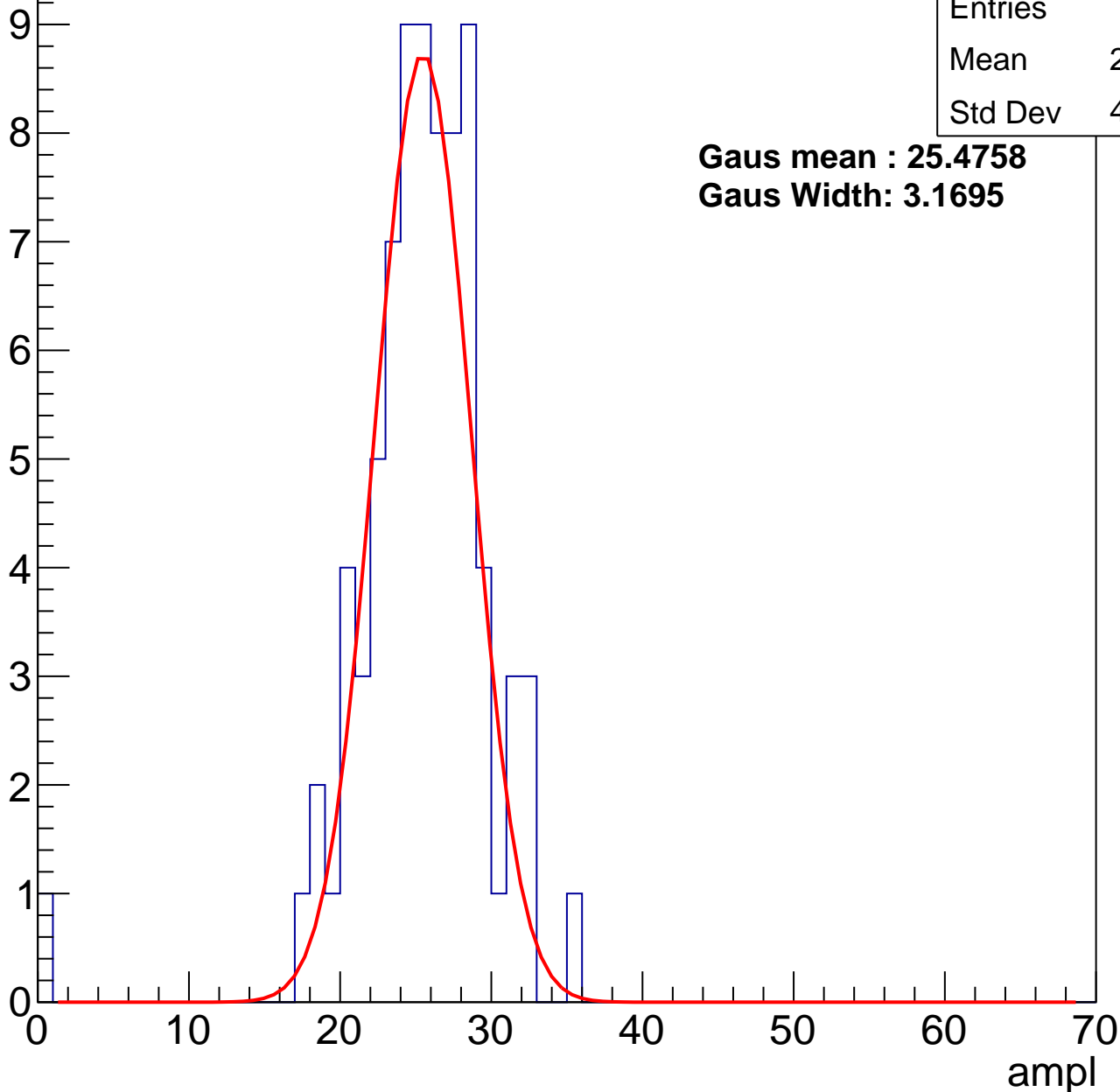
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	24.97
Std Dev	4.526

**Gaus mean : 25.4758**

**Gaus Width: 3.1695**



# B1L002S, U17-ch34, adc1

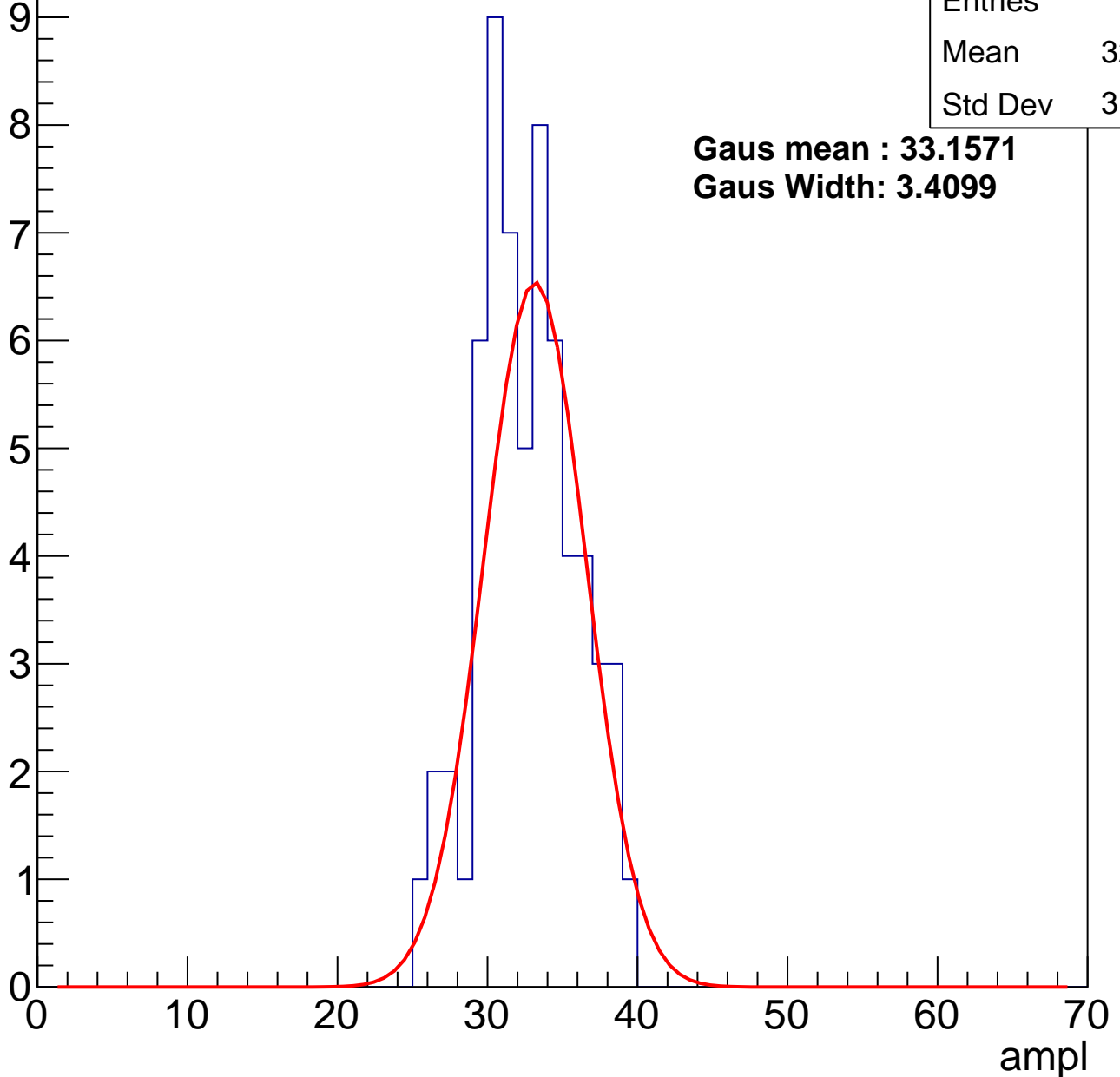
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	32.19
Std Dev	3.227

**Gaus mean : 33.1571**

**Gaus Width: 3.4099**



# B1L002S, U17-ch34, adc2

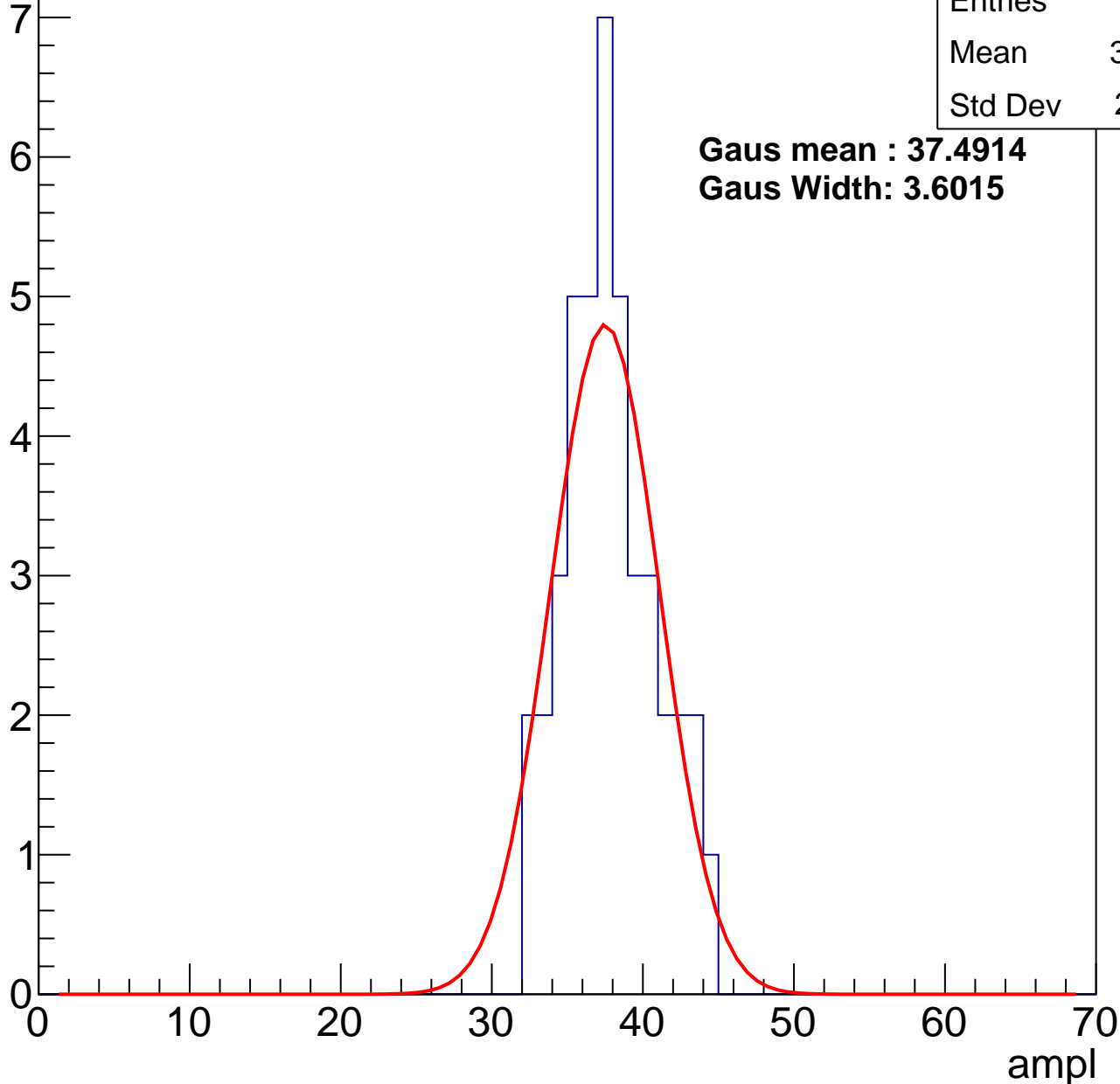
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	37.36
Std Dev	2.991

**Gaus mean : 37.4914**

**Gaus Width: 3.6015**

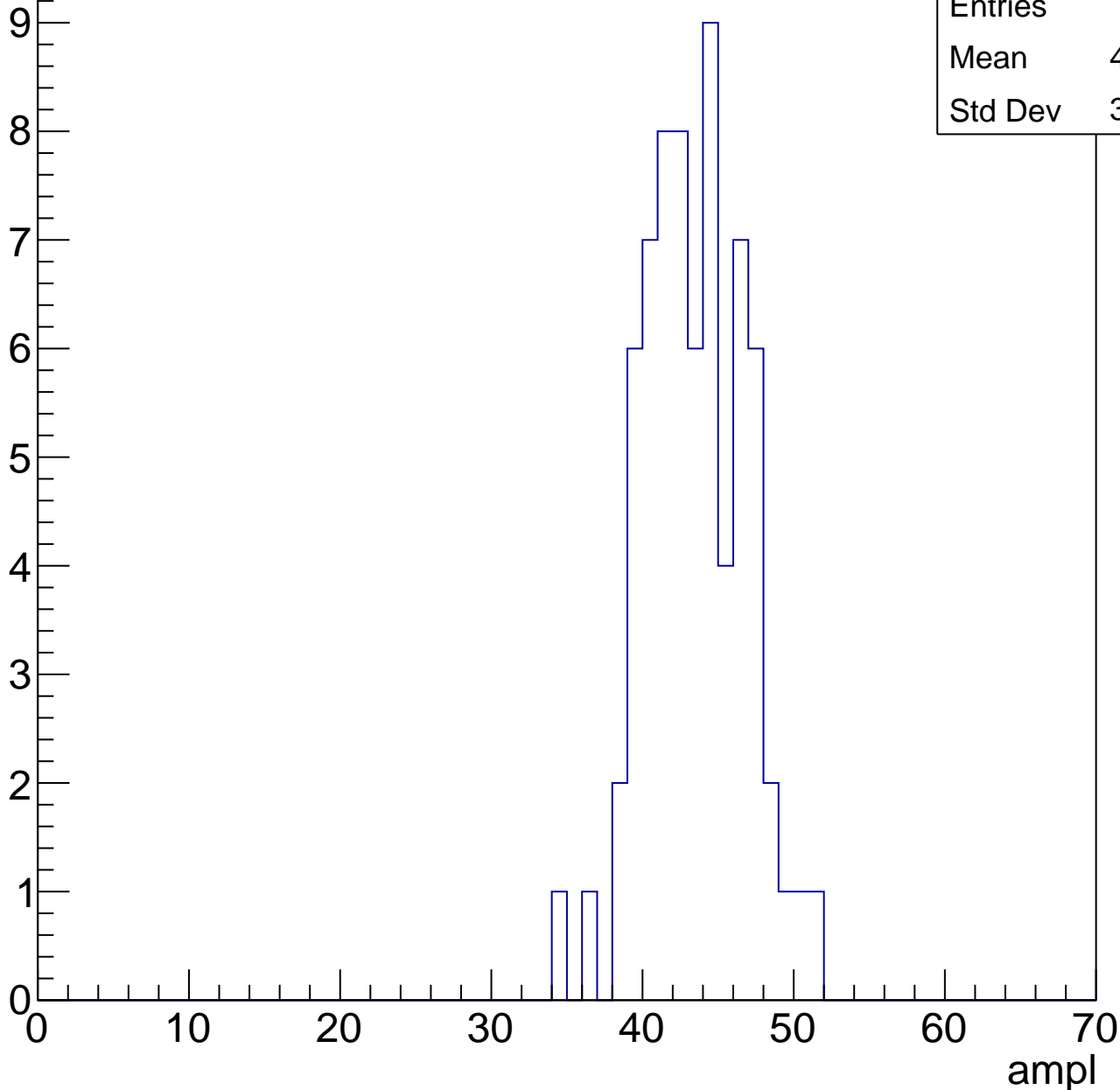


# B1L002S, U17-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	42.97
Std Dev	3.299

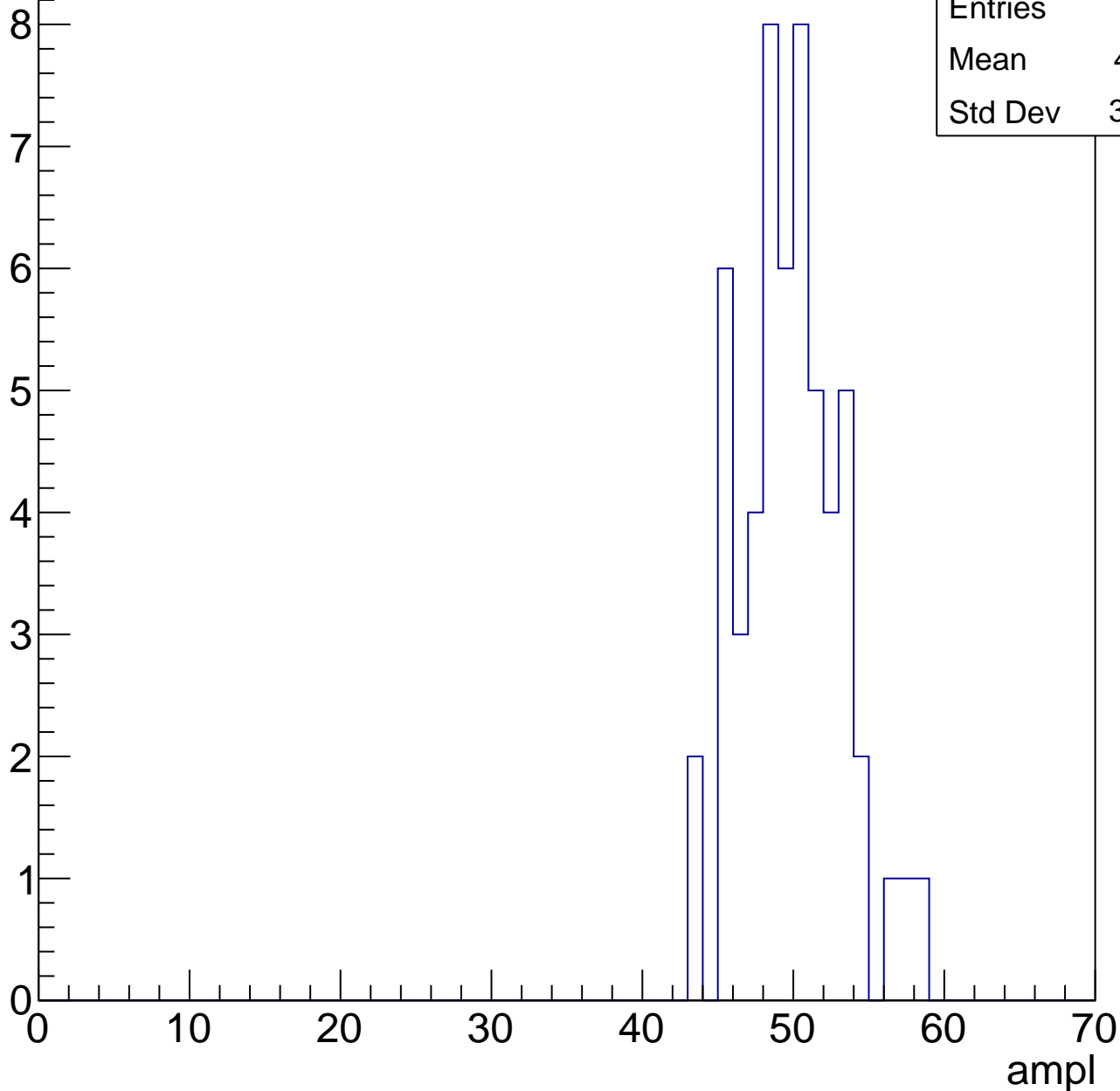


# B1L002S, U17-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	49.41
Std Dev	3.267



# B1L002S, U17-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

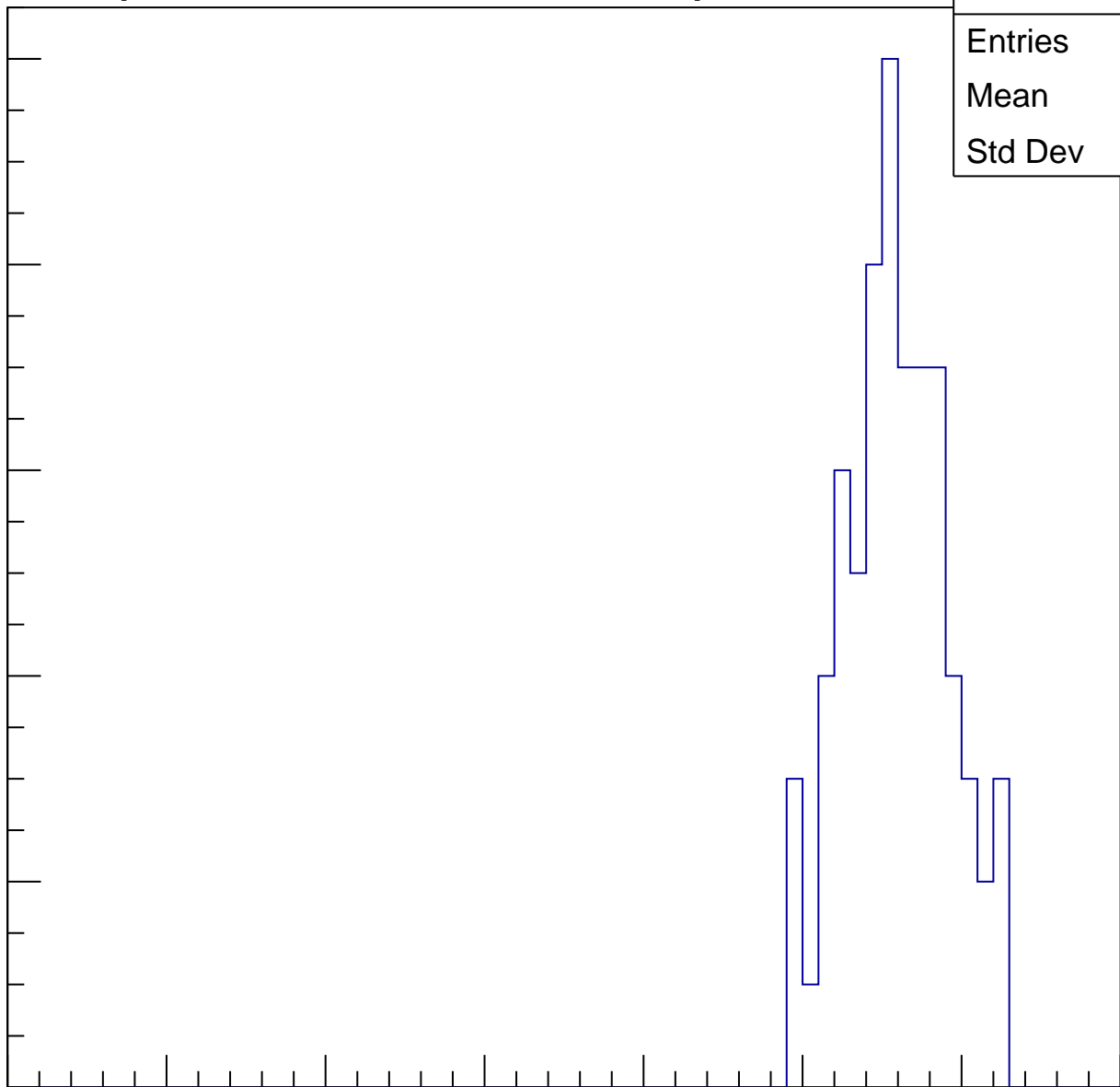
Entries	70
Mean	55.44
Std Dev	3.21

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

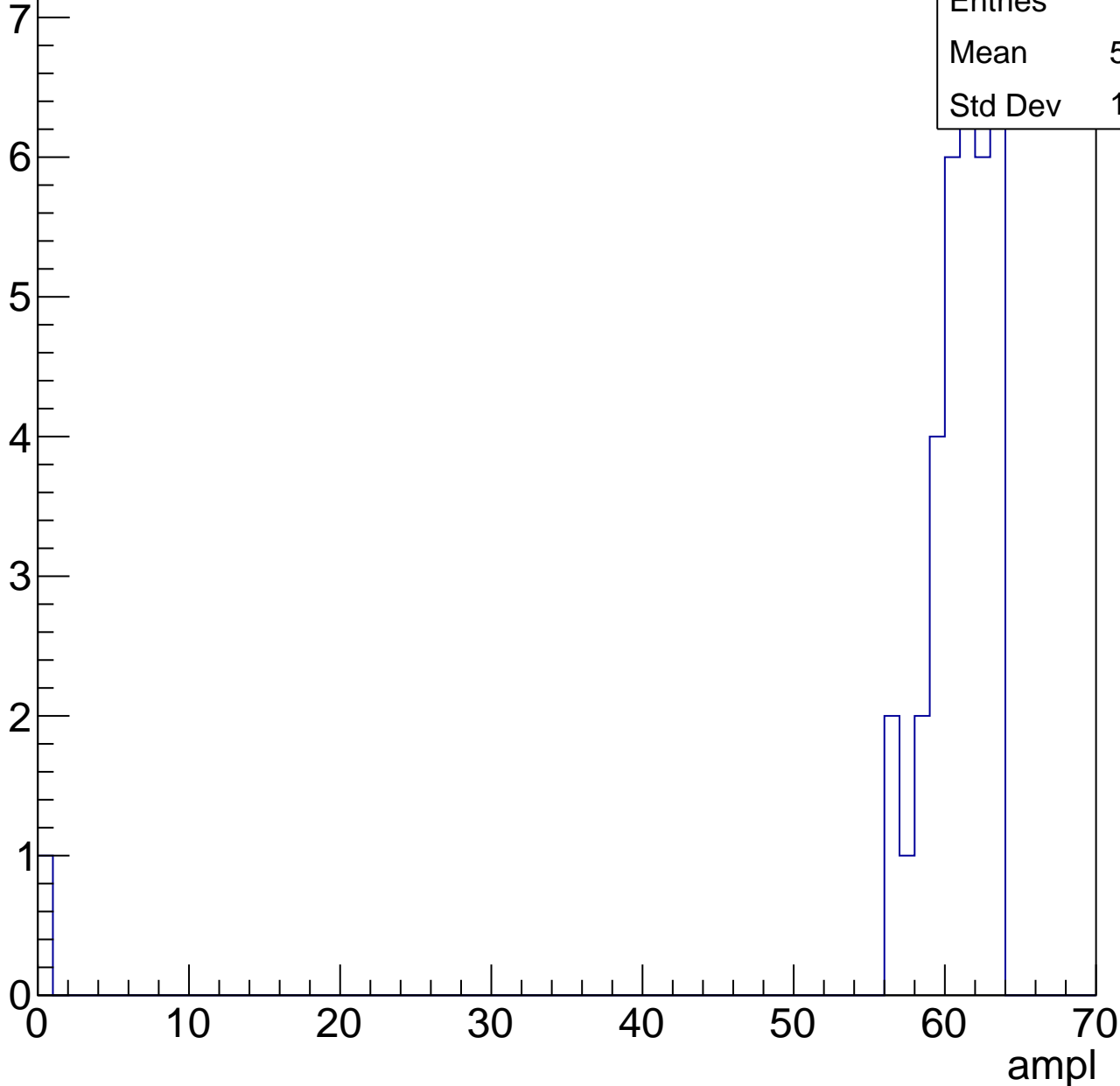


# B1L002S, U17-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.92
Std Dev	10.14

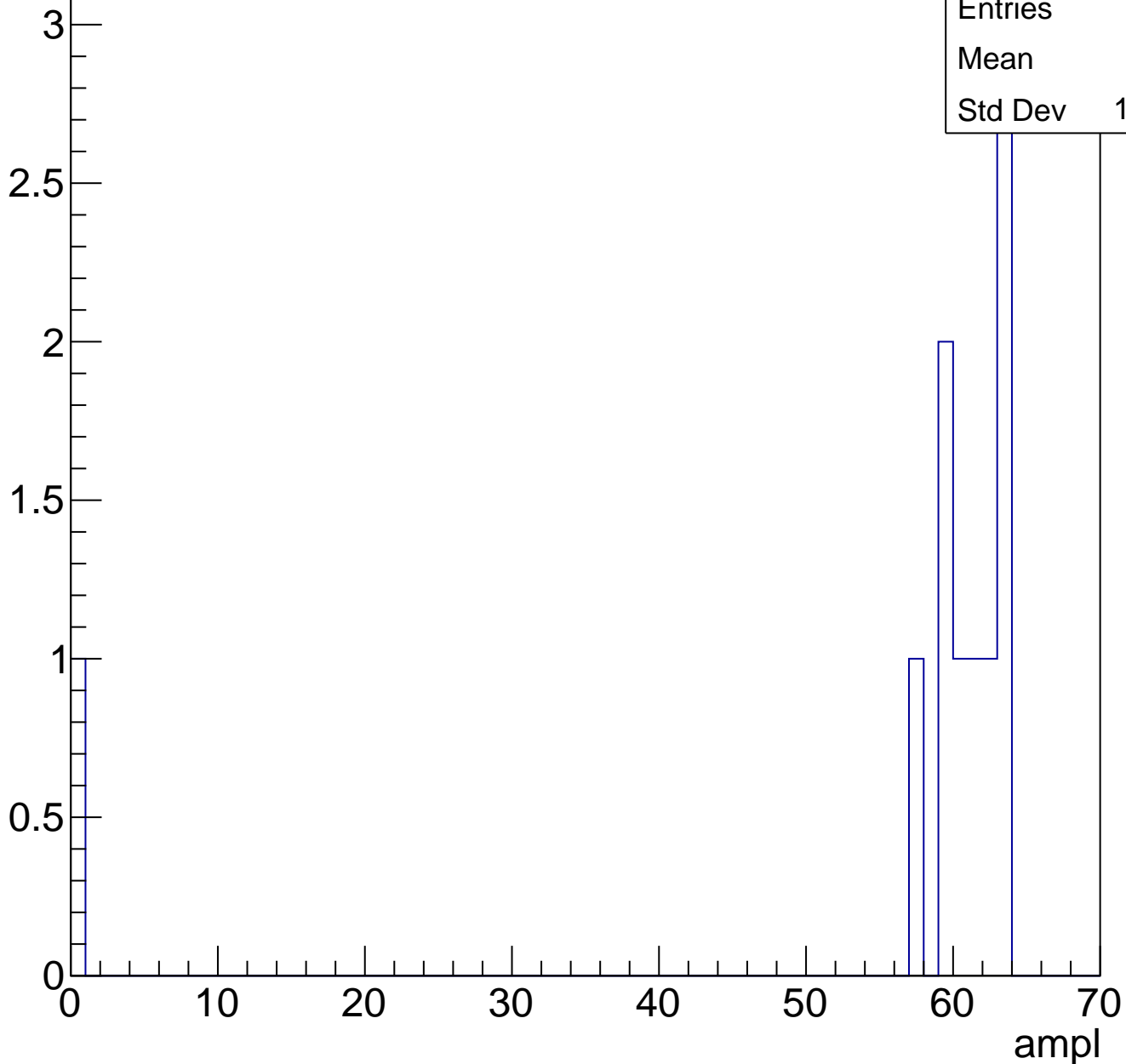




# B1L002S, U17-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch35, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	27.68
Std Dev	2.867

**Gaus mean : 28.2195**

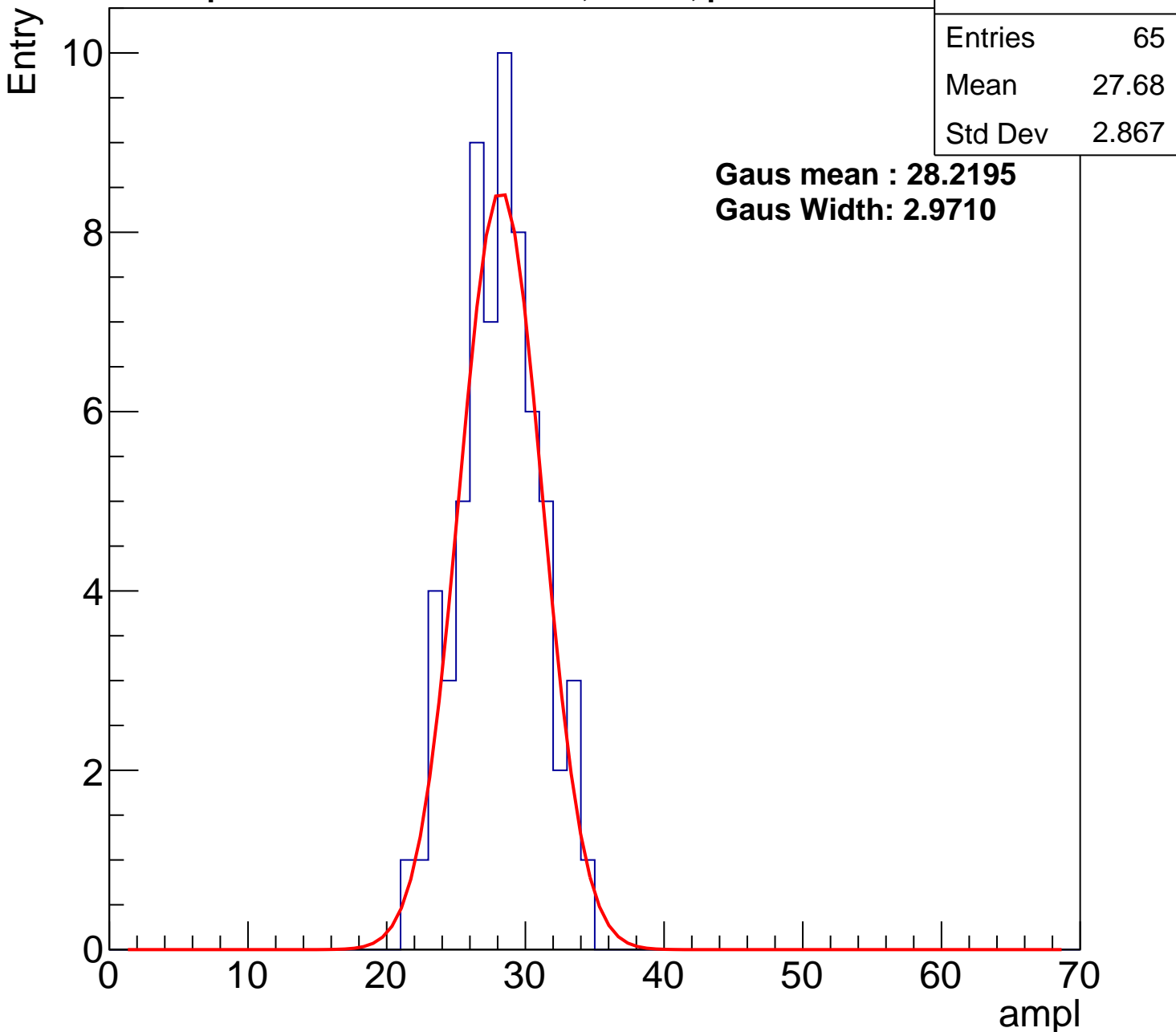
**Gaus Width: 2.9710**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch35, adc1

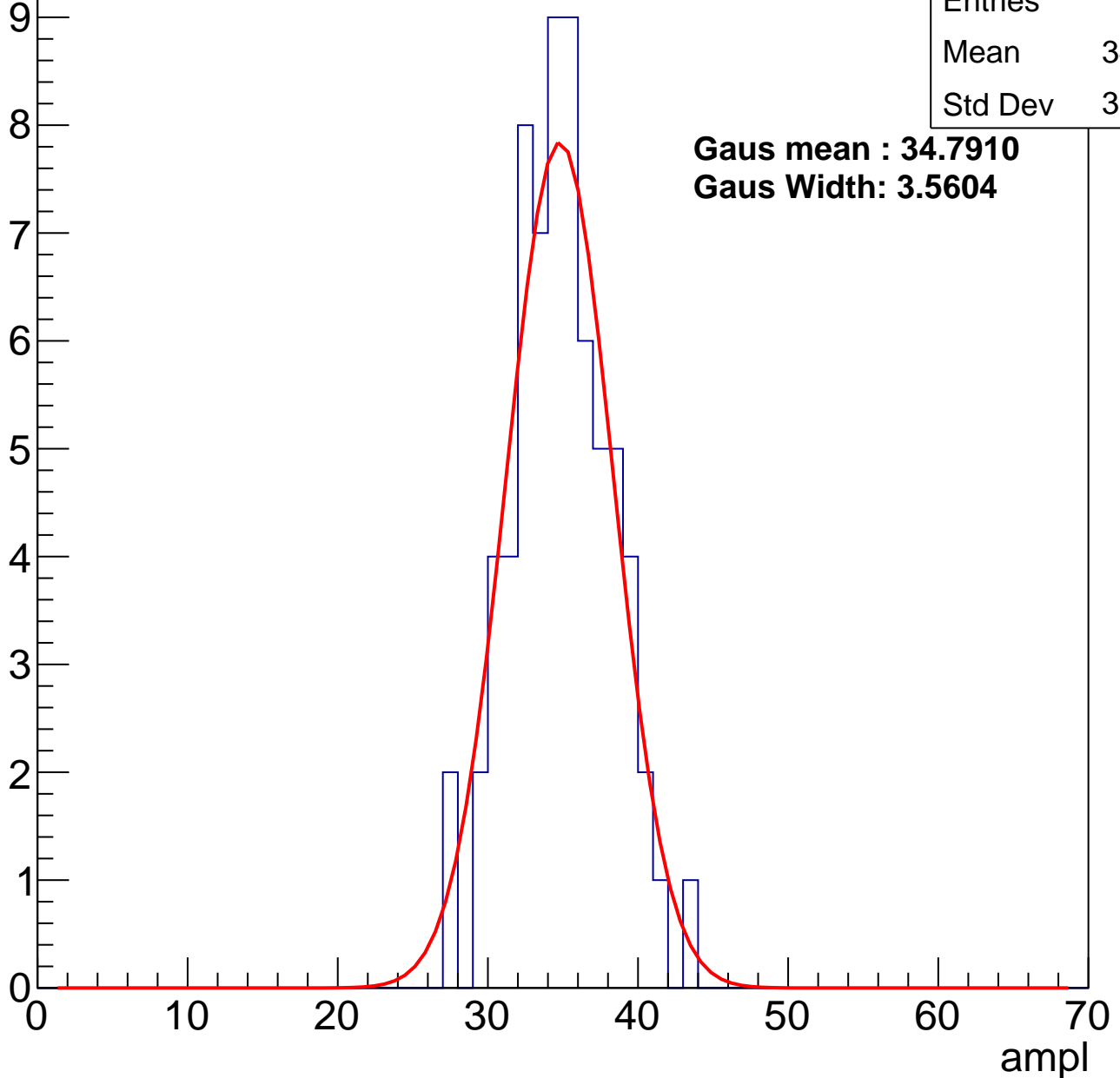
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.42
Std Dev	3.263

**Gaus mean : 34.7910**

**Gaus Width: 3.5604**



# B1L002S, U17-ch35, adc2

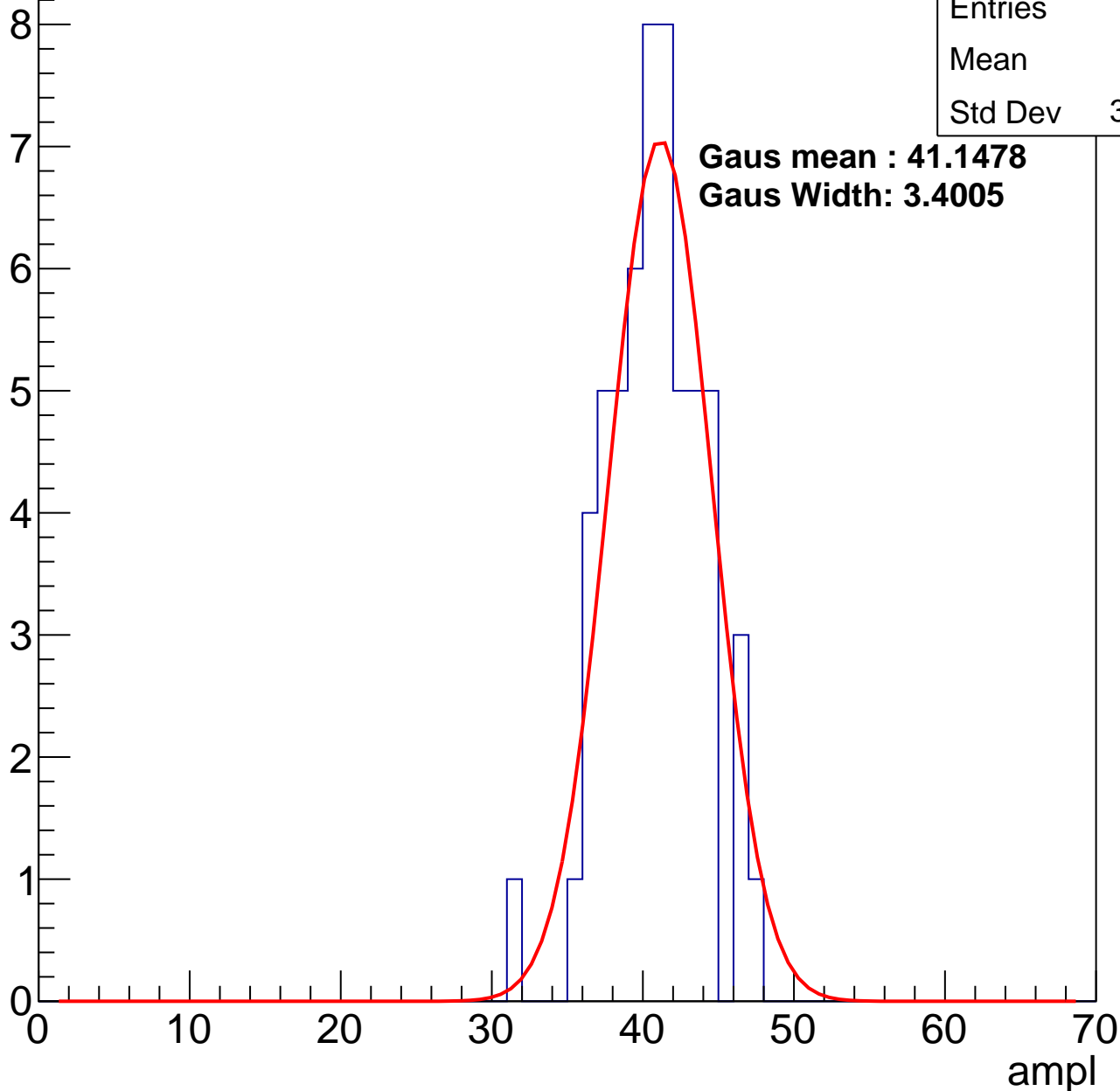
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.3
Std Dev	3.095

**Gaus mean : 41.1478**

**Gaus Width: 3.4005**



# B1L002S, U17-ch35, adc3

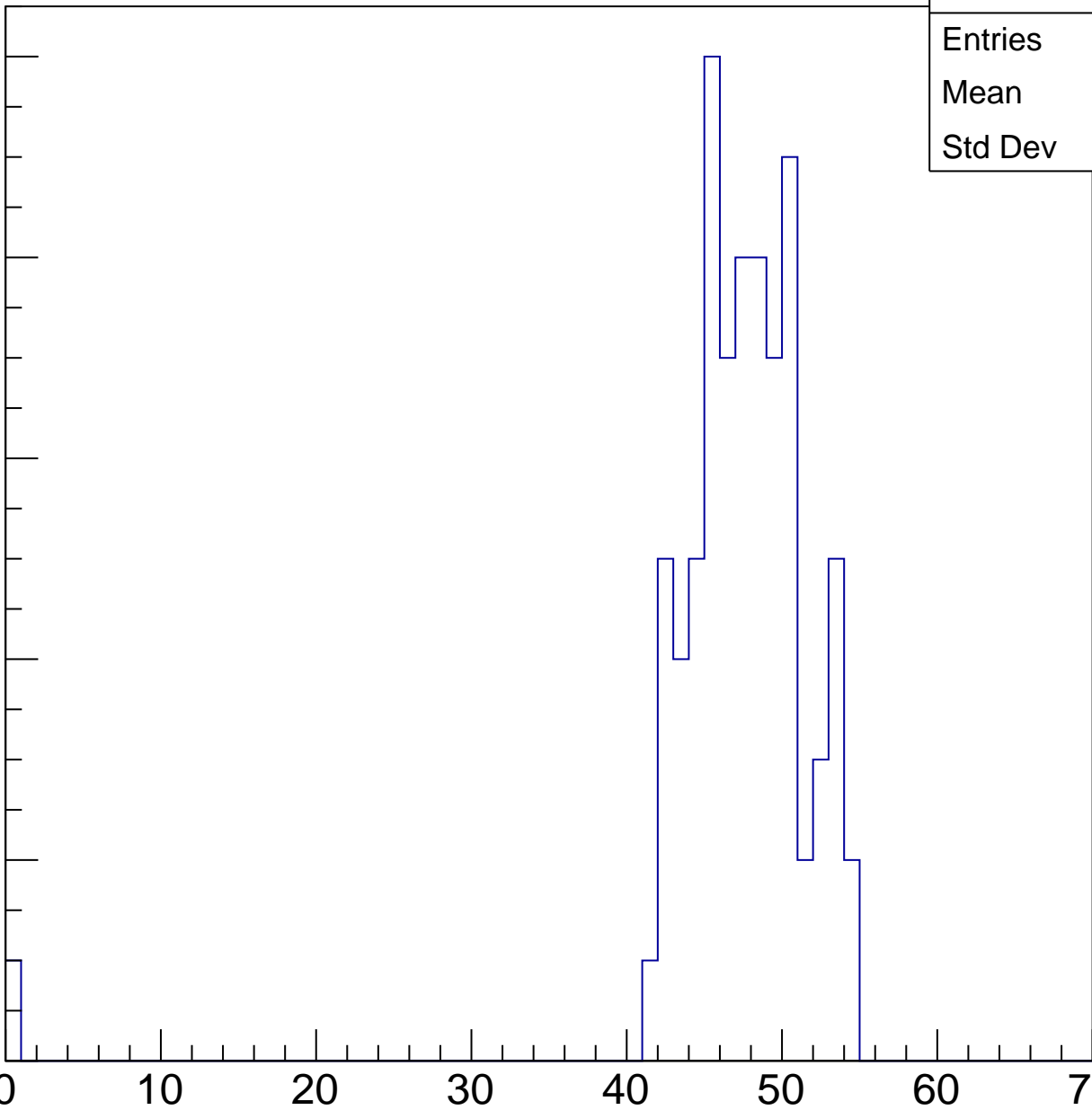
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	46.74
Std Dev	6.266

Entry

10  
8  
6  
4  
2  
0

ampl

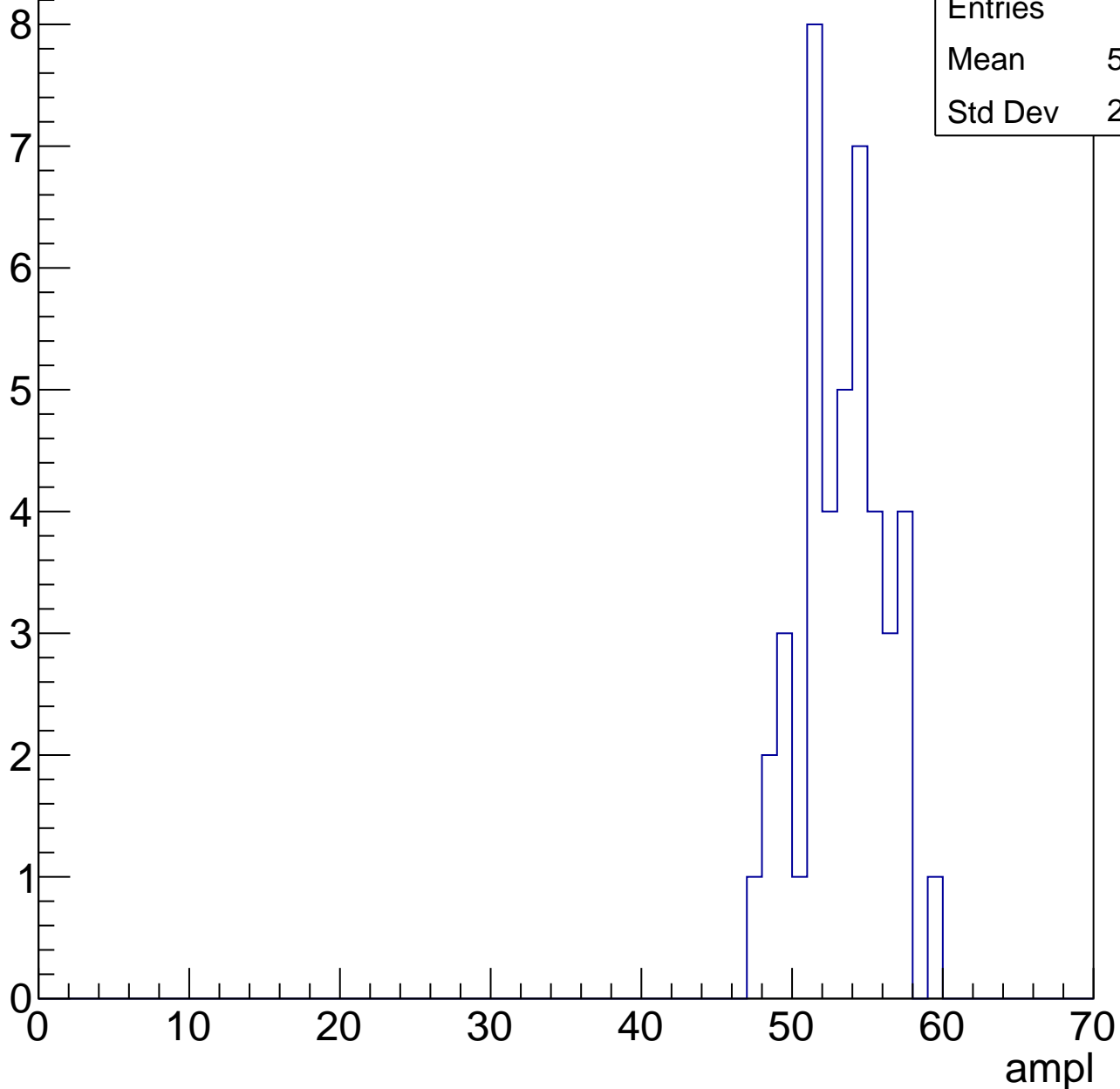


# B1L002S, U17-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

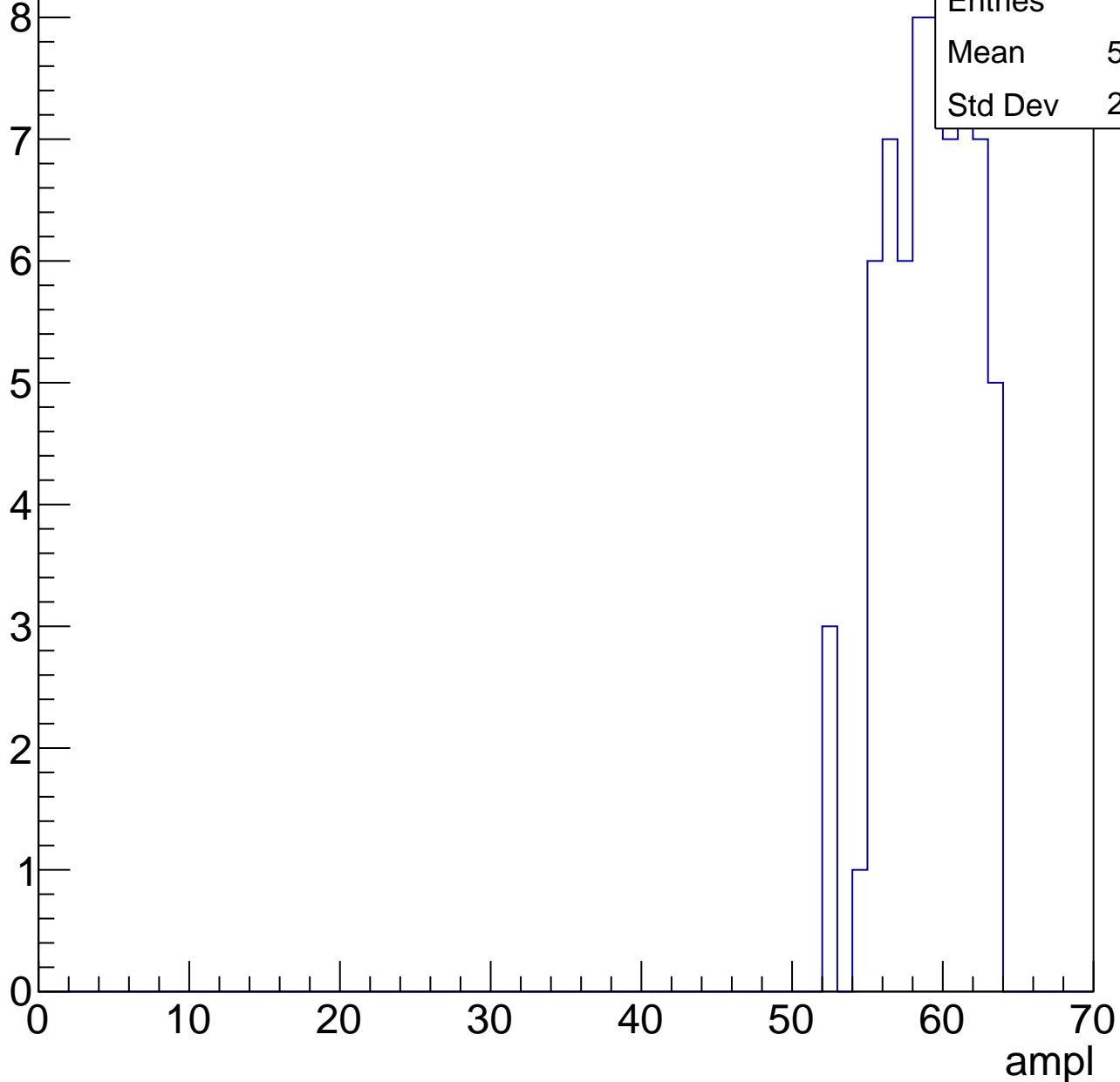
Entries	43
Mean	52.88
Std Dev	2.764



# B1L002S, U17-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

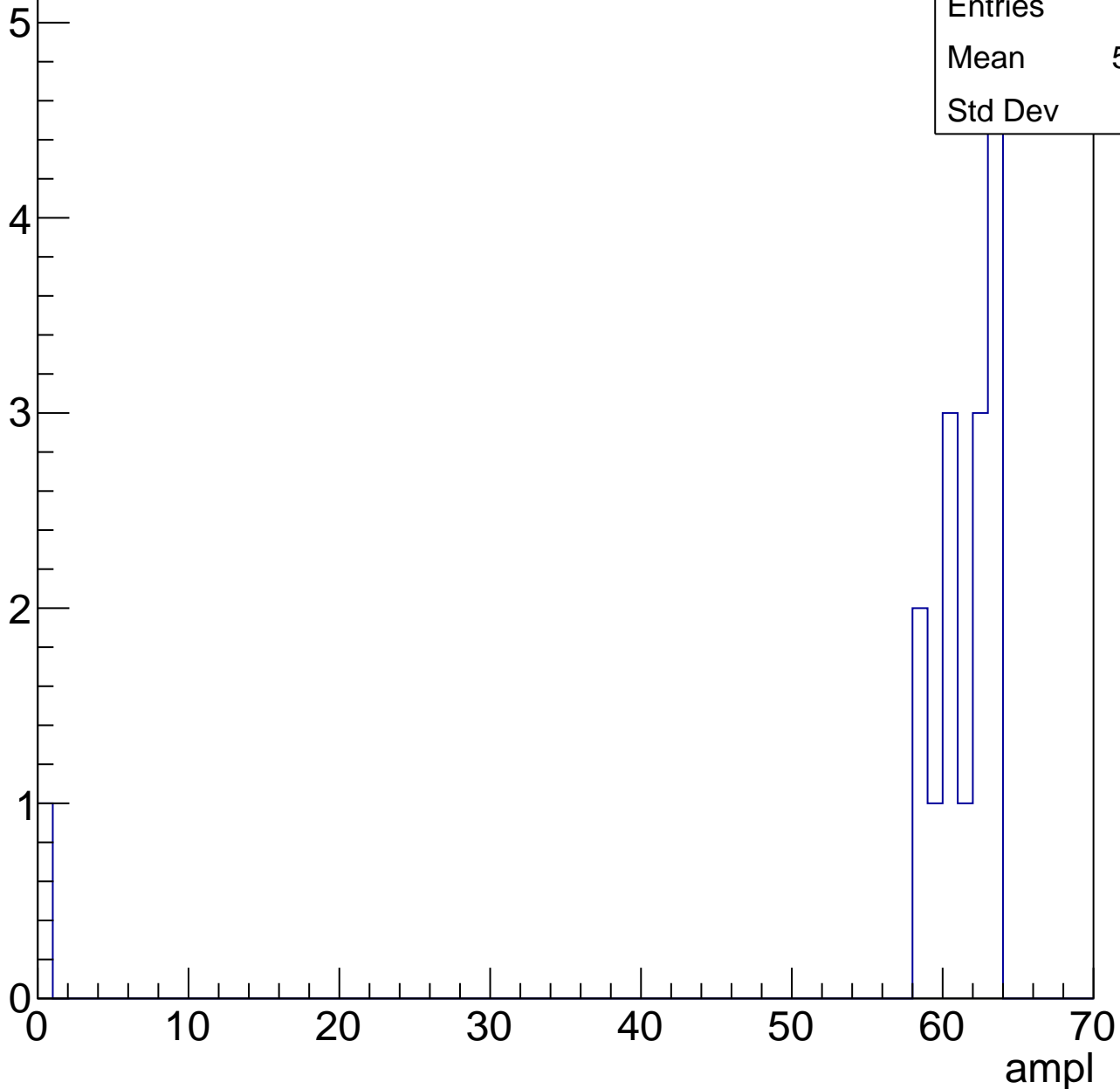


# B1L002S, U17-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.31
Std Dev	14.9

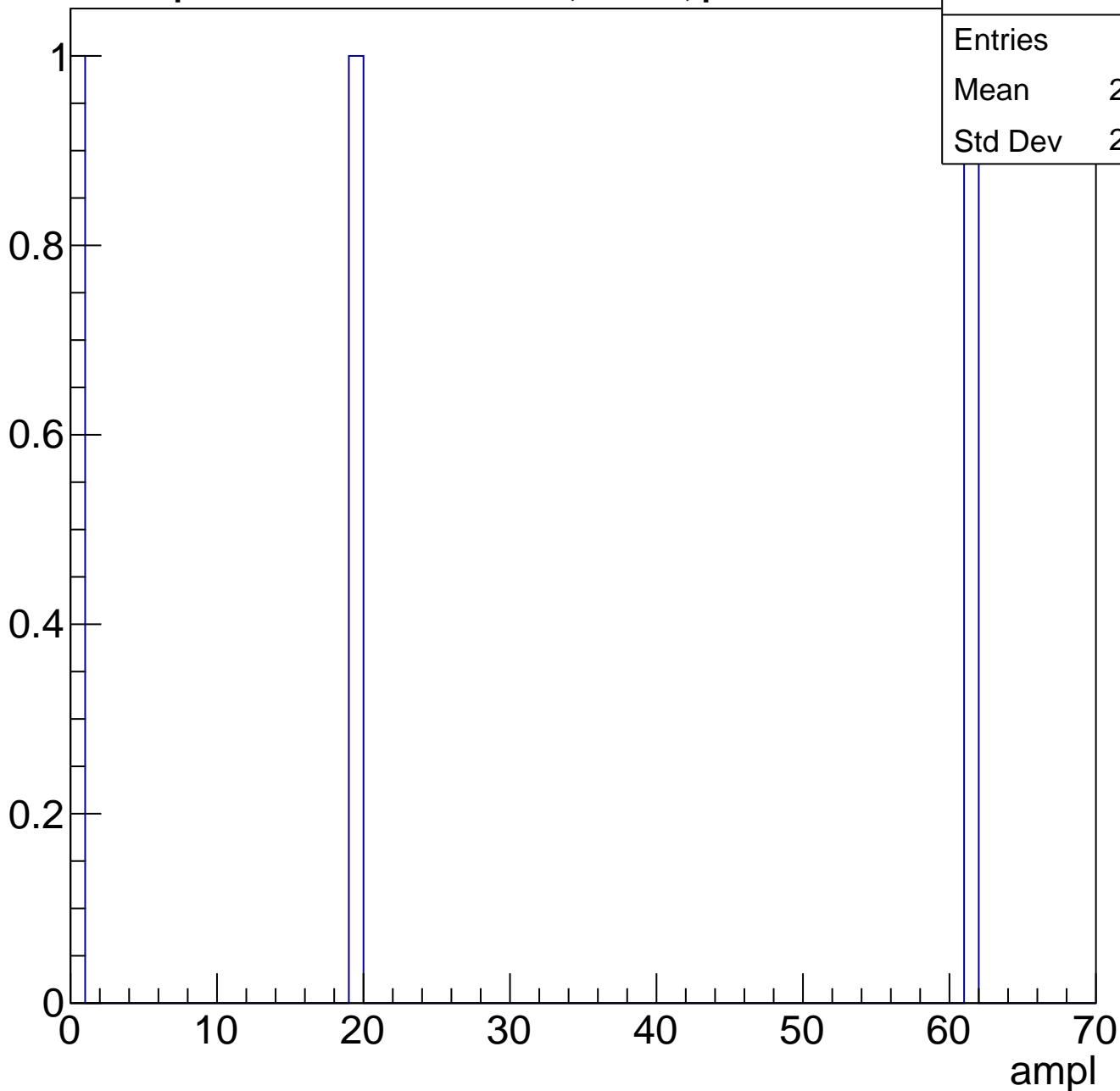




# B1L002S, U17-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	26.67
Std Dev	25.49

# B1L002S, U17-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	28.29
Std Dev	4.273

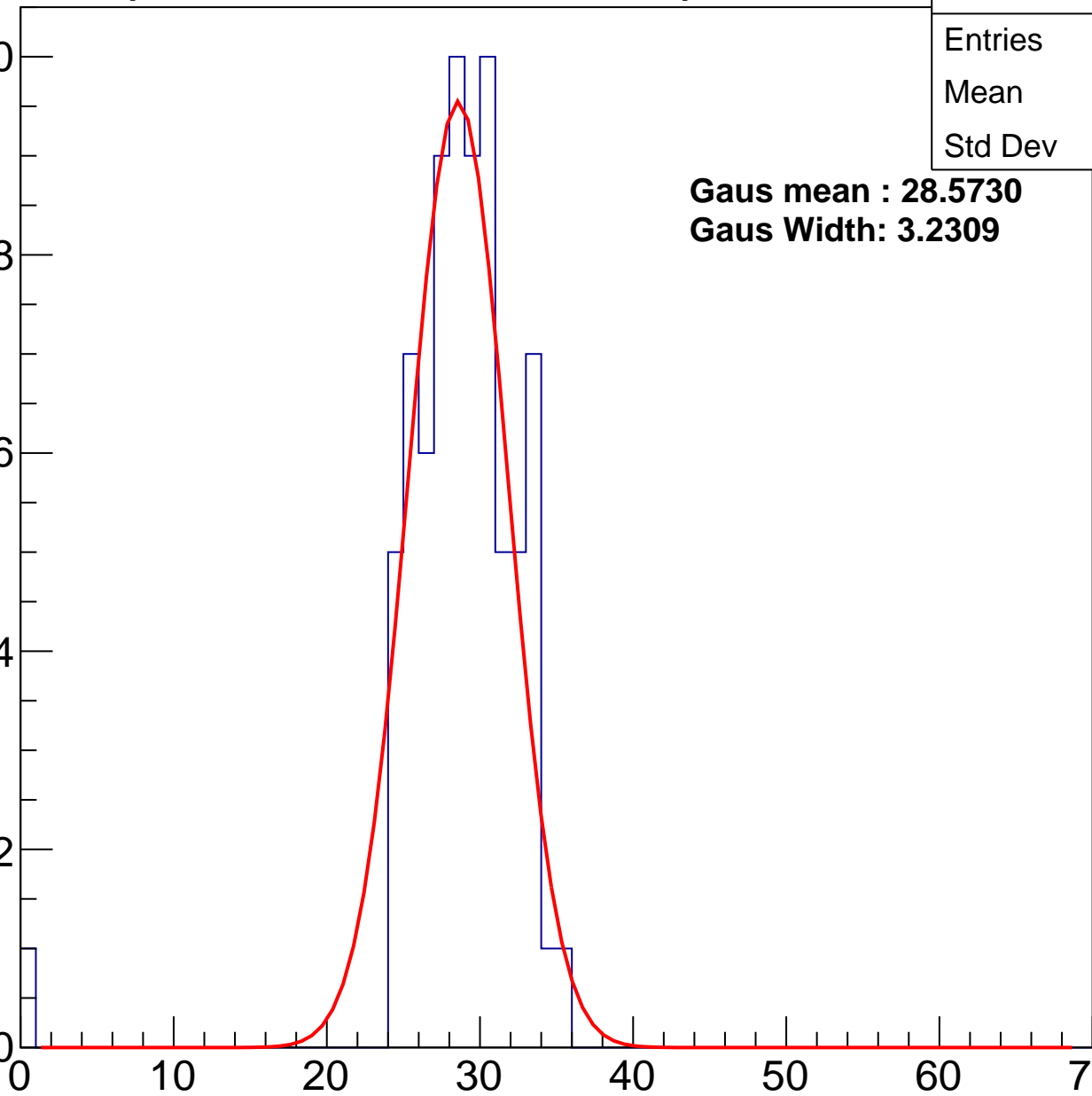
**Gaus mean : 28.5730**

**Gaus Width: 3.2309**

Entry

10  
8  
6  
4  
2  
0

ampl



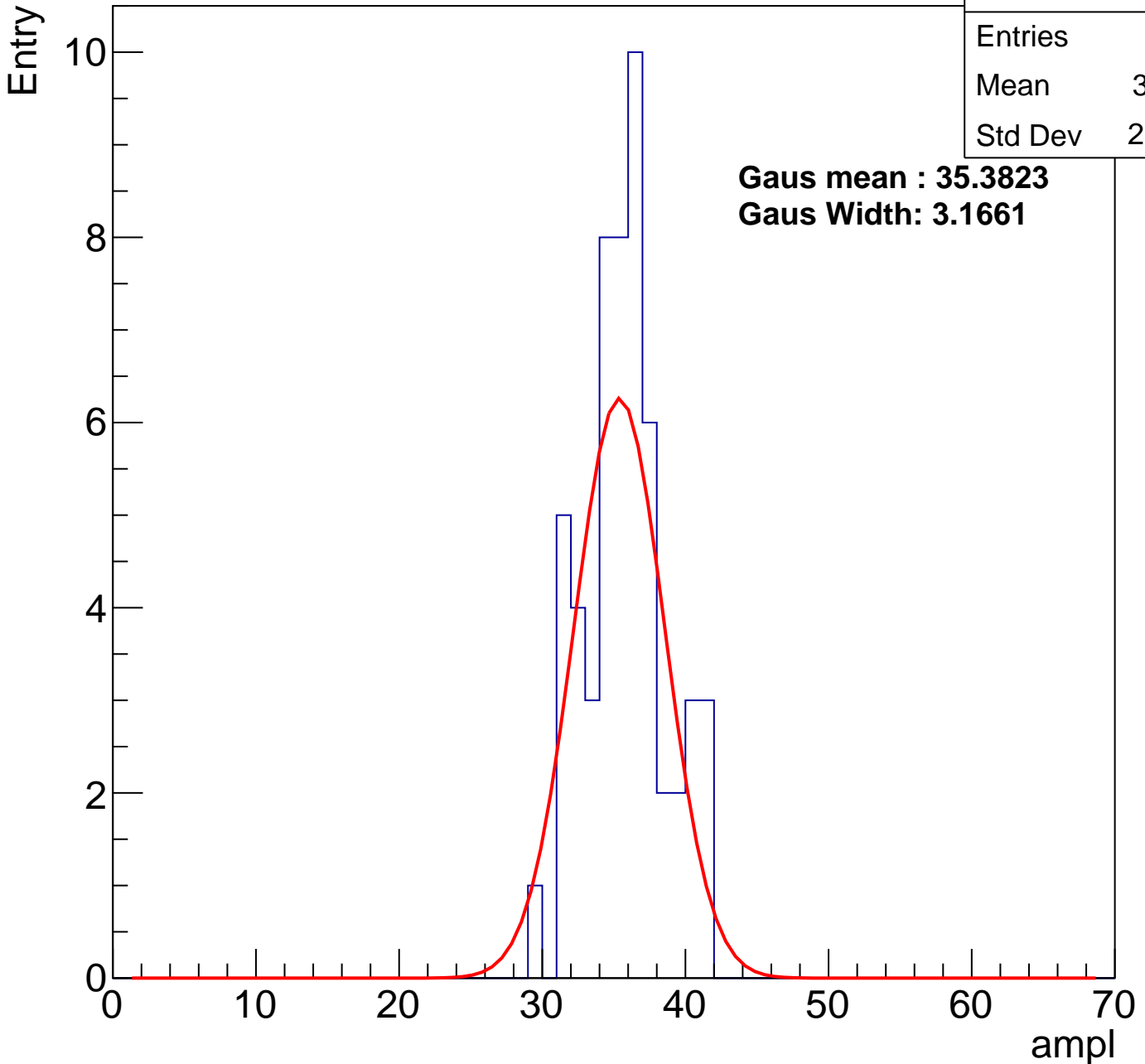
# B1L002S, U17-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	55
Mean	35.31
Std Dev	2.808

**Gaus mean : 35.3823**

**Gaus Width: 3.1661**



# B1L002S, U17-ch36, adc2

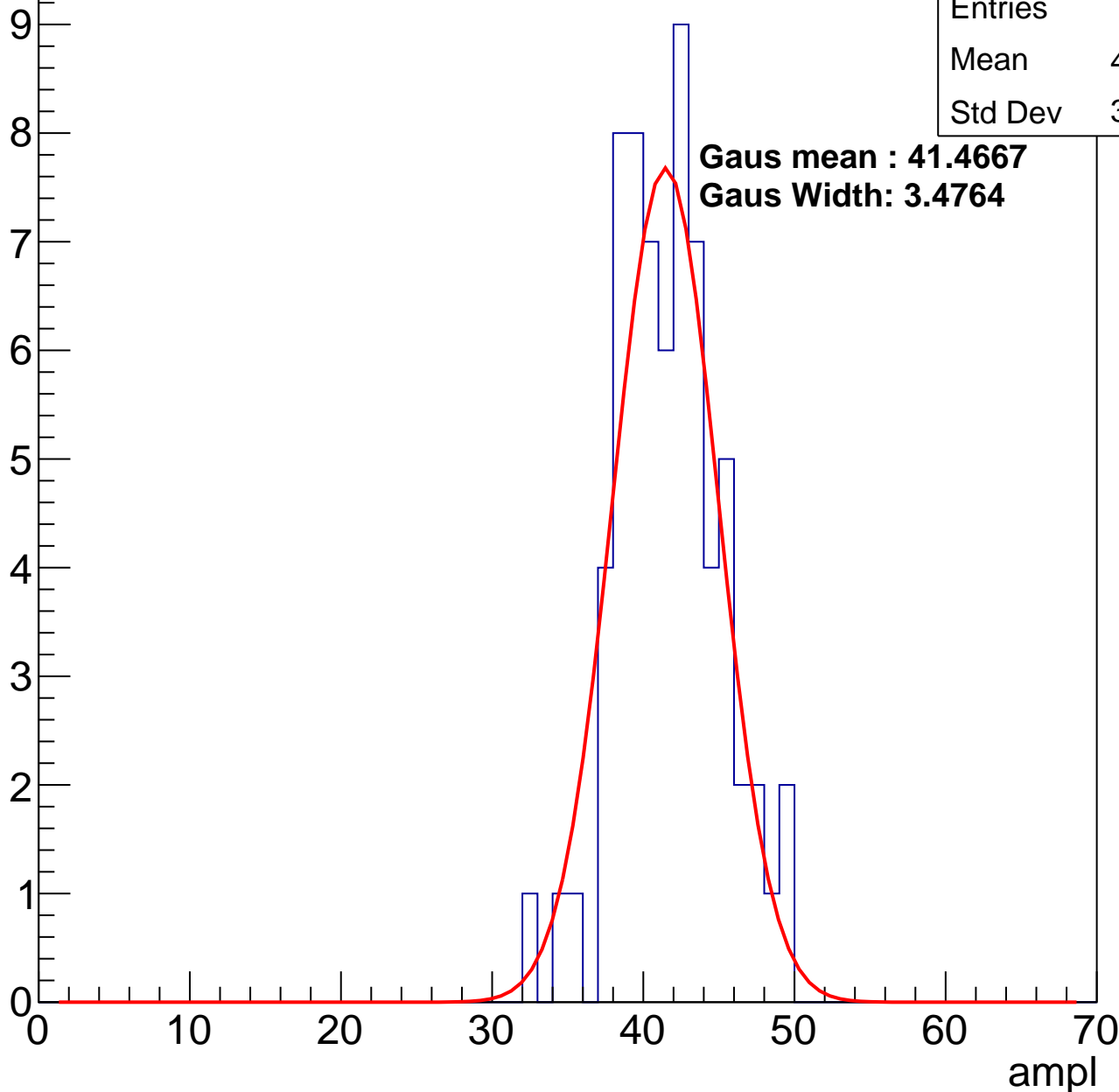
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.22
Std Dev	3.408

**Gaus mean : 41.4667**

**Gaus Width: 3.4764**

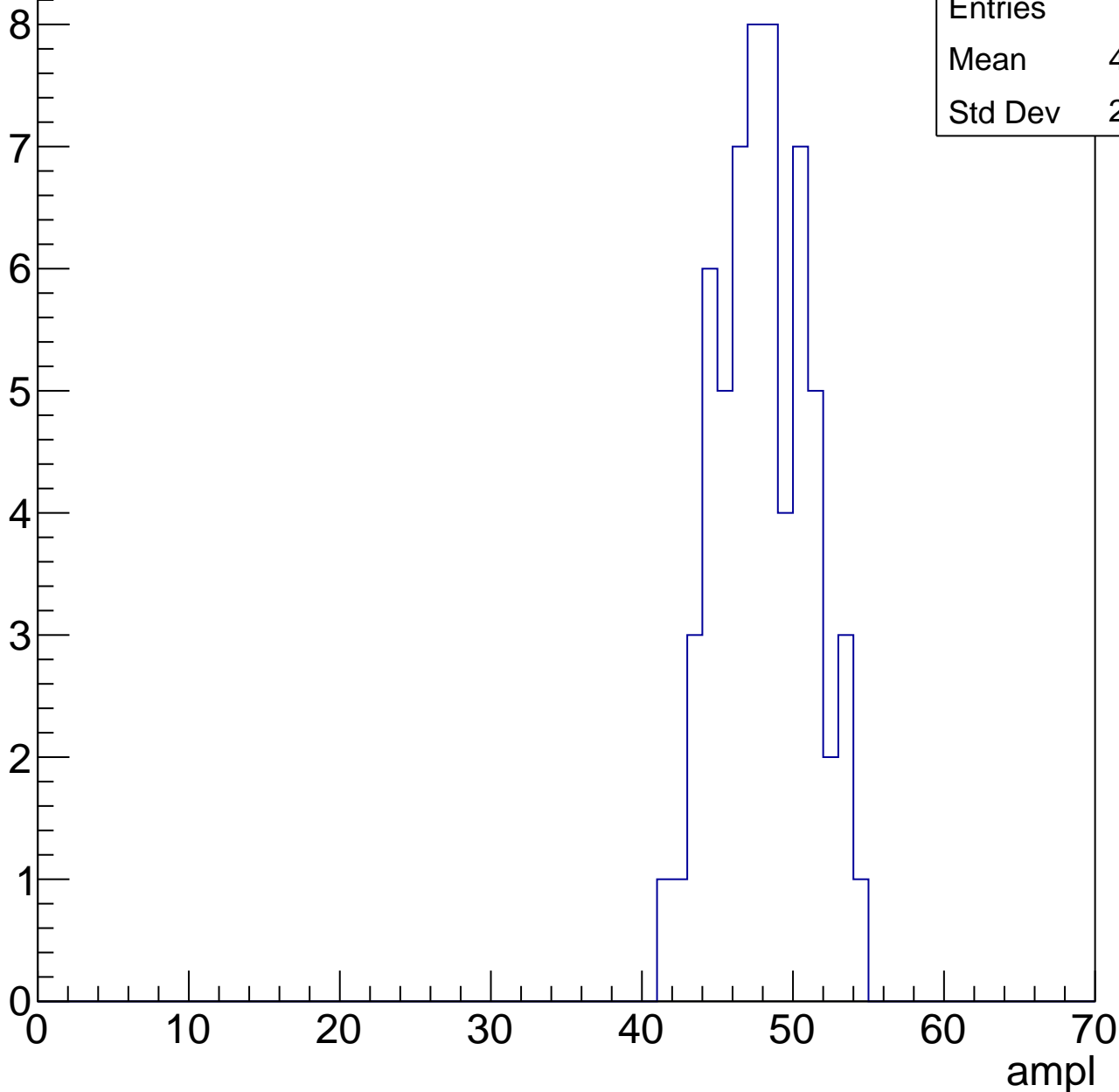


# B1L002S, U17-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	47.56
Std Dev	2.989

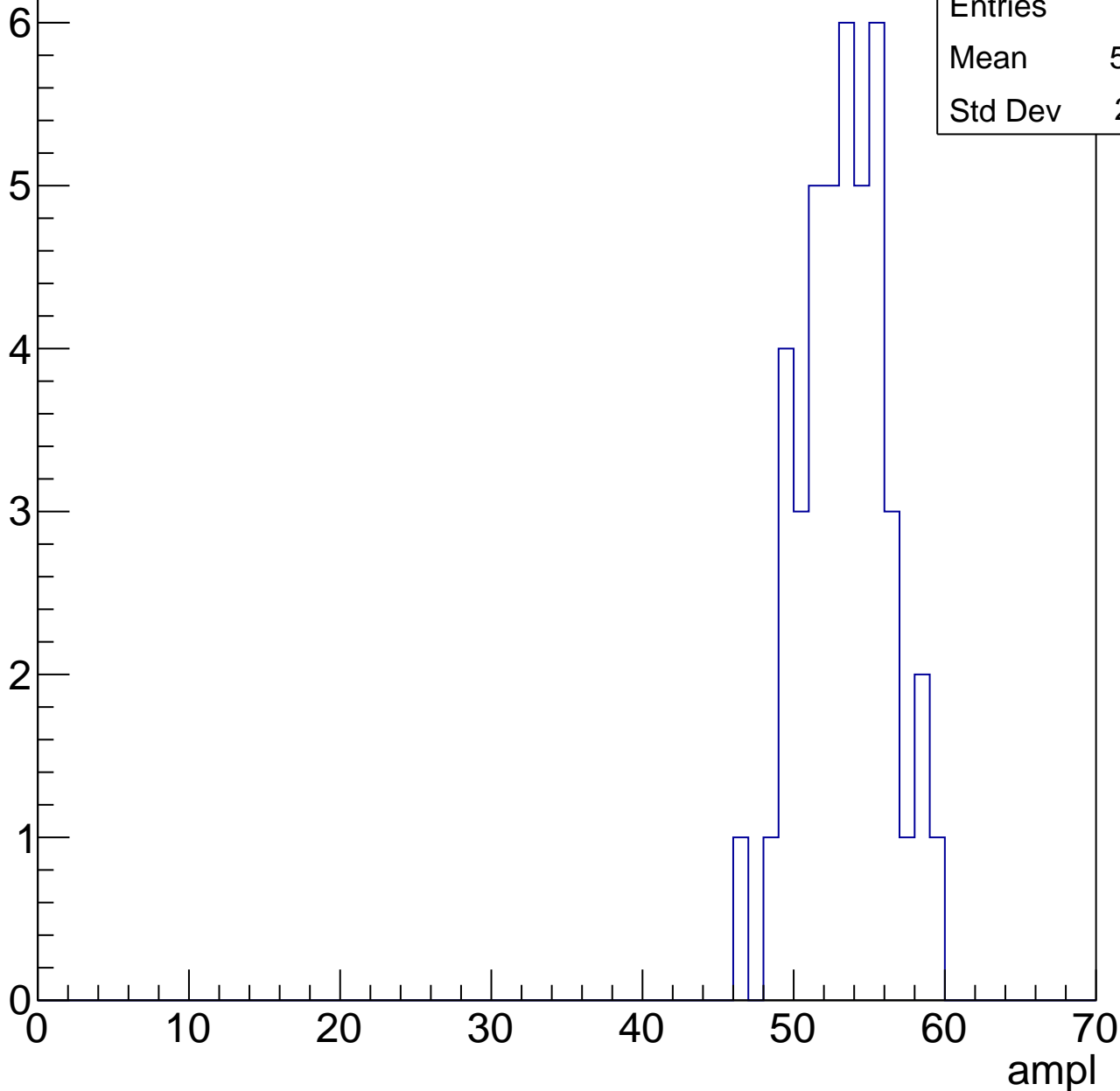


# B1L002S, U17-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

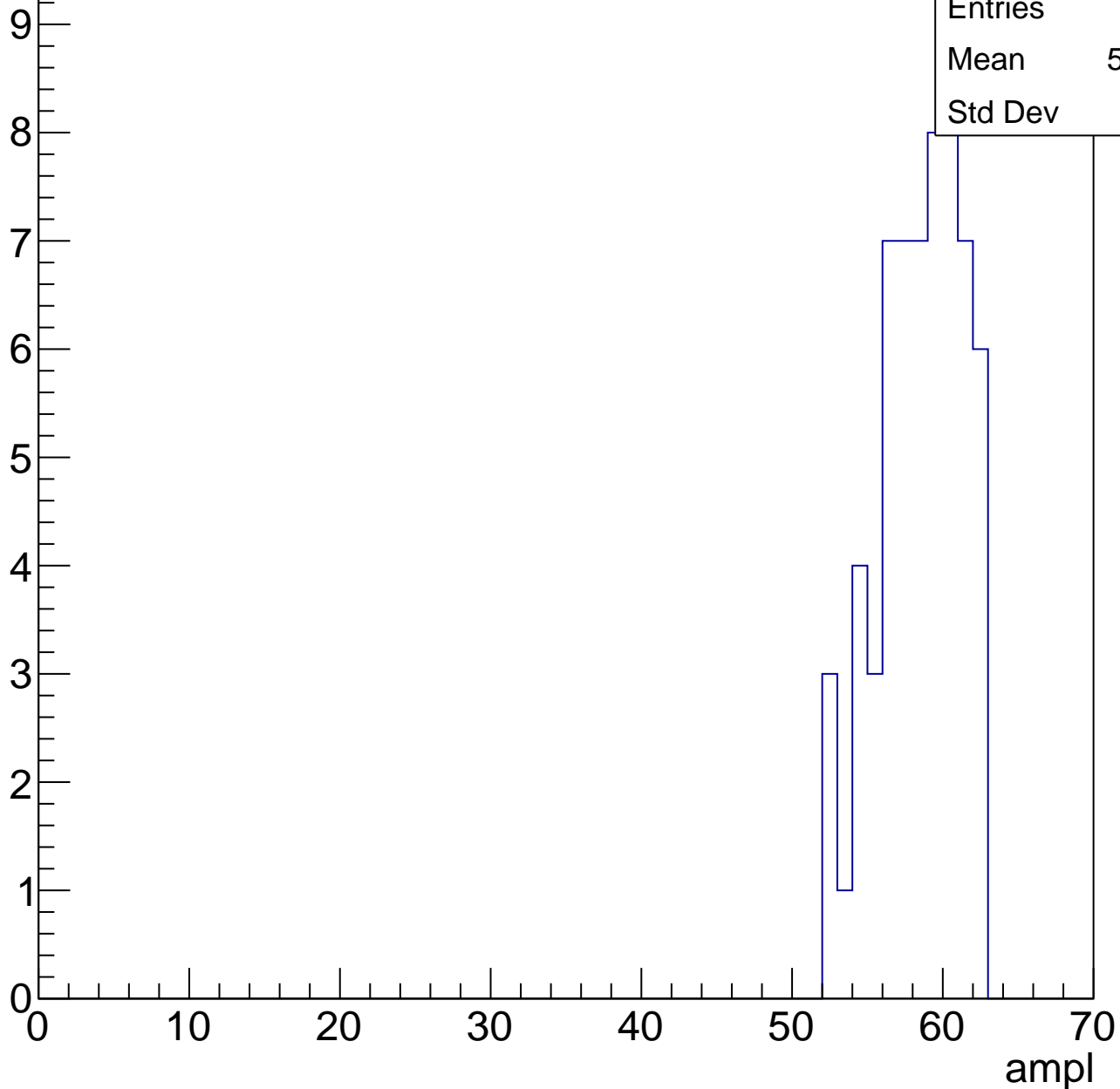
Entries	43
Mean	52.86
Std Dev	2.841



# B1L002S, U17-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

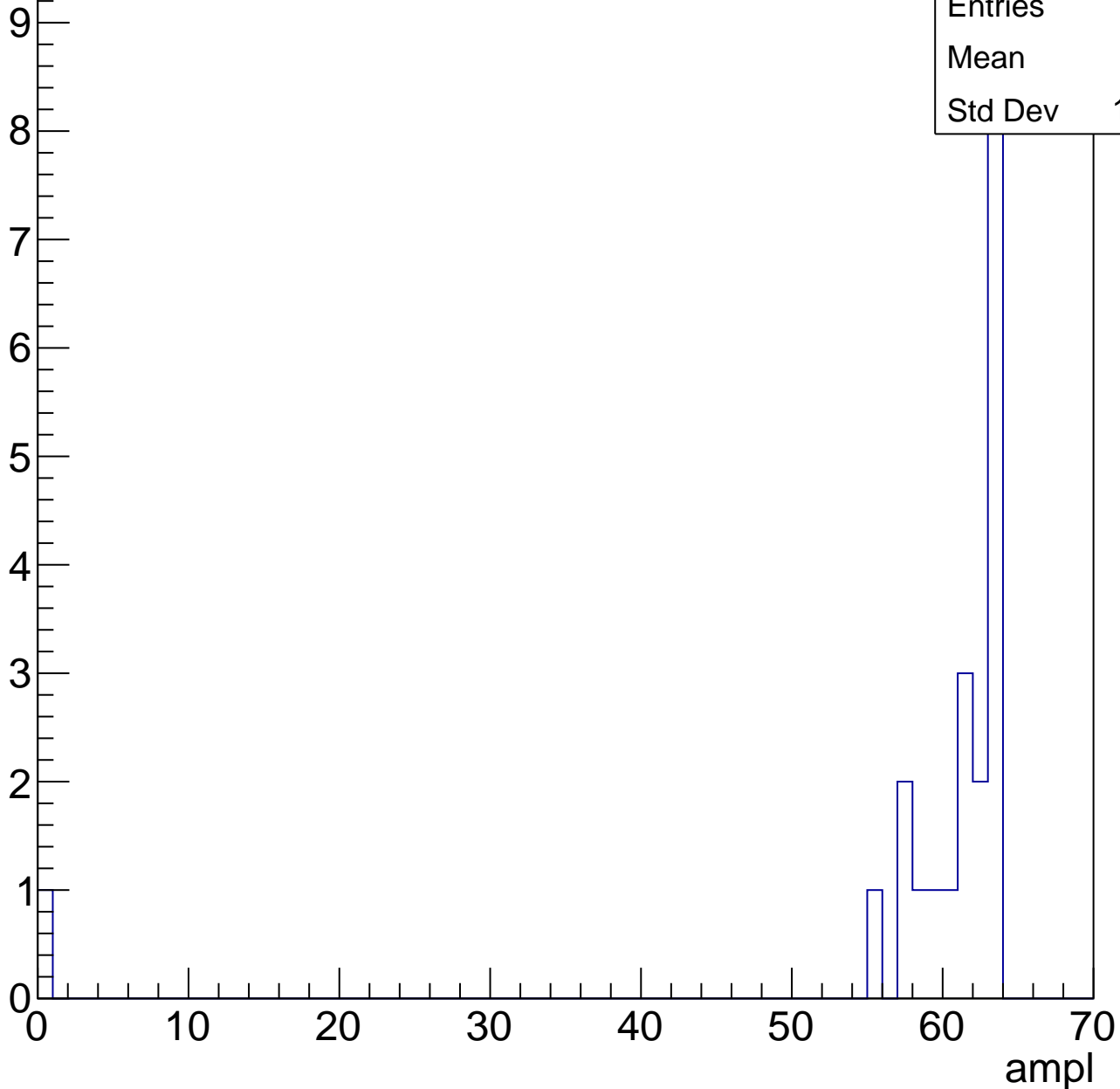


# B1L002S, U17-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.1
Std Dev	13.21

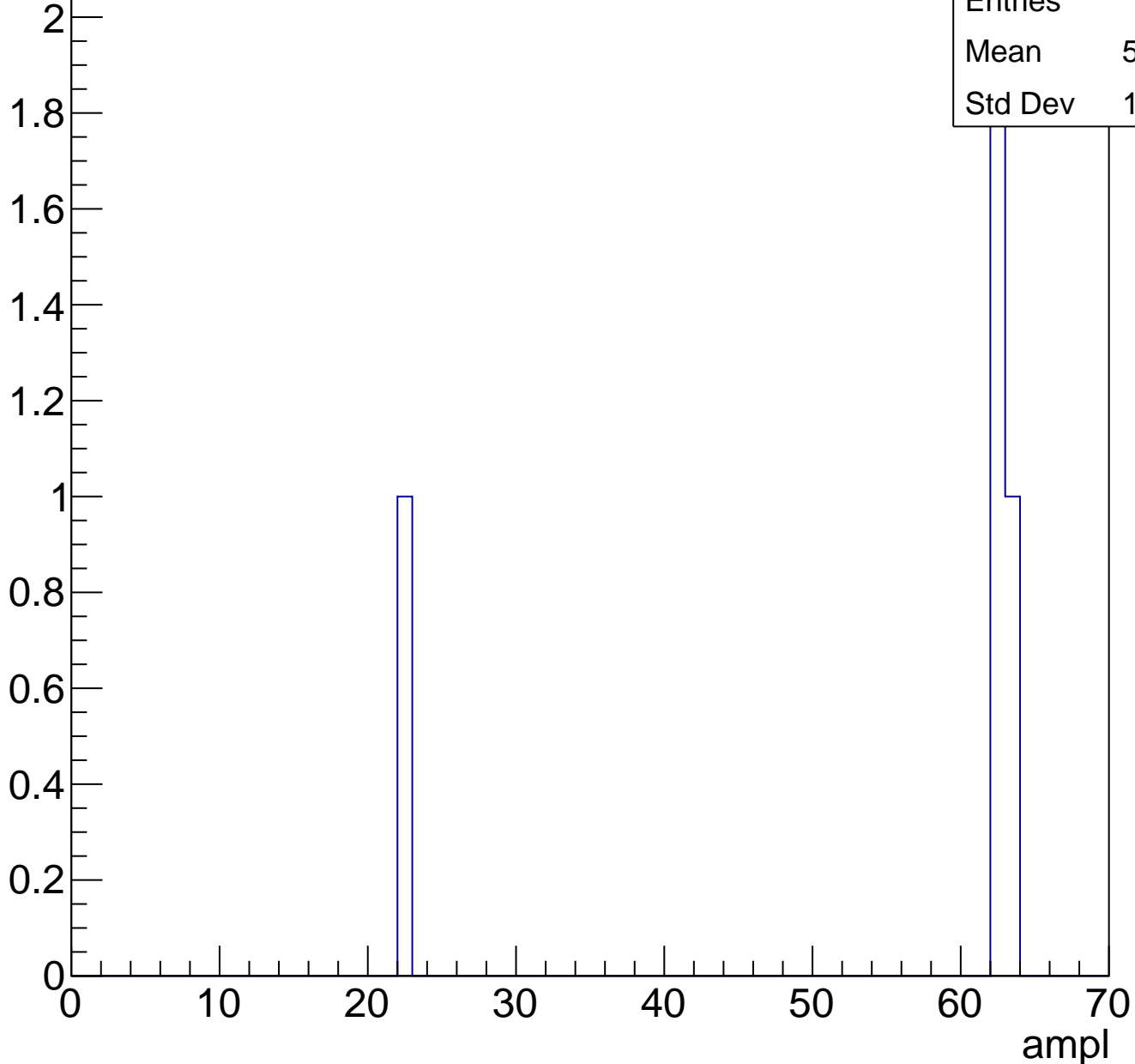




# B1L002S, U17-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	52.25
Std Dev	17.47

# B1L002S, U17-ch37, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	26.85
Std Dev	3.261

**Gaus mean : 27.4164**

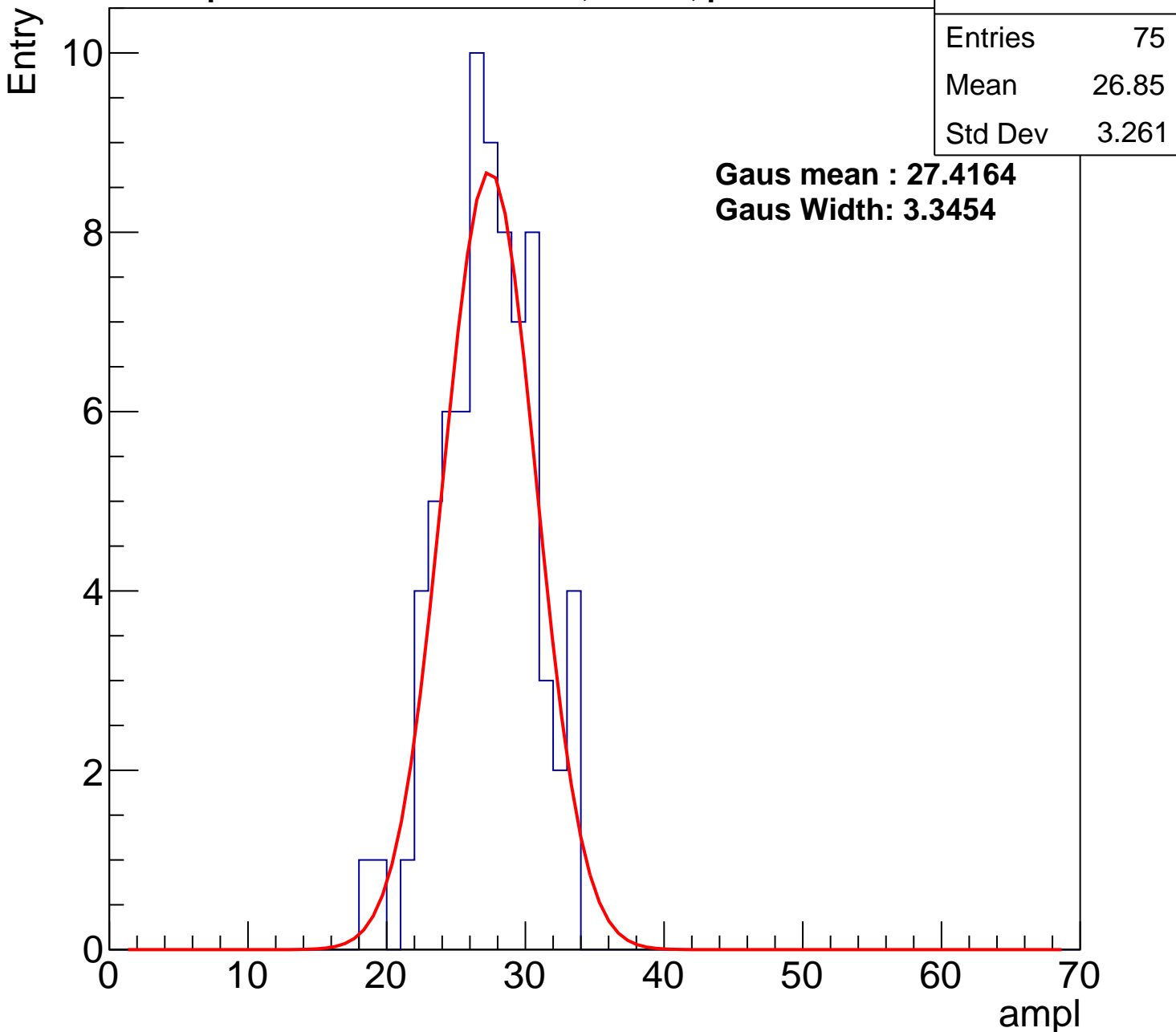
**Gaus Width: 3.3454**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch37, adc1

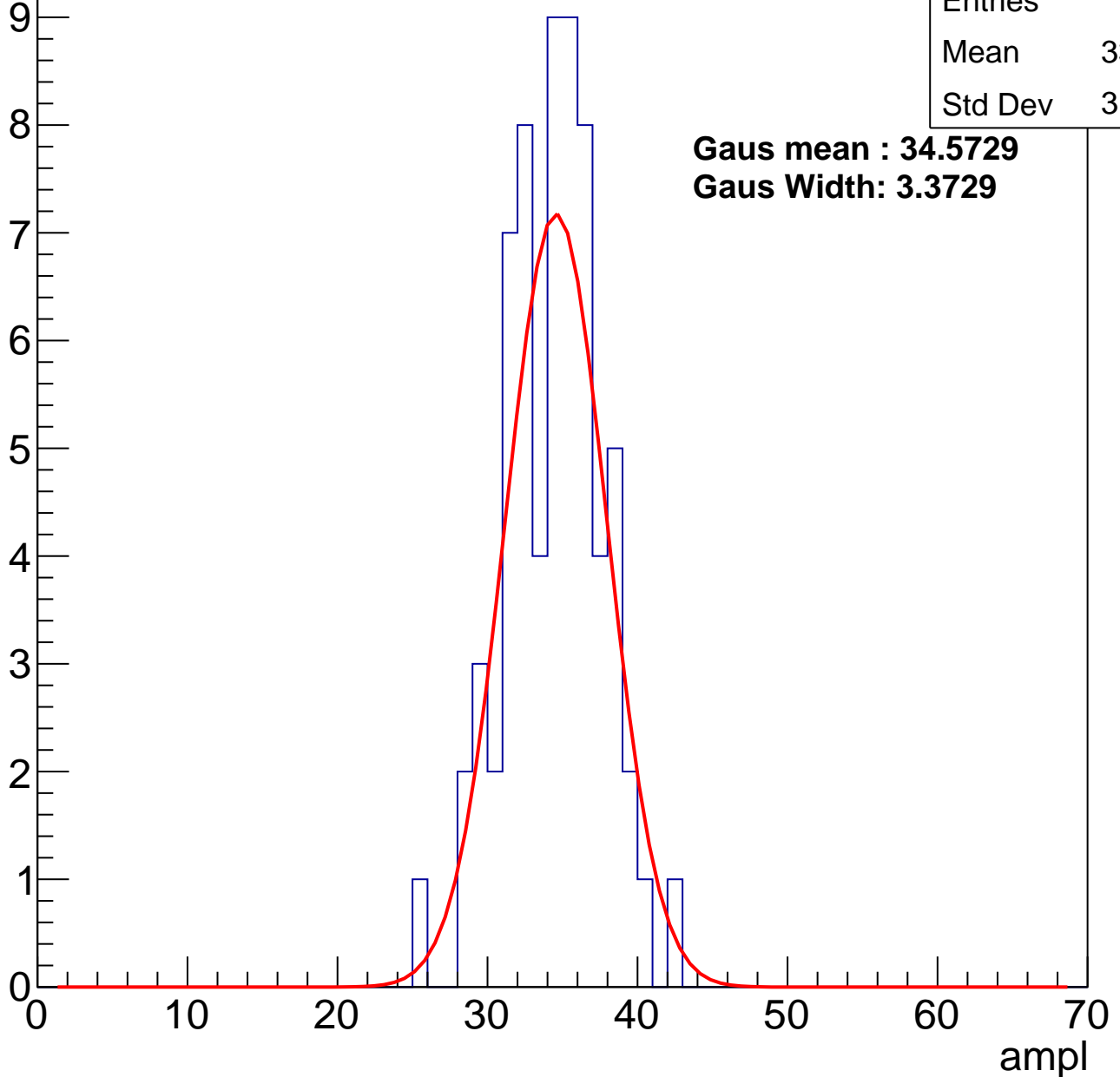
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.94
Std Dev	3.176

**Gaus mean : 34.5729**

**Gaus Width: 3.3729**



# B1L002S, U17-ch37, adc2

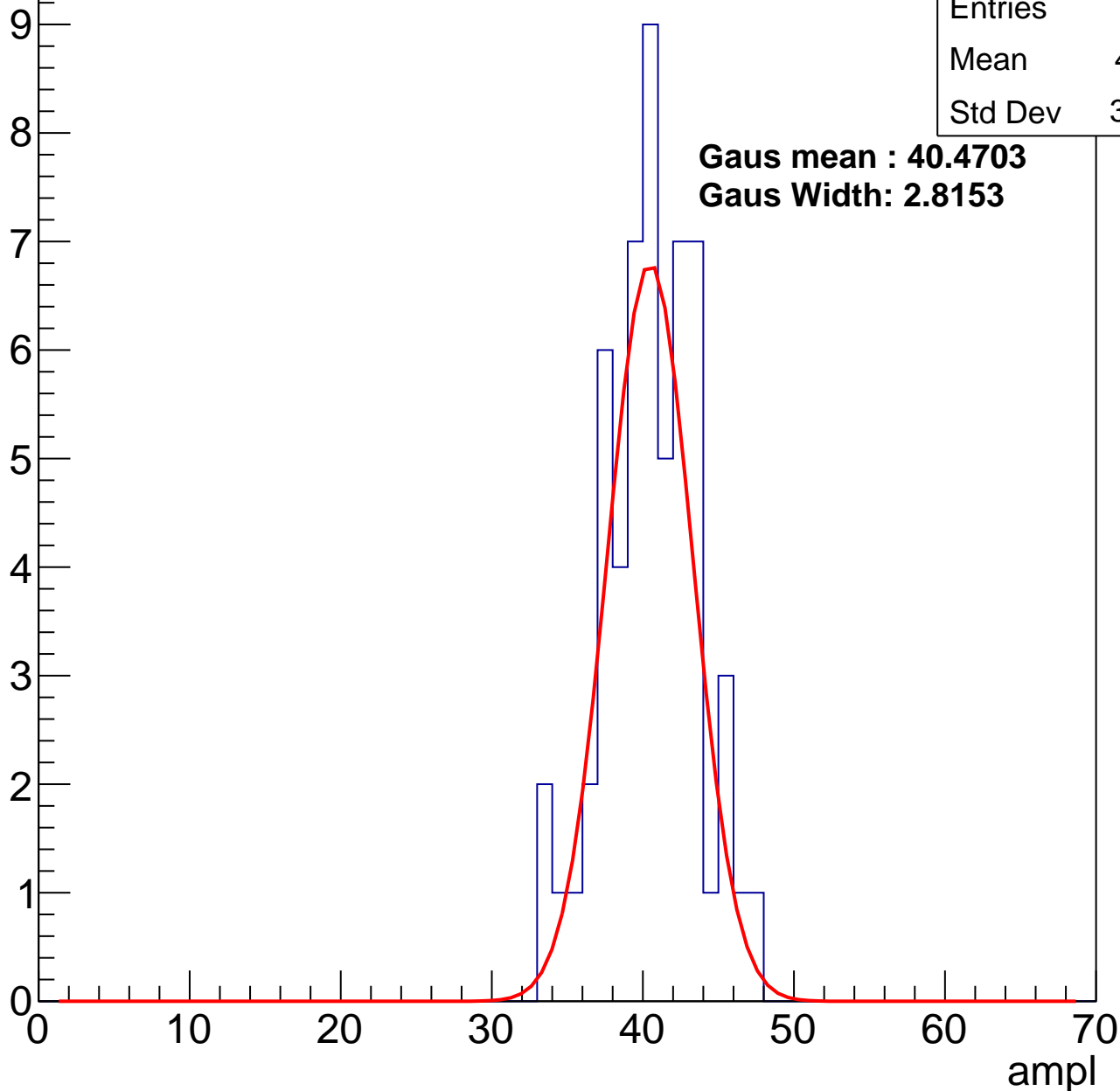
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.11
Std Dev	3.076

**Gaus mean : 40.4703**

**Gaus Width: 2.8153**

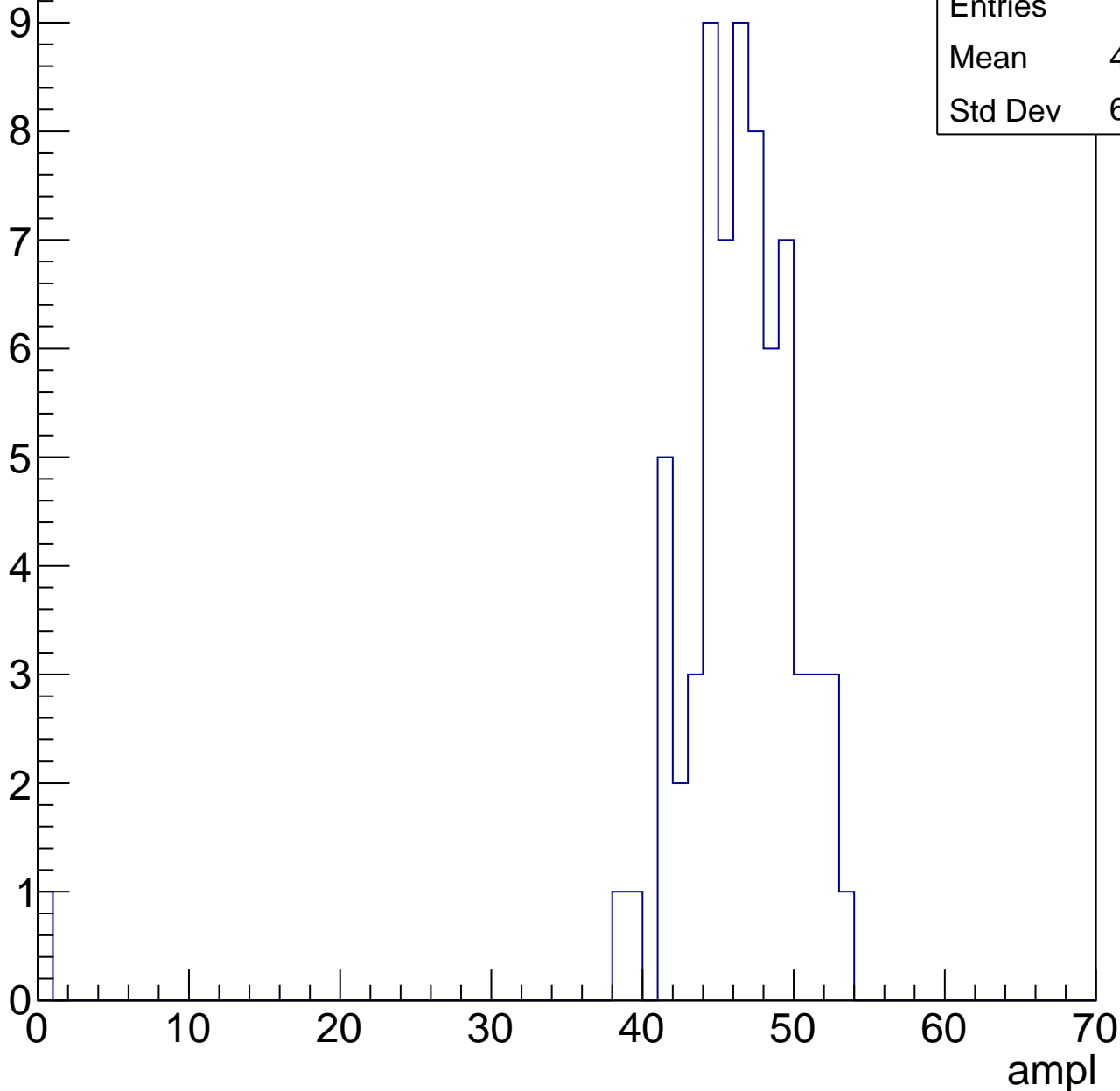


# B1L002S, U17-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.49
Std Dev	6.385

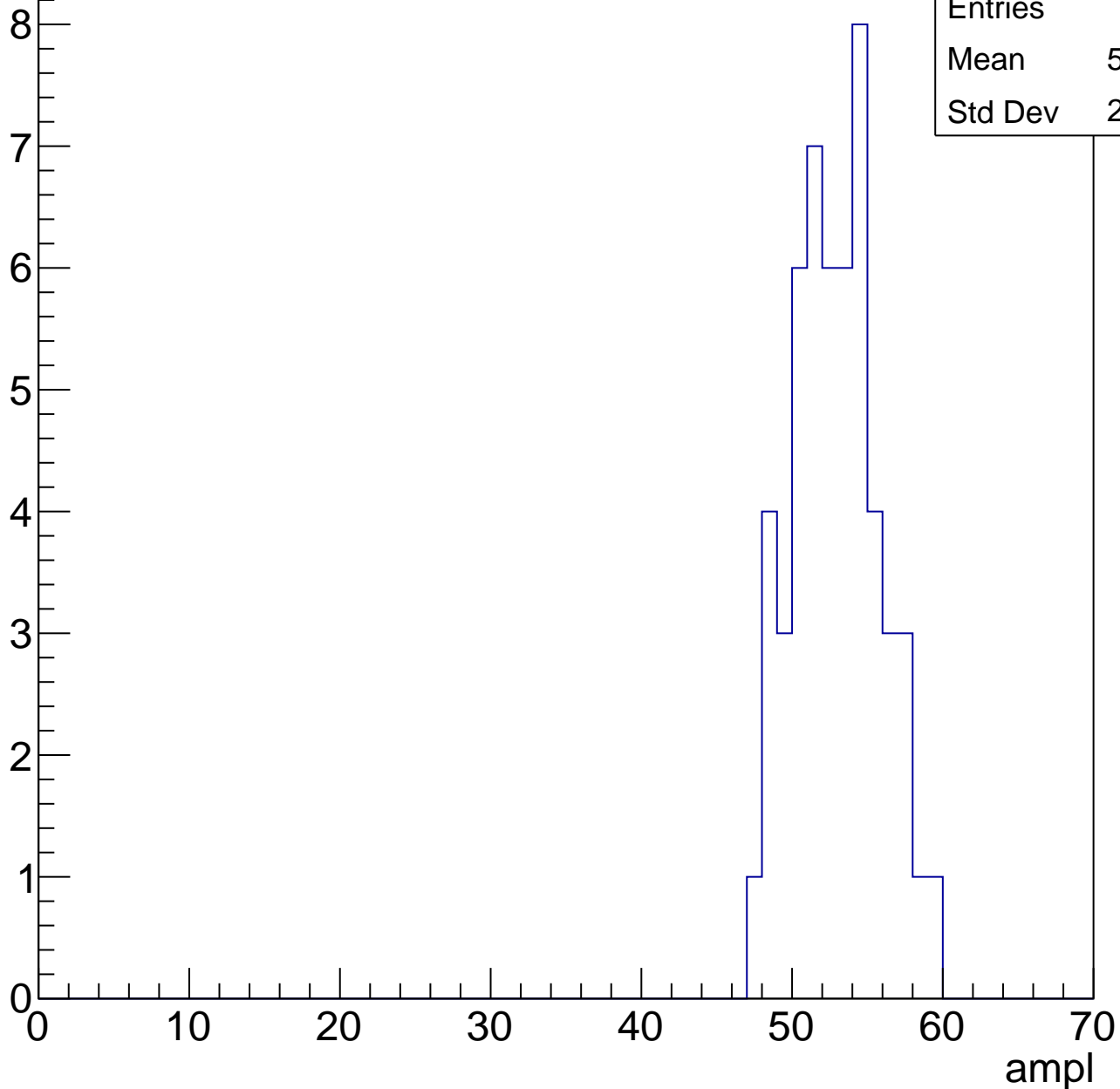


# B1L002S, U17-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.47
Std Dev	2.806

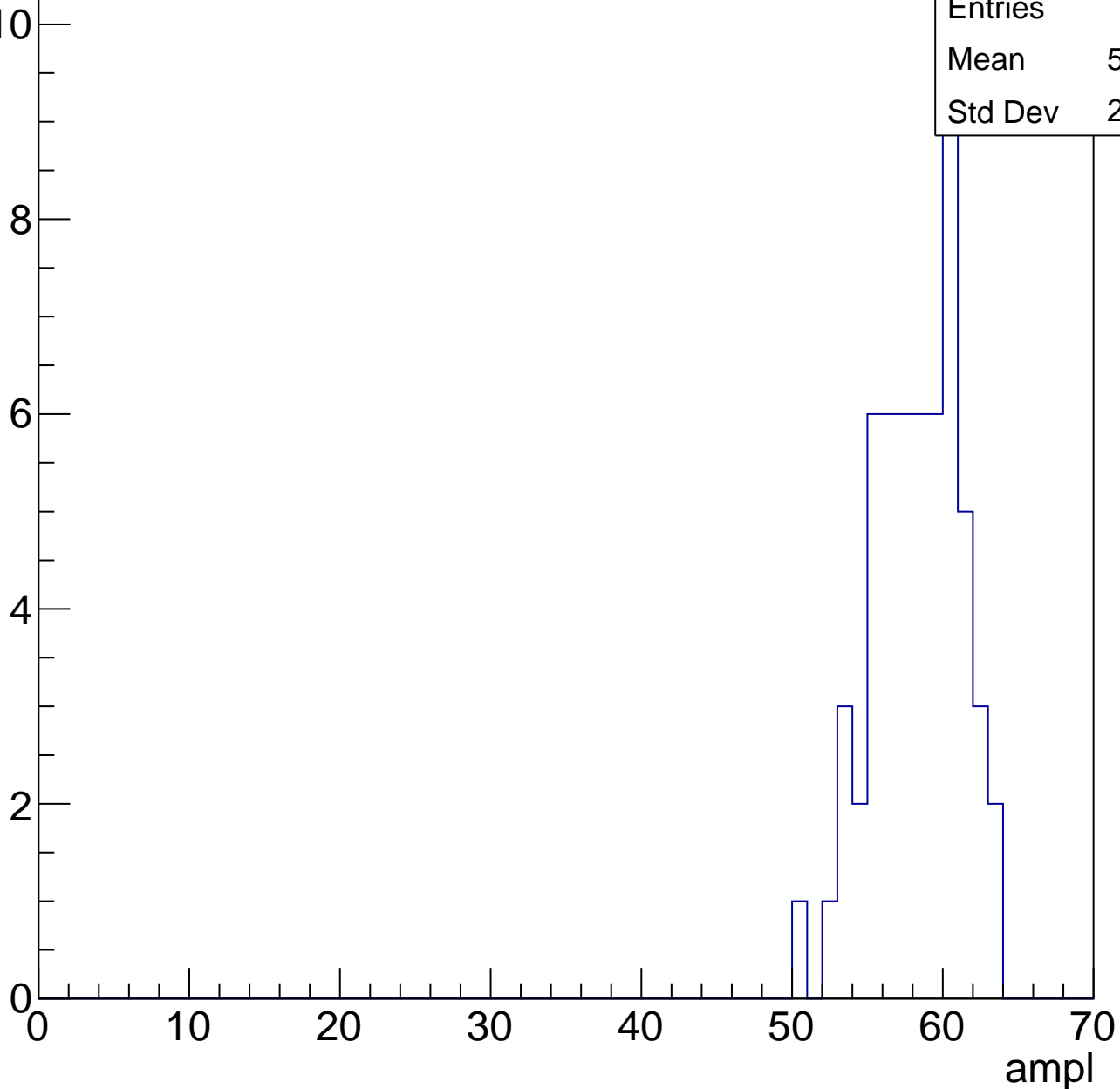


# B1L002S, U17-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.82
Std Dev	2.897

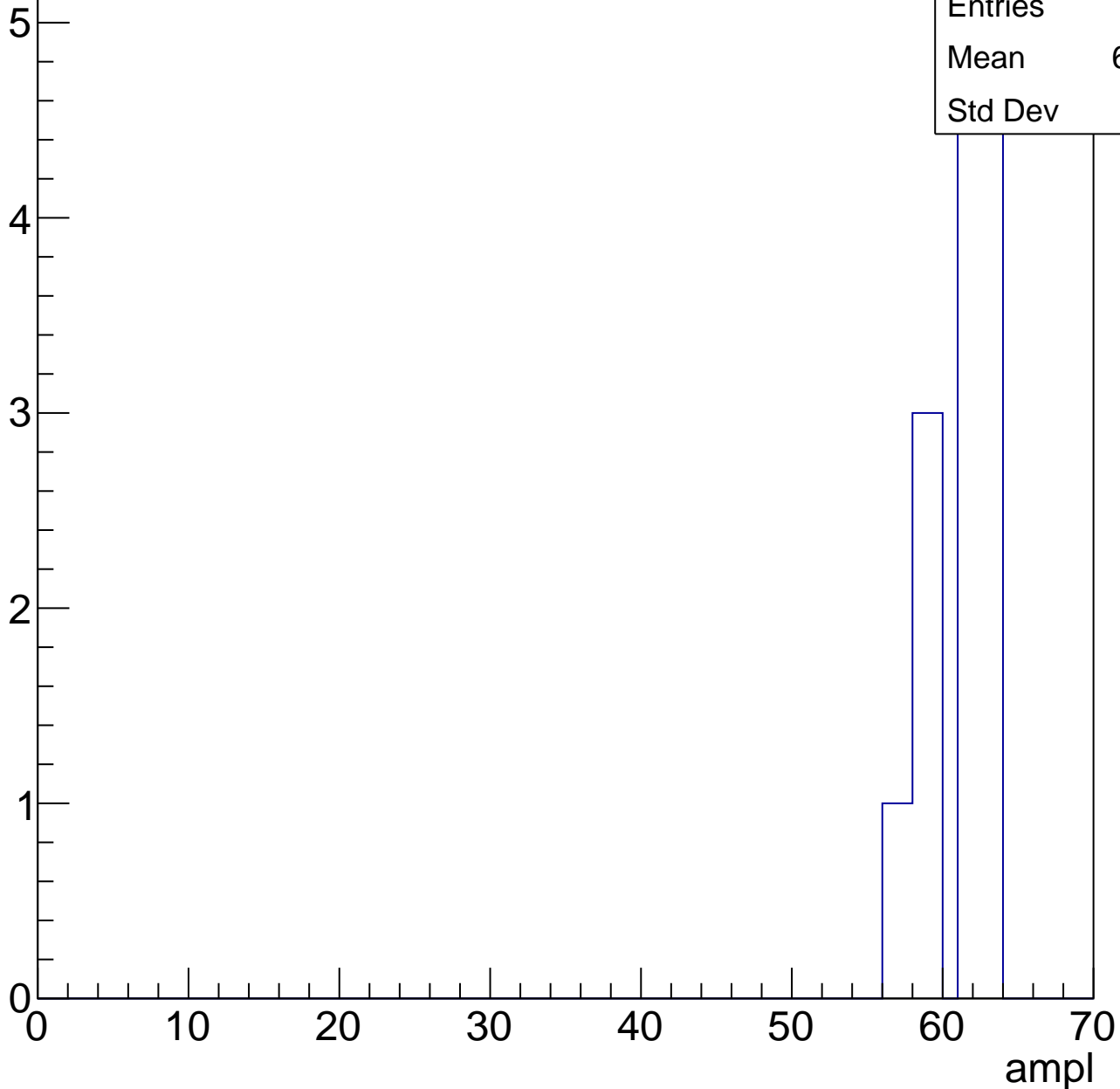


# B1L002S, U17-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	60.61
Std Dev	2.1

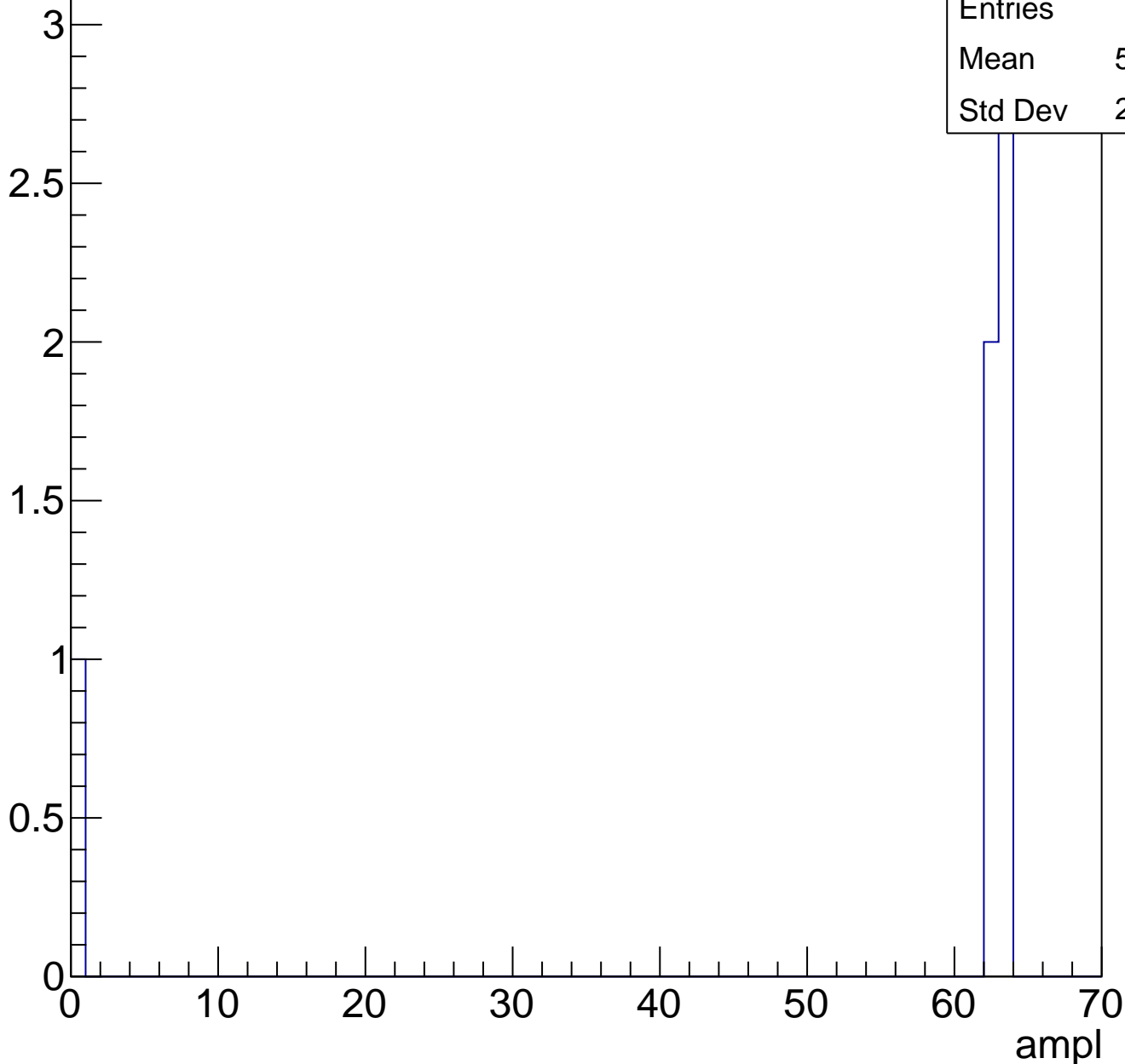




# B1L002S, U17-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch38, adc0

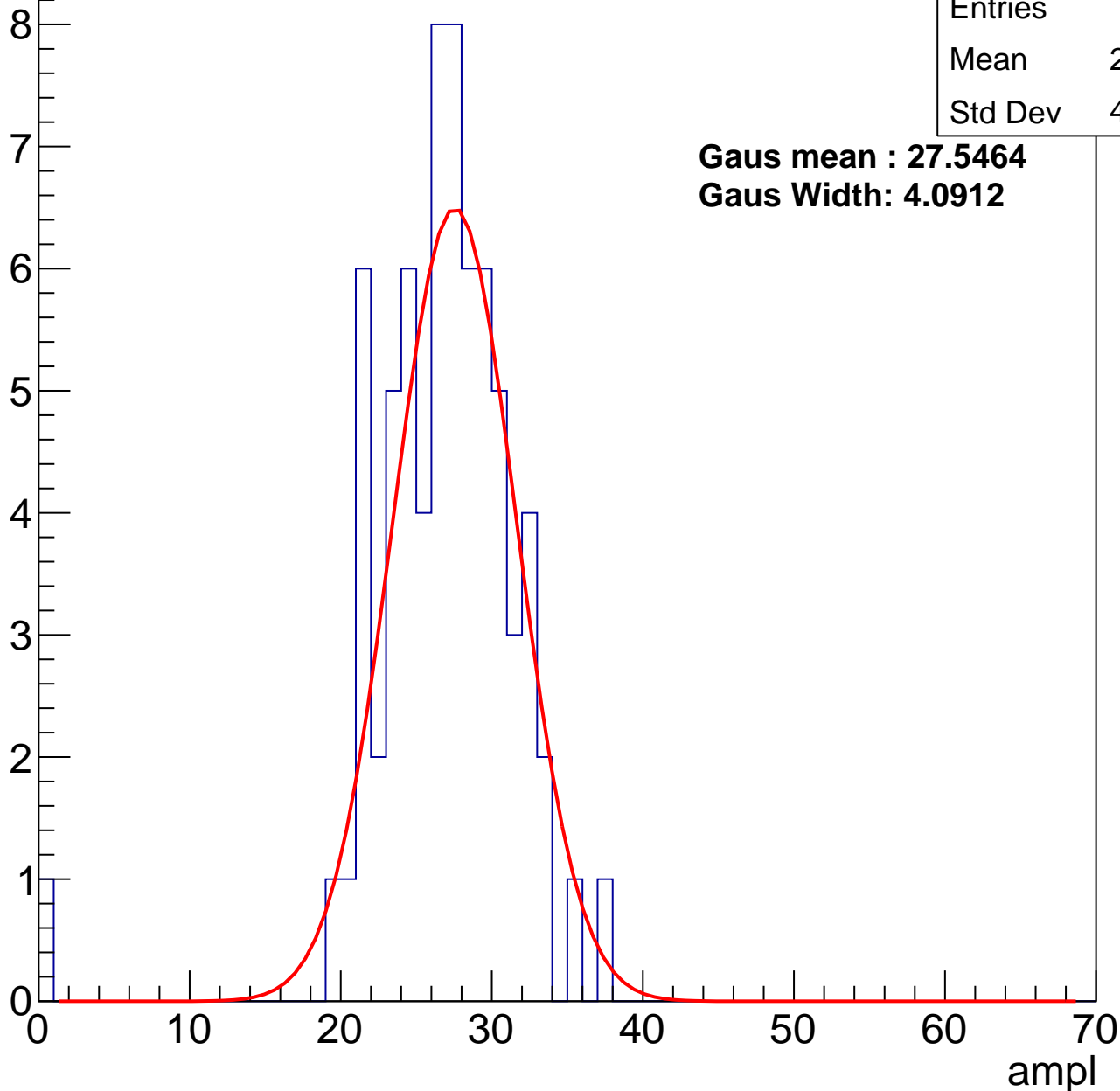
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	26.33
Std Dev	4.919

**Gaus mean : 27.5464**

**Gaus Width: 4.0912**



# B1L002S, U17-ch38, adc1

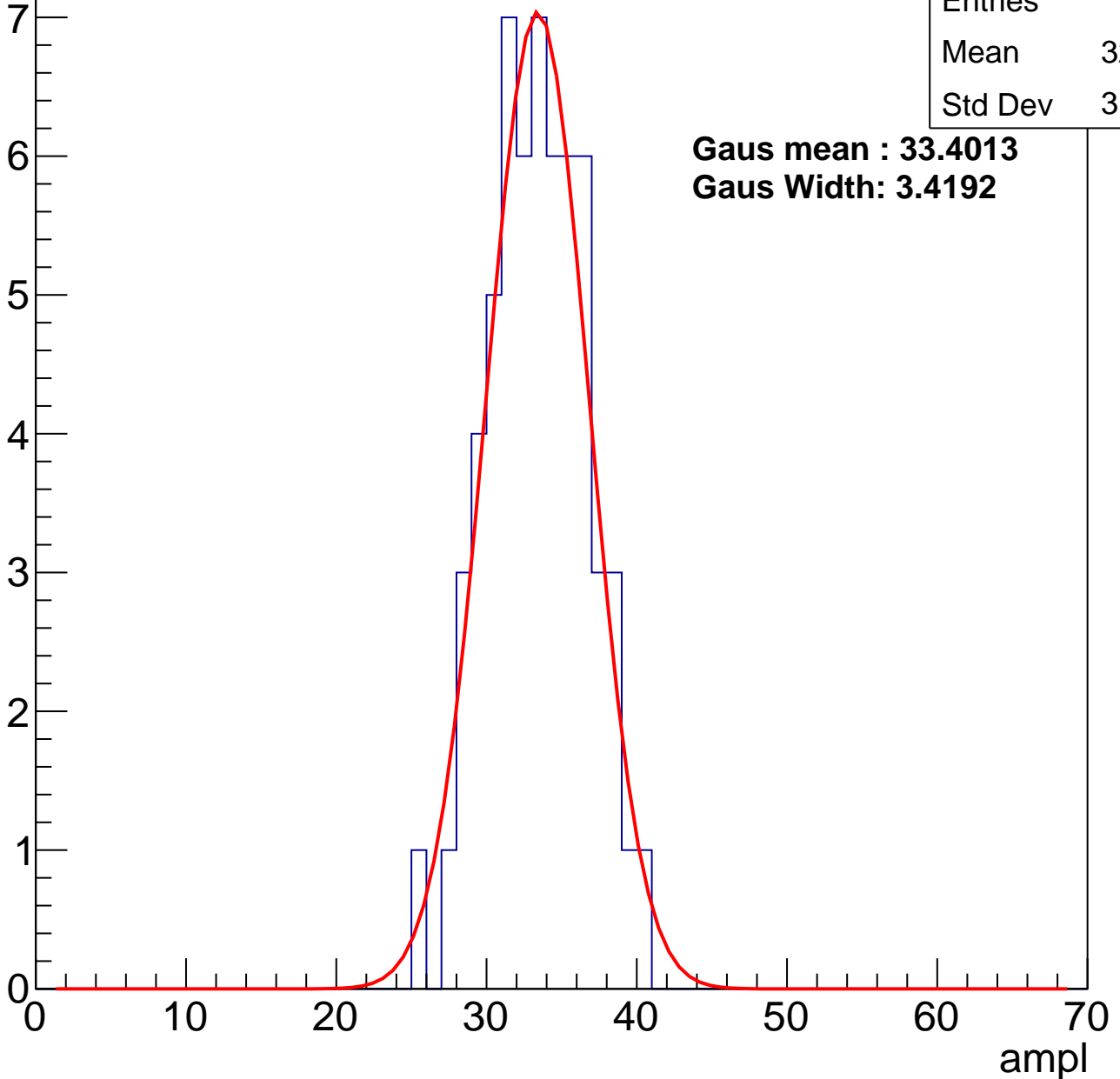
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	32.93
Std Dev	3.188

**Gaus mean : 33.4013**

**Gaus Width: 3.4192**



# B1L002S, U17-ch38, adc2

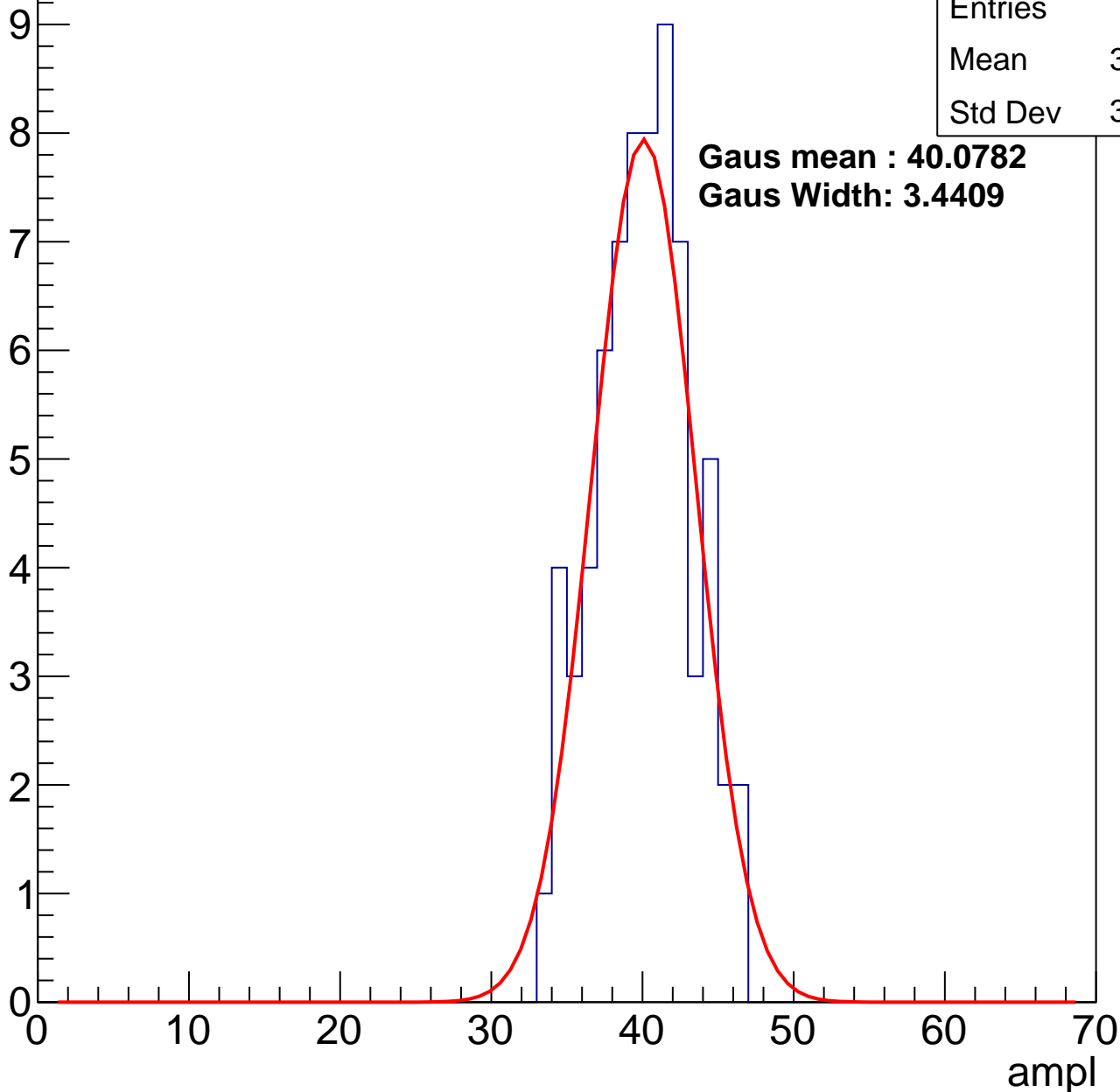
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.59
Std Dev	3.132

**Gaus mean : 40.0782**

**Gaus Width: 3.4409**

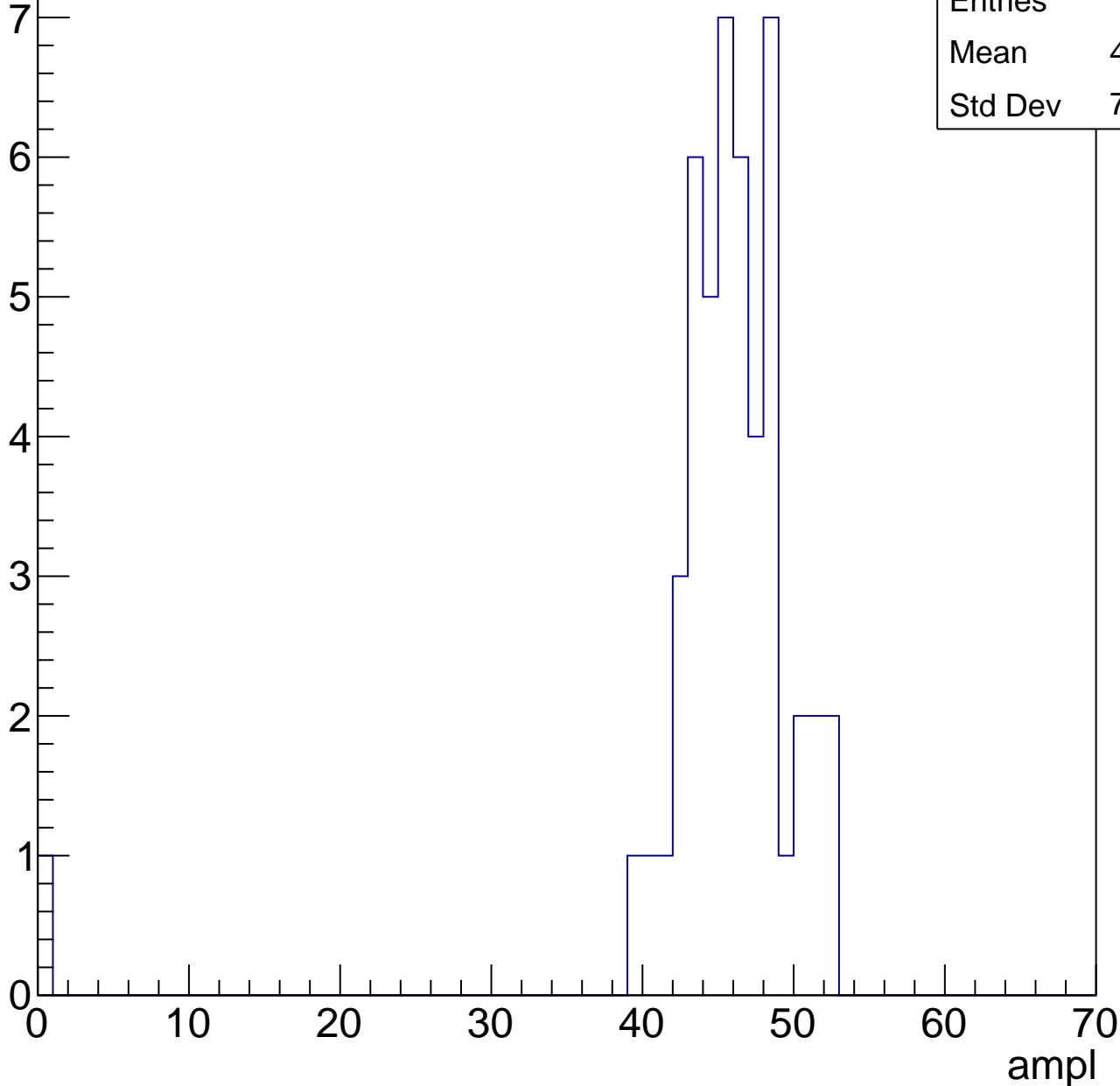


# B1L002S, U17-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	44.78
Std Dev	7.109

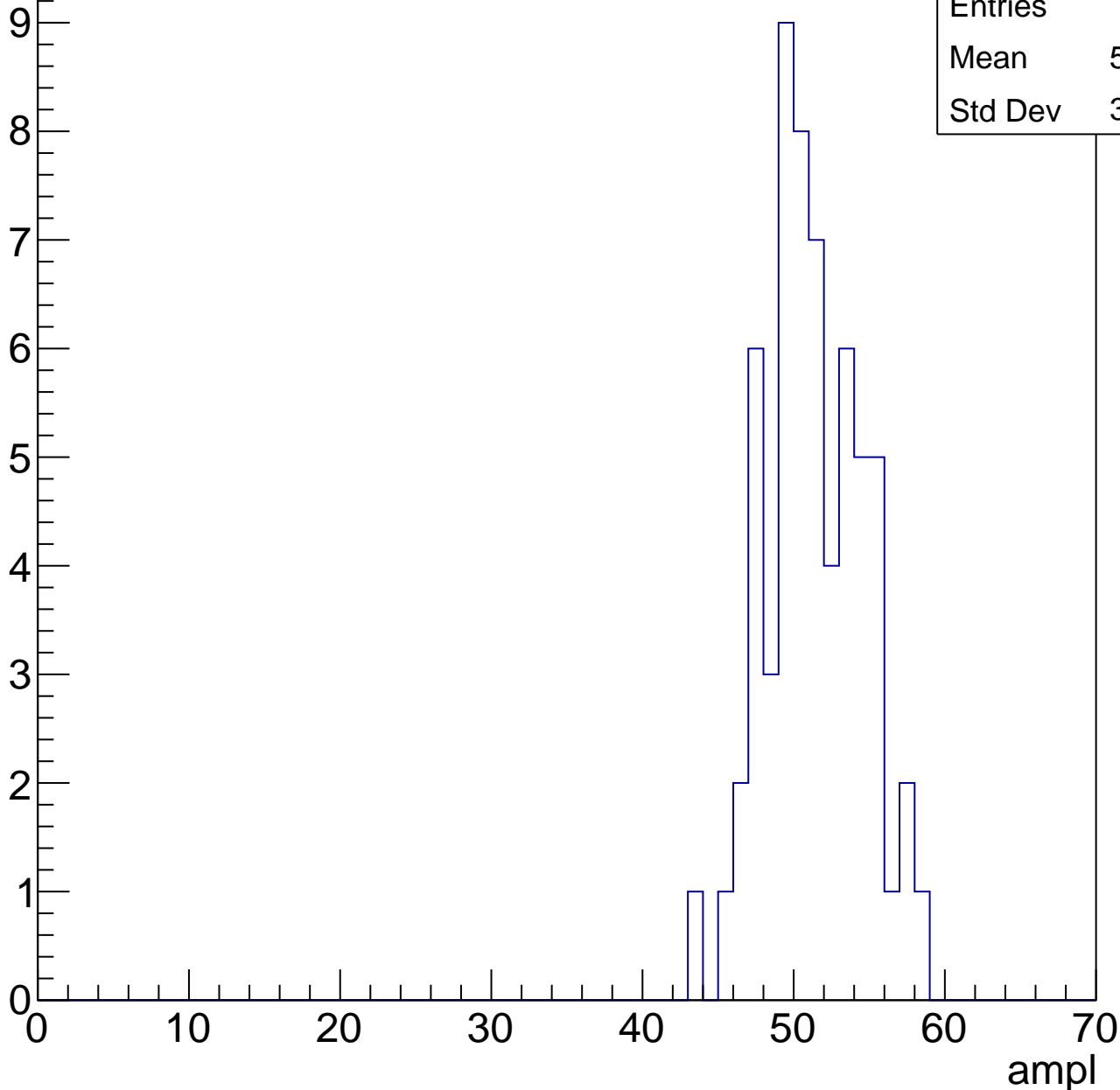


# B1L002S, U17-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

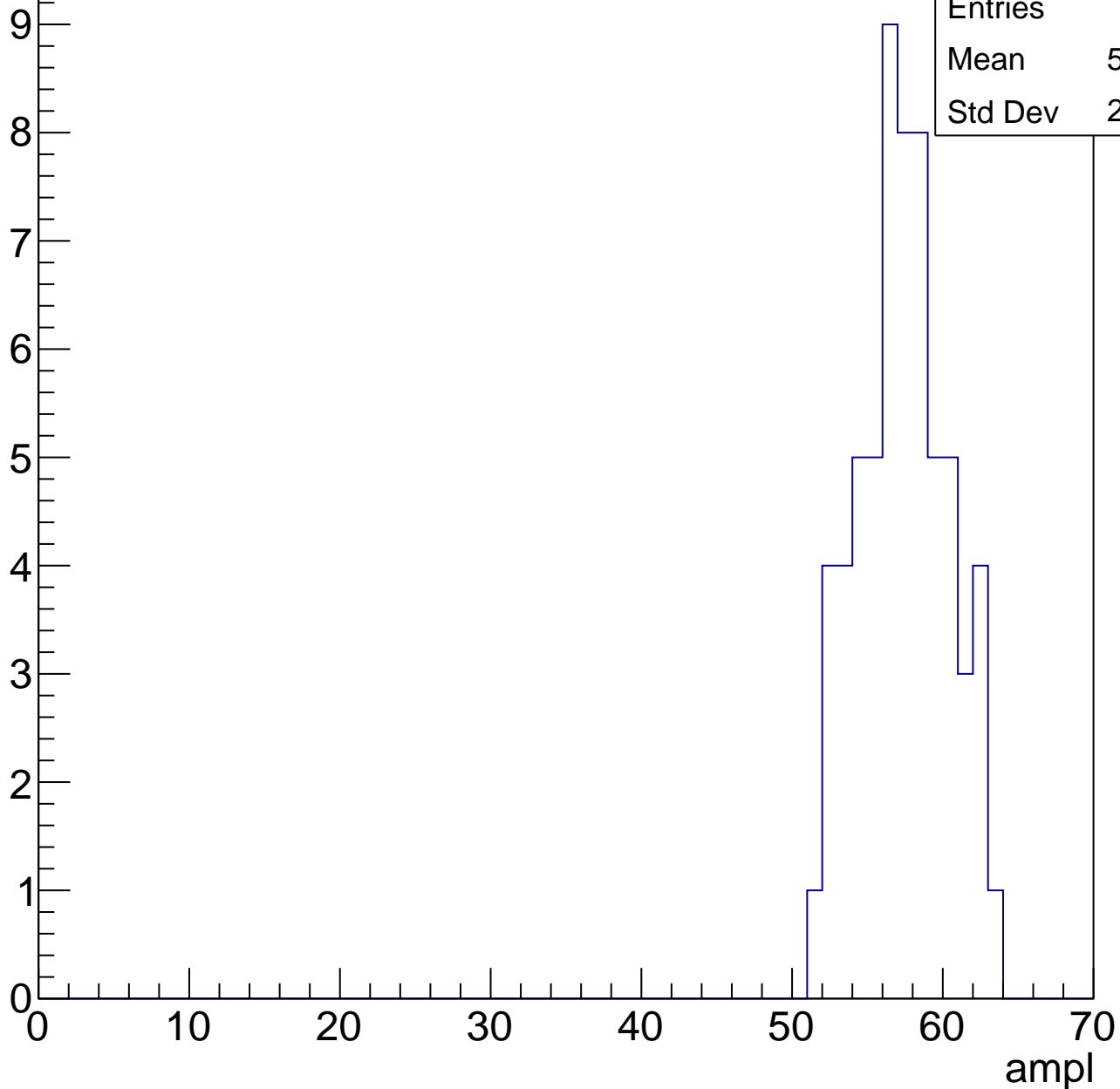
Entries	61
Mean	50.87
Std Dev	3.175



# B1L002S, U17-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



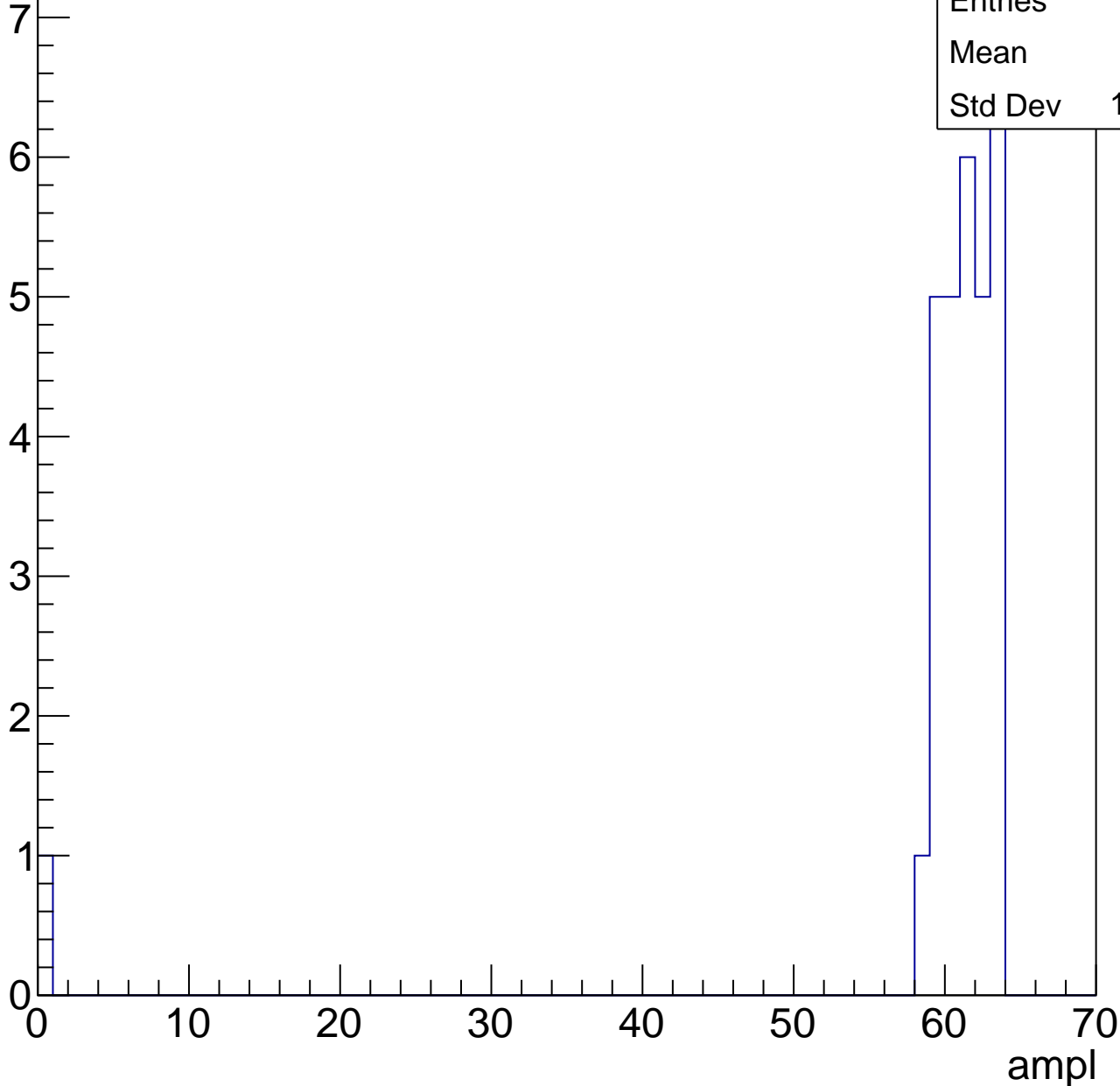
Entries	62
Mean	56.92
Std Dev	2.925

# B1L002S, U17-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59
Std Dev	11.06

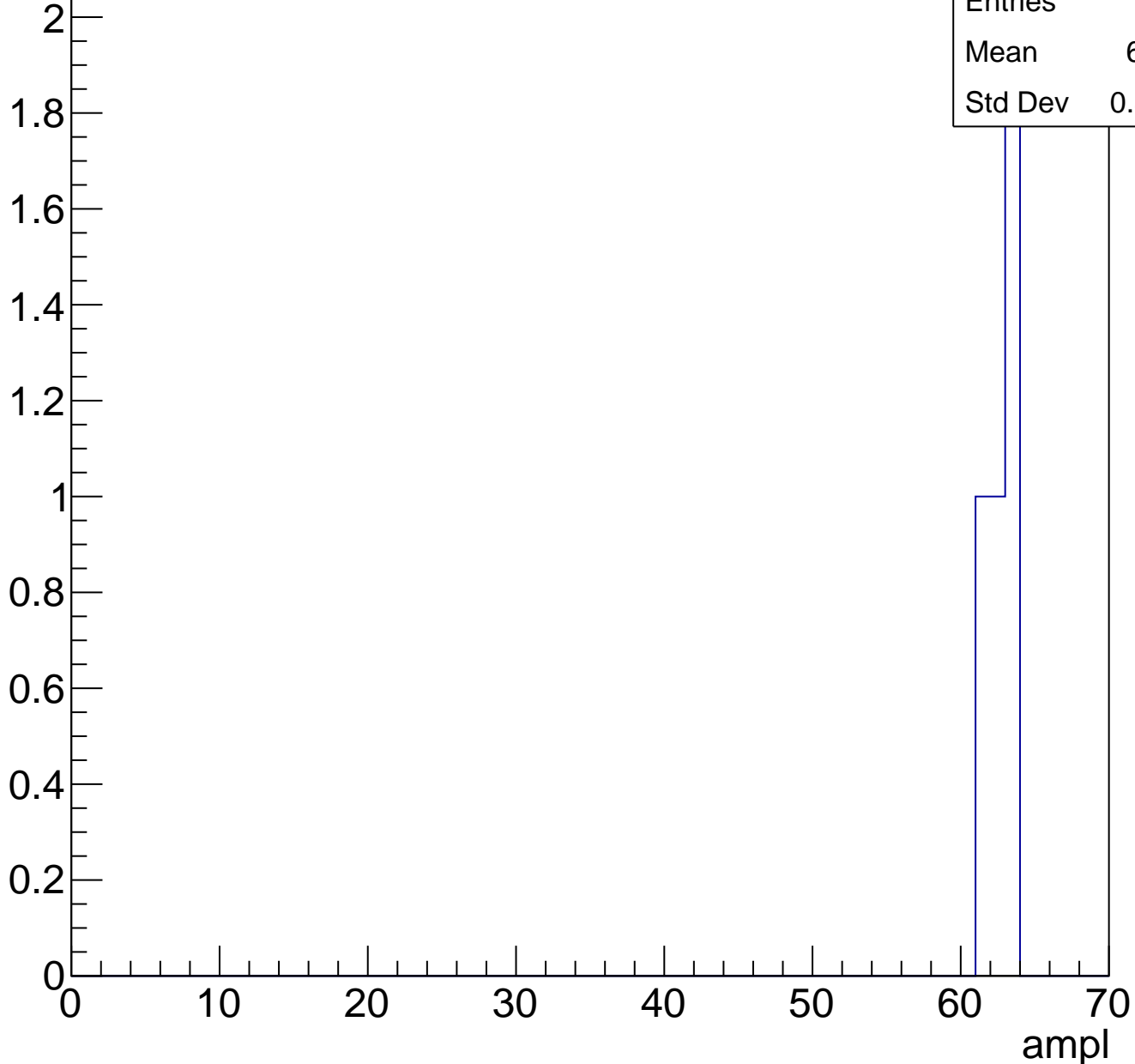




# B1L002S, U17-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch39, adc0

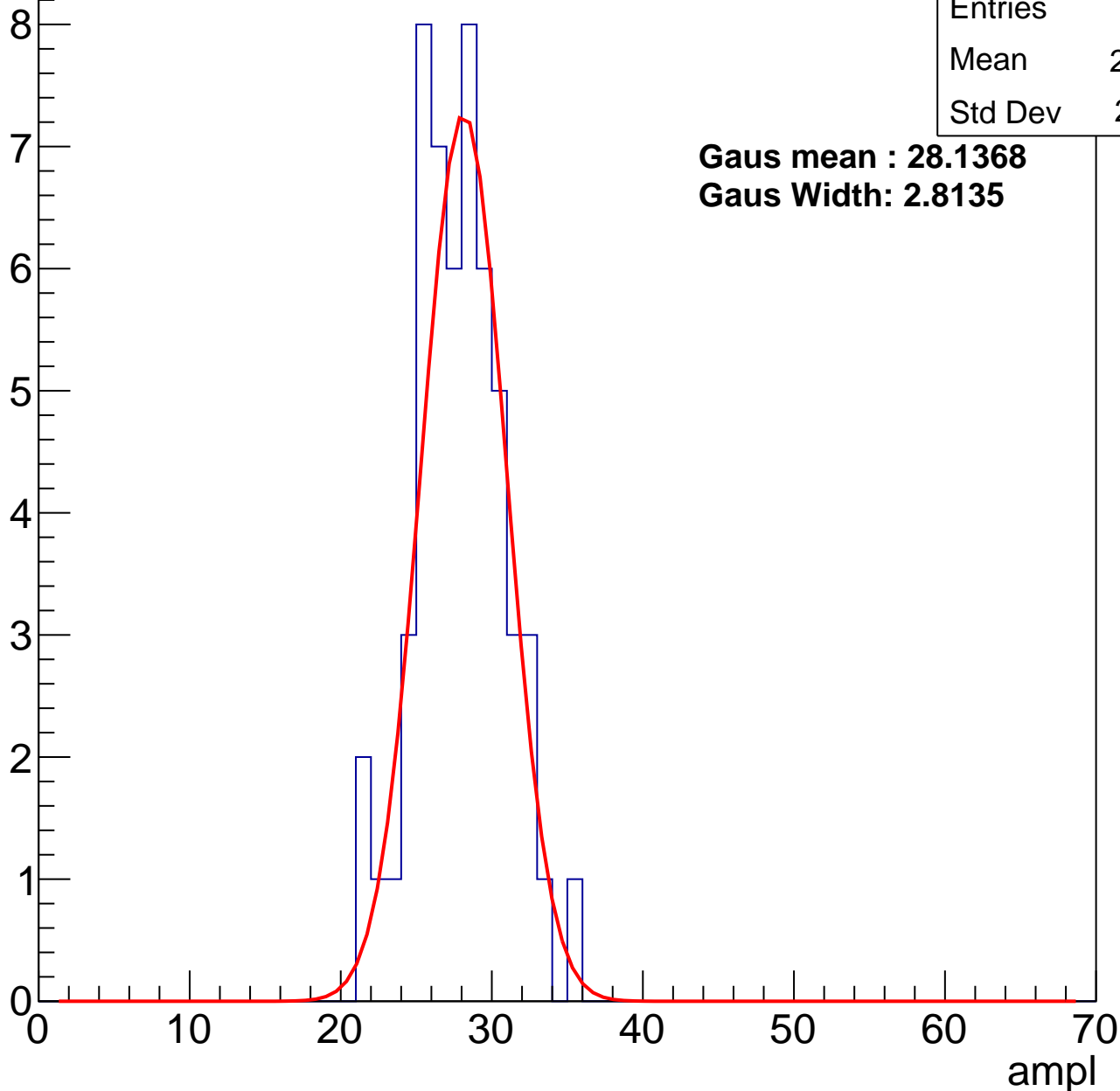
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.42
Std Dev	2.921

**Gaus mean : 28.1368**

**Gaus Width: 2.8135**



# B1L002S, U17-ch39, adc1

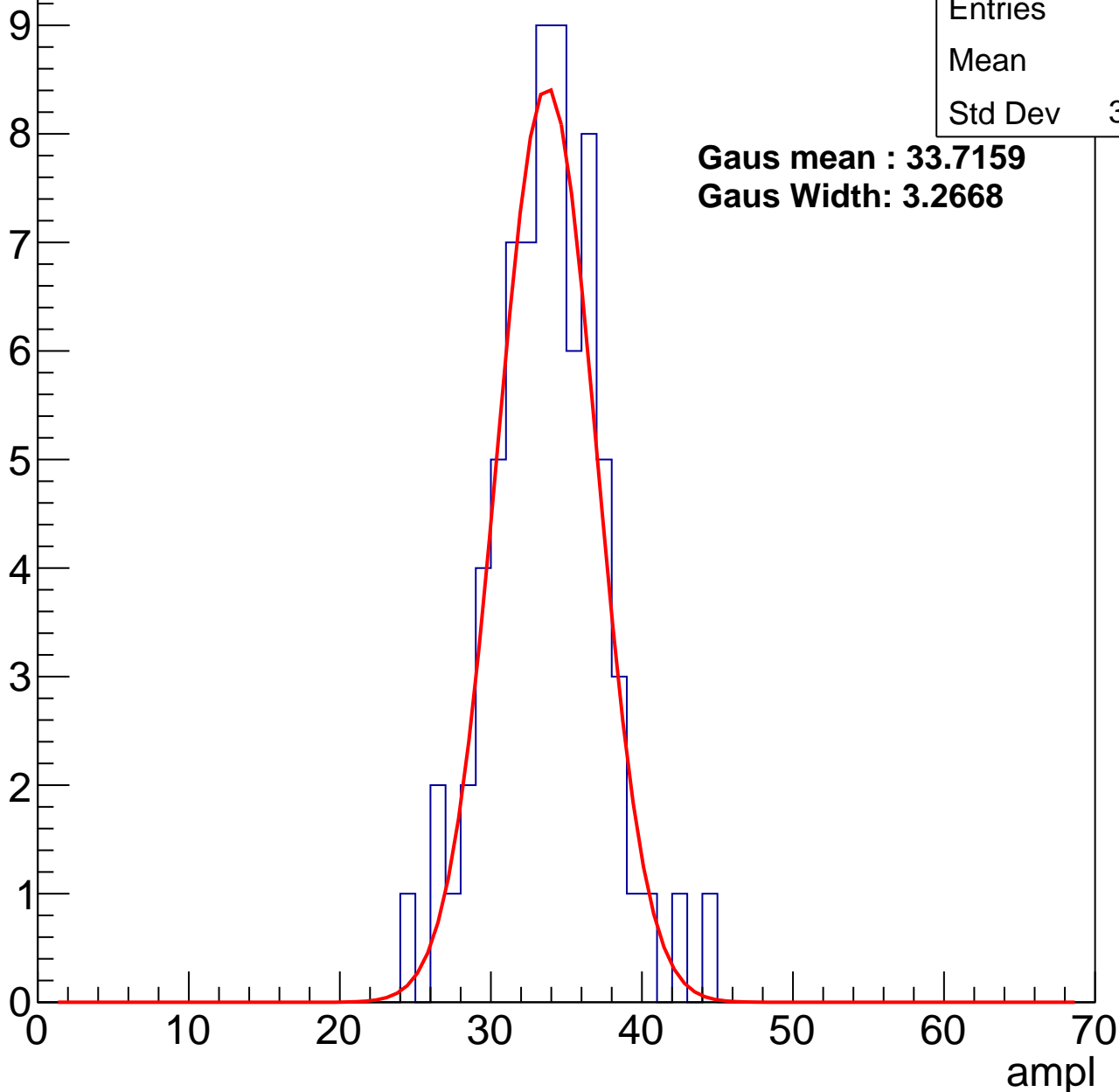
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	33.3
Std Dev	3.606

**Gaus mean : 33.7159**

**Gaus Width: 3.2668**



# B1L002S, U17-ch39, adc2

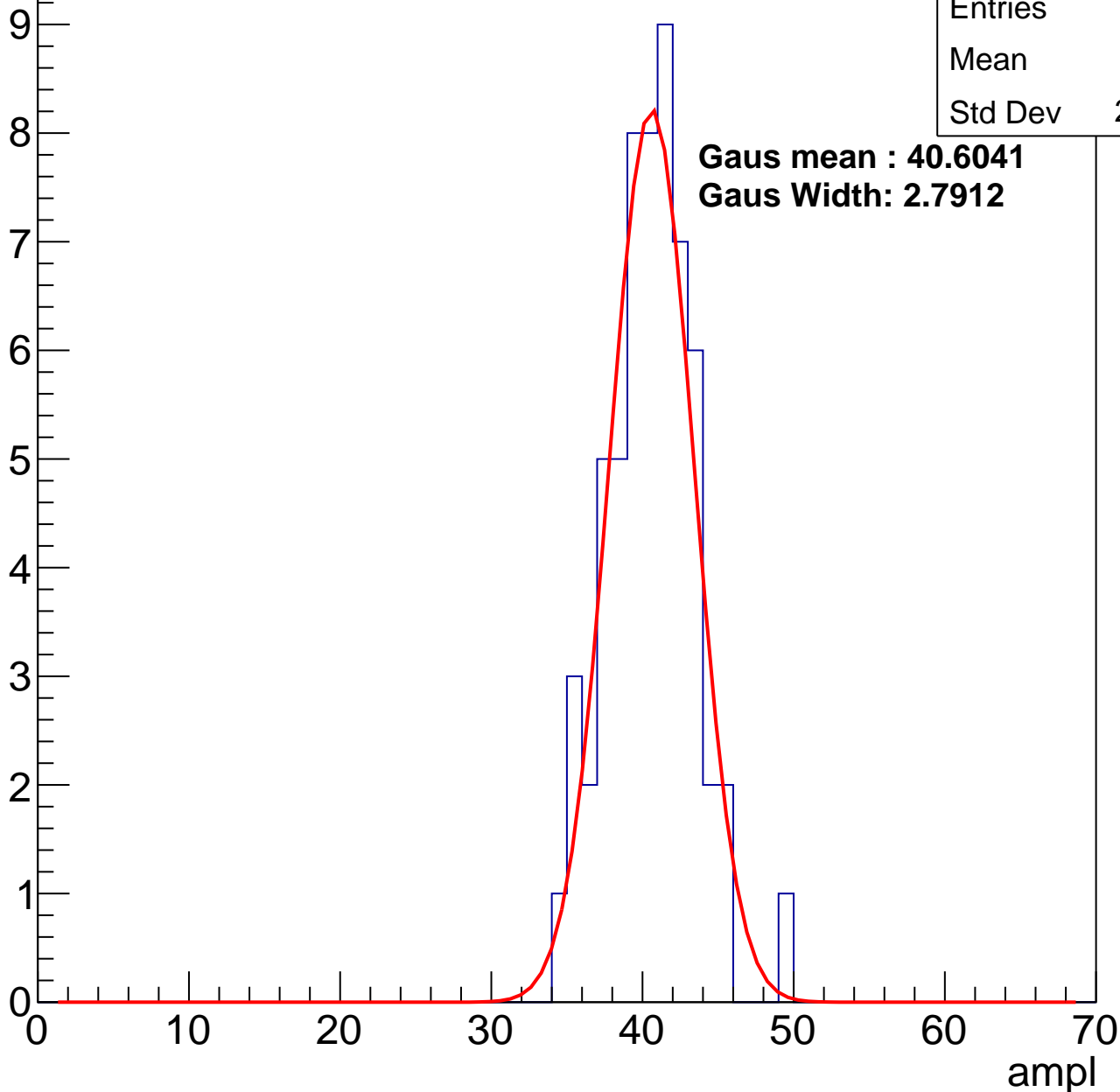
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.1
Std Dev	2.821

**Gaus mean : 40.6041**

**Gaus Width: 2.7912**

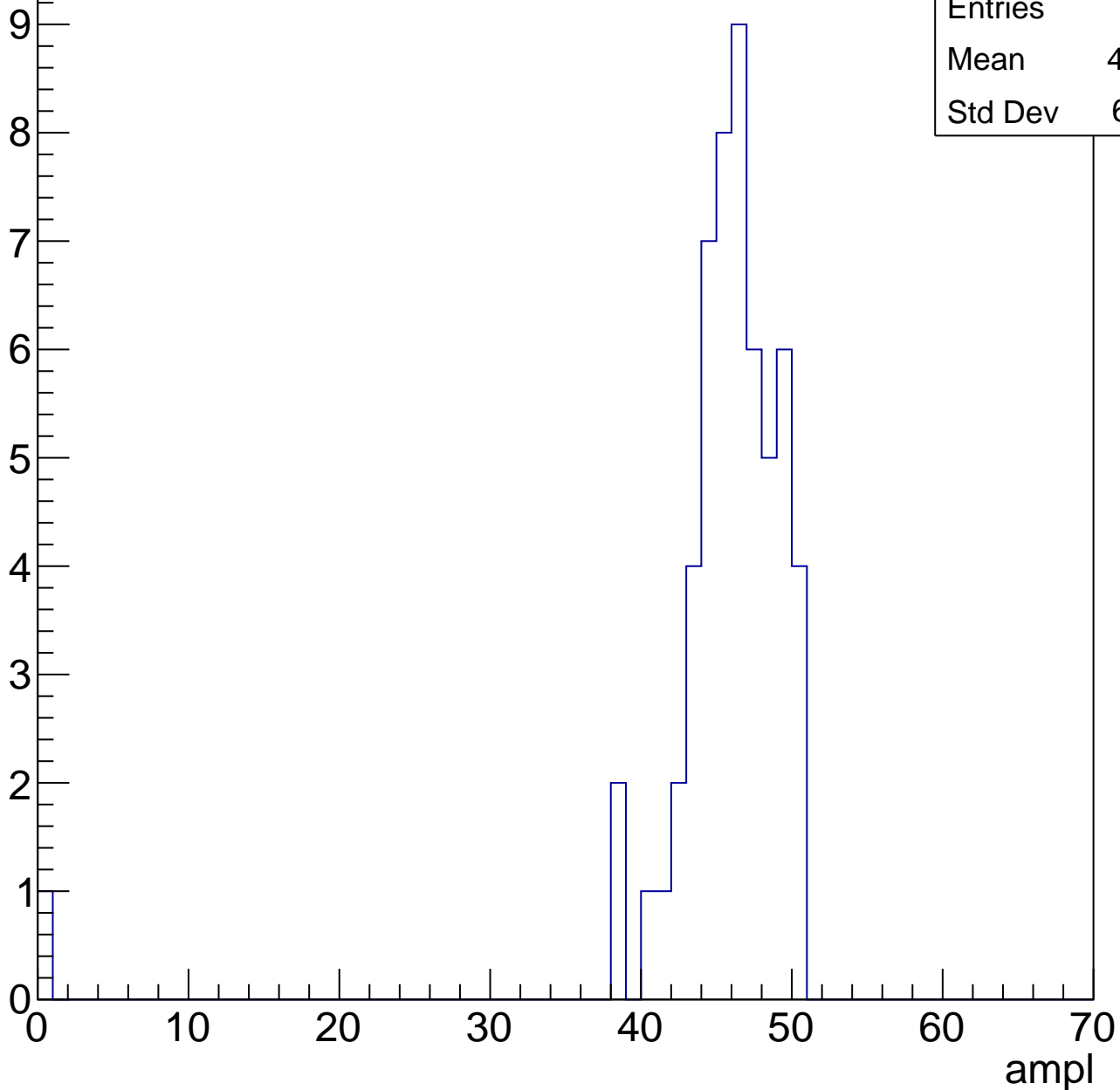


# B1L002S, U17-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	44.84
Std Dev	6.651

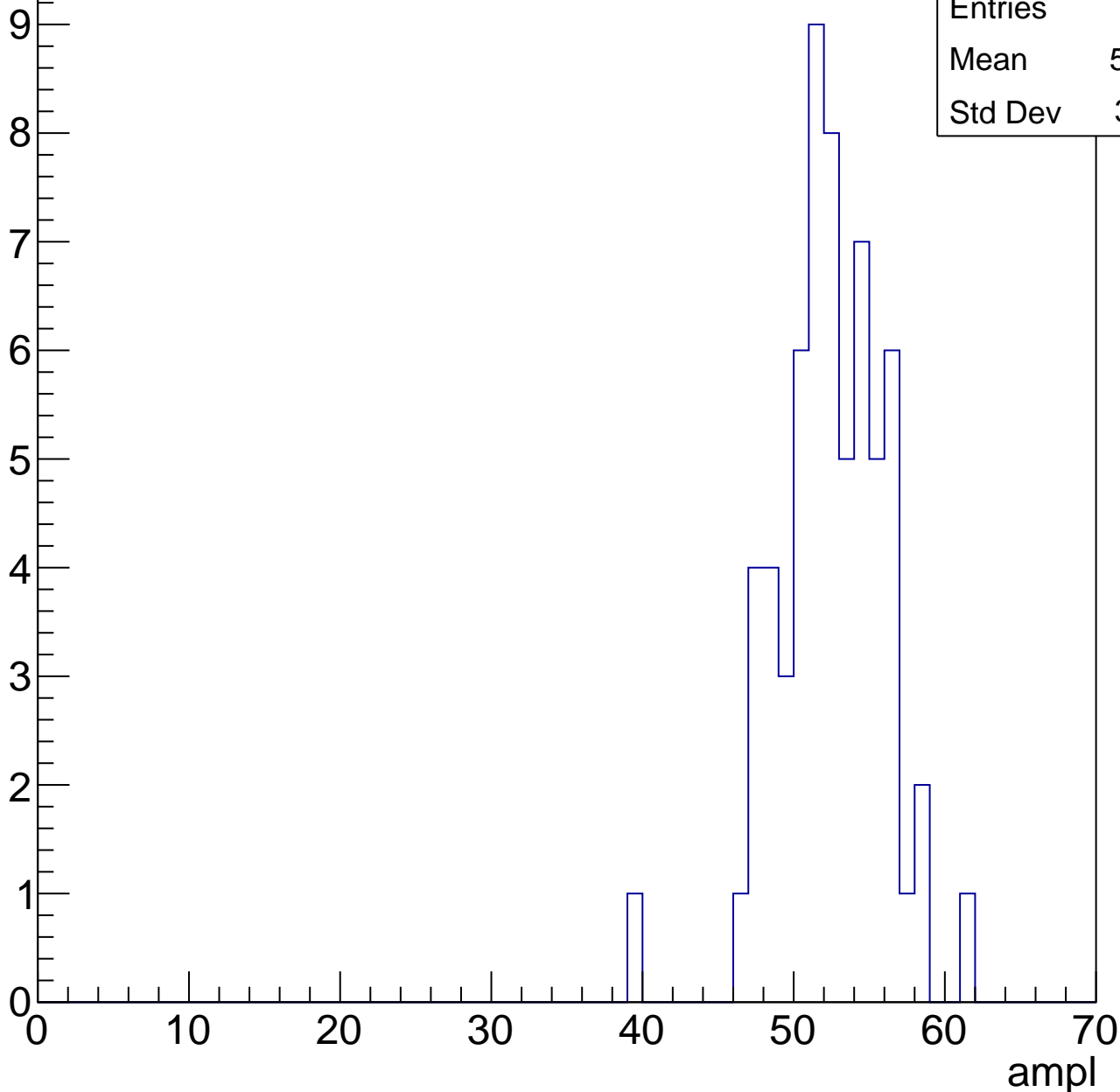


# B1L002S, U17-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	51.98
Std Dev	3.521

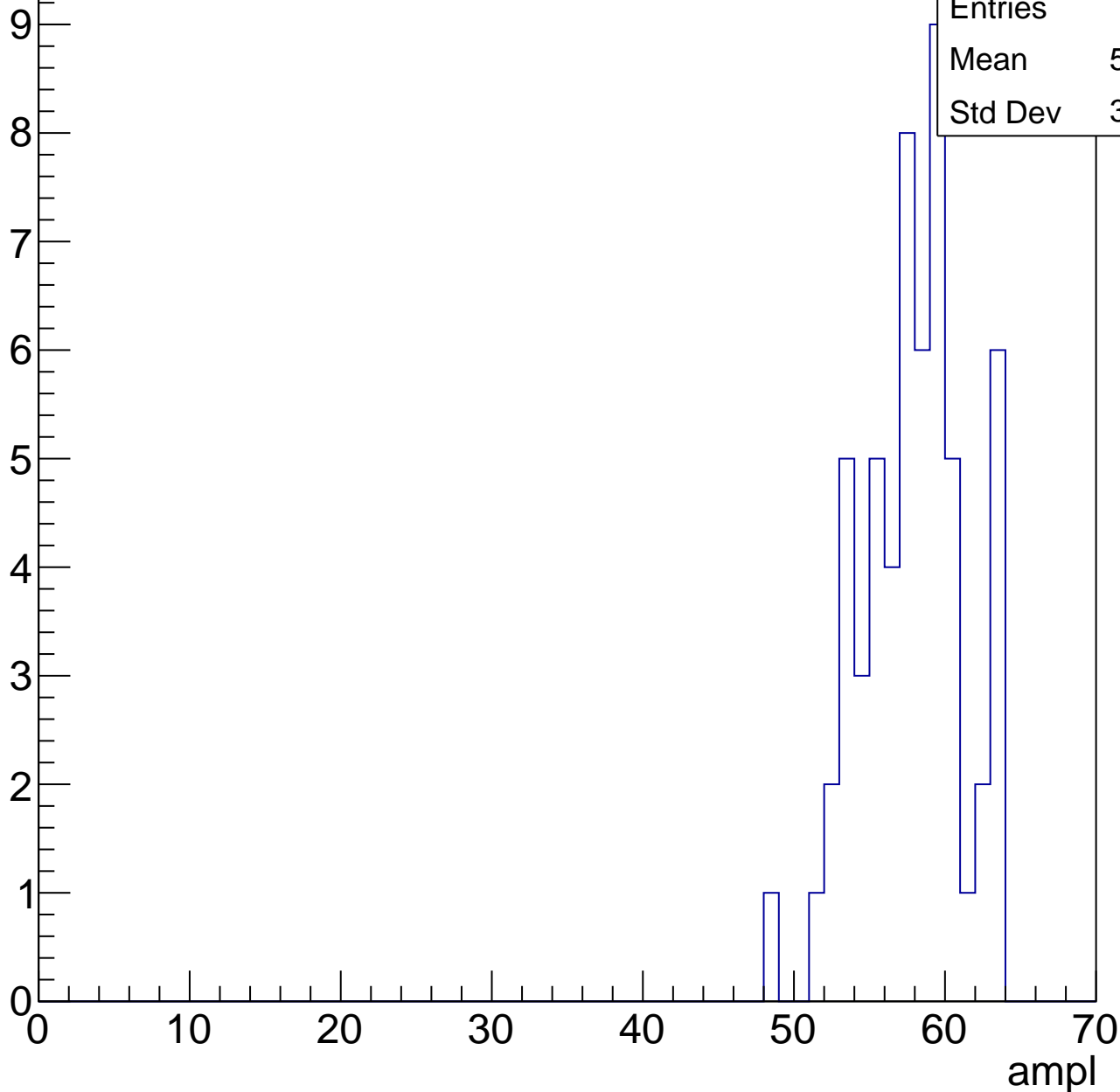


# B1L002S, U17-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.36
Std Dev	3.372

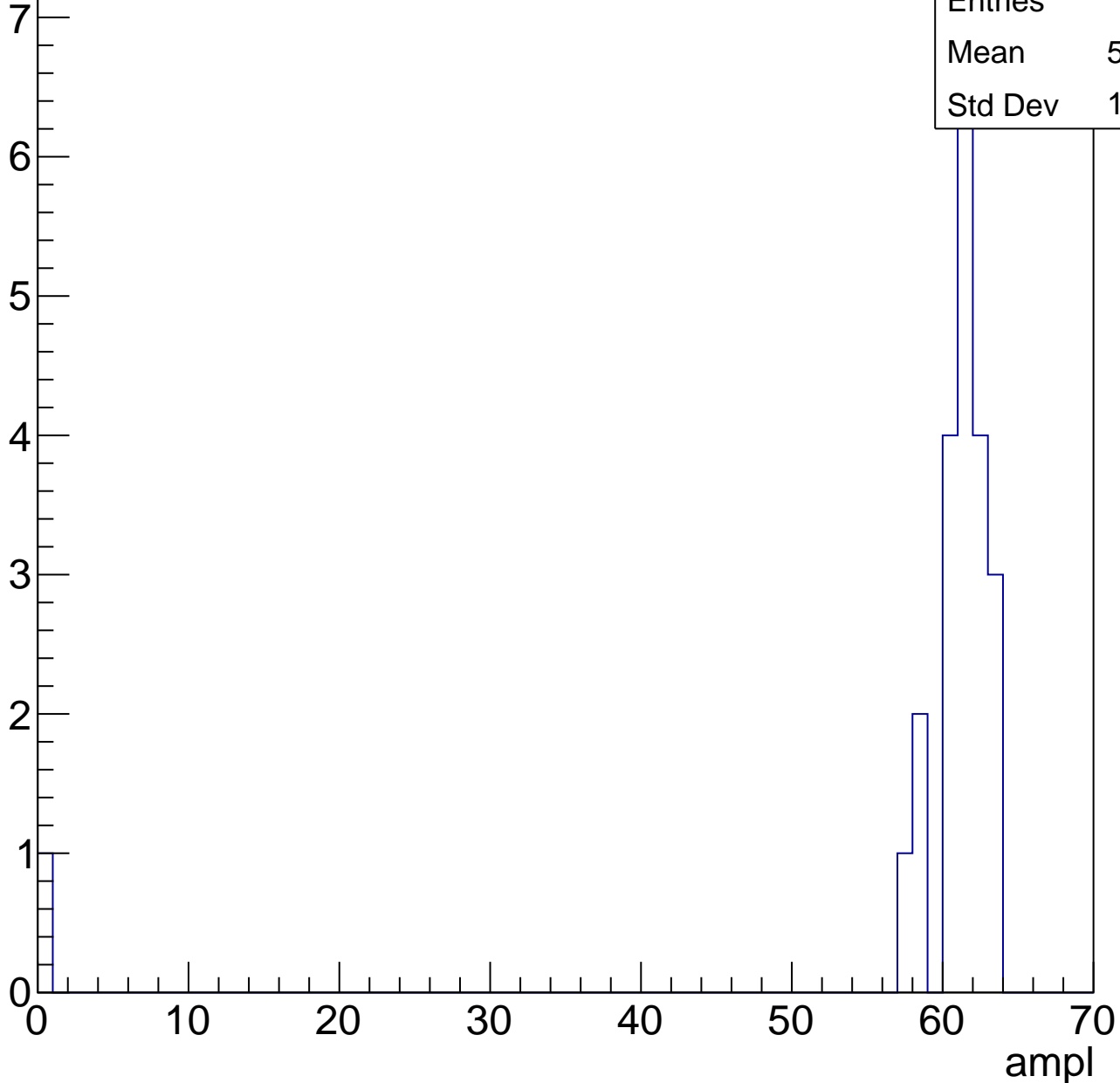


# B1L002S, U17-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.05
Std Dev	12.76

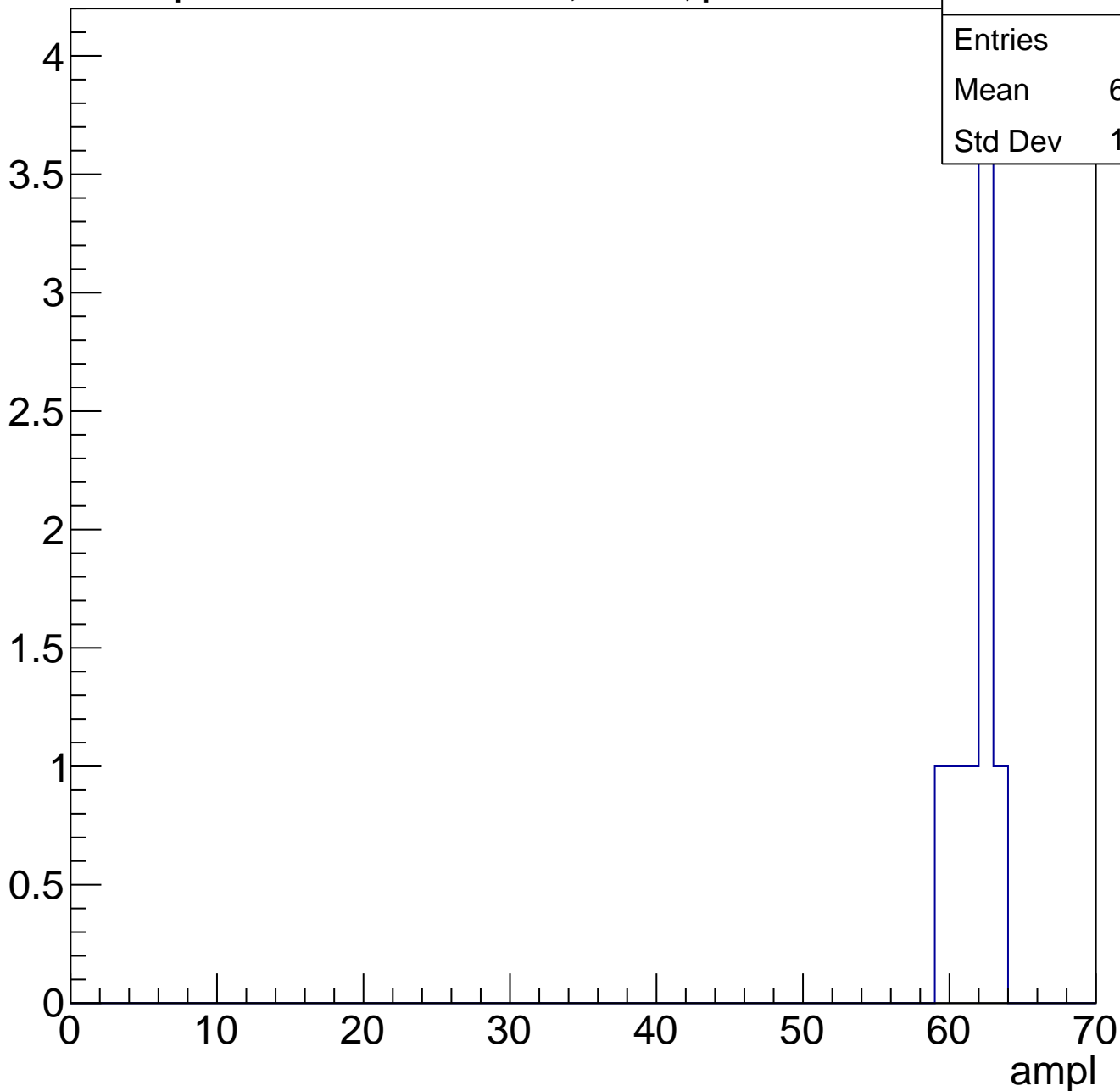




# B1L002S, U17-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch40, adc0

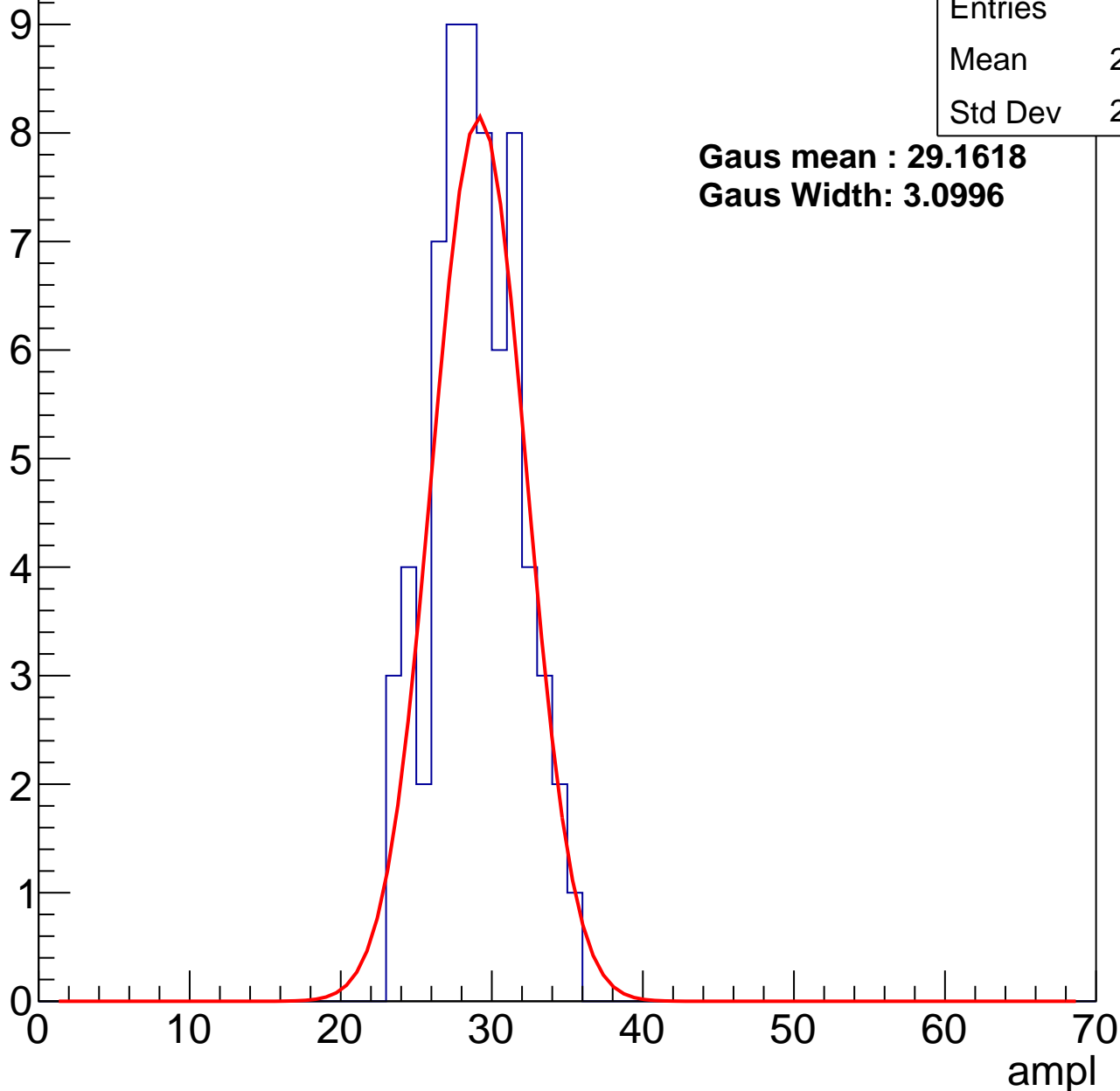
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	28.52
Std Dev	2.862

**Gaus mean : 29.1618**

**Gaus Width: 3.0996**



# B1L002S, U17-ch40, adc1

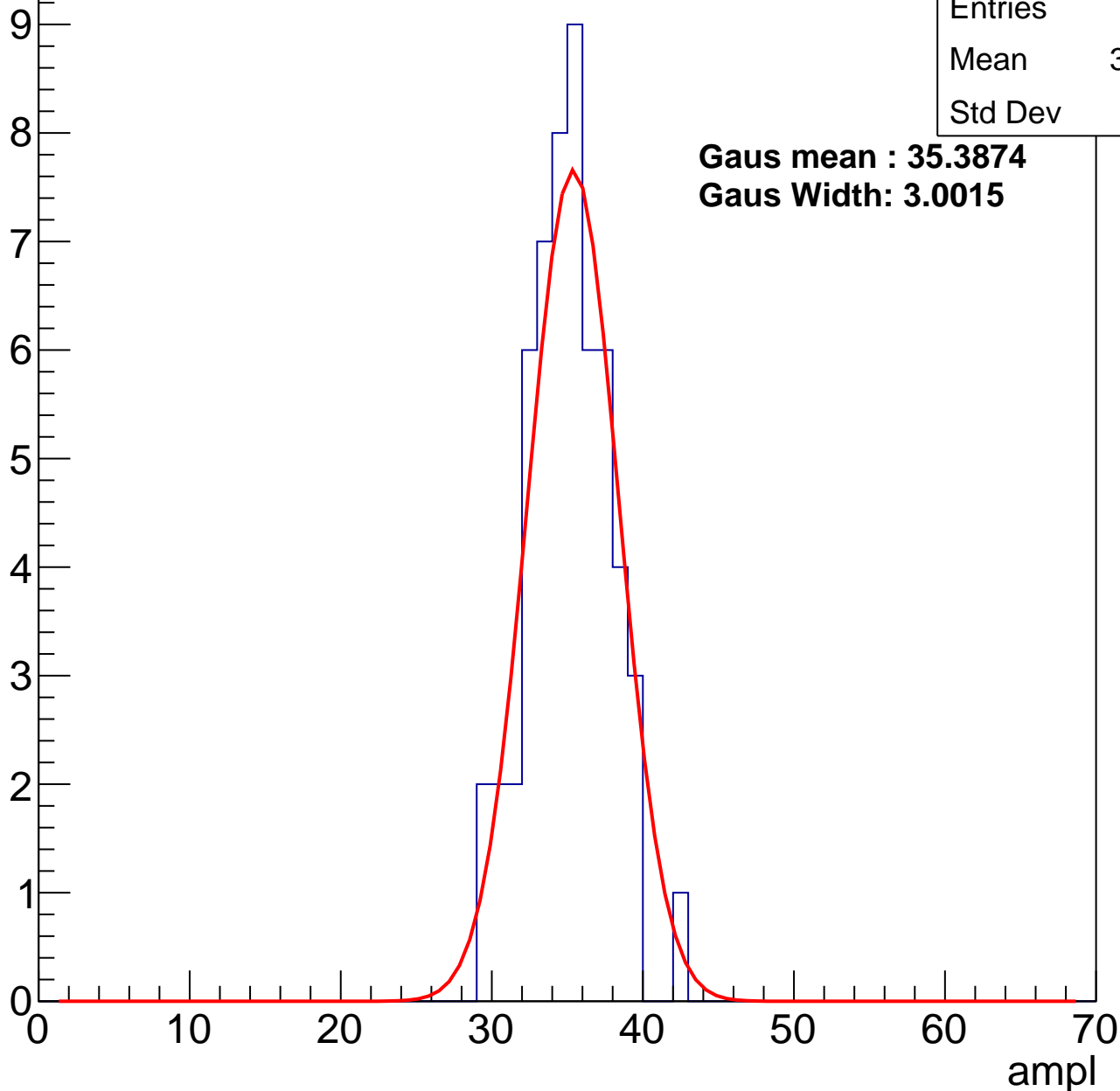
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.62
Std Dev	2.67

**Gaus mean : 35.3874**

**Gaus Width: 3.0015**



# B1L002S, U17-ch40, adc2

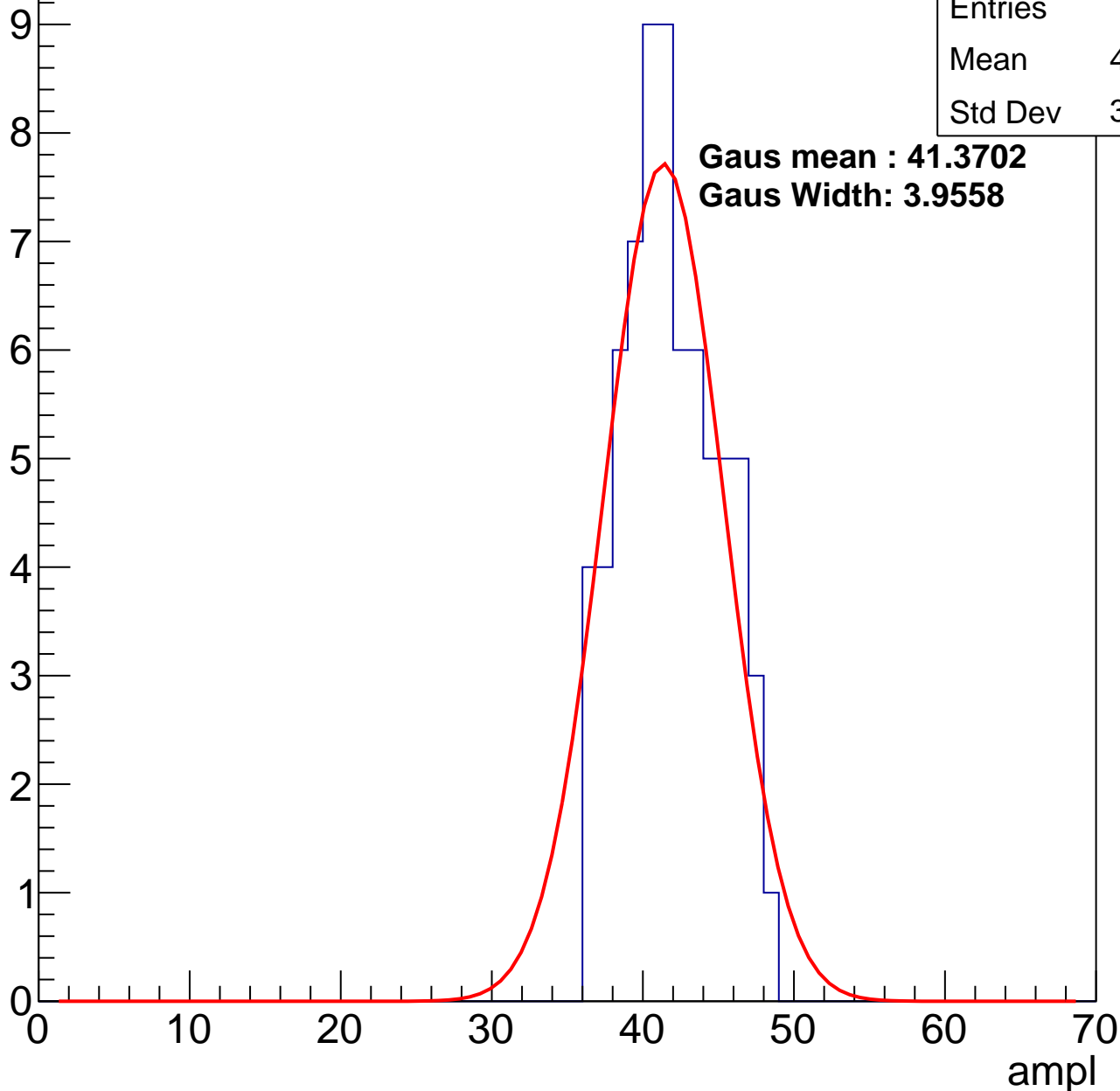
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	41.37
Std Dev	3.122

**Gaus mean : 41.3702**

**Gaus Width: 3.9558**

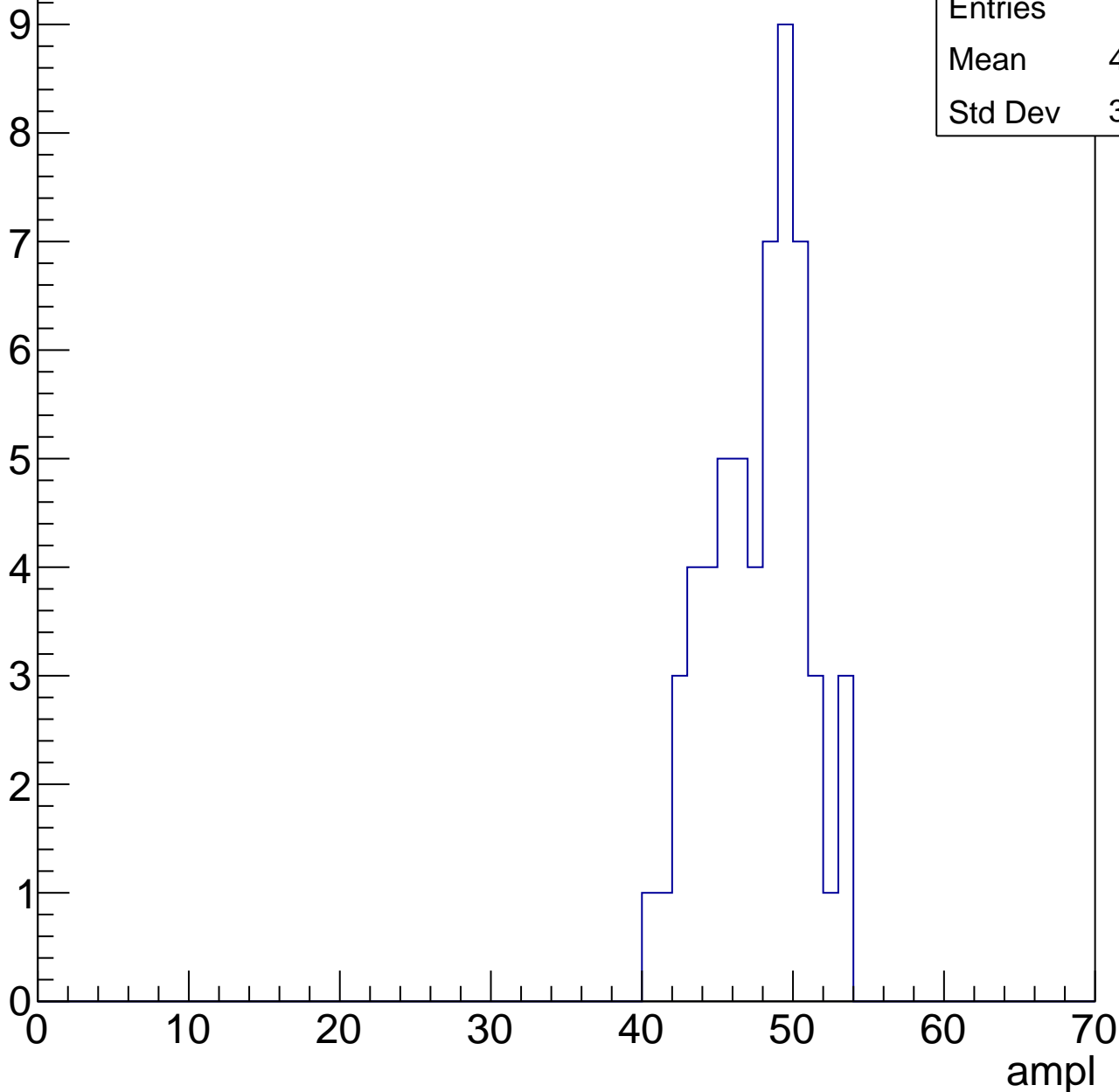


# B1L002S, U17-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	47.18
Std Dev	3.163

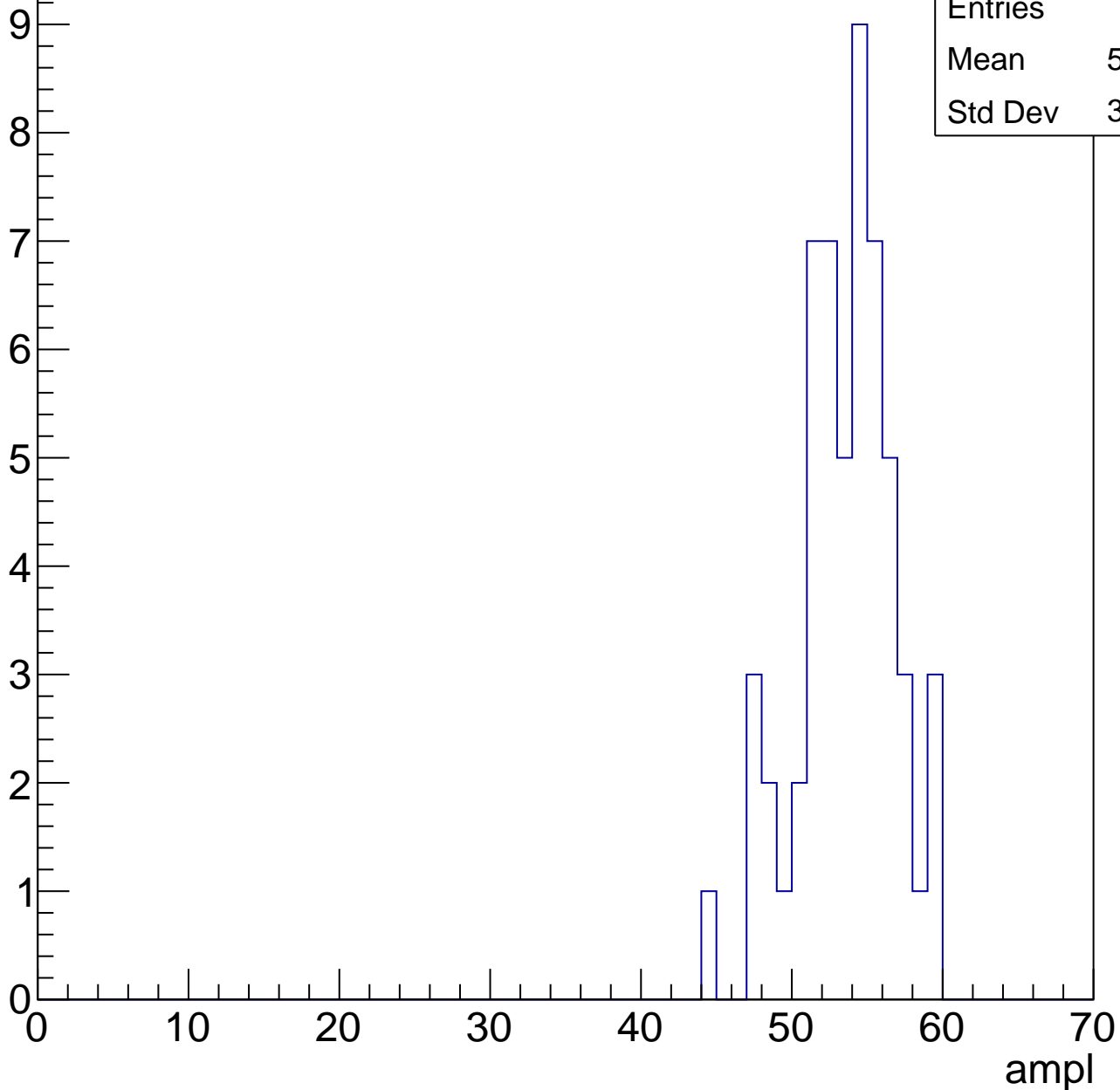


# B1L002S, U17-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.09
Std Dev	3.192



# B1L002S, U17-ch40, adc5

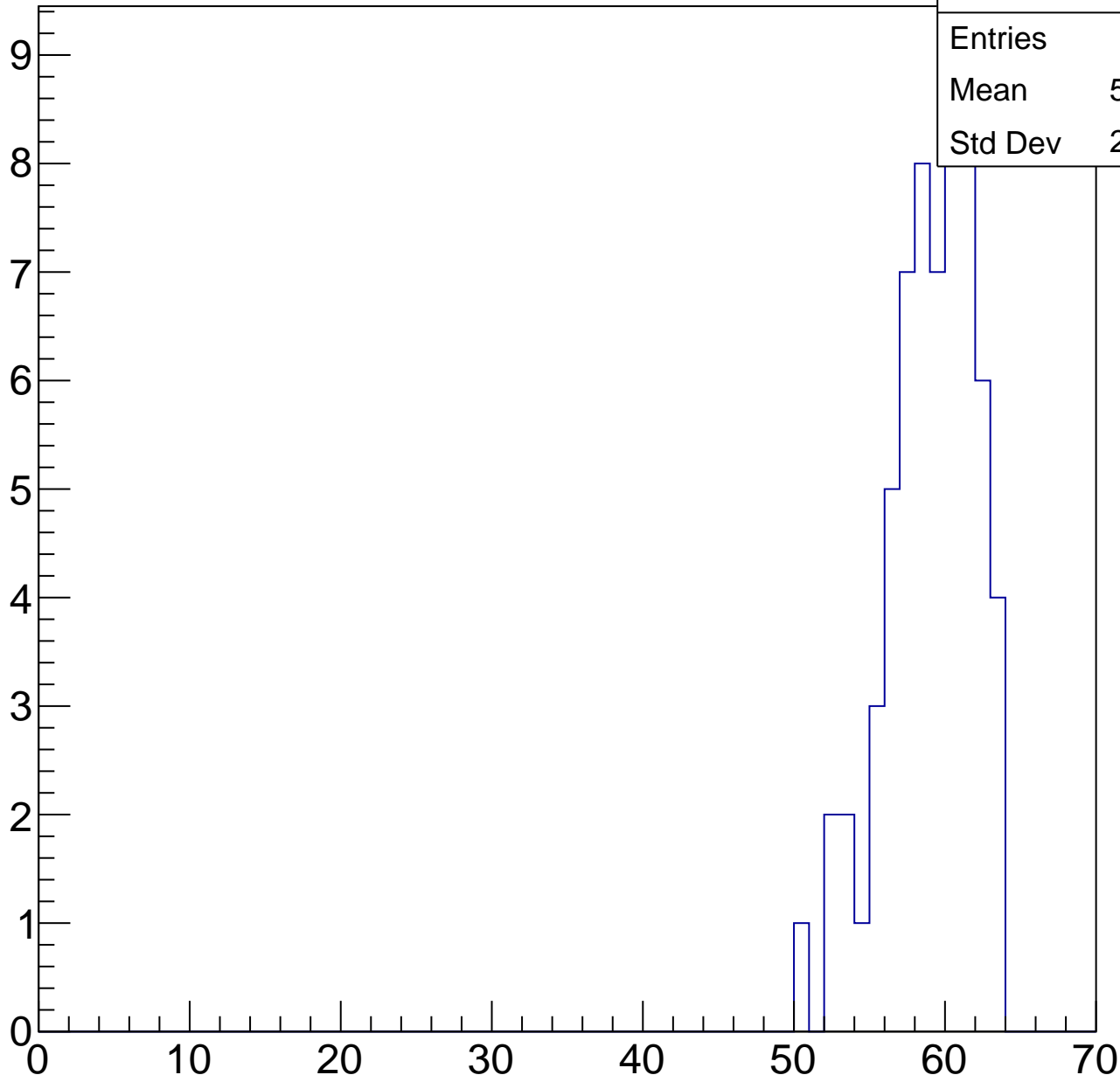
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.52
Std Dev	2.954

ampl

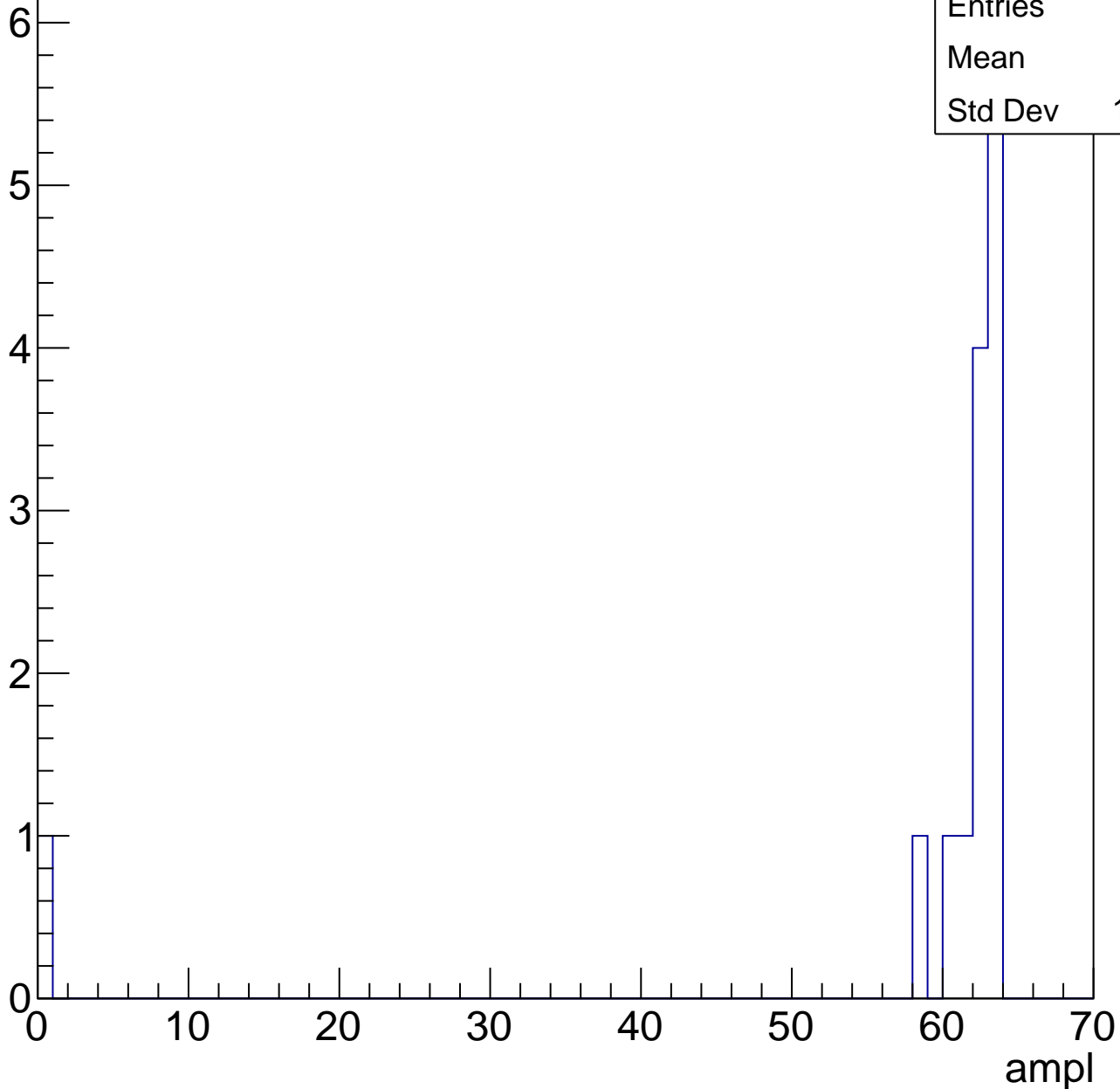


# B1L002S, U17-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.5
Std Dev	16.01

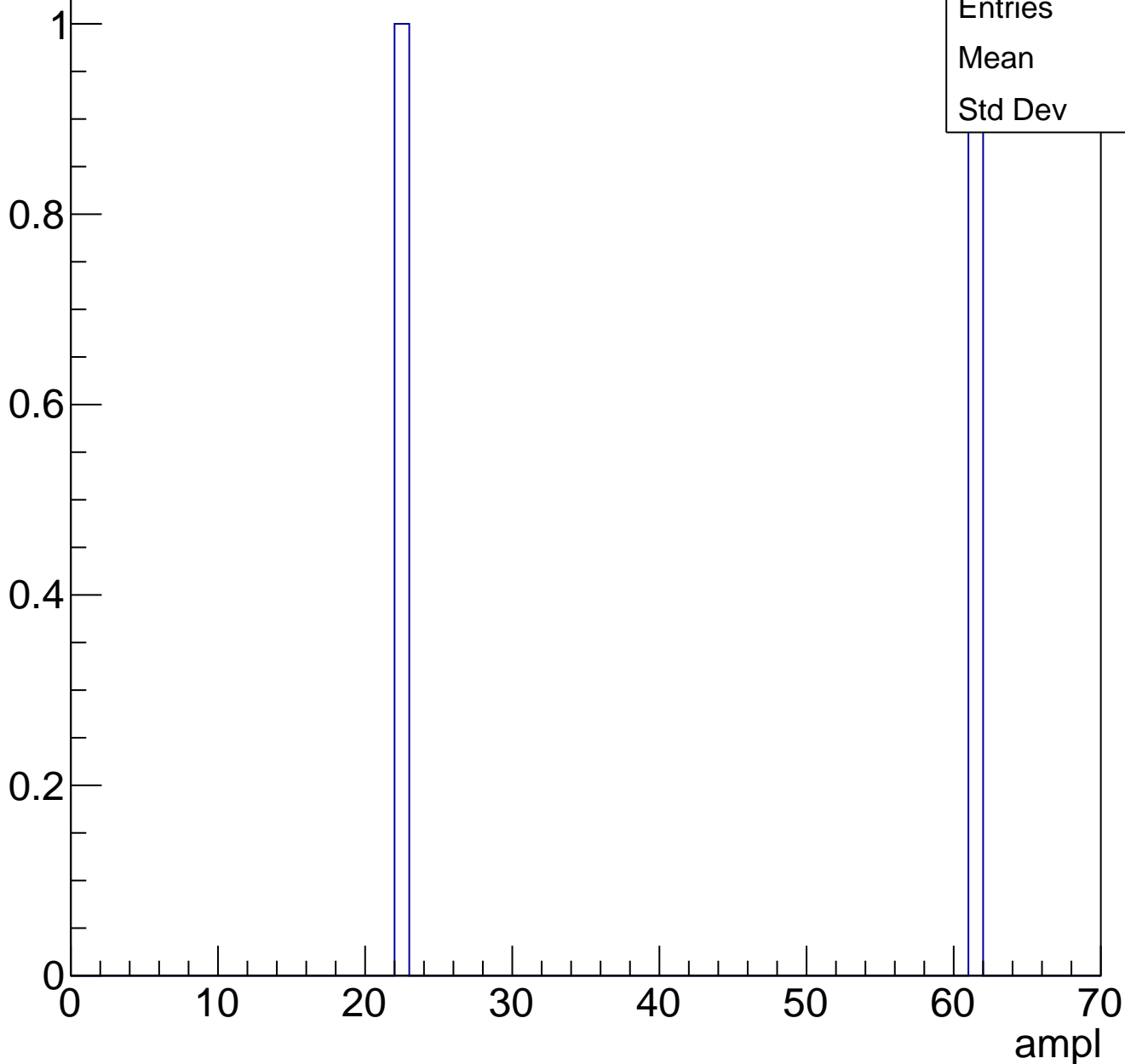




# B1L002S, U17-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch41, adc0

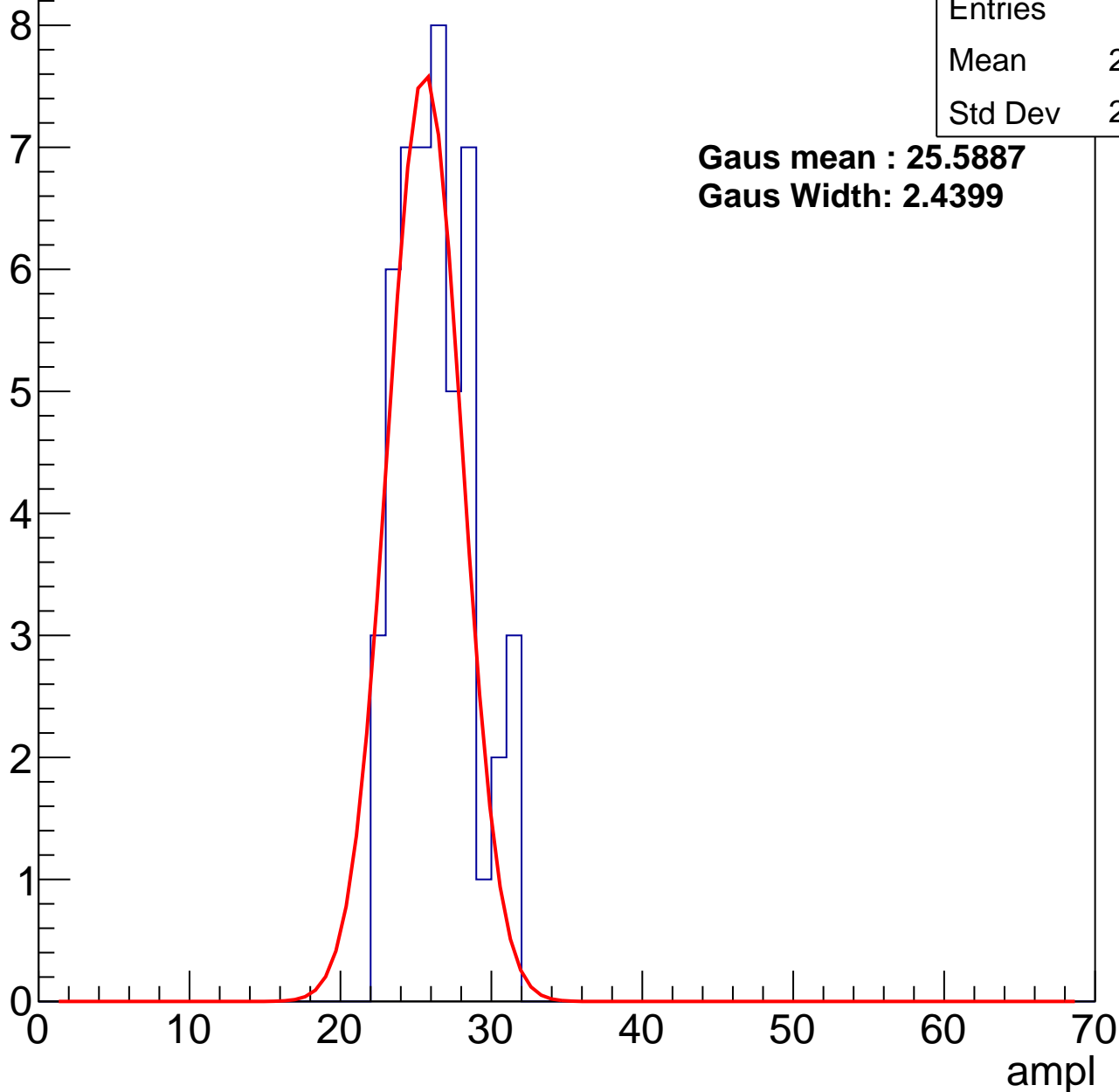
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	25.88
Std Dev	2.413

**Gaus mean : 25.5887**

**Gaus Width: 2.4399**



# B1L002S, U17-ch41, adc1

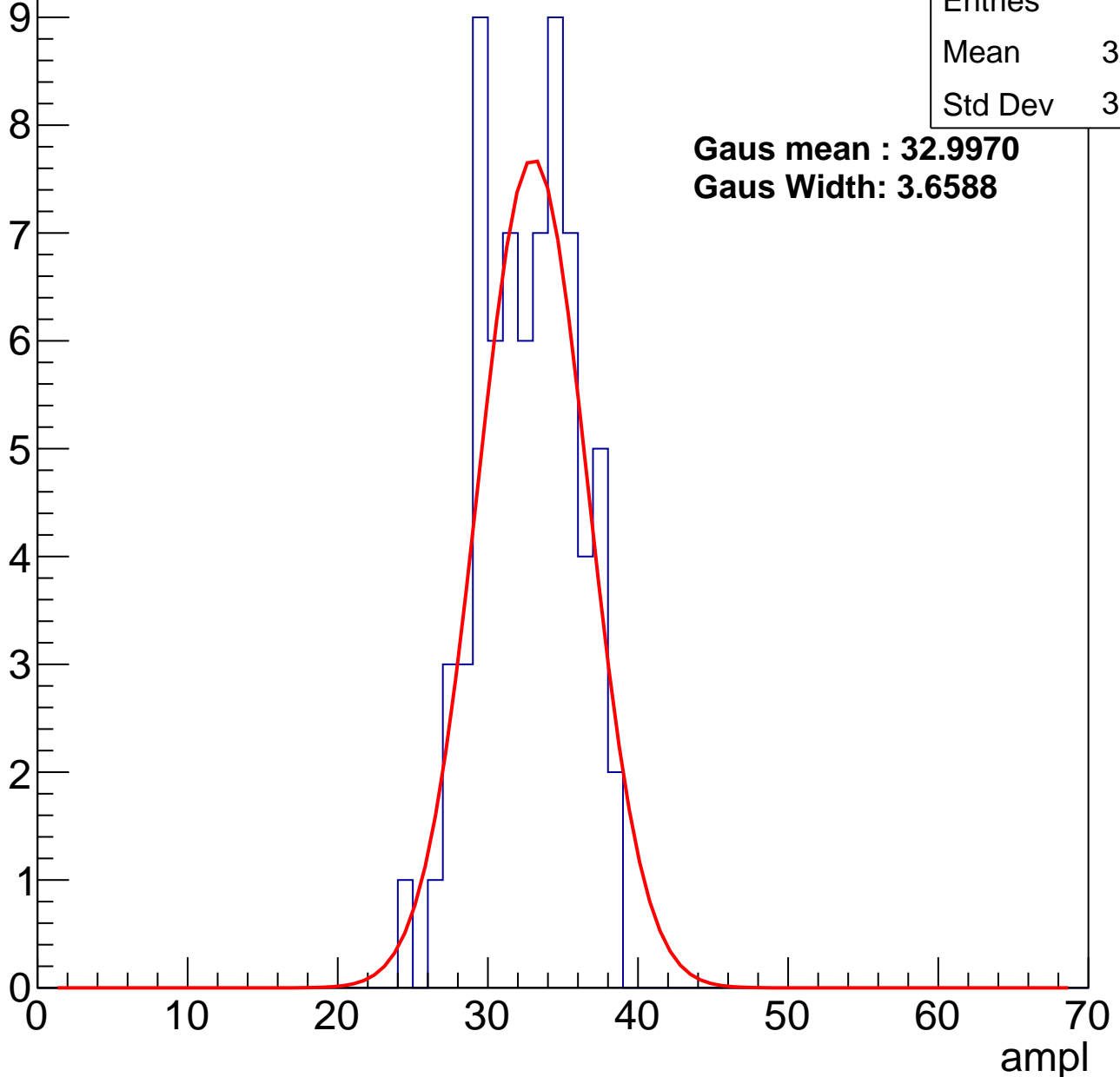
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	32.17
Std Dev	3.162

**Gaus mean : 32.9970**

**Gaus Width: 3.6588**



# B1L002S, U17-ch41, adc2

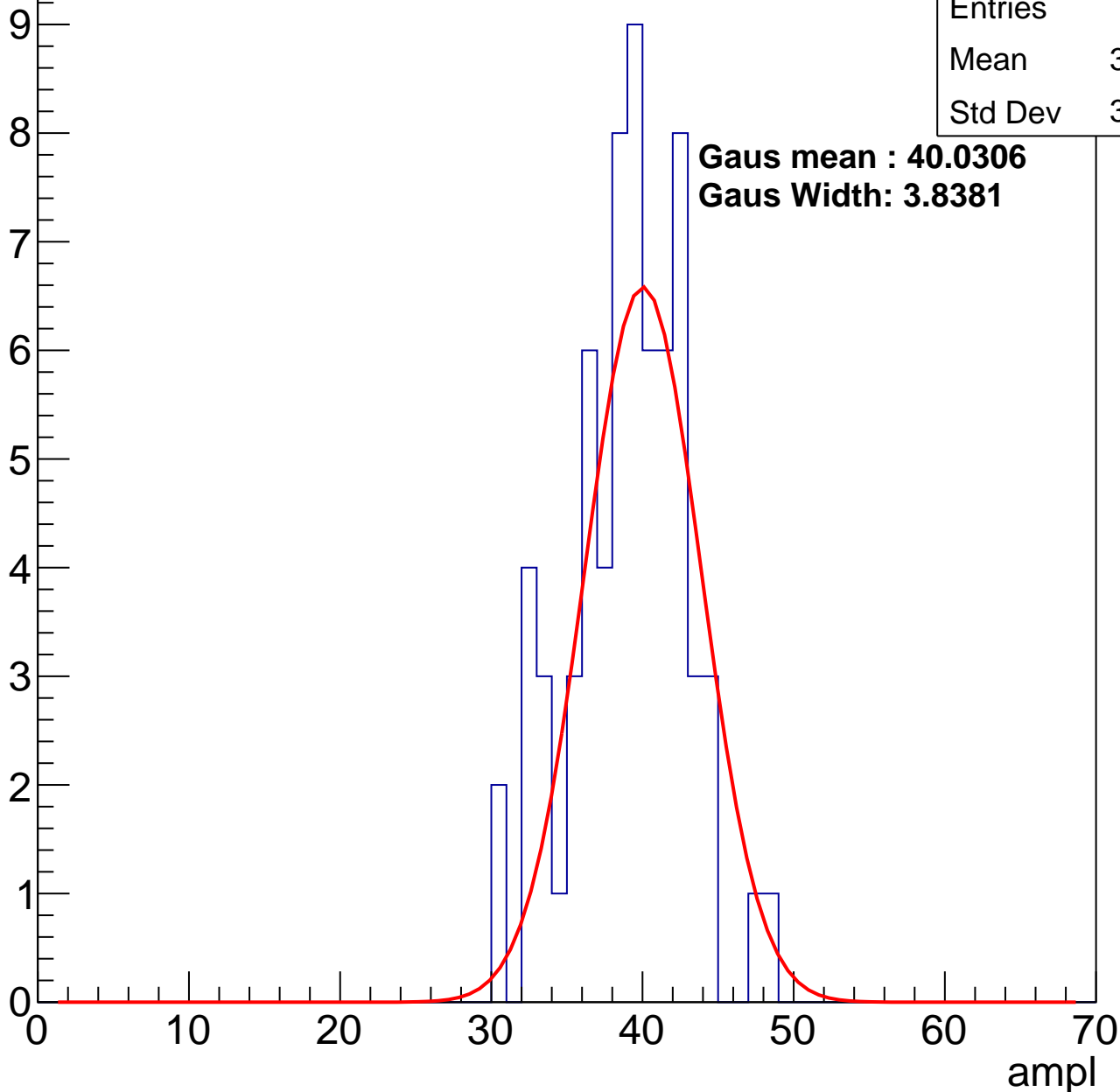
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	38.57
Std Dev	3.786

**Gaus mean : 40.0306**

**Gaus Width: 3.8381**

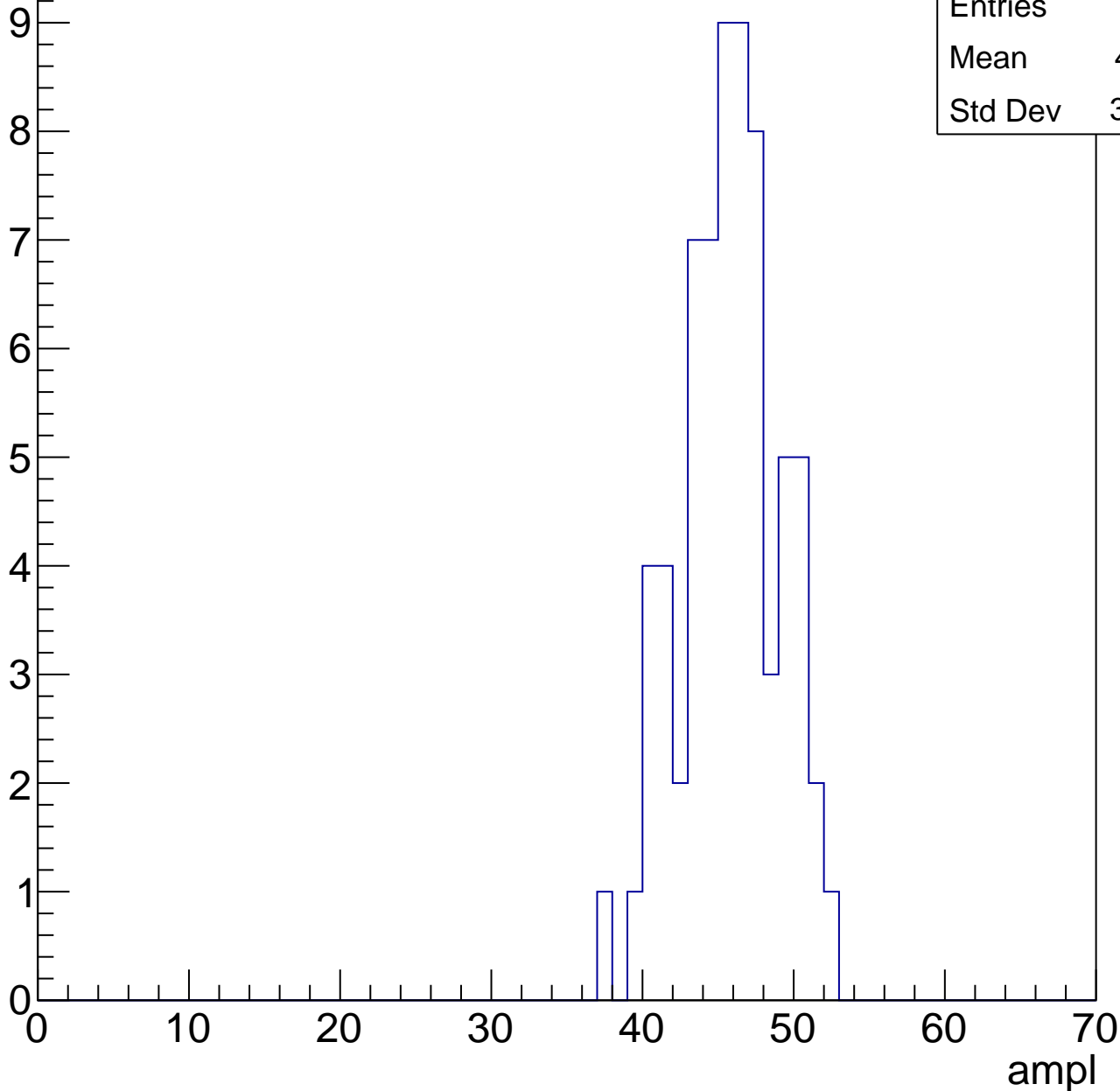


# B1L002S, U17-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	45.31
Std Dev	3.219

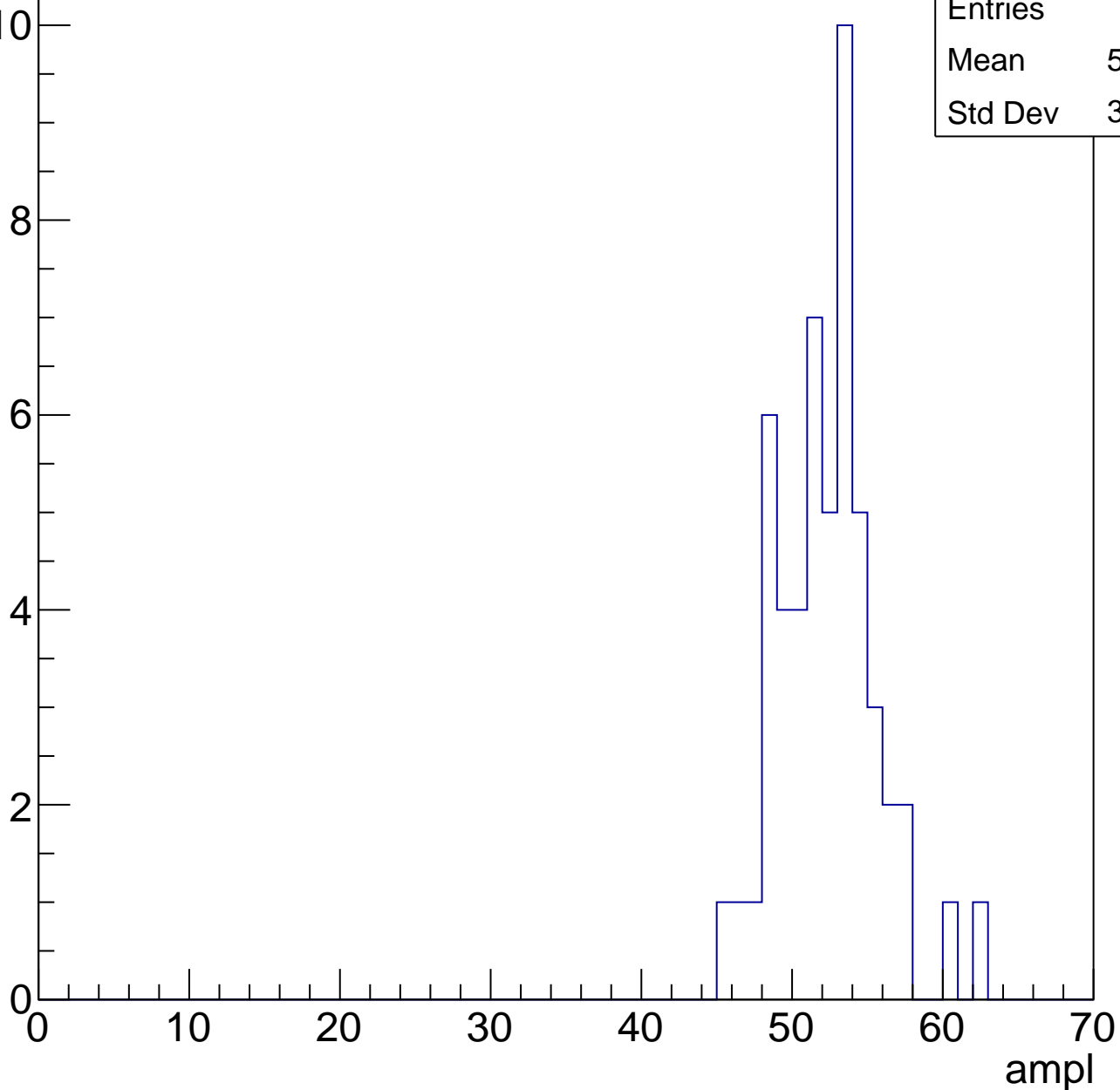


# B1L002S, U17-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	51.92
Std Dev	3.273

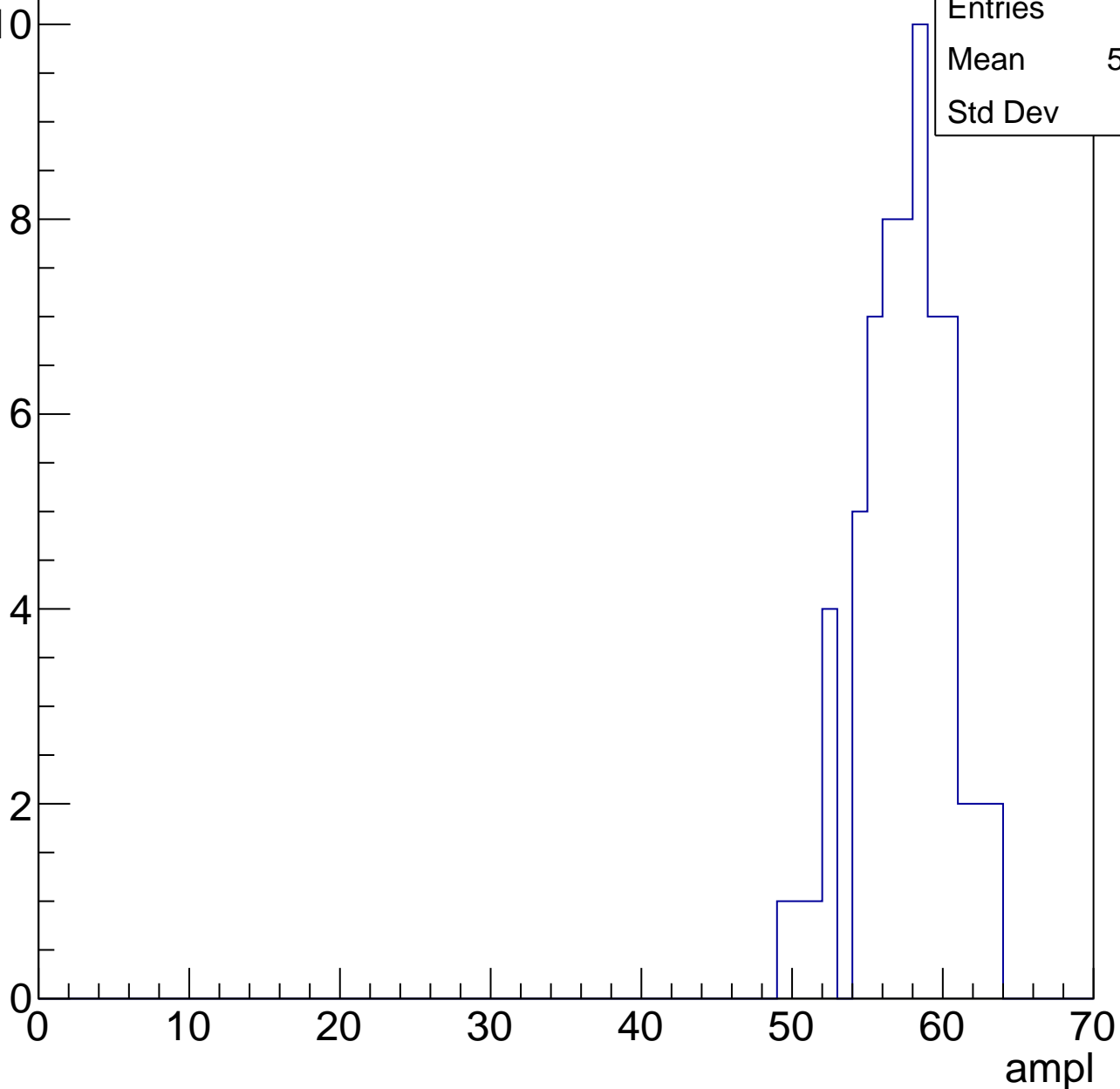


# B1L002S, U17-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

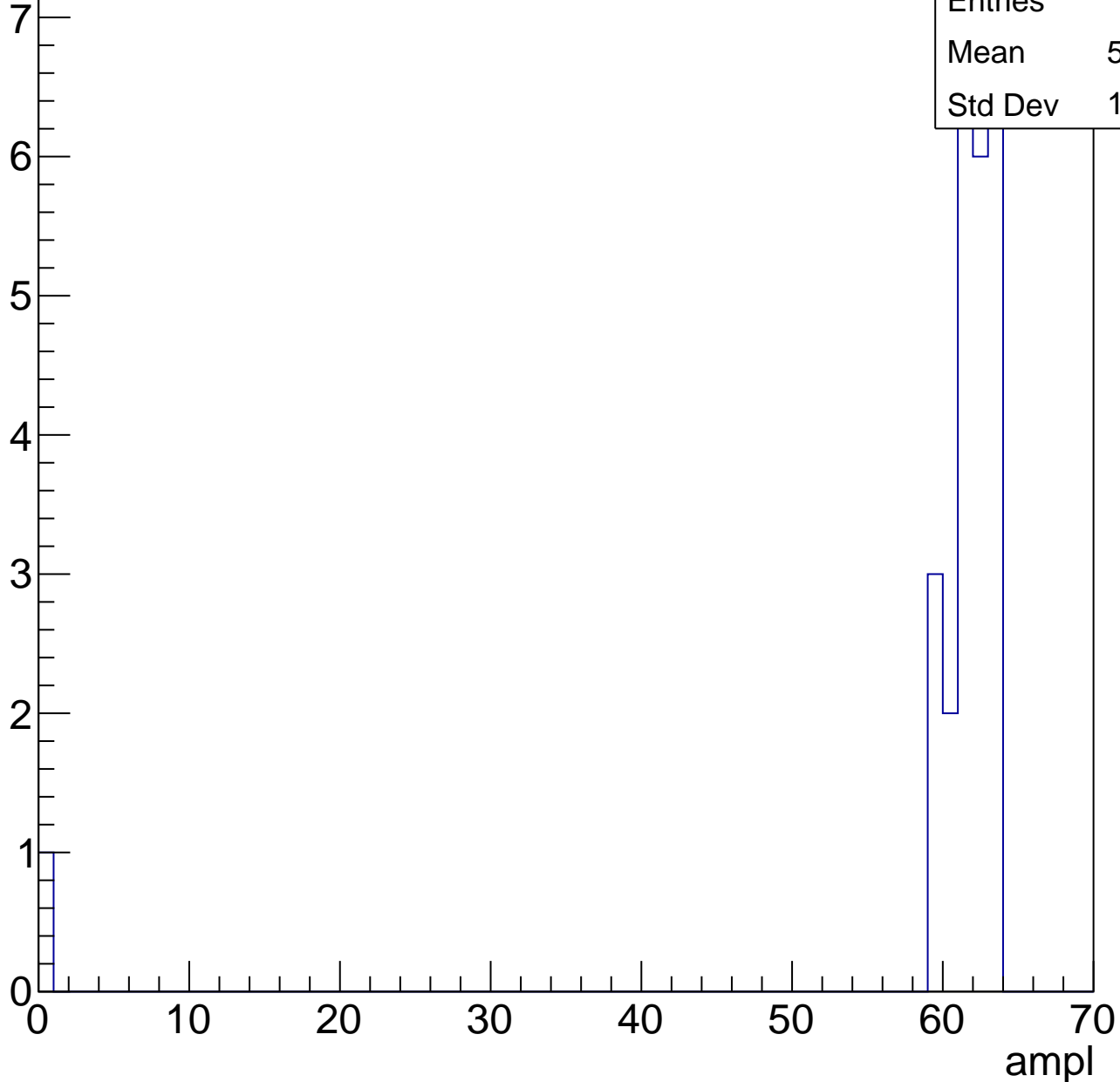
Entries	65
Mean	56.95
Std Dev	3



# B1L002S, U17-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

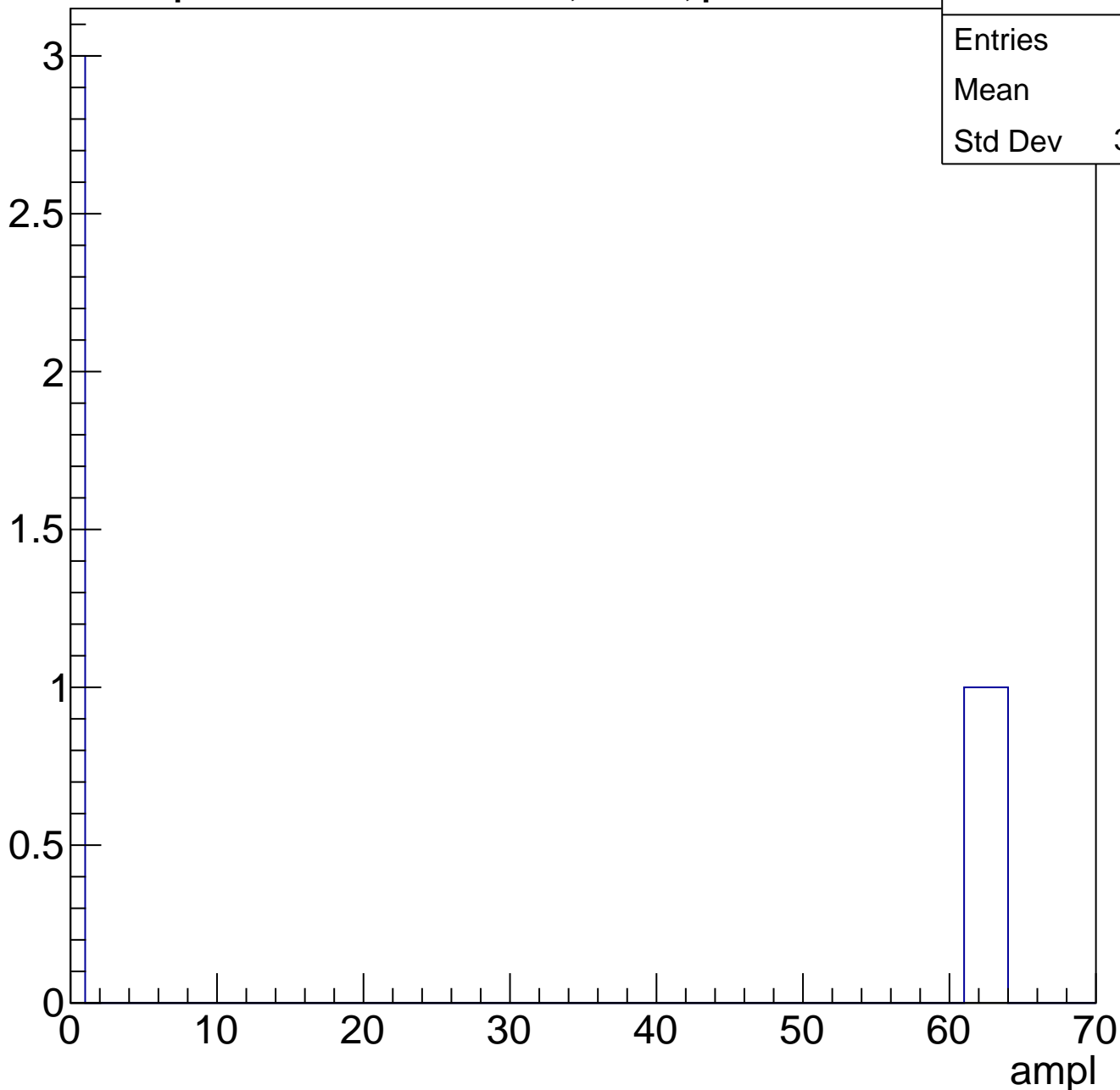




# B1L002S, U17-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch42, adc0

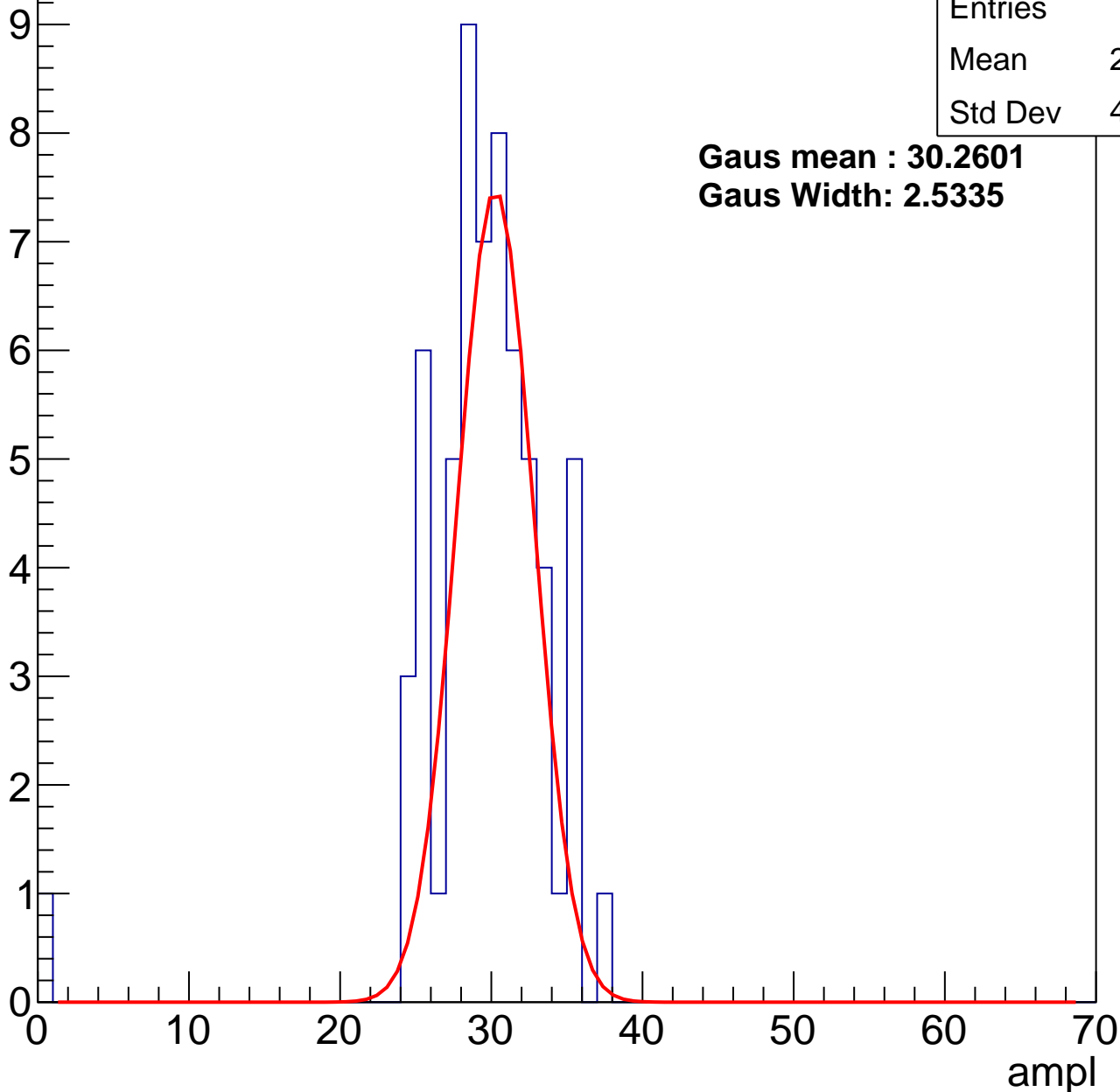
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.06
Std Dev	4.852

**Gaus mean : 30.2601**

**Gaus Width: 2.5335**



# B1L002S, U17-ch42, adc1

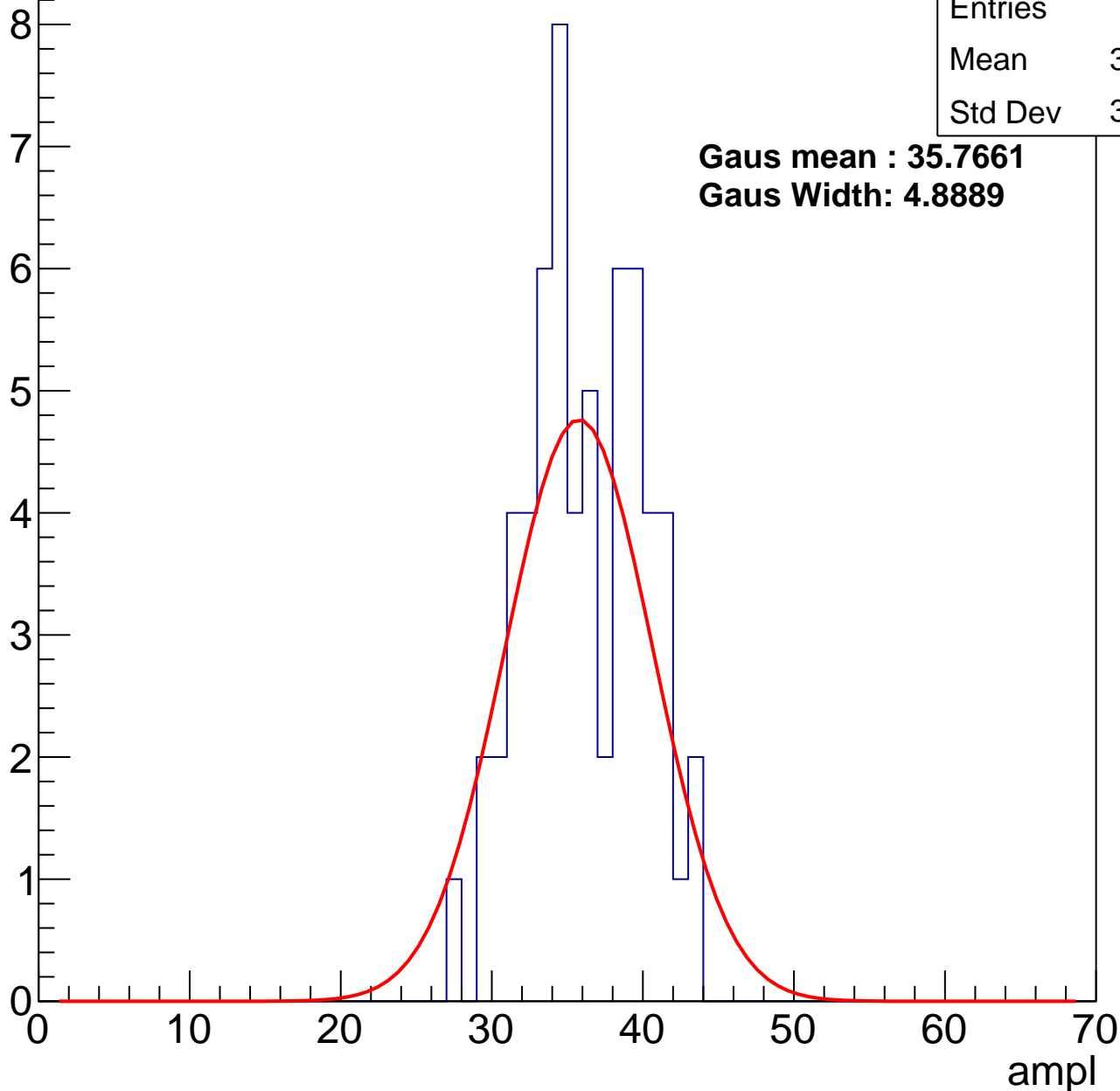
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.66
Std Dev	3.789

**Gaus mean : 35.7661**

**Gaus Width: 4.8889**



# B1L002S, U17-ch42, adc2

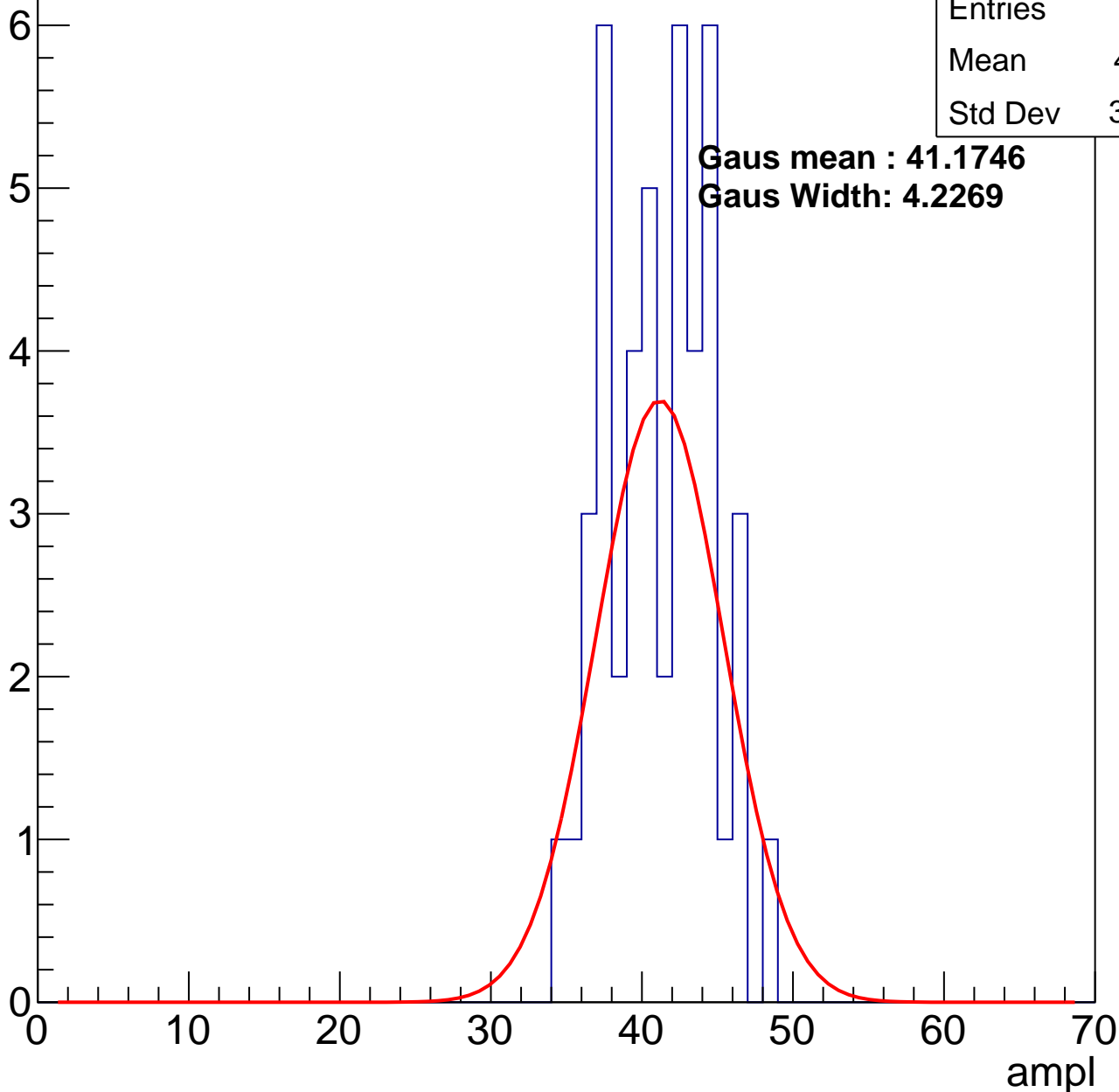
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	40.71
Std Dev	3.357

**Gaus mean : 41.1746**

**Gaus Width: 4.2269**

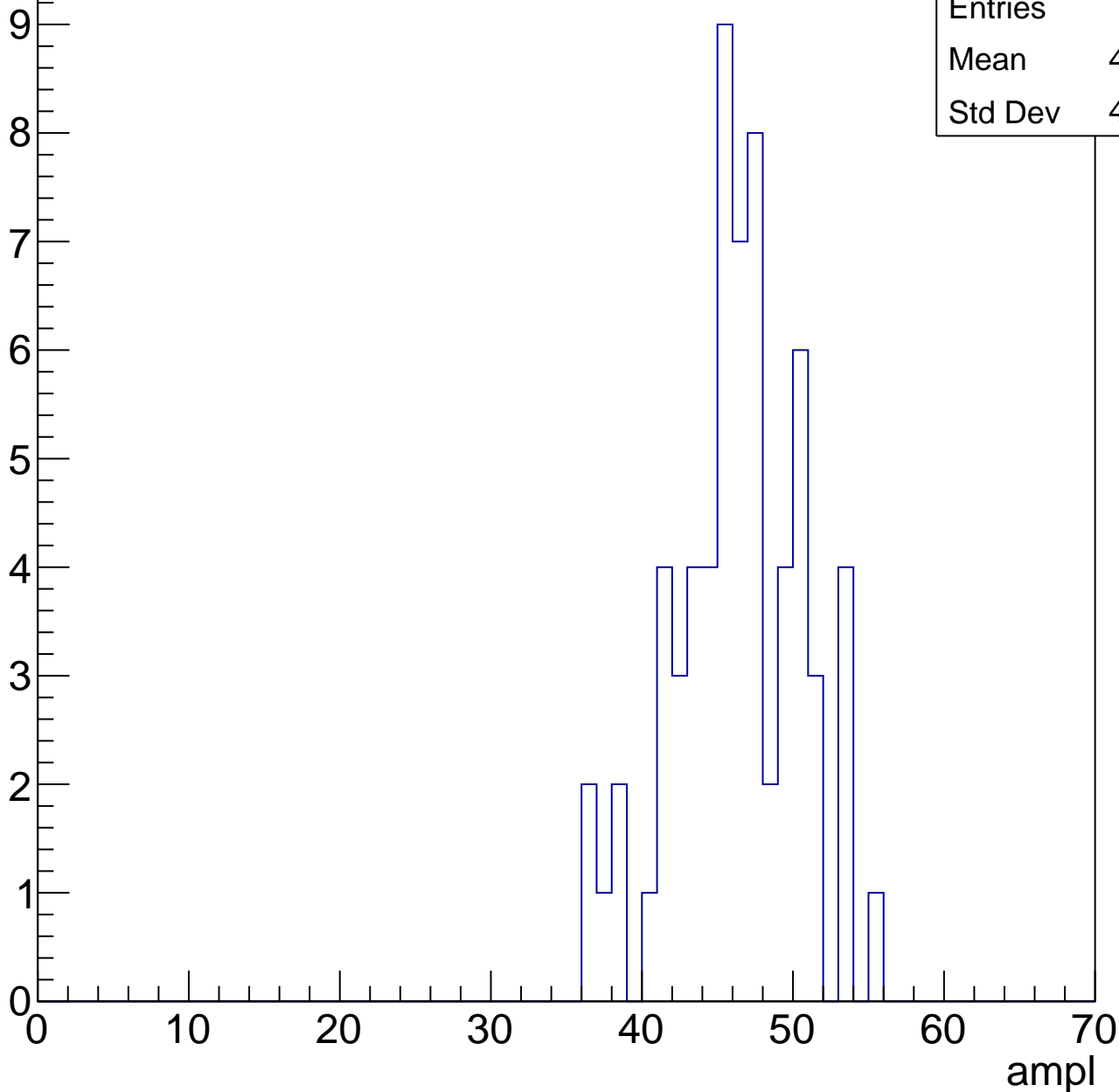


# B1L002S, U17-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	45.82
Std Dev	4.209

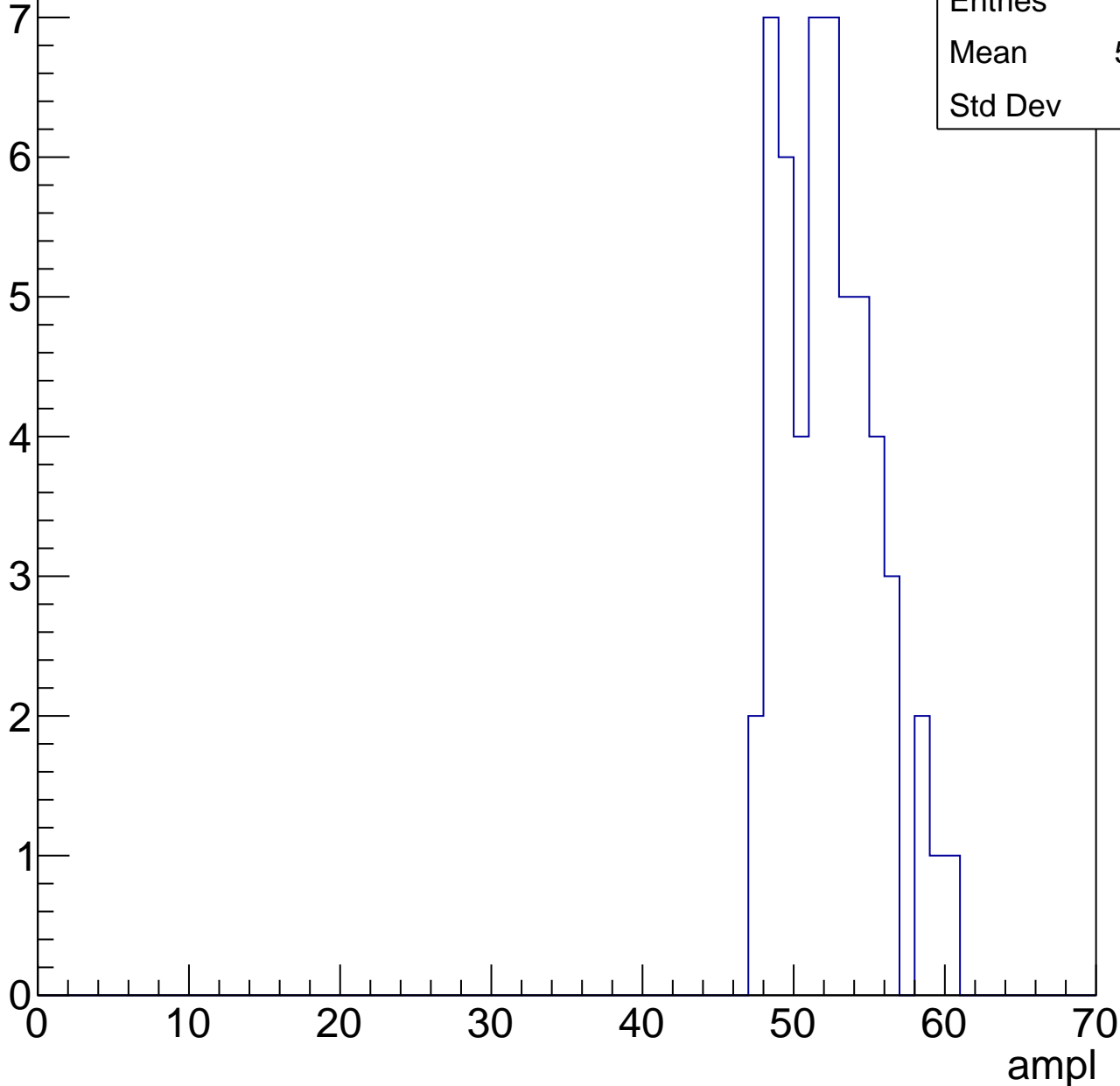


# B1L002S, U17-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

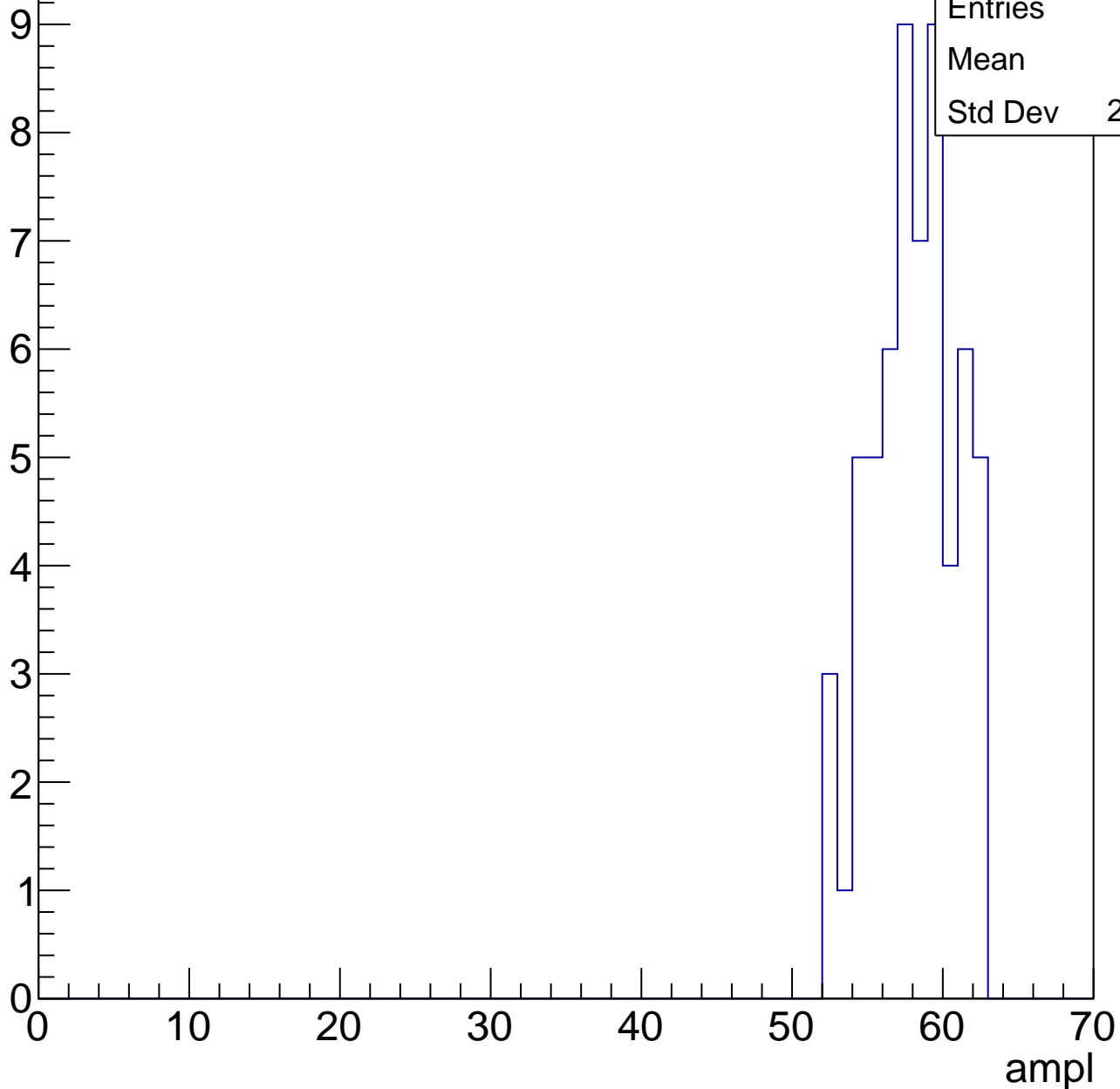
Entries	54
Mean	51.91
Std Dev	3.14



# B1L002S, U17-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



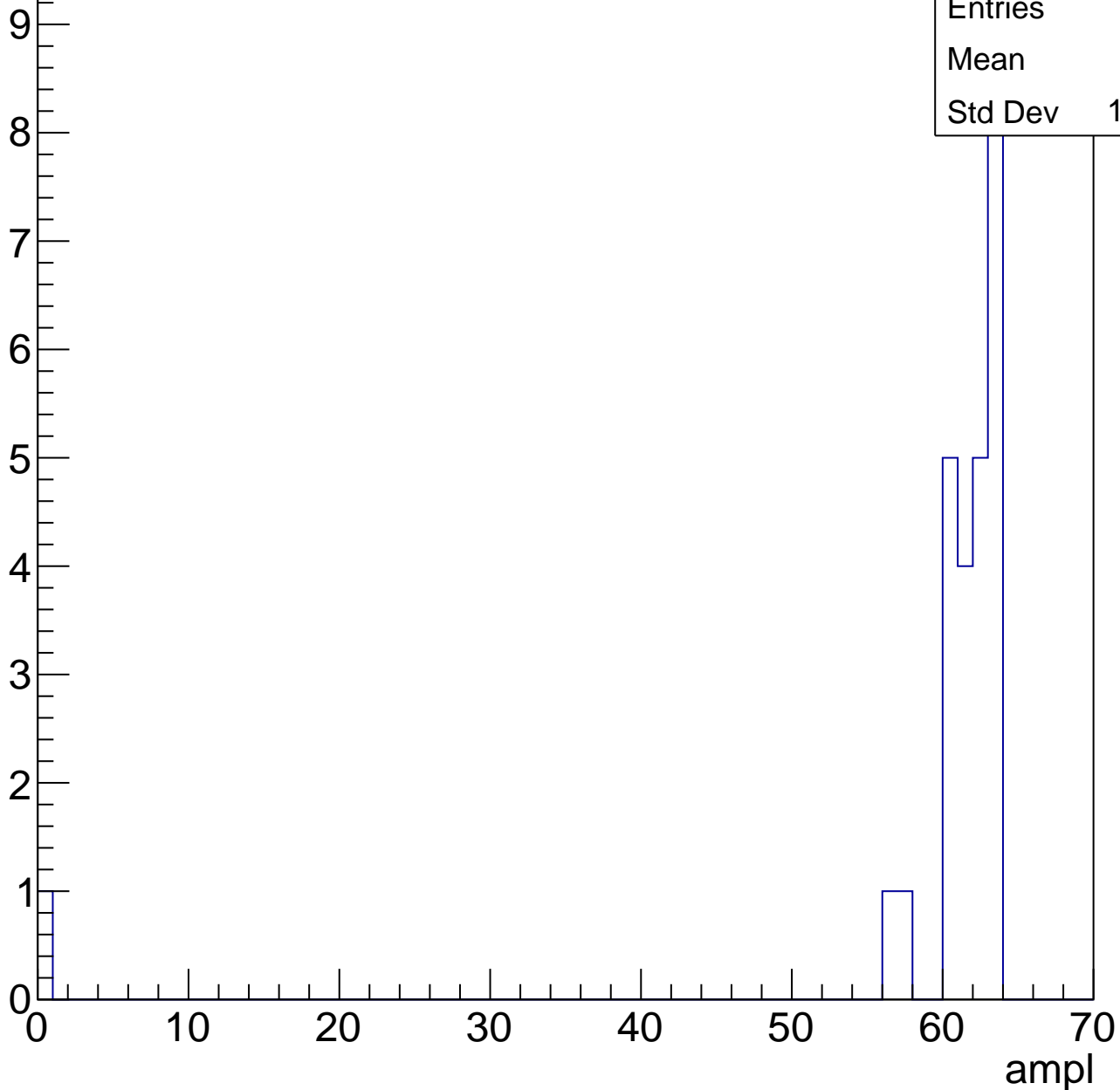
Entries	60
Mean	57.6
Std Dev	2.709

# B1L002S, U17-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59
Std Dev	11.94

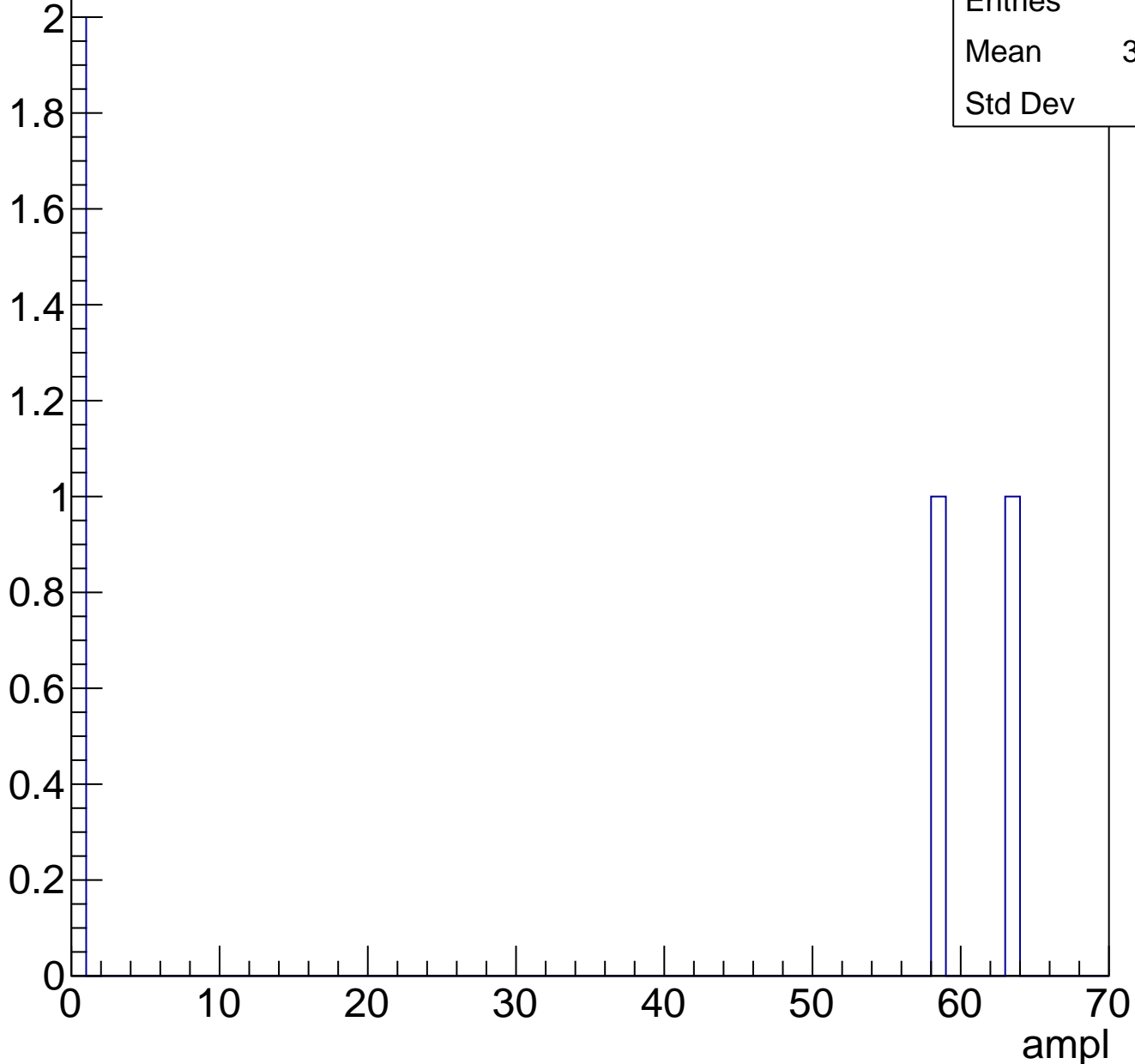




# B1L002S, U17-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	30.25
Std Dev	30.3

# B1L002S, U17-ch43, adc0

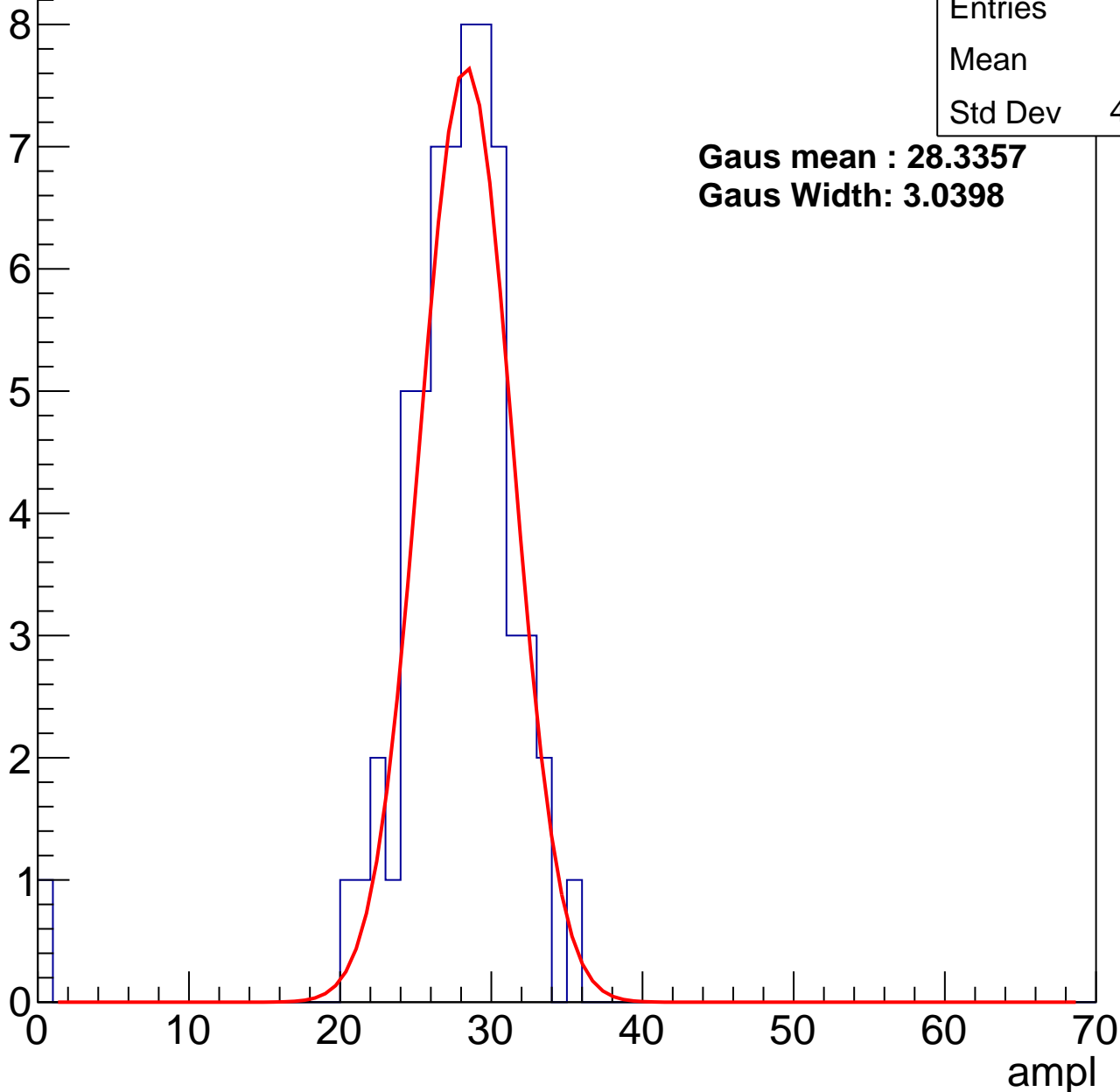
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.1
Std Dev	4.603

**Gaus mean : 28.3357**

**Gaus Width: 3.0398**



# B1L002S, U17-ch43, adc1

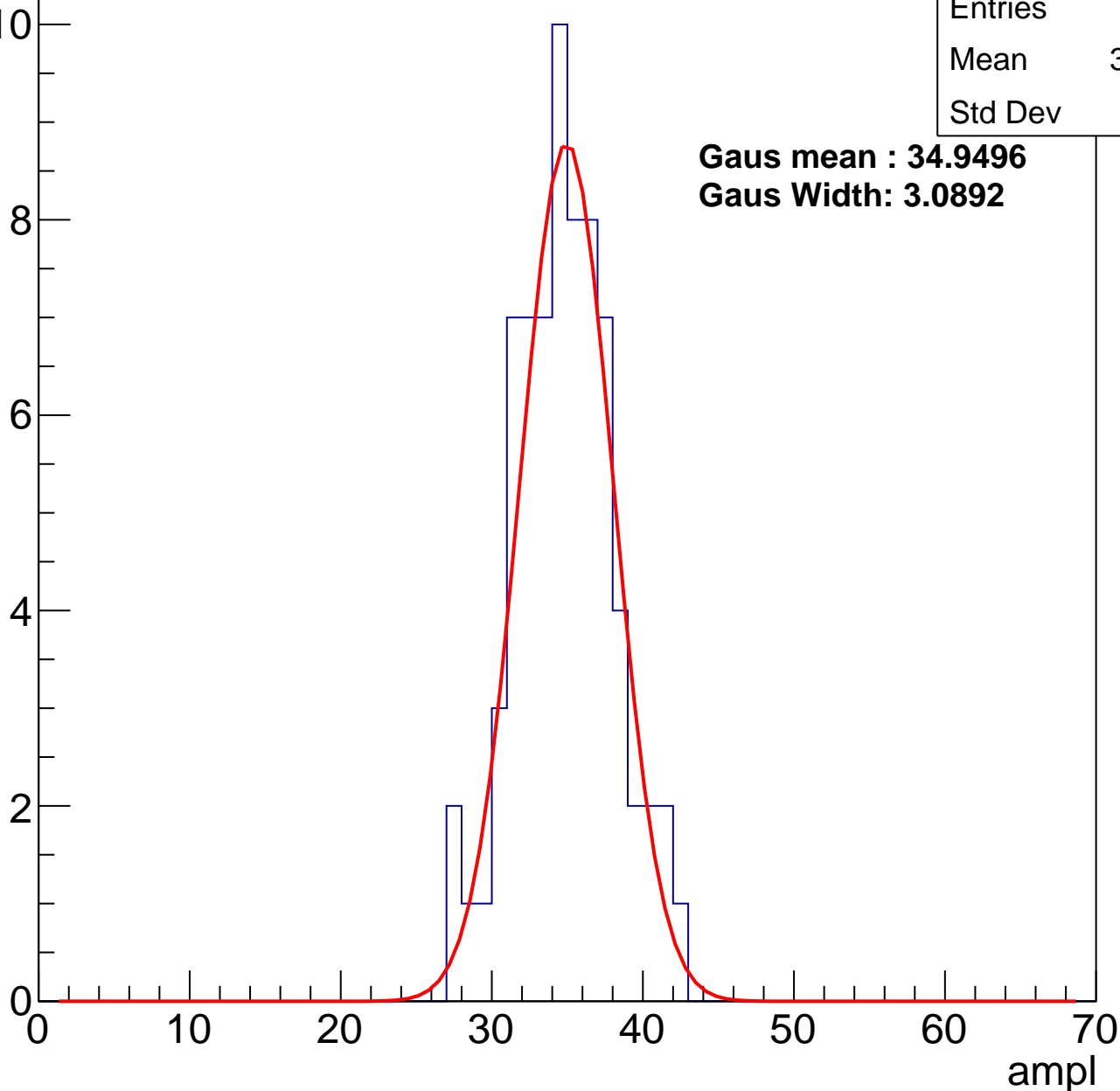
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.36
Std Dev	3.22

**Gaus mean : 34.9496**

**Gaus Width: 3.0892**



# B1L002S, U17-ch43, adc2

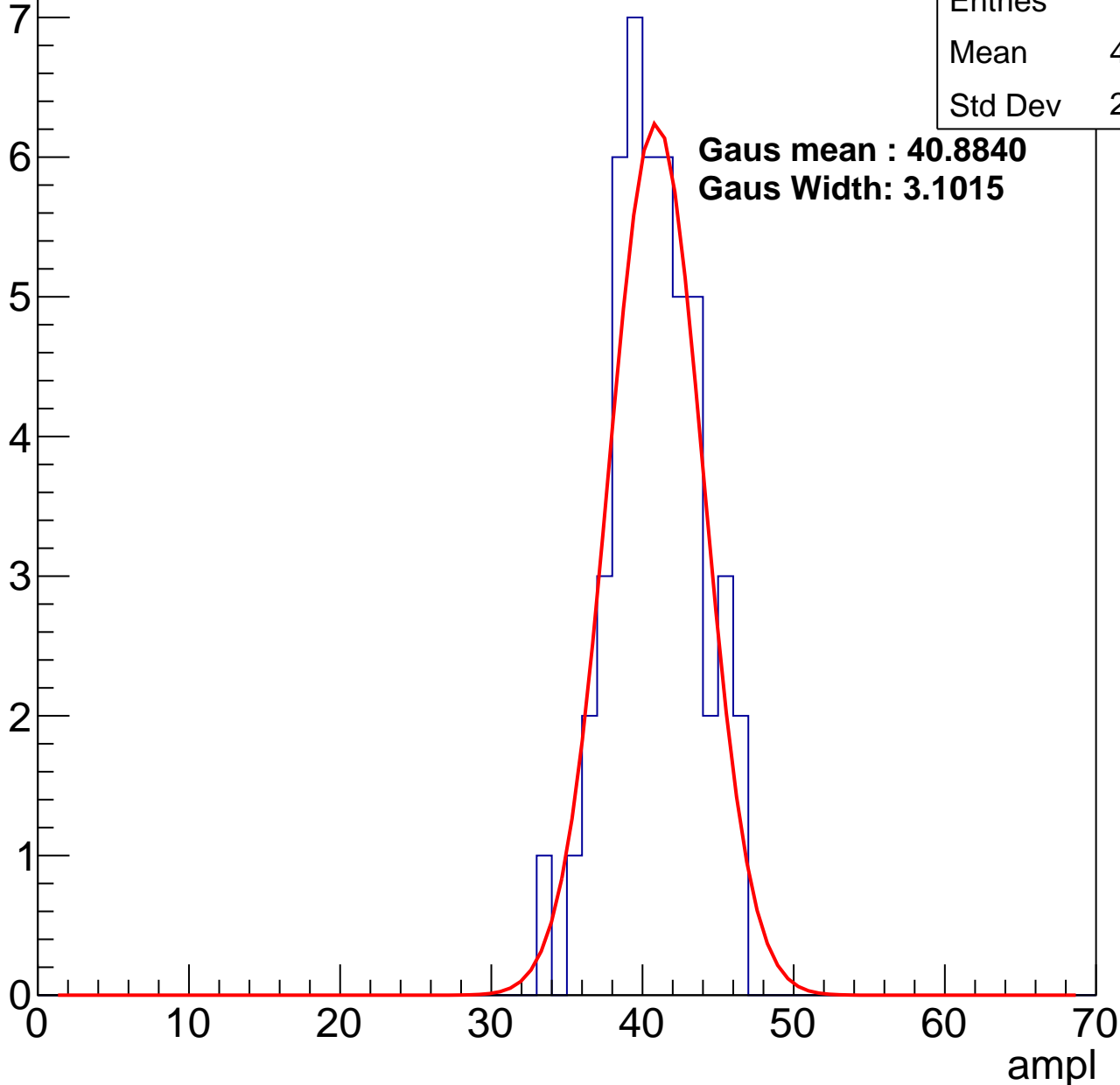
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.37
Std Dev	2.883

**Gaus mean : 40.8840**

**Gaus Width: 3.1015**

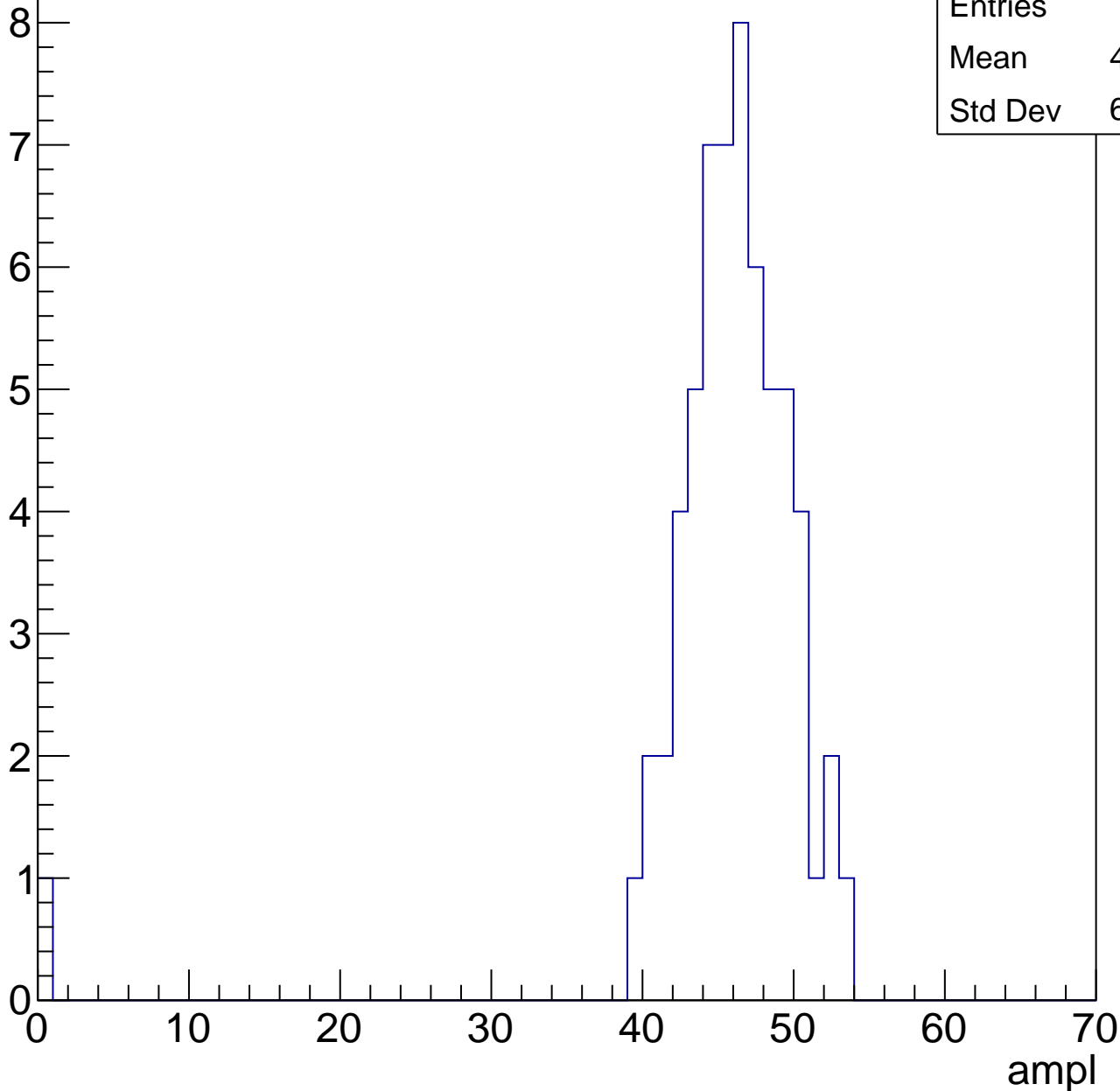


# B1L002S, U17-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.08
Std Dev	6.604

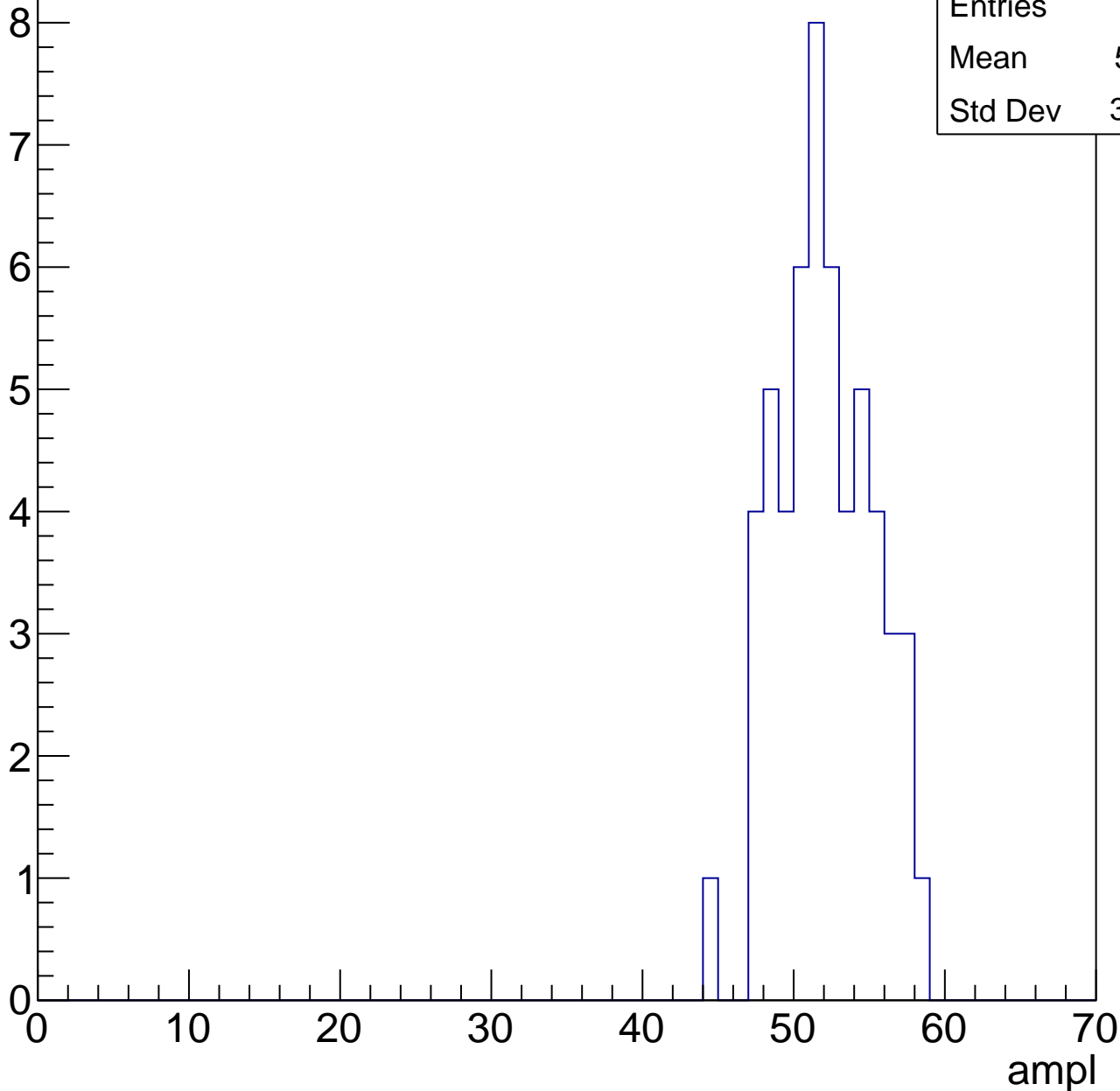


# B1L002S, U17-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.61
Std Dev	3.112



# B1L002S, U17-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	70
Mean	57.51
Std Dev	3.237

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

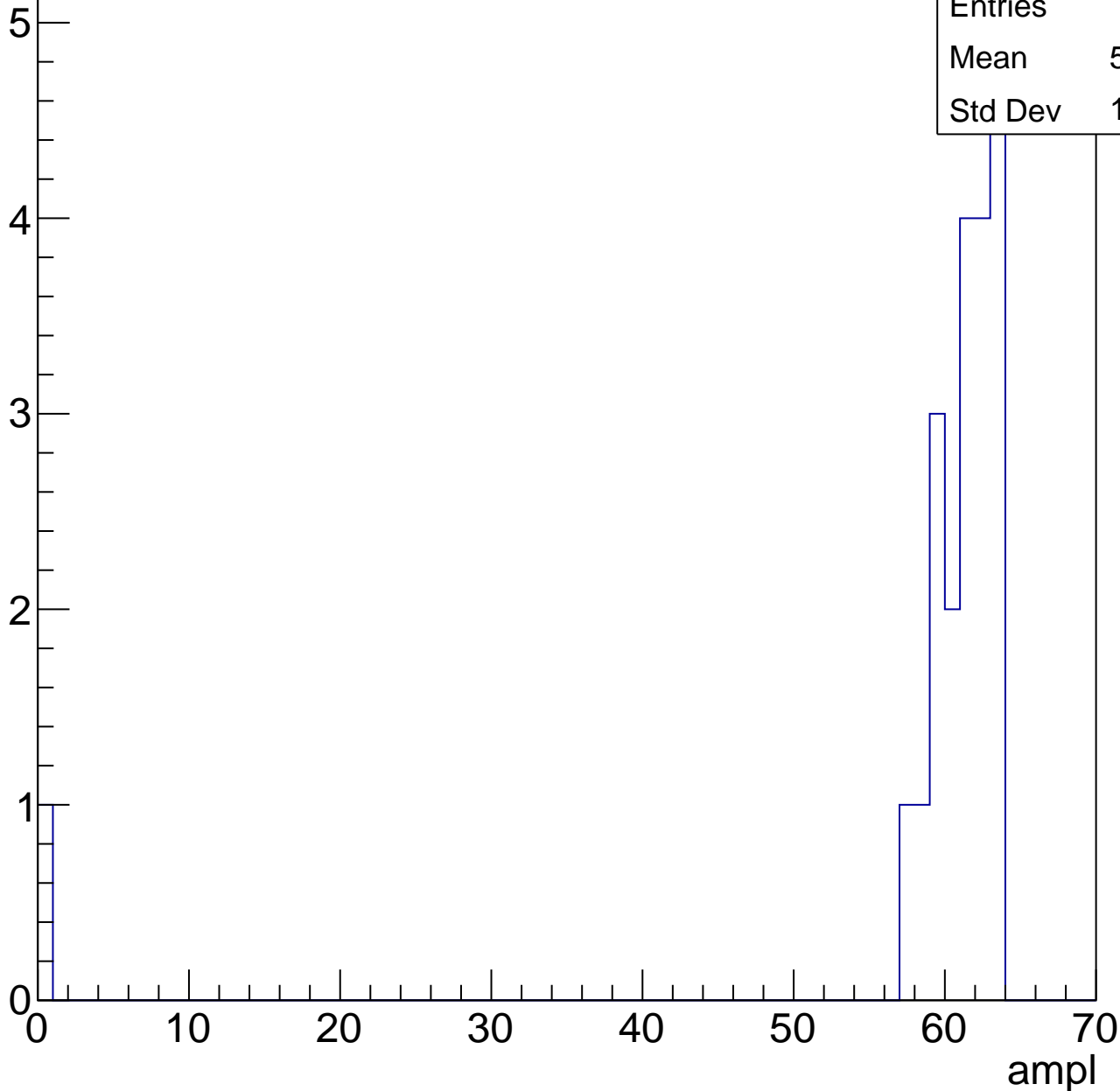
8

# B1L002S, U17-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.05
Std Dev	13.09





# B1L002S, U17-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch44, adc0

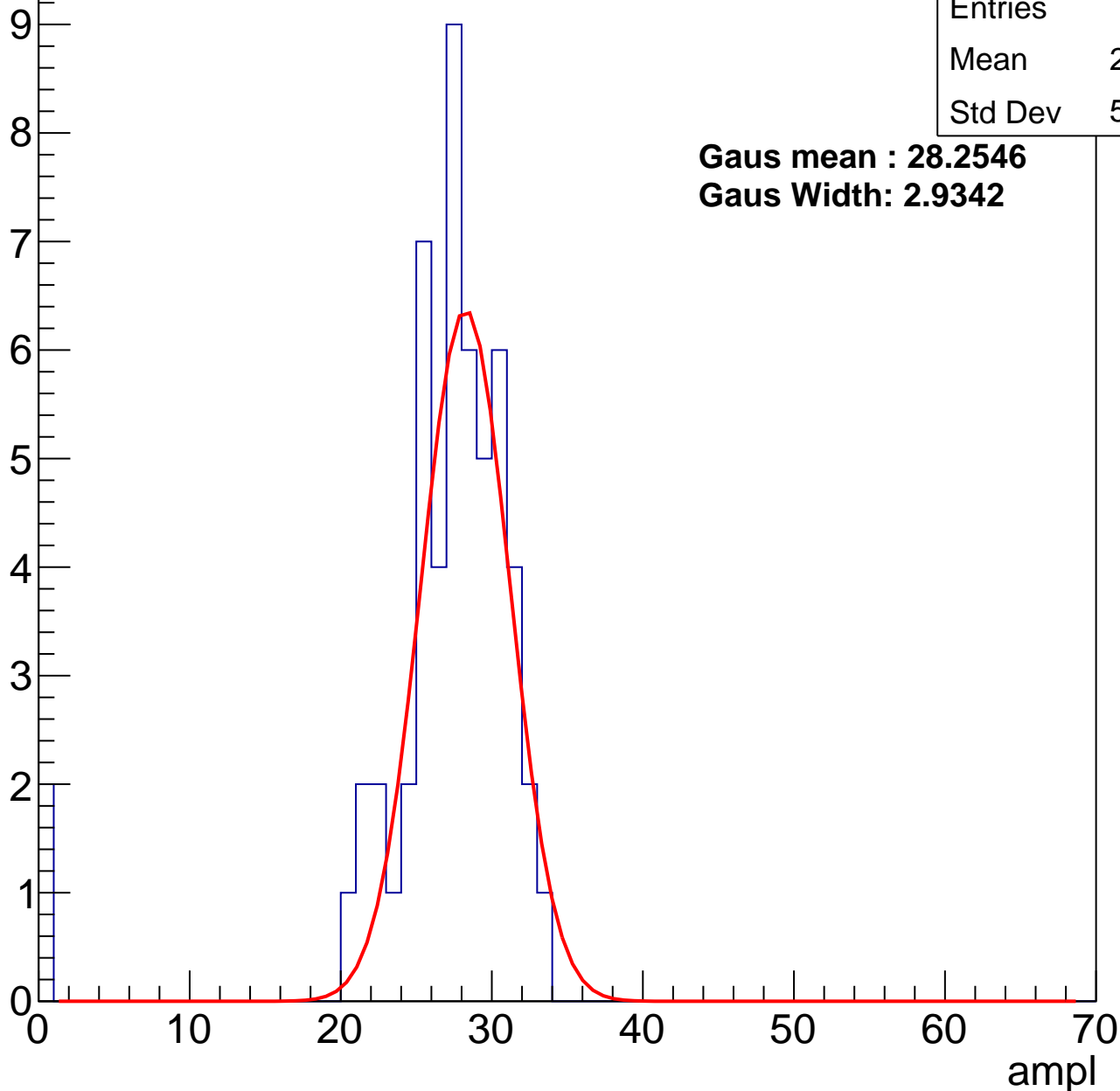
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	26.17
Std Dev	5.912

**Gaus mean : 28.2546**

**Gaus Width: 2.9342**



# B1L002S, U17-ch44, adc1

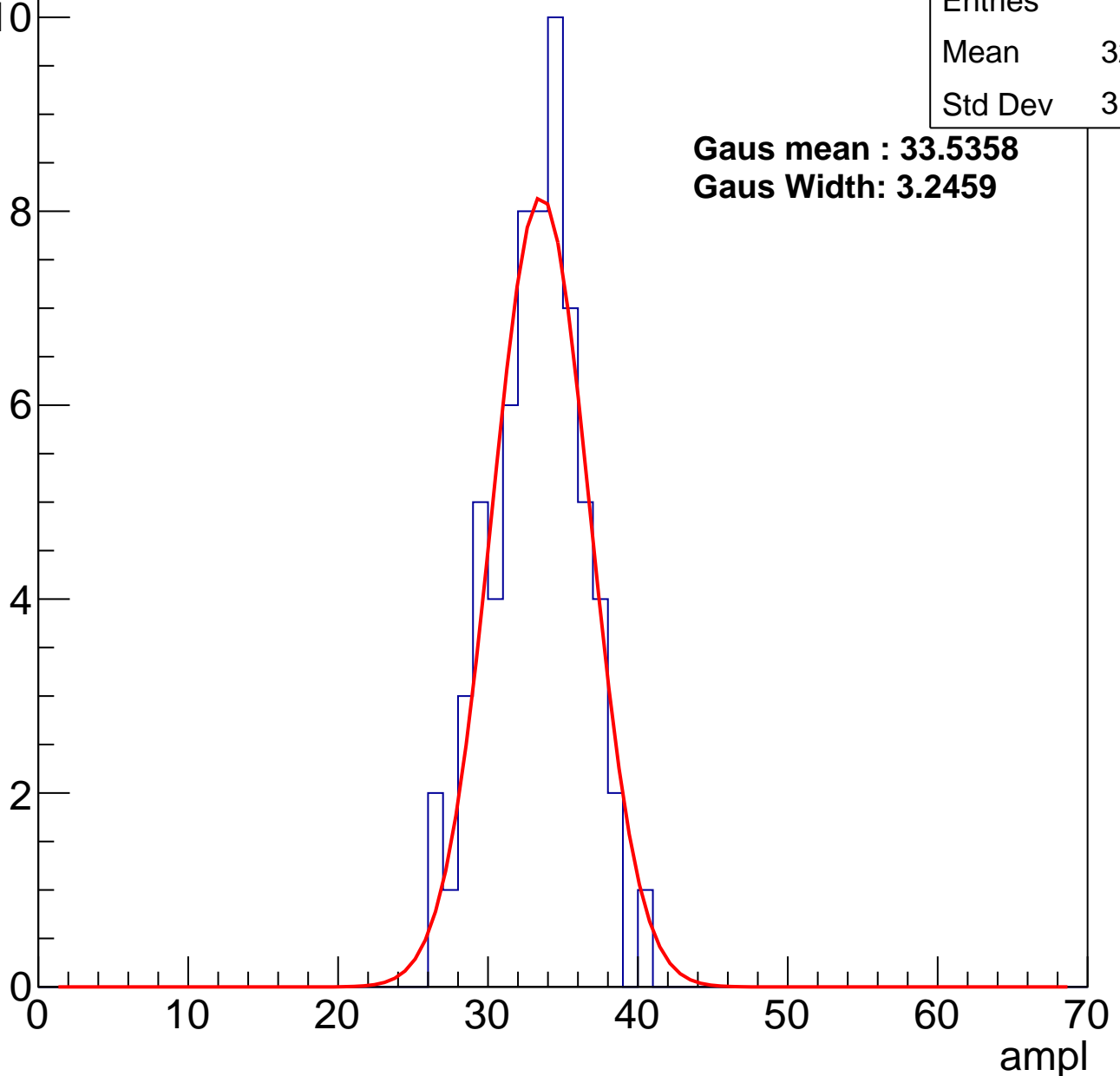
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	32.77
Std Dev	3.014

**Gaus mean : 33.5358**

**Gaus Width: 3.2459**



# B1L002S, U17-ch44, adc2

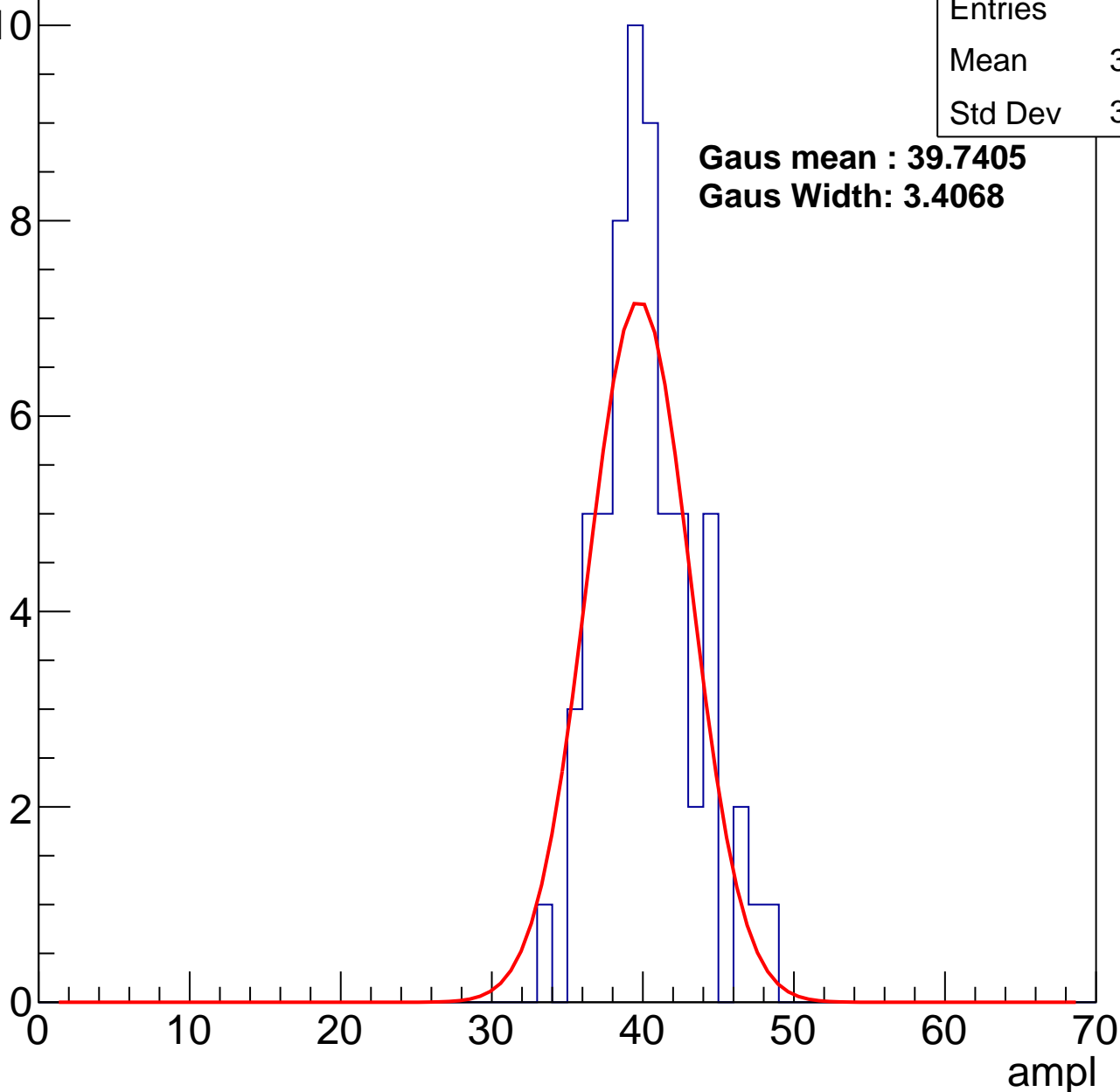
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.76
Std Dev	3.099

**Gaus mean : 39.7405**

**Gaus Width: 3.4068**

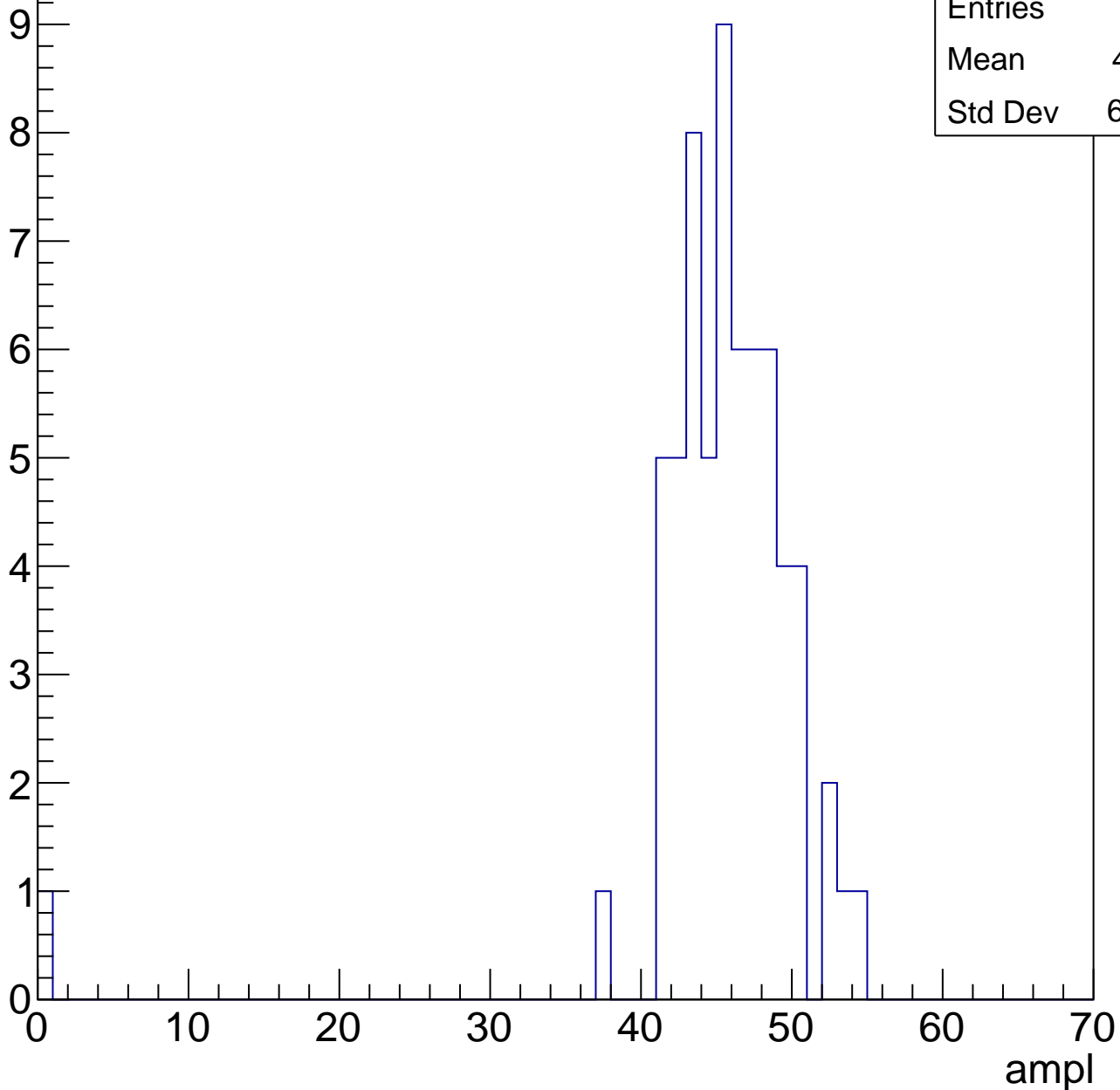


# B1L002S, U17-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	44.91
Std Dev	6.545

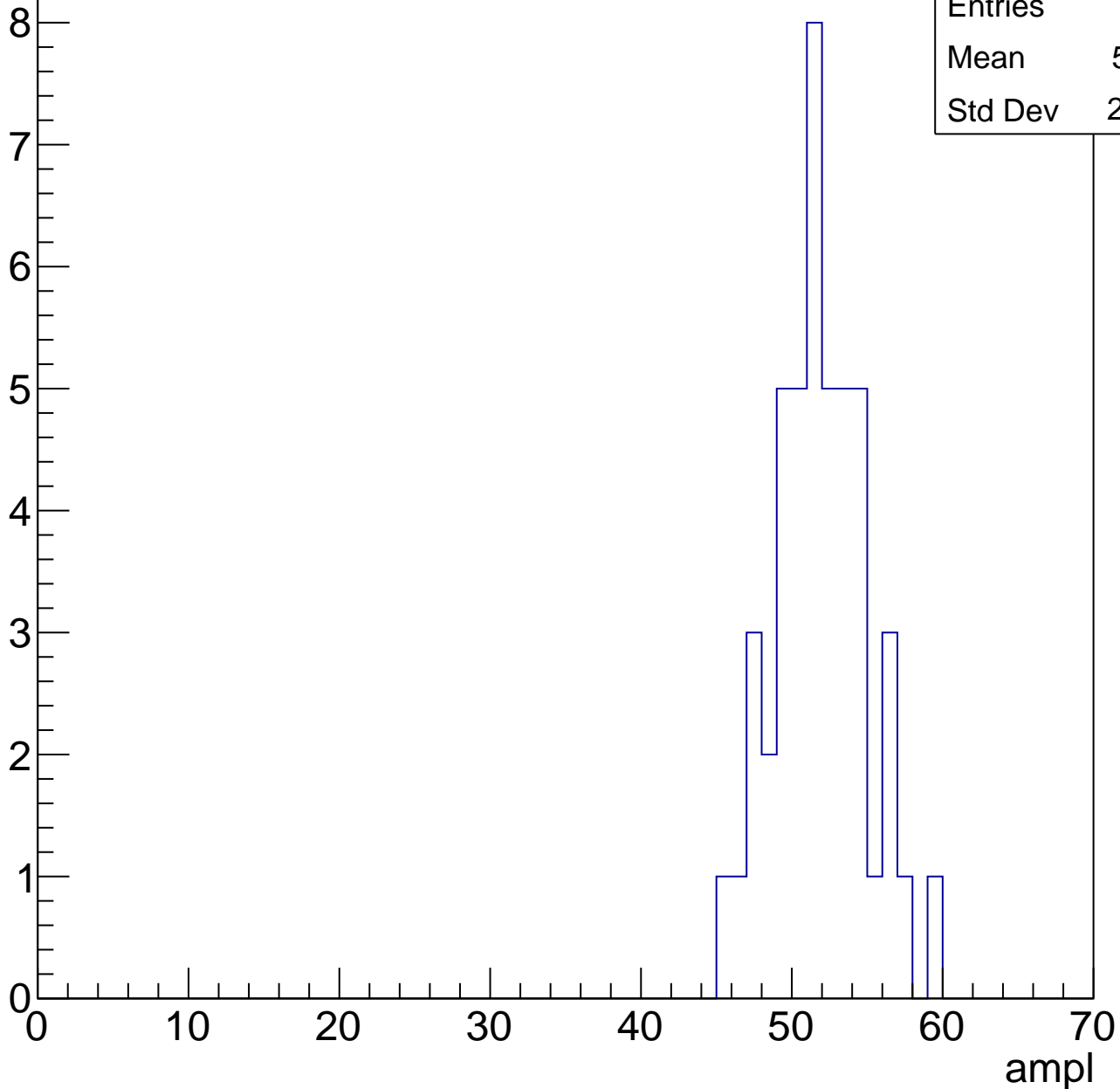


# B1L002S, U17-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

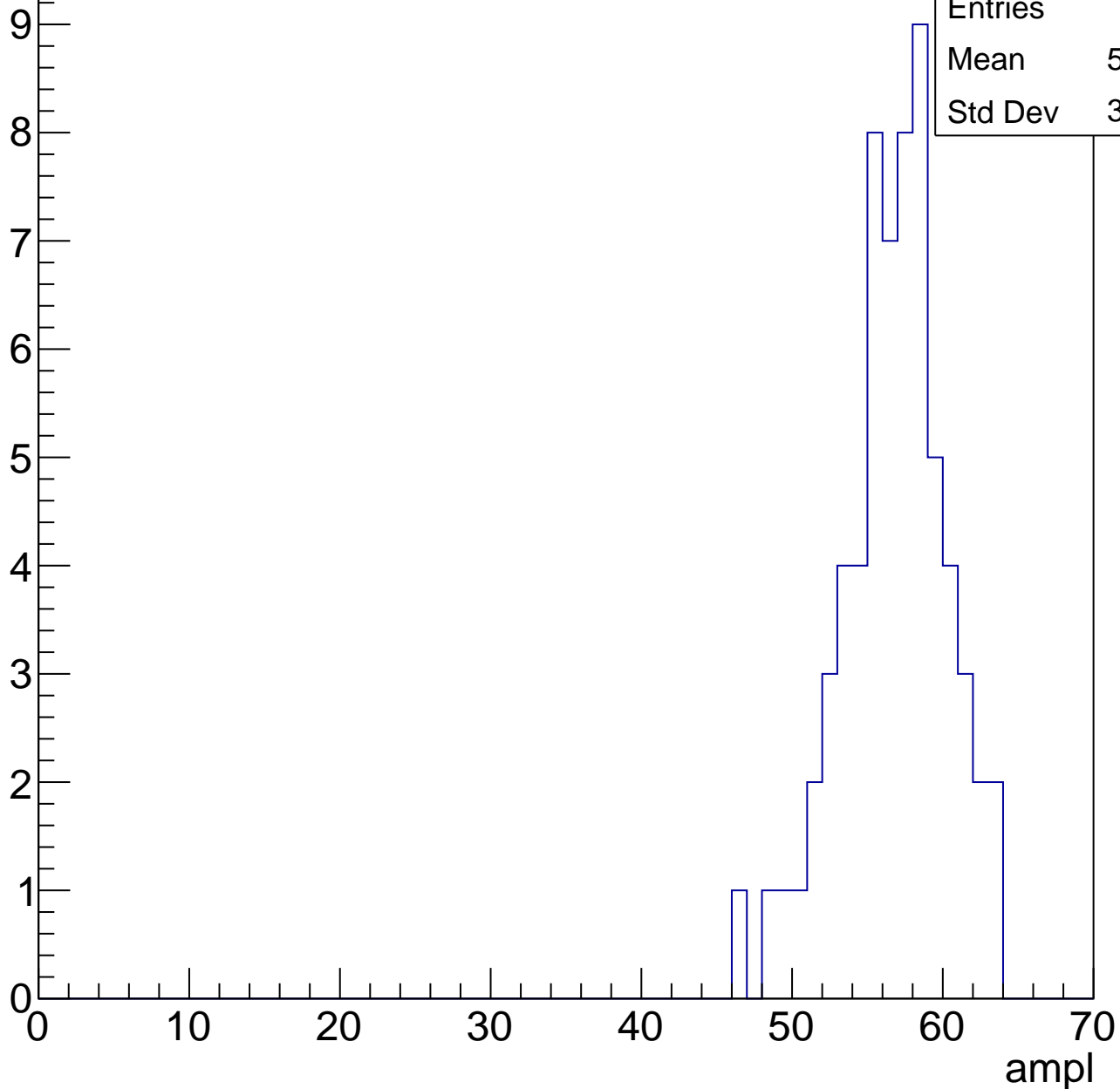
Entries	46
Mean	51.41
Std Dev	2.968



# B1L002S, U17-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



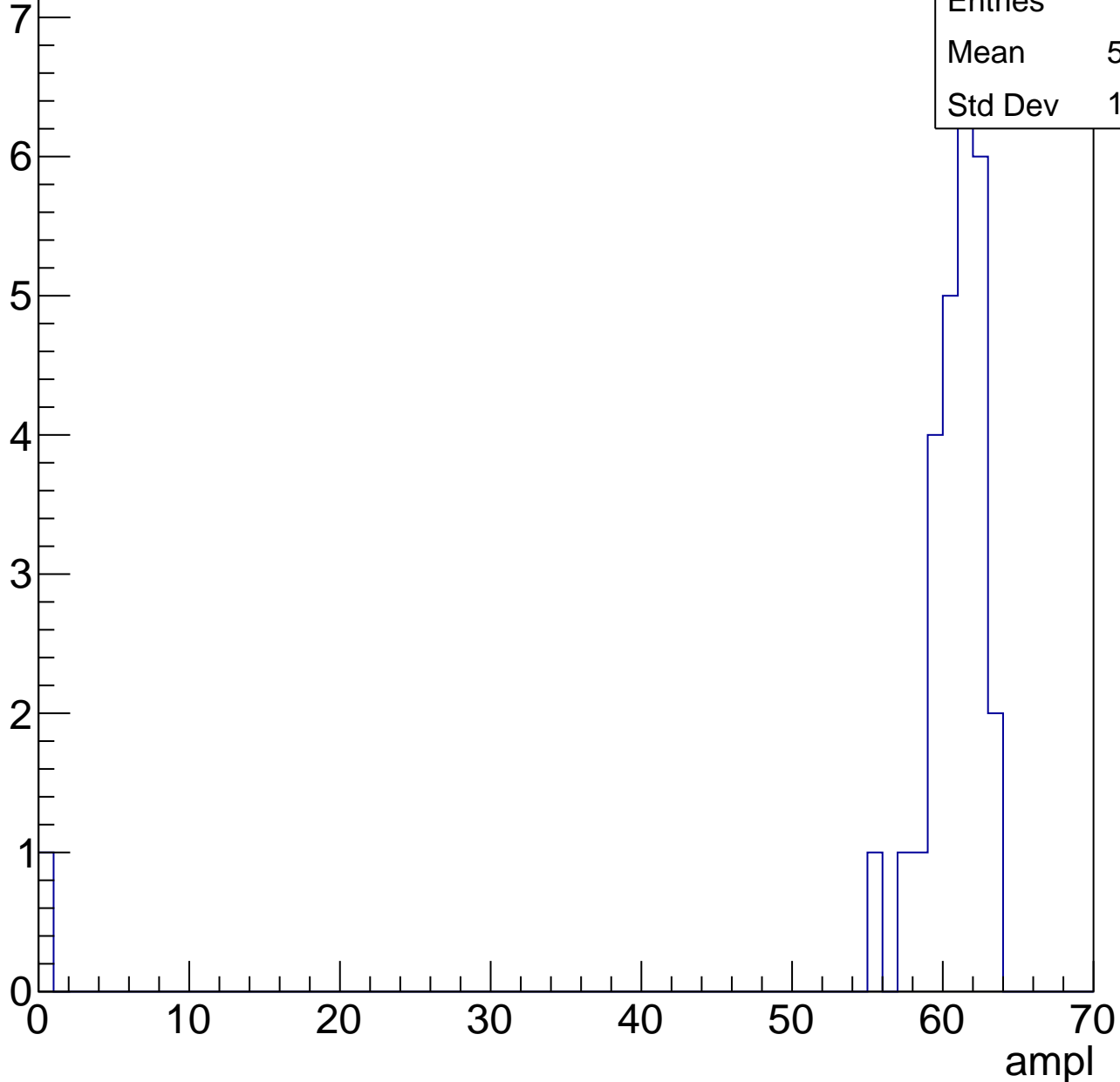
Entries	65
Mean	56.26
Std Dev	3.509

# B1L002S, U17-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.25
Std Dev	11.35



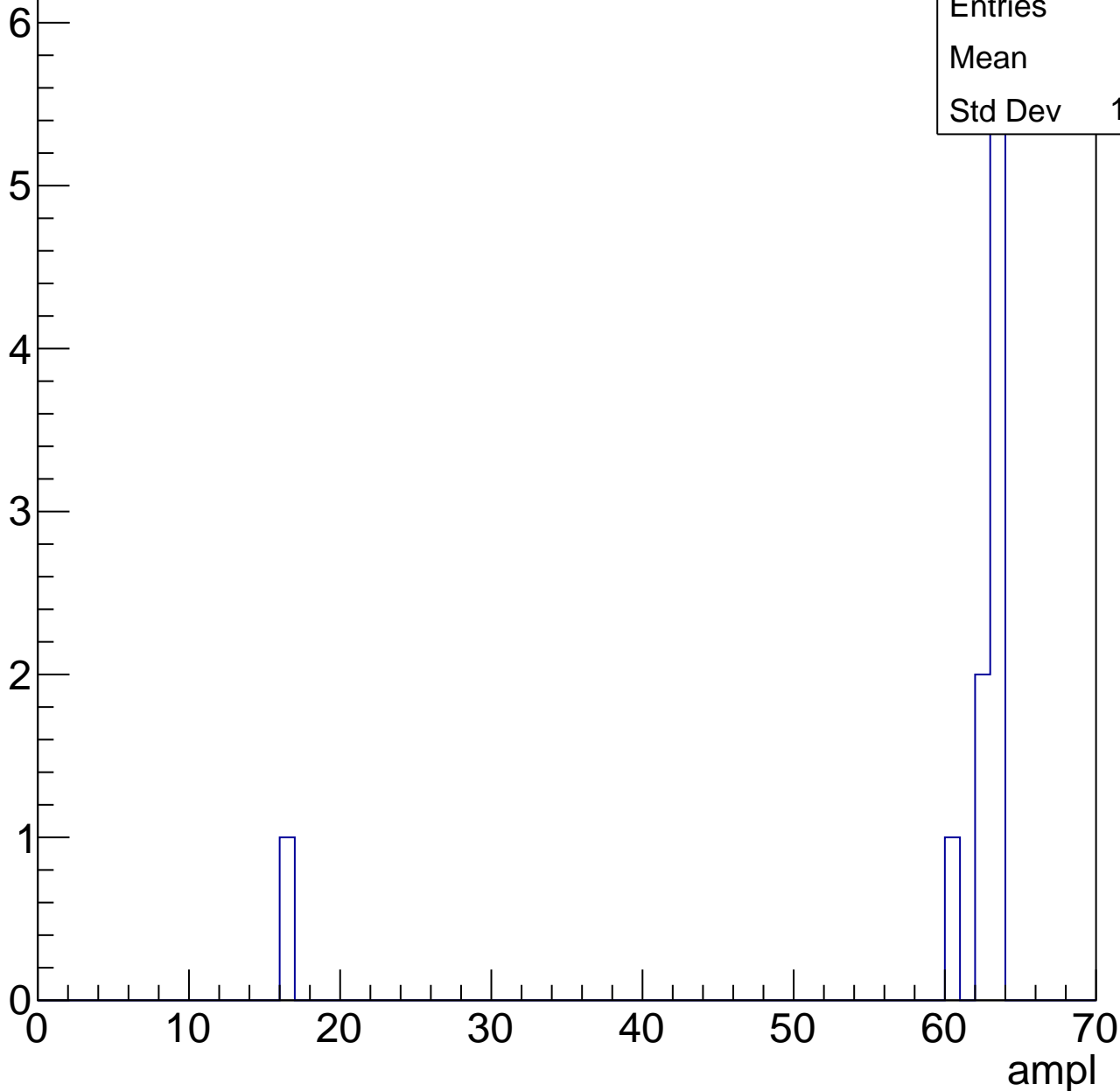


# B1L002S, U17-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	10
Mean	57.8
Std Dev	13.96



# B1L002S, U17-ch45, adc0

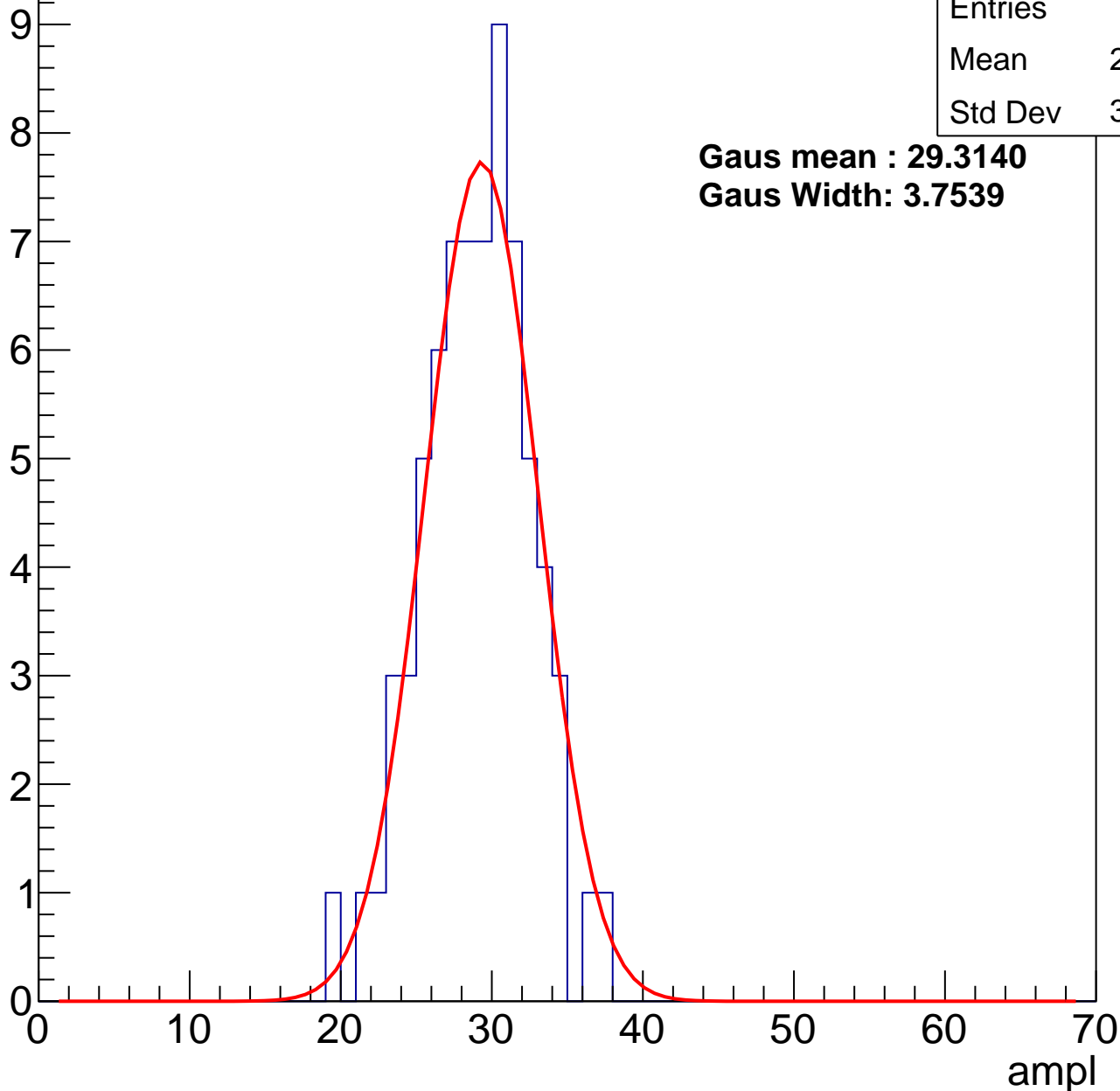
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.54
Std Dev	3.528

**Gaus mean : 29.3140**

**Gaus Width: 3.7539**



# B1L002S, U17-ch45, adc1

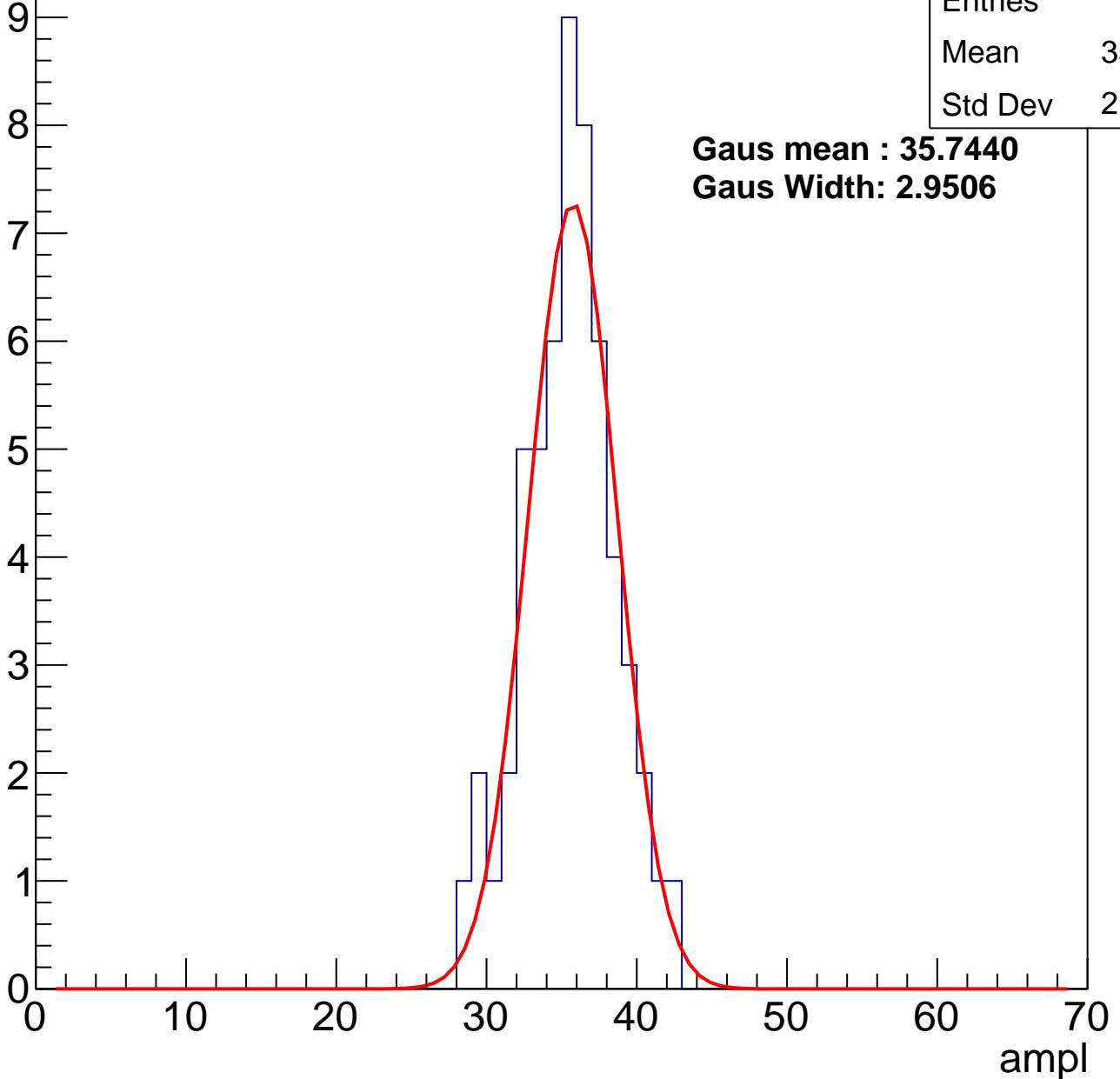
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	35.07
Std Dev	2.987

**Gaus mean : 35.7440**

**Gaus Width: 2.9506**



# B1L002S, U17-ch45, adc2

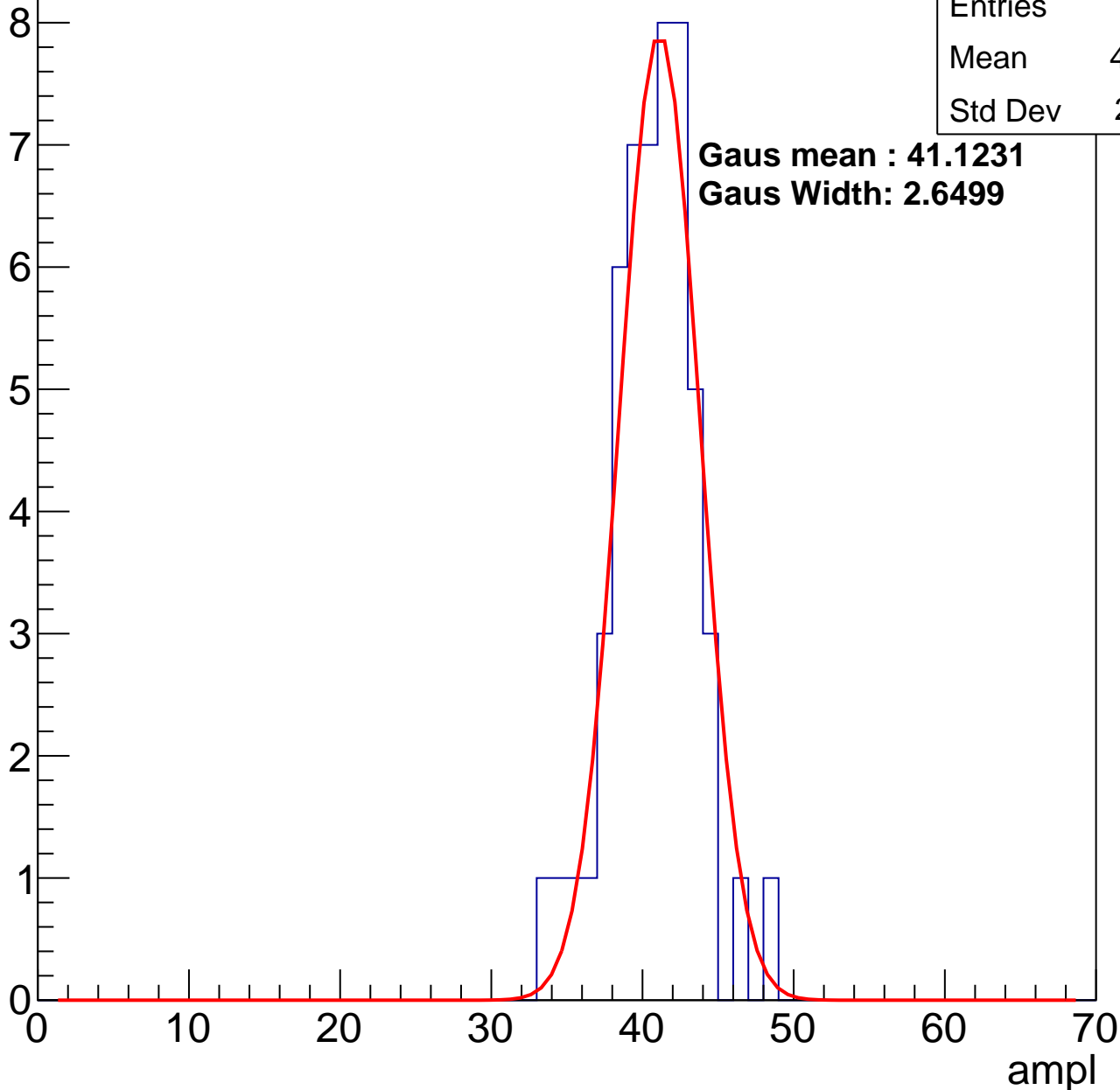
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.28
Std Dev	2.791

**Gaus mean : 41.1231**

**Gaus Width: 2.6499**

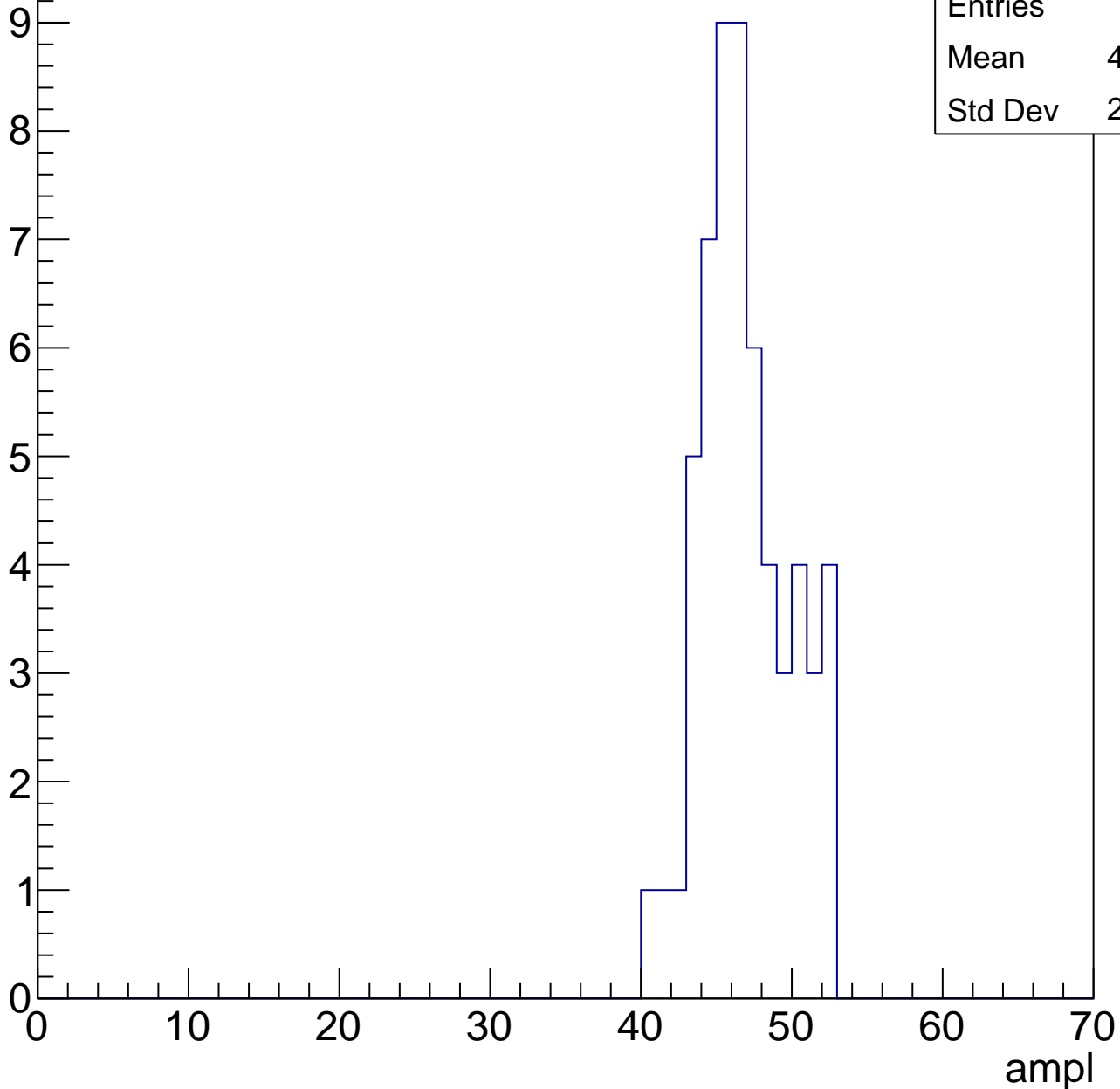


# B1L002S, U17-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.44
Std Dev	2.902

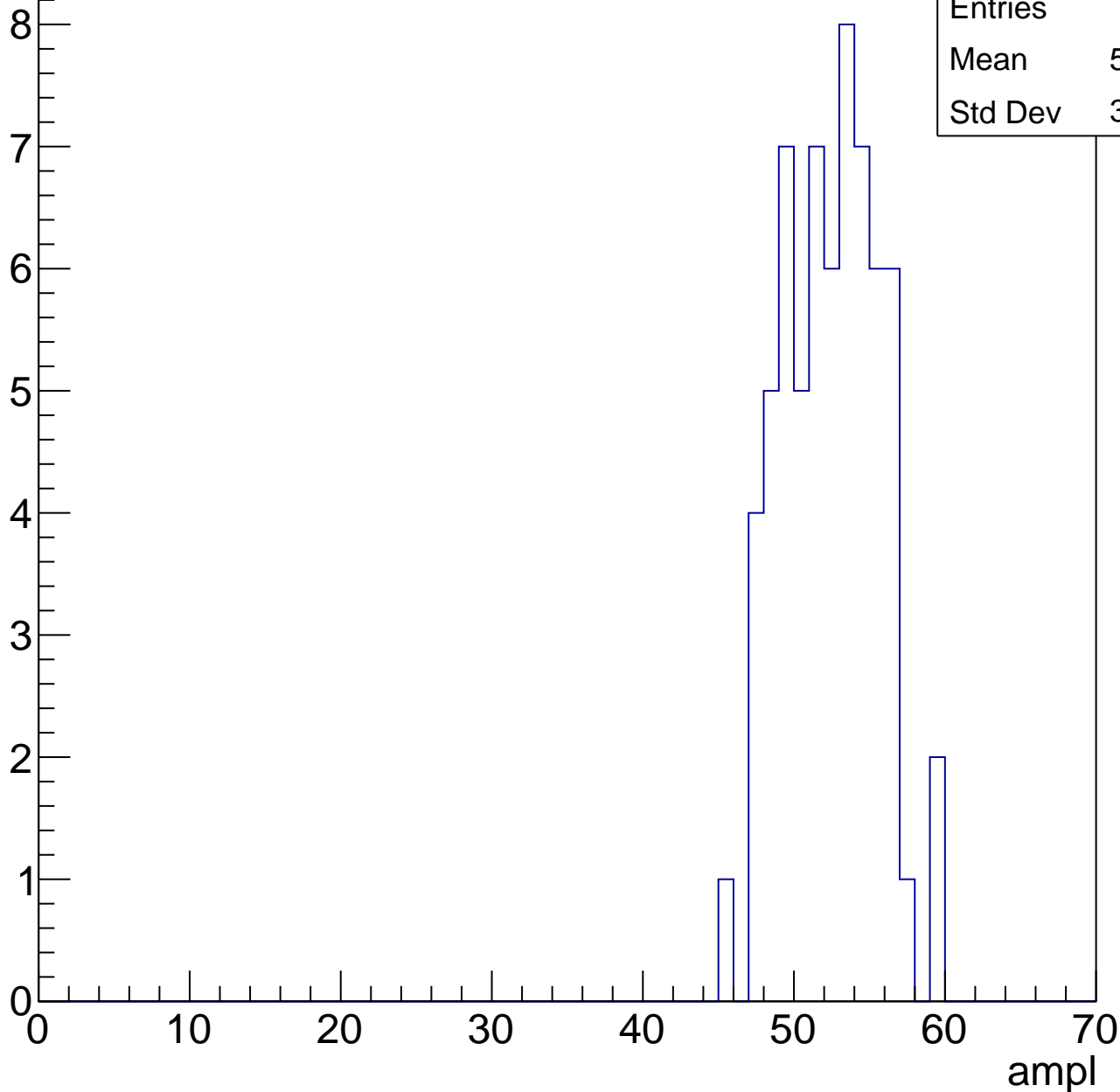


# B1L002S, U17-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

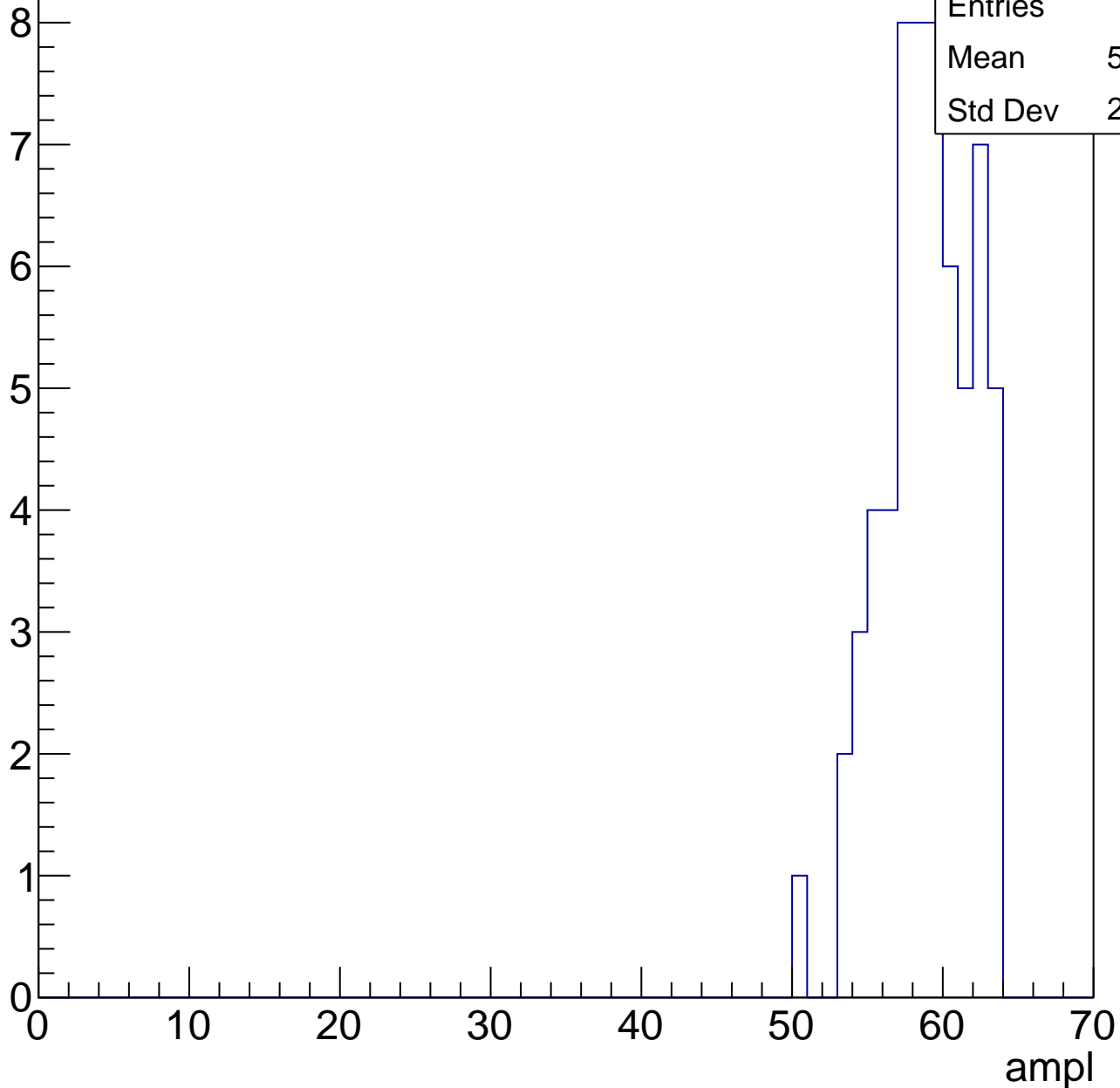
Entries	65
Mean	51.97
Std Dev	3.108



# B1L002S, U17-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



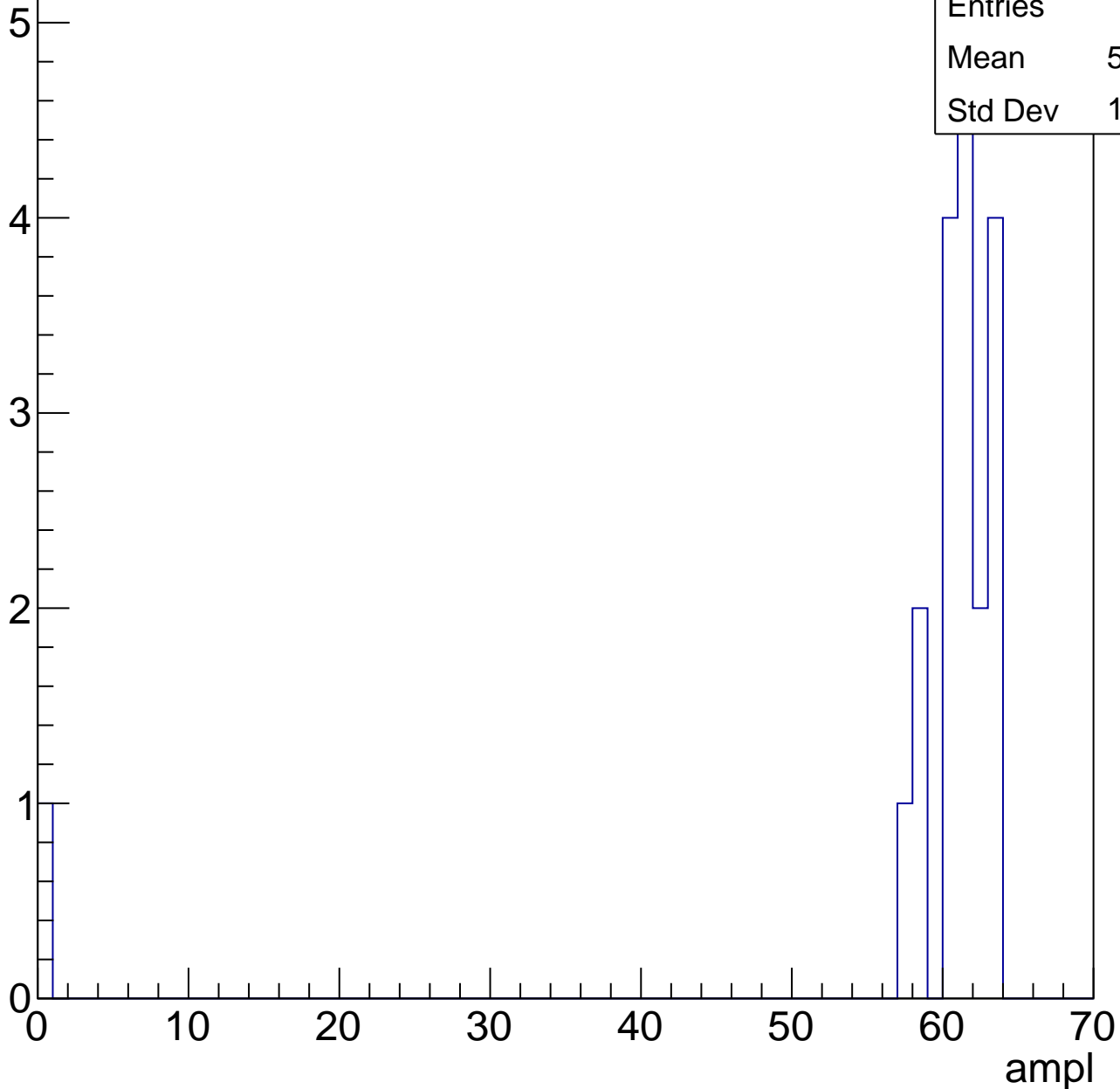
Entries	61
Mean	58.49
Std Dev	2.923

# B1L002S, U17-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	57.58
Std Dev	13.68





# B1L002S, U17-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch46, adc0

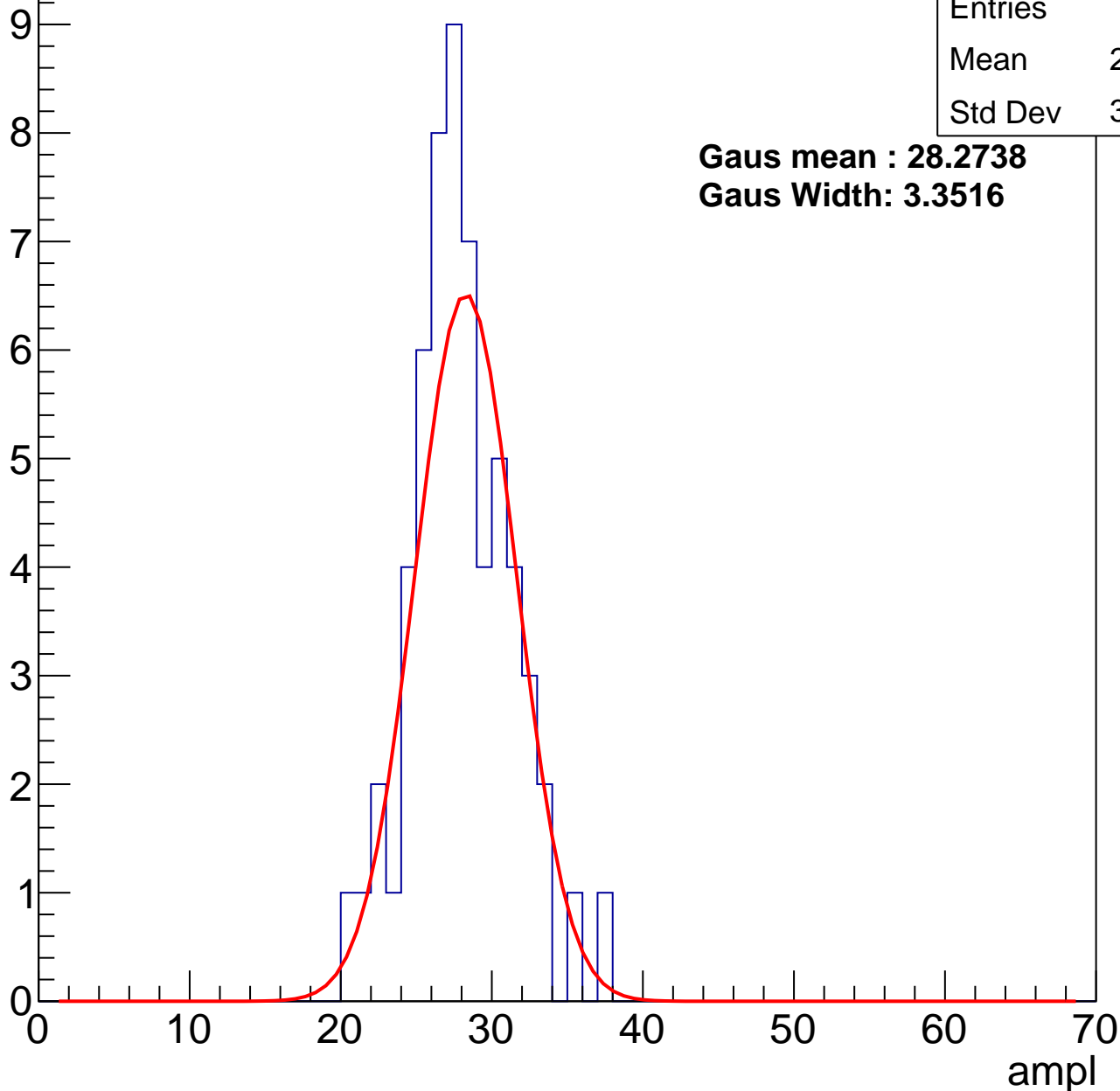
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.54
Std Dev	3.305

**Gaus mean : 28.2738**

**Gaus Width: 3.3516**



# B1L002S, U17-ch46, adc1

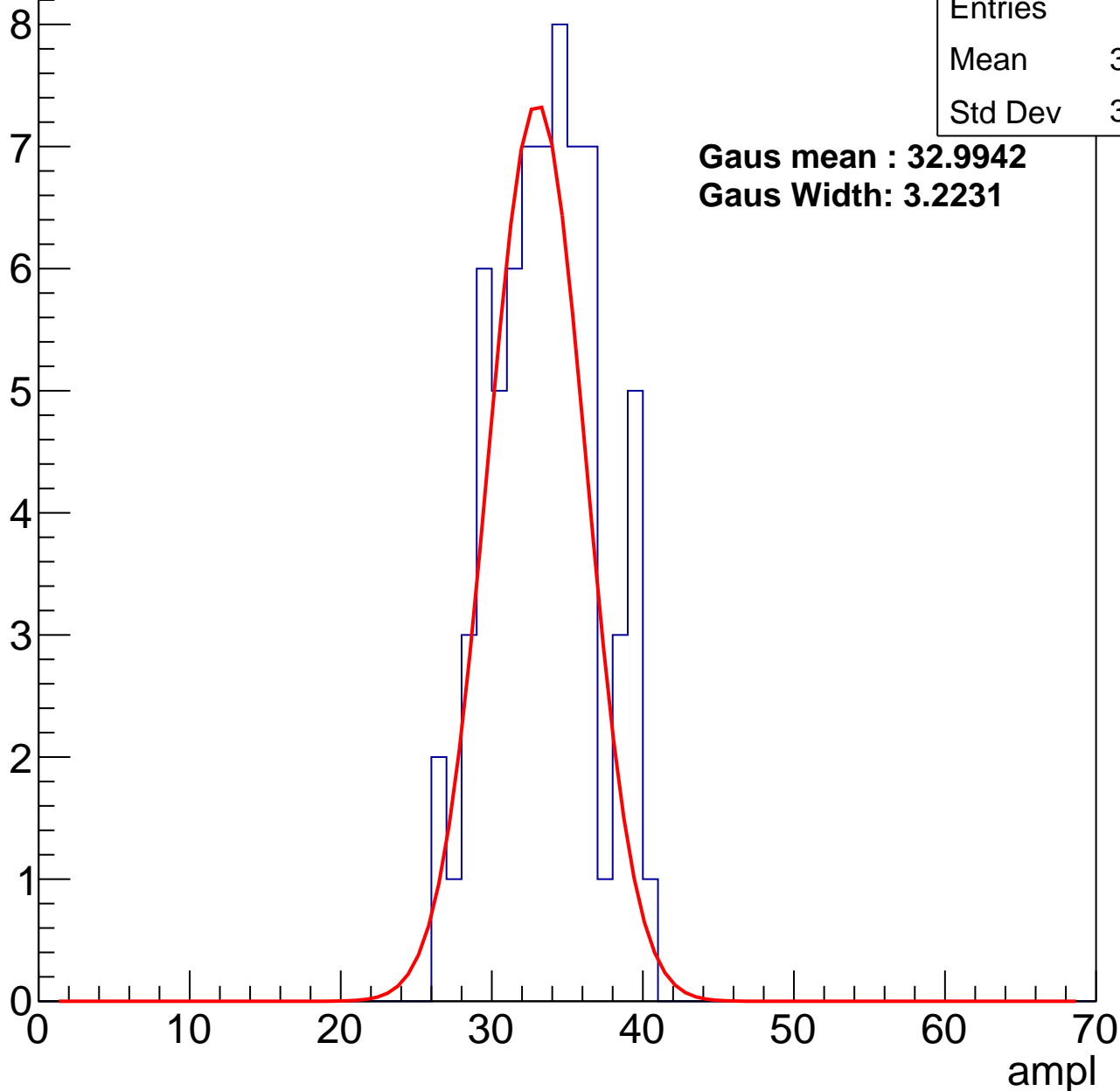
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	33.09
Std Dev	3.404

**Gaus mean : 32.9942**

**Gaus Width: 3.2231**



# B1L002S, U17-ch46, adc2

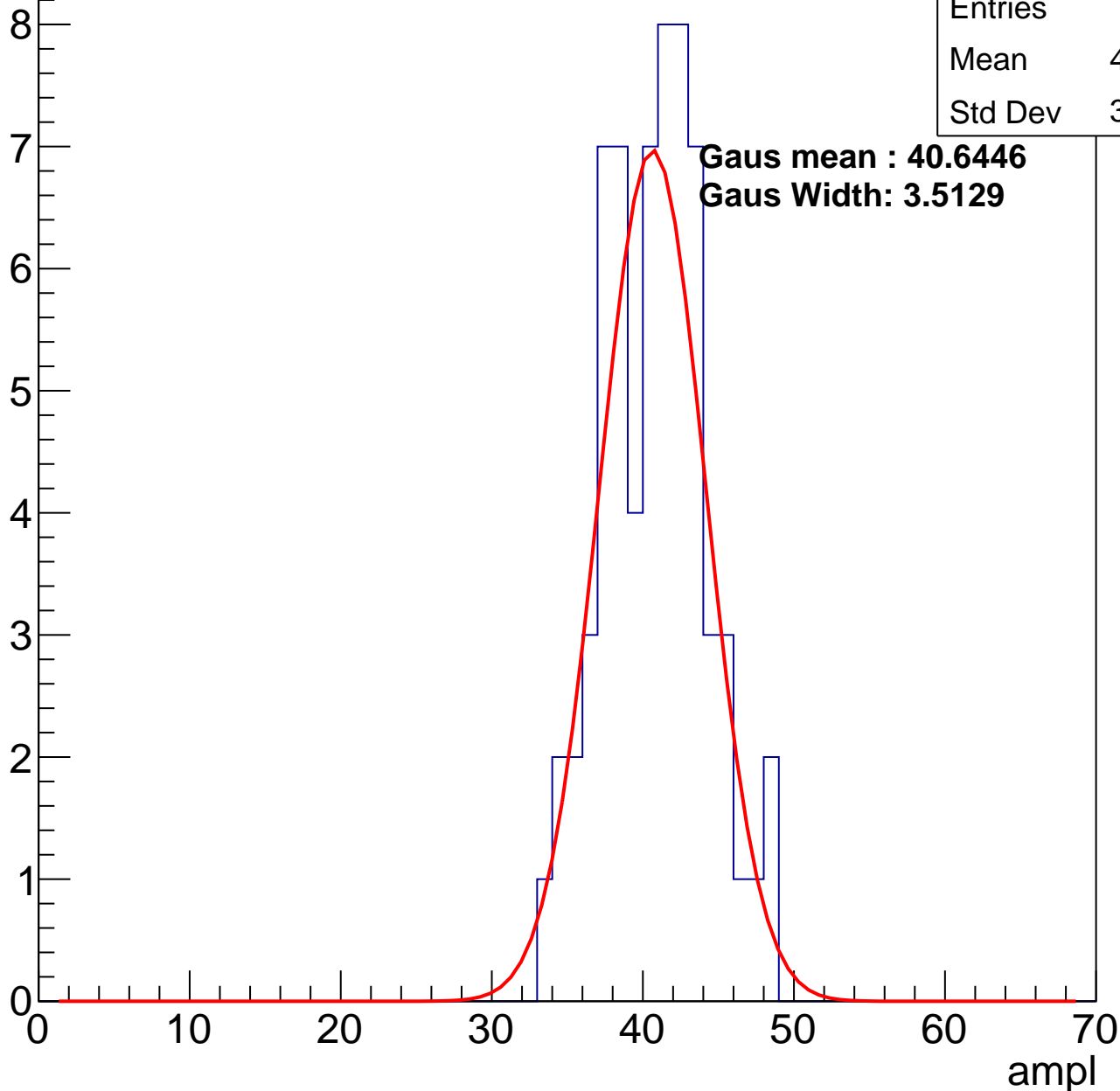
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.32
Std Dev	3.363

**Gaus mean : 40.6446**

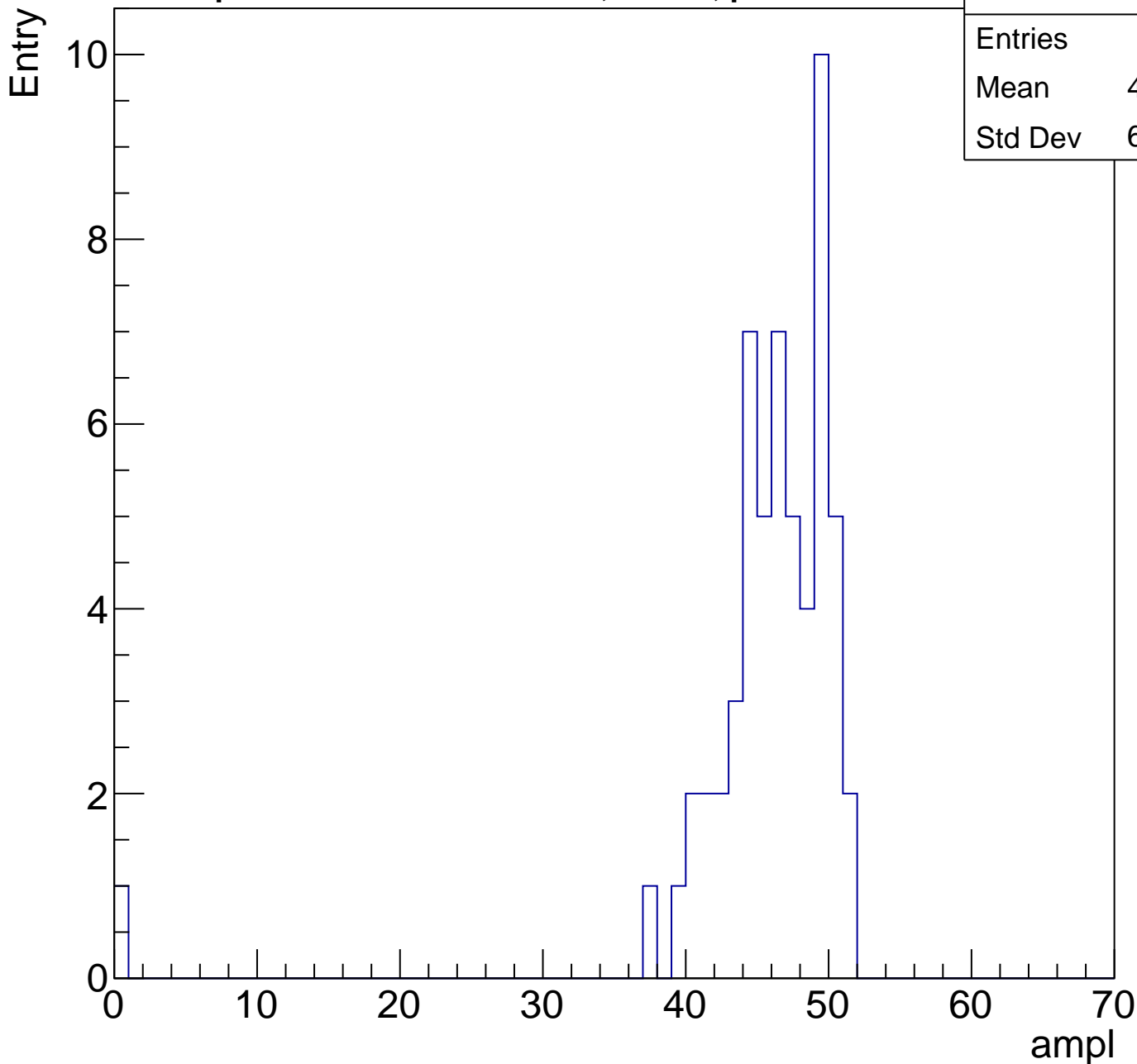
**Gaus Width: 3.5129**



# B1L002S, U17-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

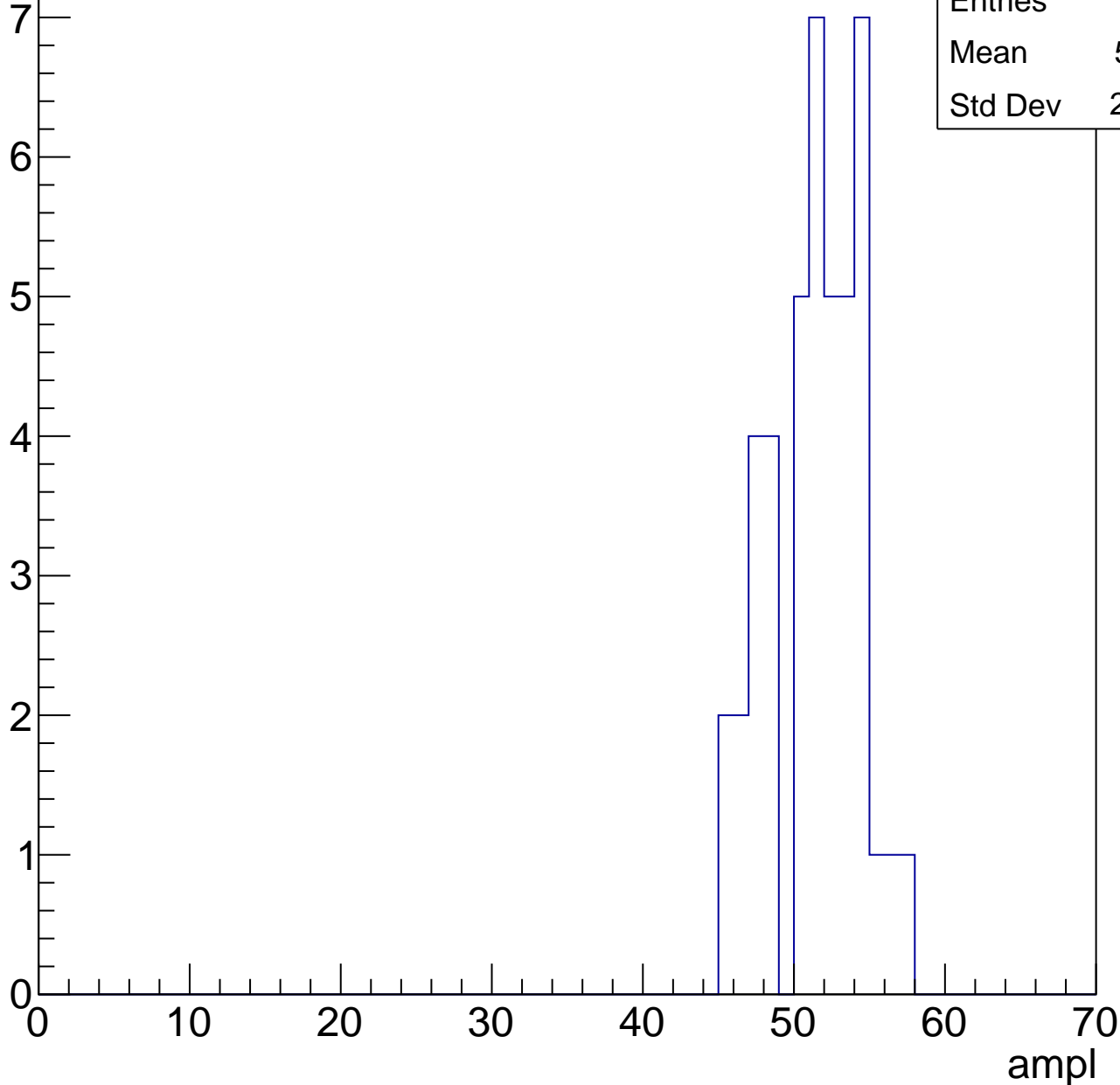
Entries	57
Mean	45.18
Std Dev	6.834



# B1L002S, U17-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



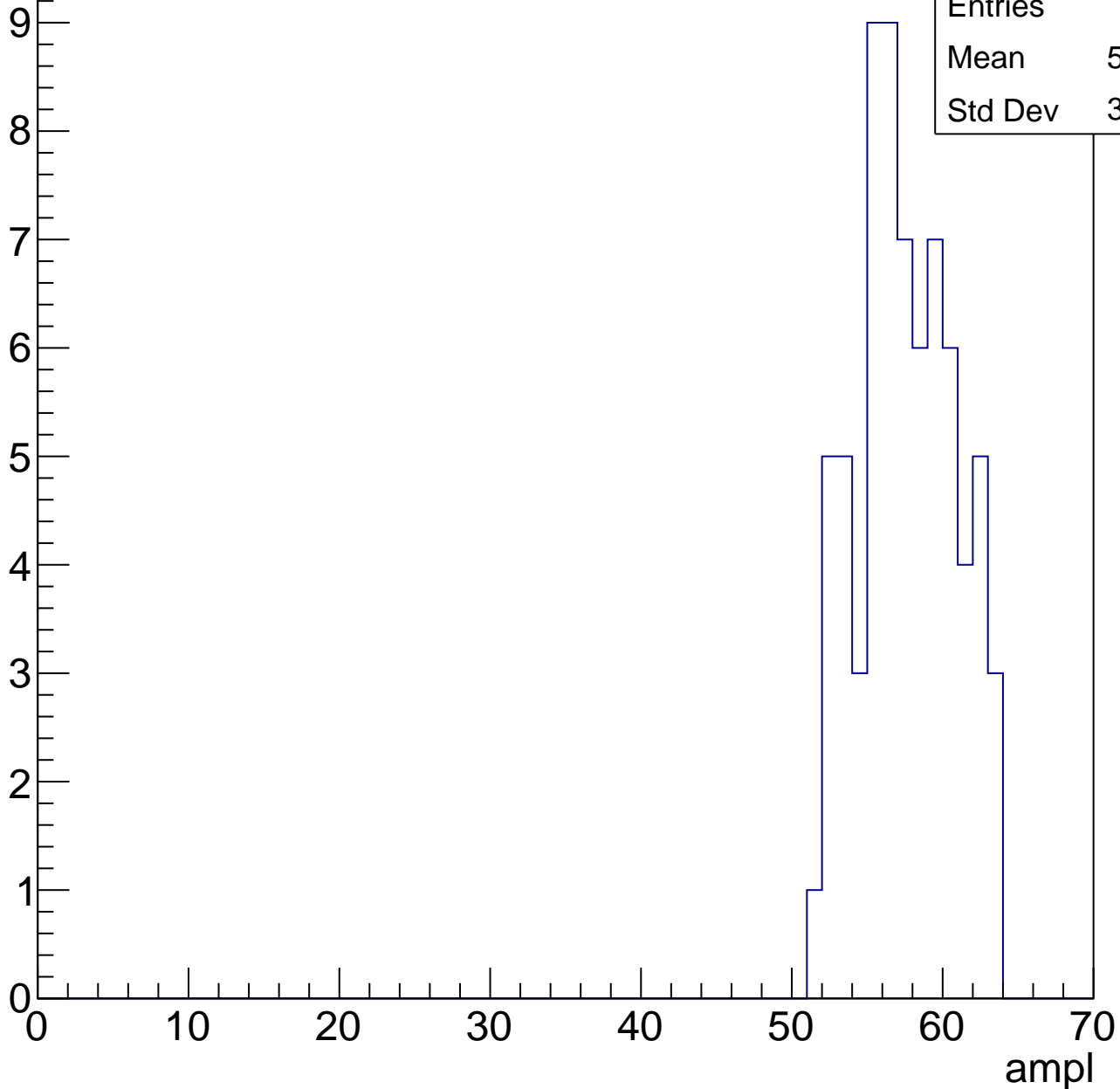
Entries	44
Mean	50.91
Std Dev	2.983

# B1L002S, U17-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	57.14
Std Dev	3.155

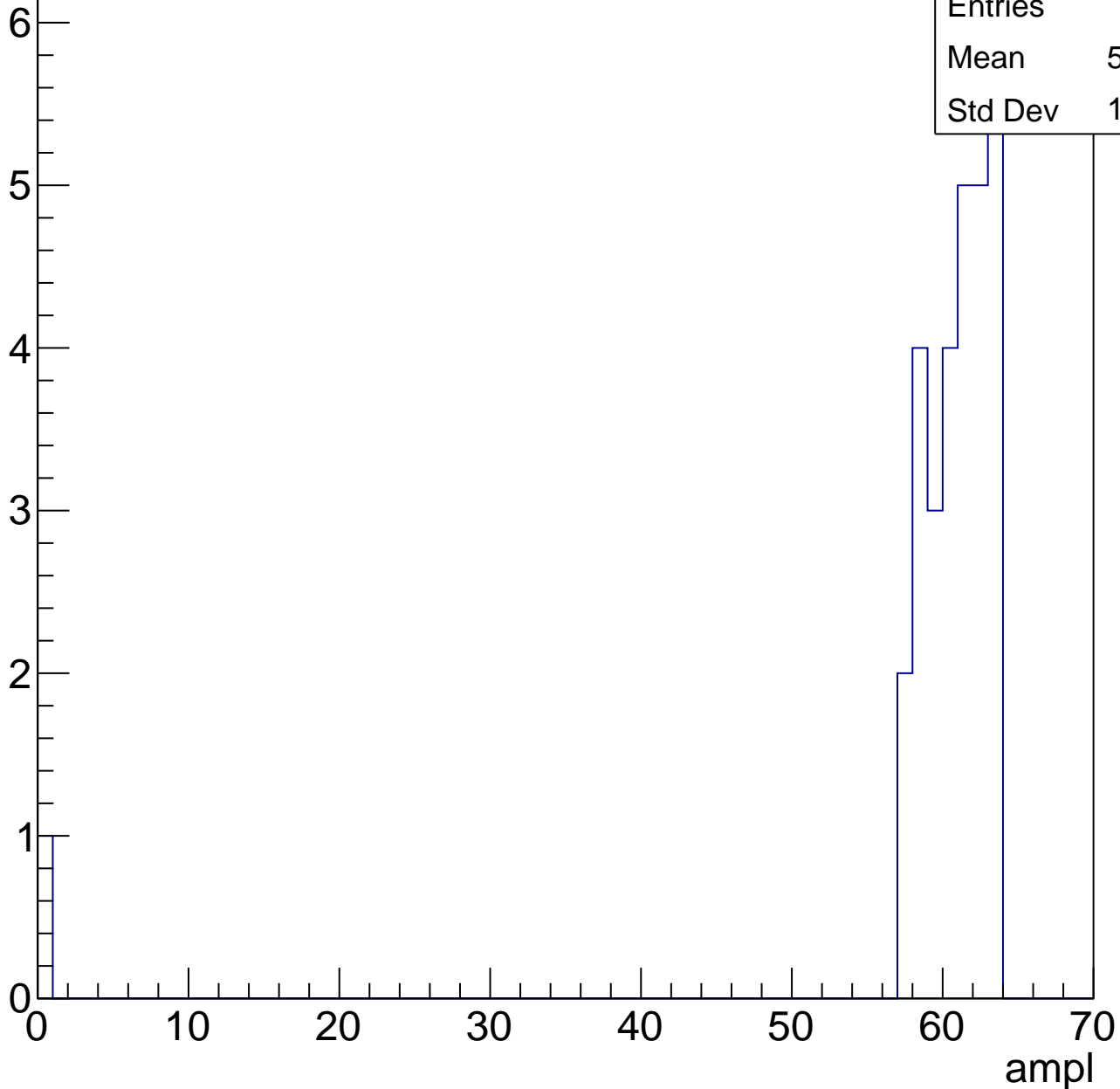


# B1L002S, U17-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.53
Std Dev	11.03

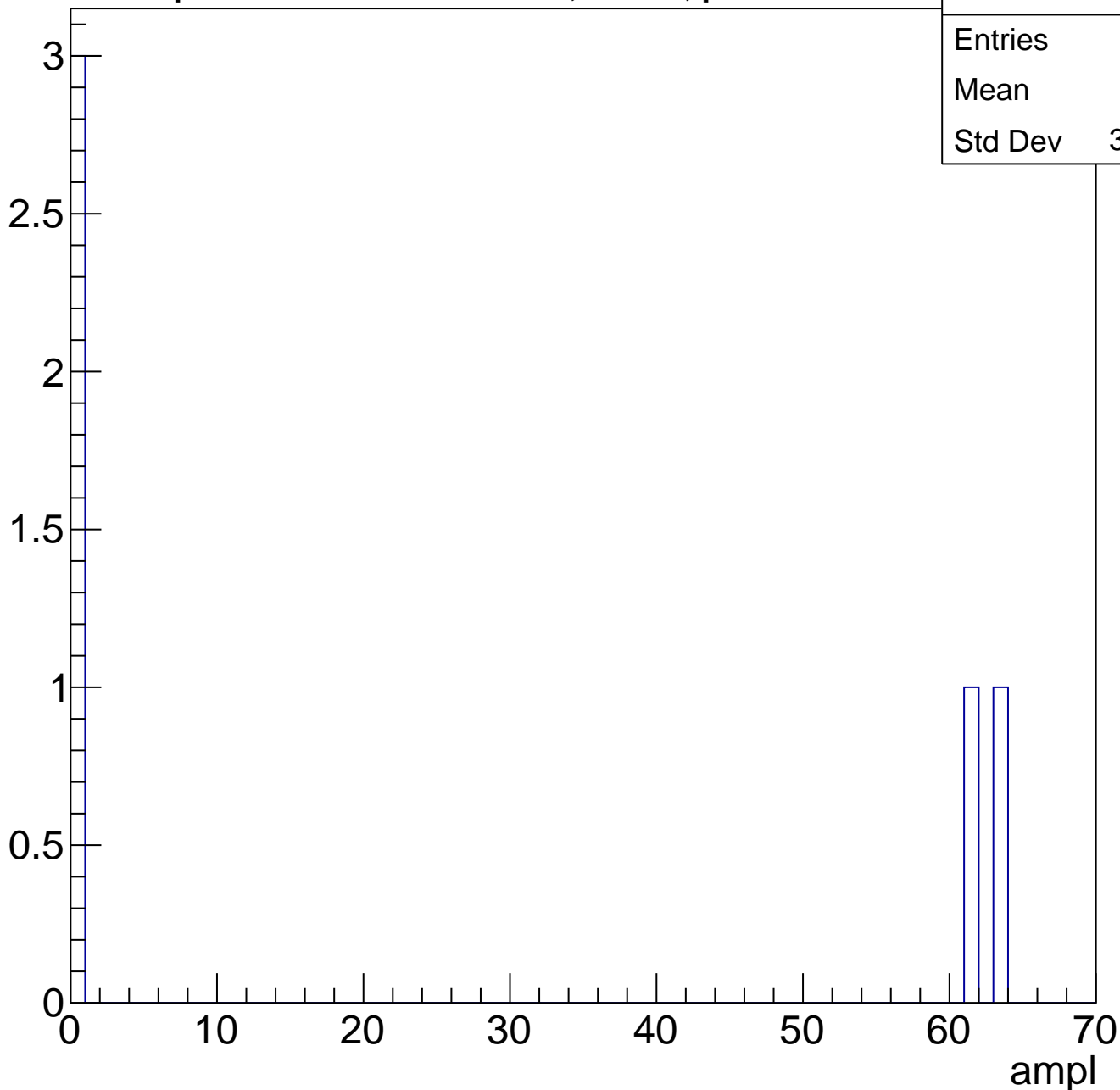




# B1L002S, U17-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	24.8
Std Dev	30.38

# B1L002S, U17-ch47, adc0

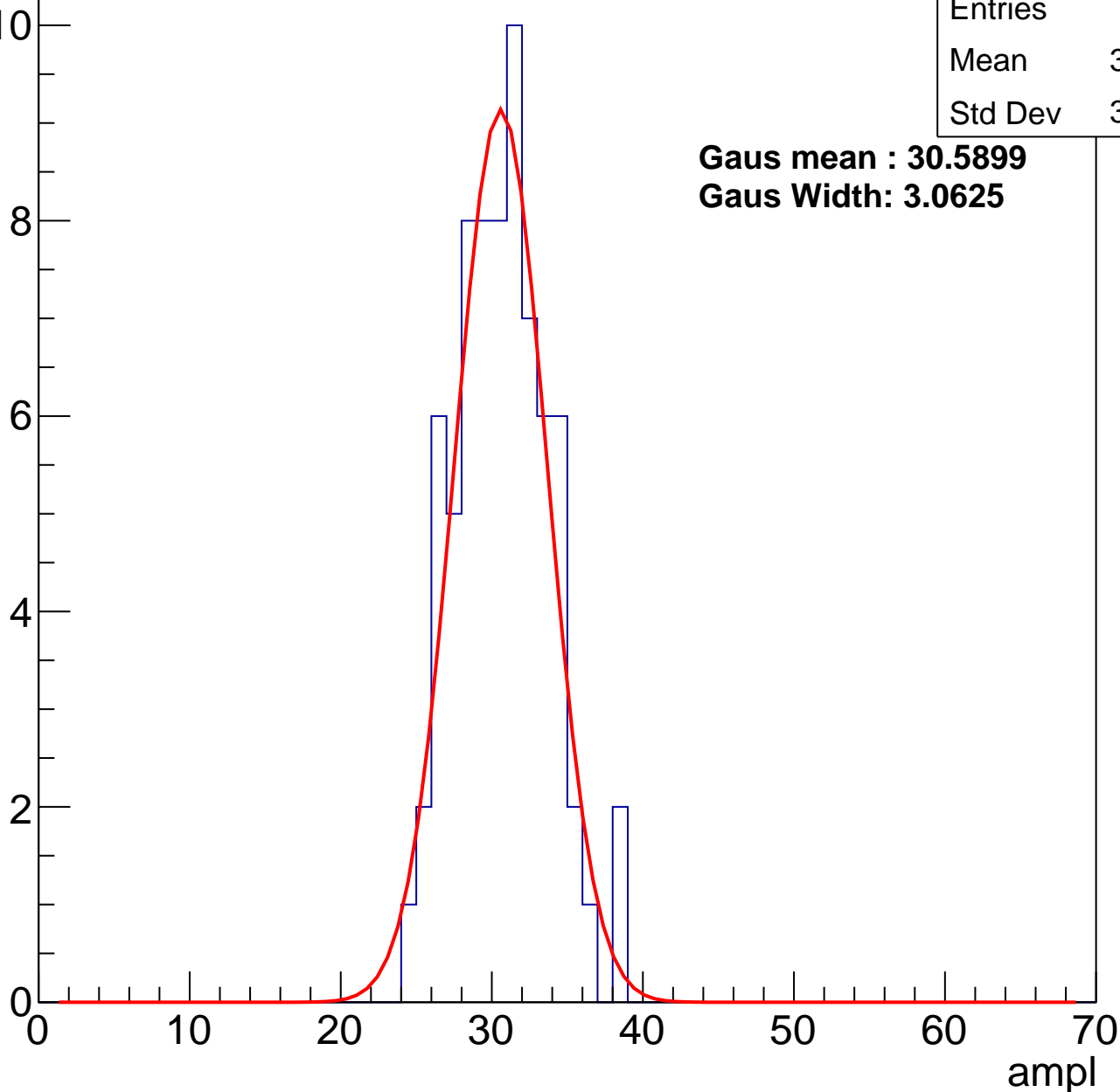
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	30.26
Std Dev	3.037

**Gaus mean : 30.5899**

**Gaus Width: 3.0625**



# B1L002S, U17-ch47, adc1

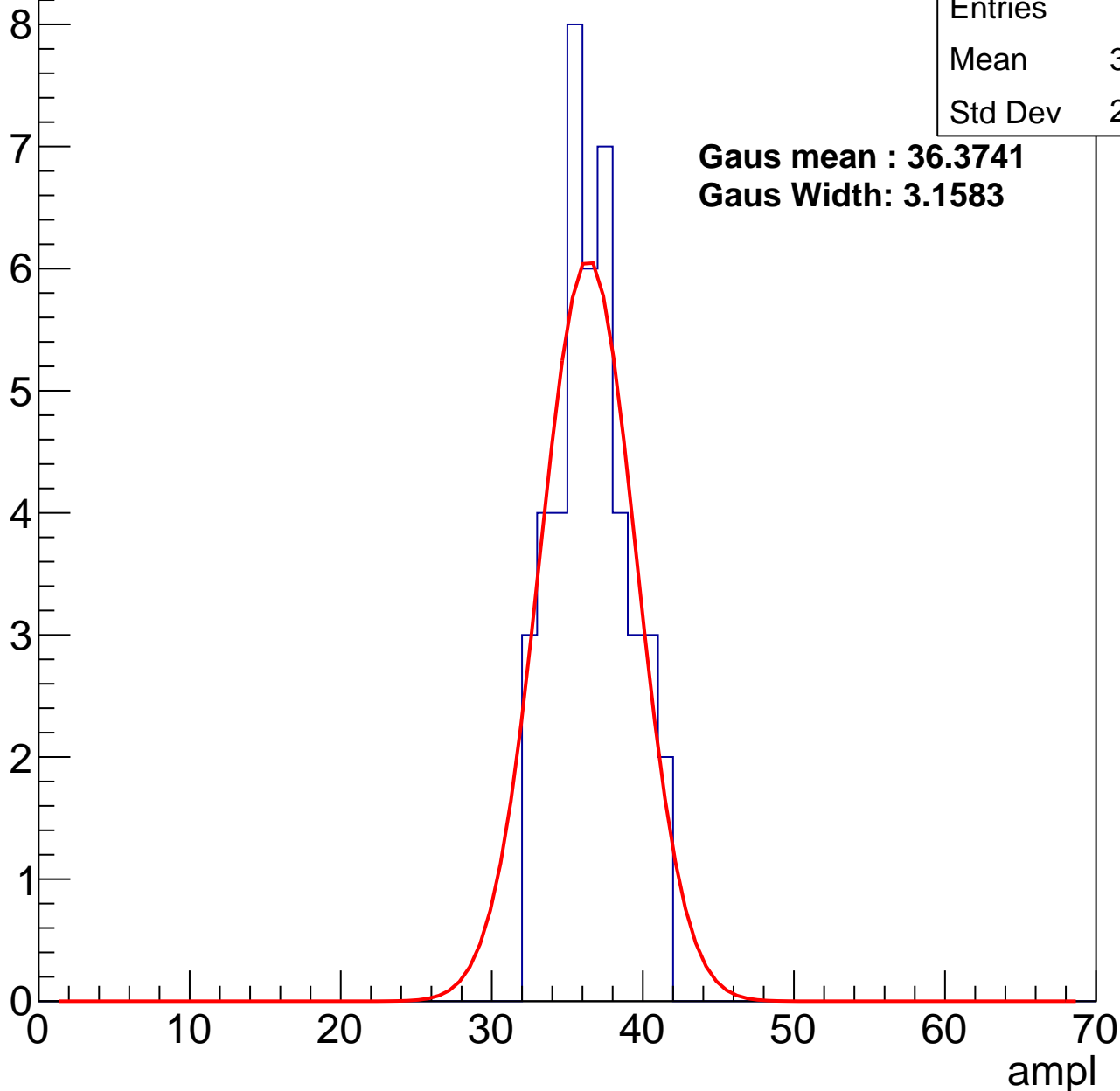
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	36.14
Std Dev	2.408

**Gaus mean : 36.3741**

**Gaus Width: 3.1583**



# B1L002S, U17-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	42.26
Std Dev	3.427

**Gaus mean : 42.3977**

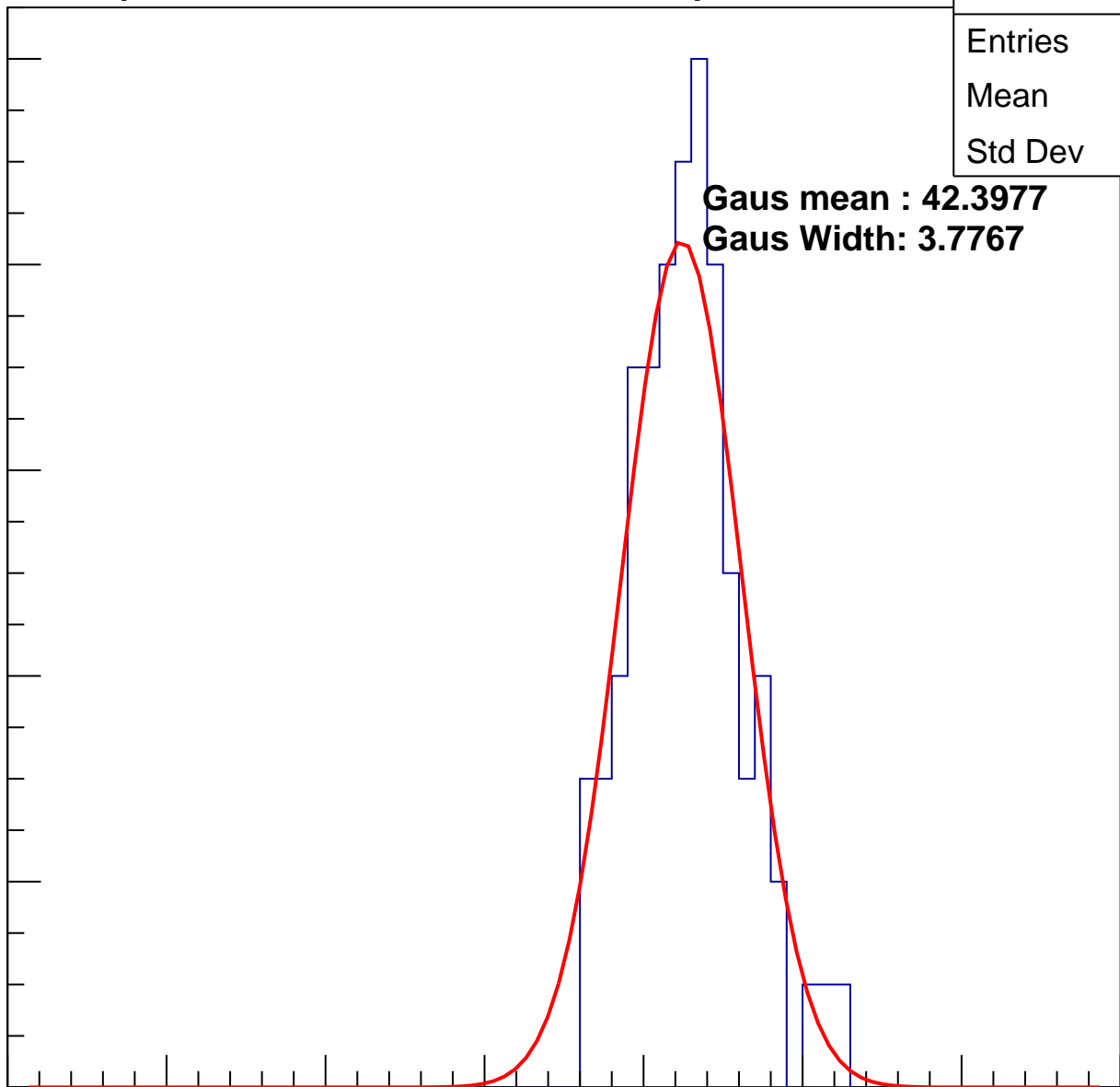
**Gaus Width: 3.7767**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

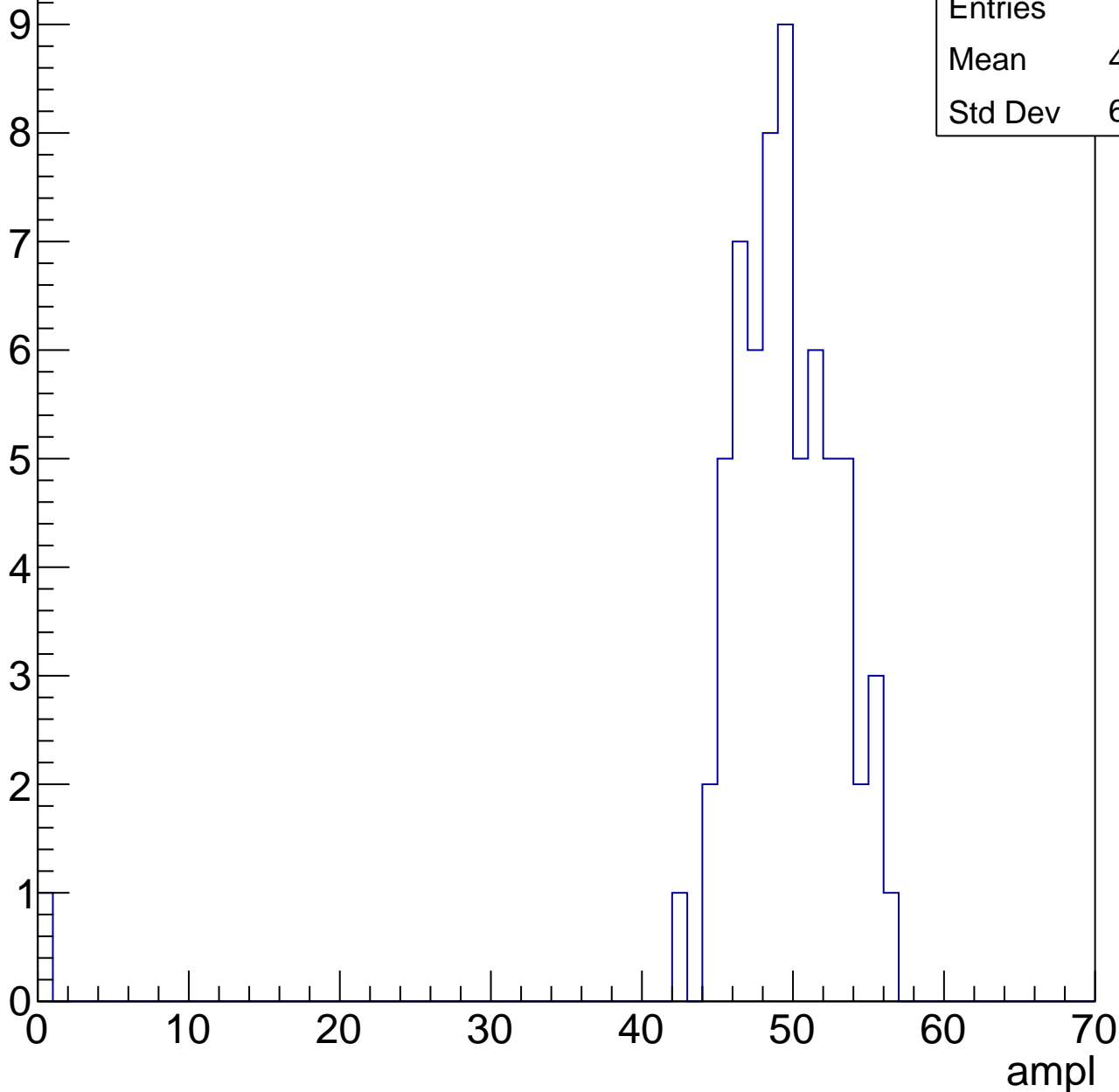


# B1L002S, U17-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	48.39
Std Dev	6.755

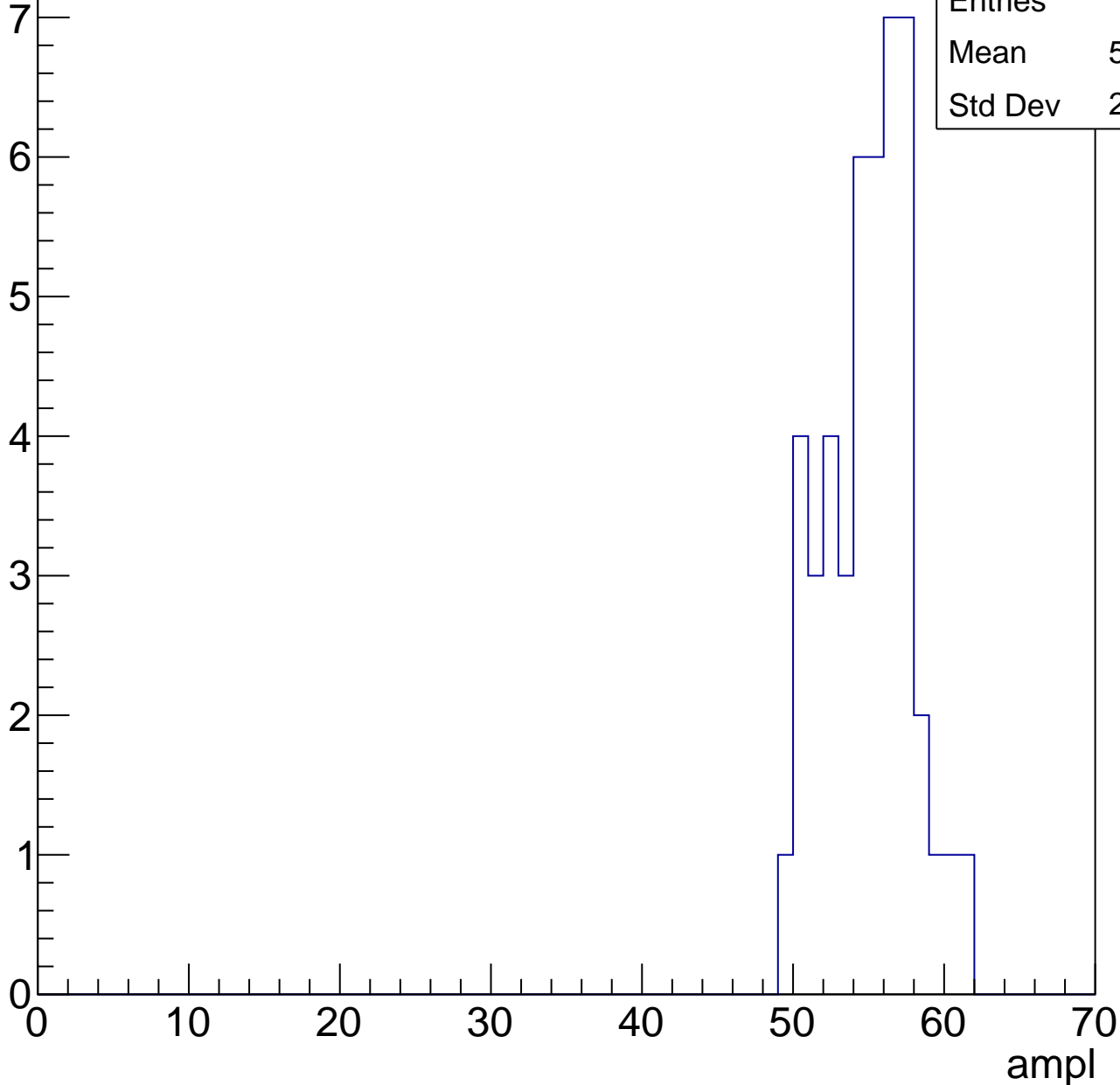


# B1L002S, U17-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	54.57
Std Dev	2.795

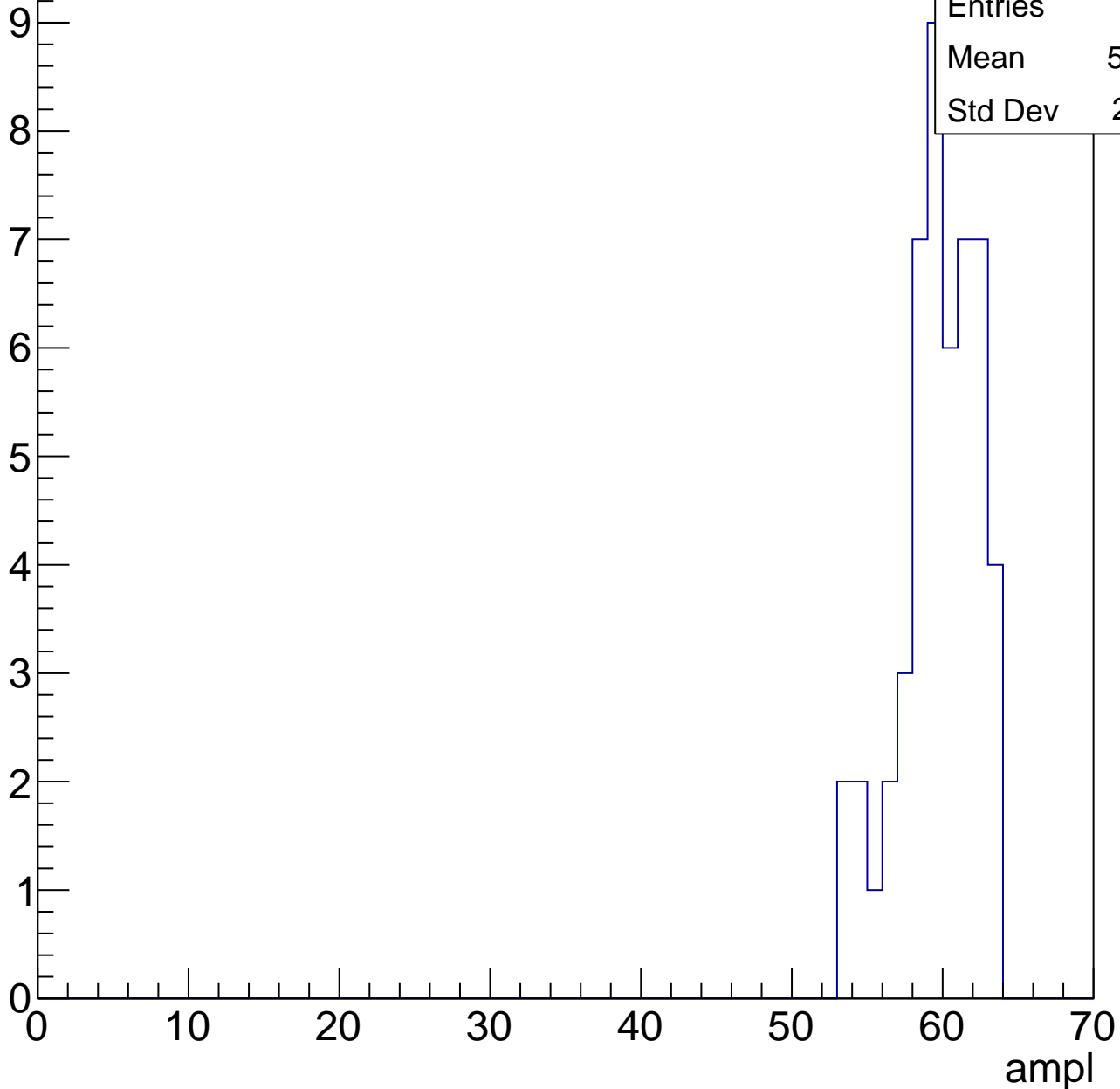


# B1L002S, U17-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	59.24
Std Dev	2.581

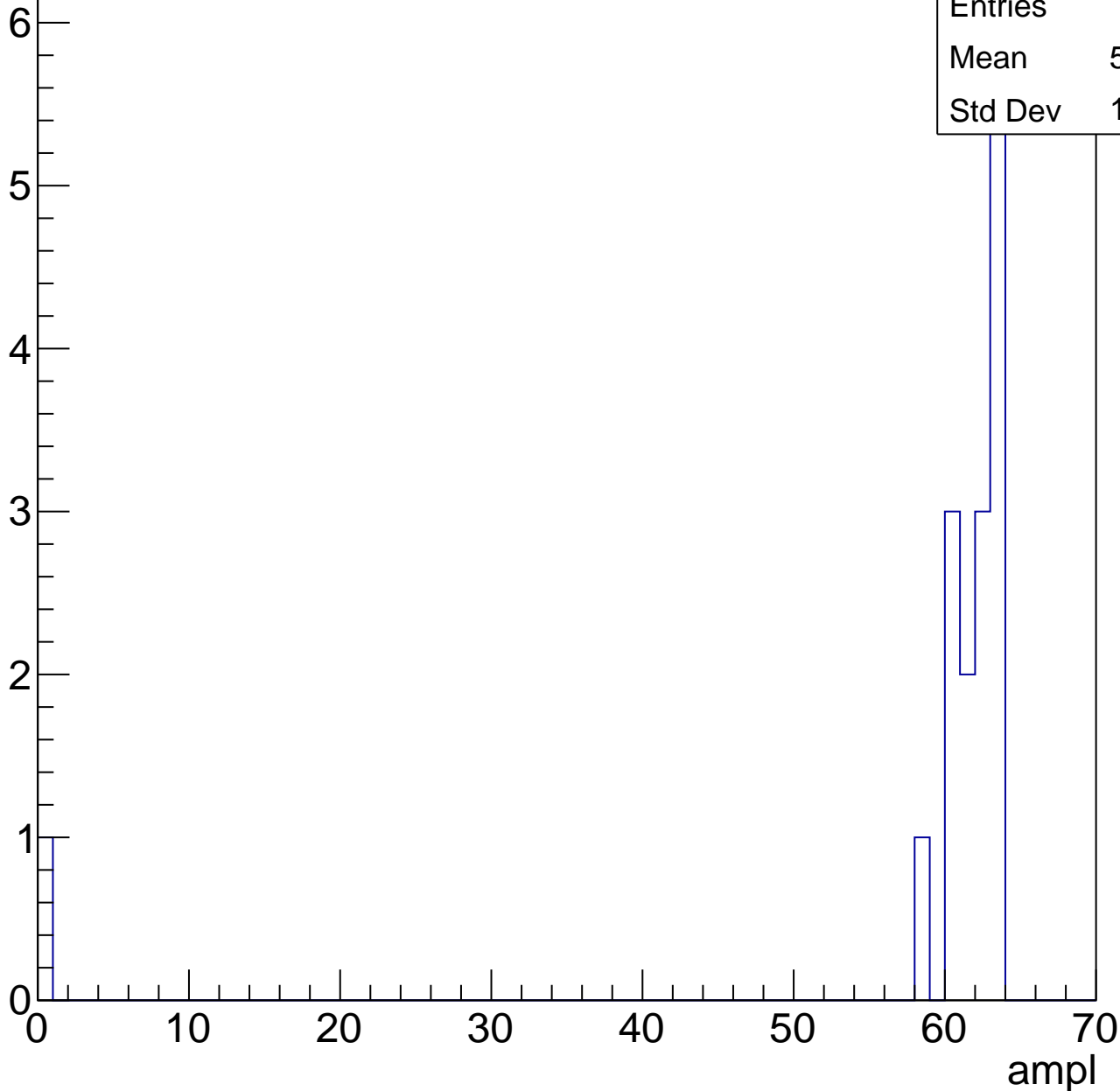


# B1L002S, U17-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.75
Std Dev	14.98





# B1L002S, U17-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	8
Std Dev	11.31

# B1L002S, U17-ch48, adc0

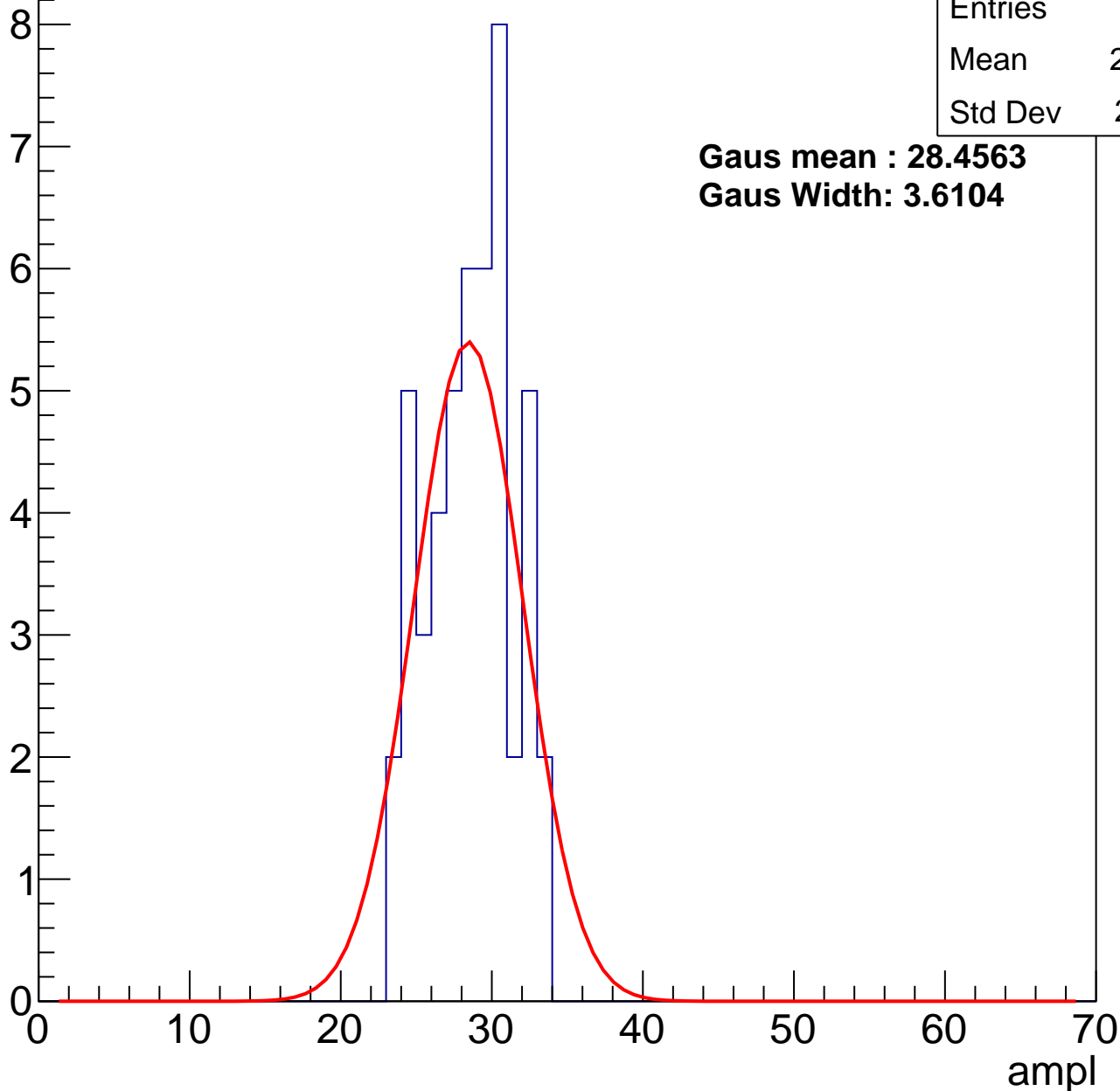
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	28.12
Std Dev	2.751

**Gaus mean : 28.4563**

**Gaus Width: 3.6104**



# B1L002S, U17-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	85
Mean	35.44
Std Dev	3.715

**Gaus mean : 35.6374**

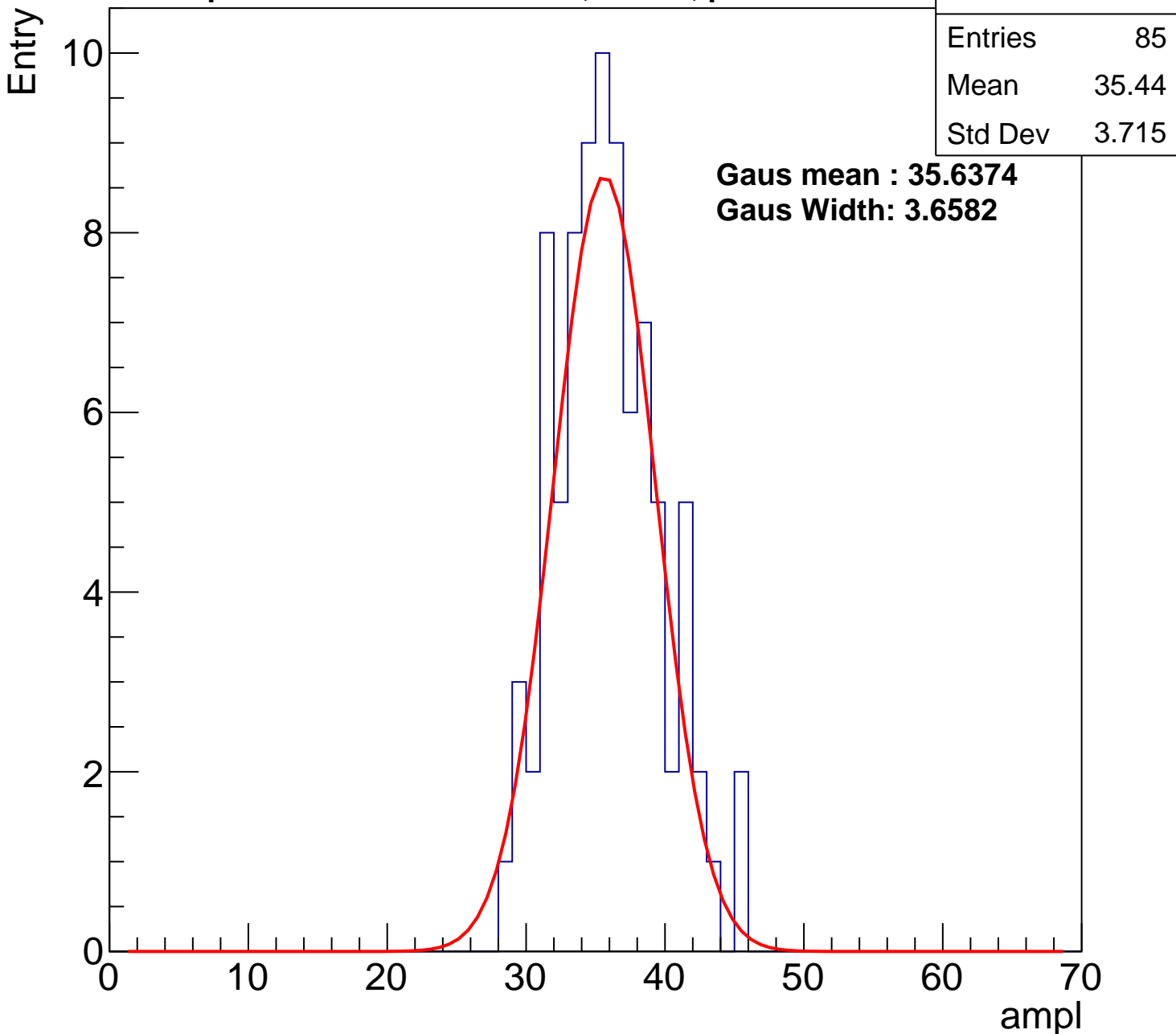
**Gaus Width: 3.6582**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch48, adc2

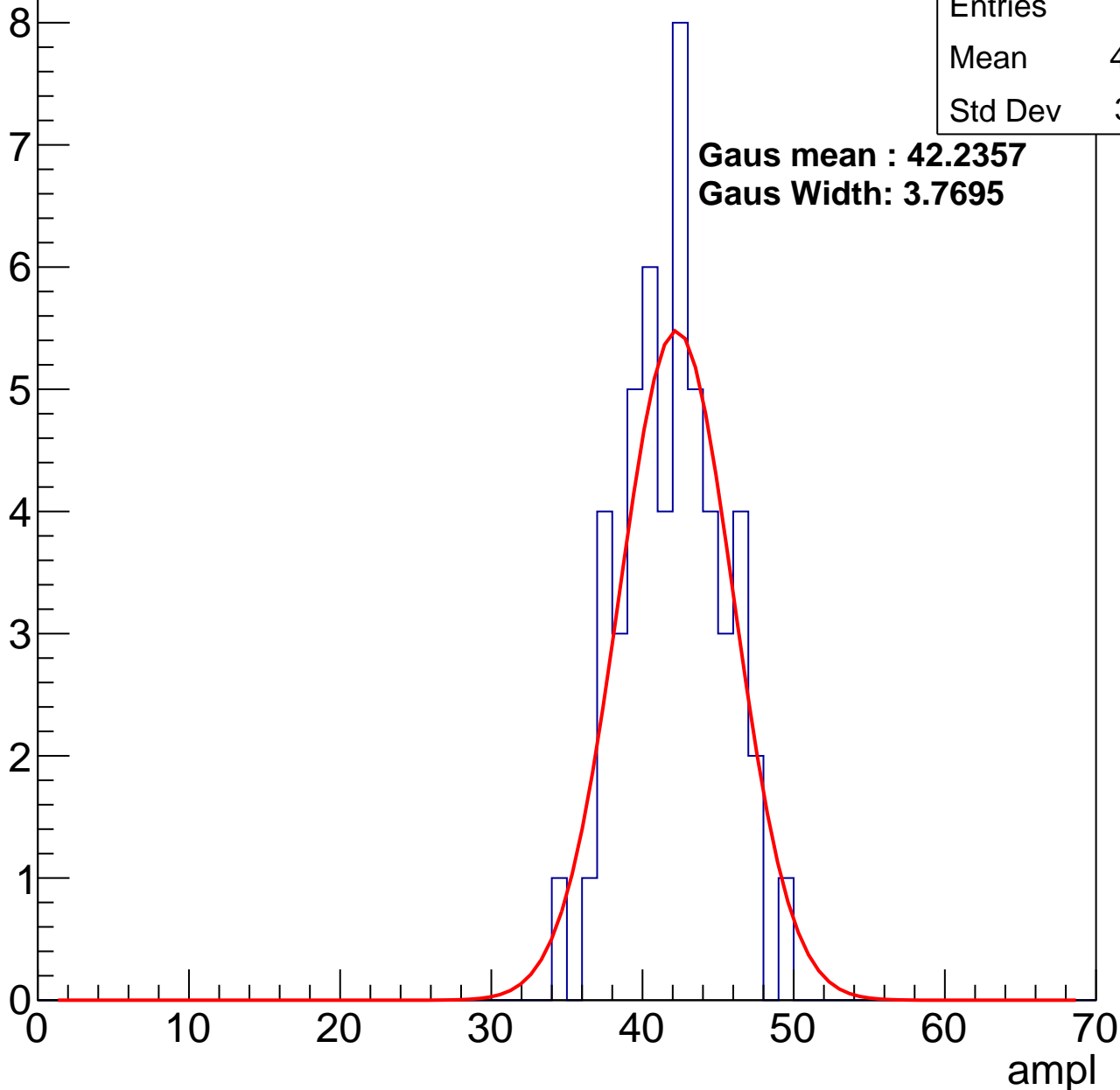
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.57
Std Dev	3.201

**Gaus mean : 42.2357**

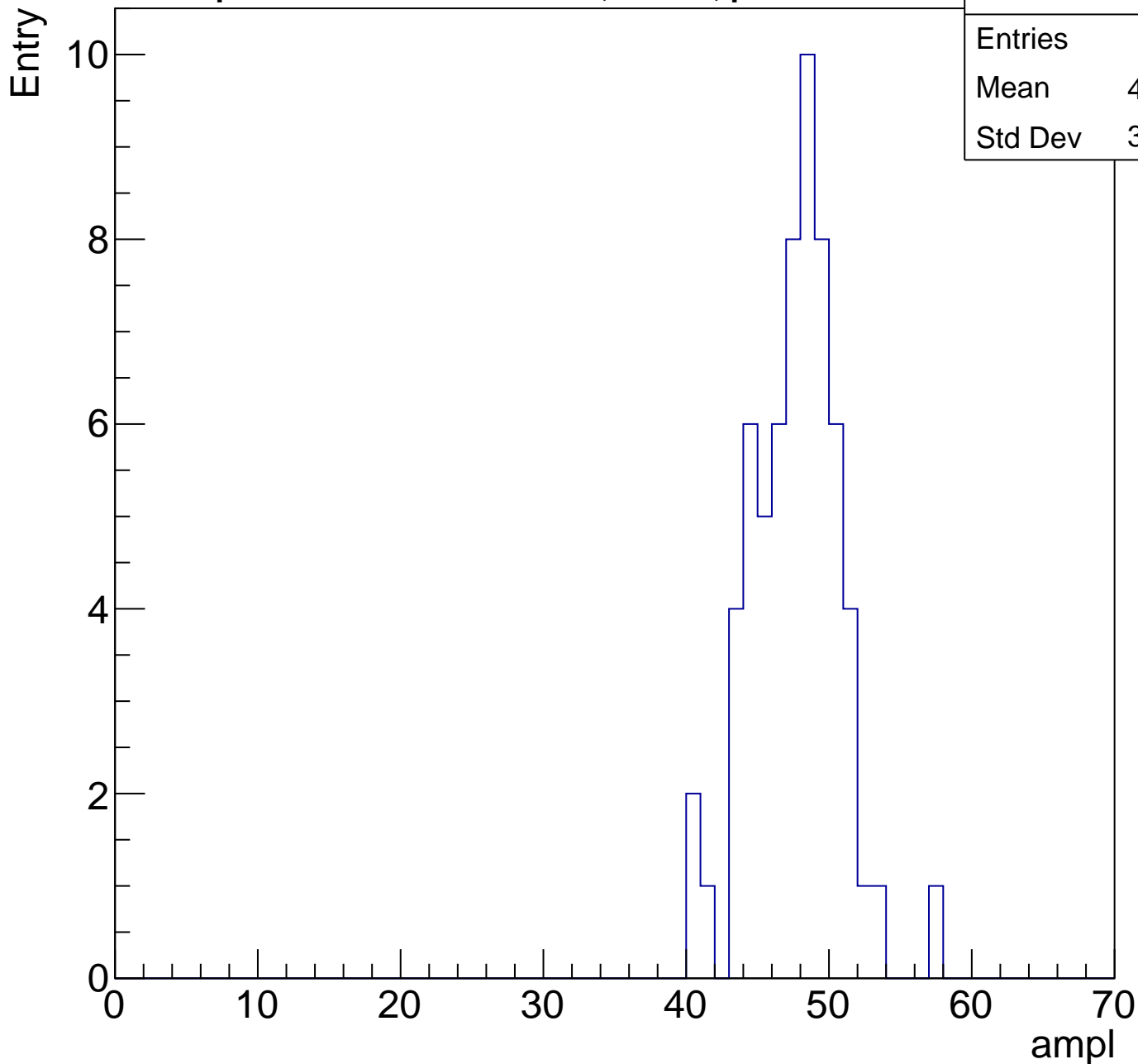
**Gaus Width: 3.7695**



# B1L002S, U17-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

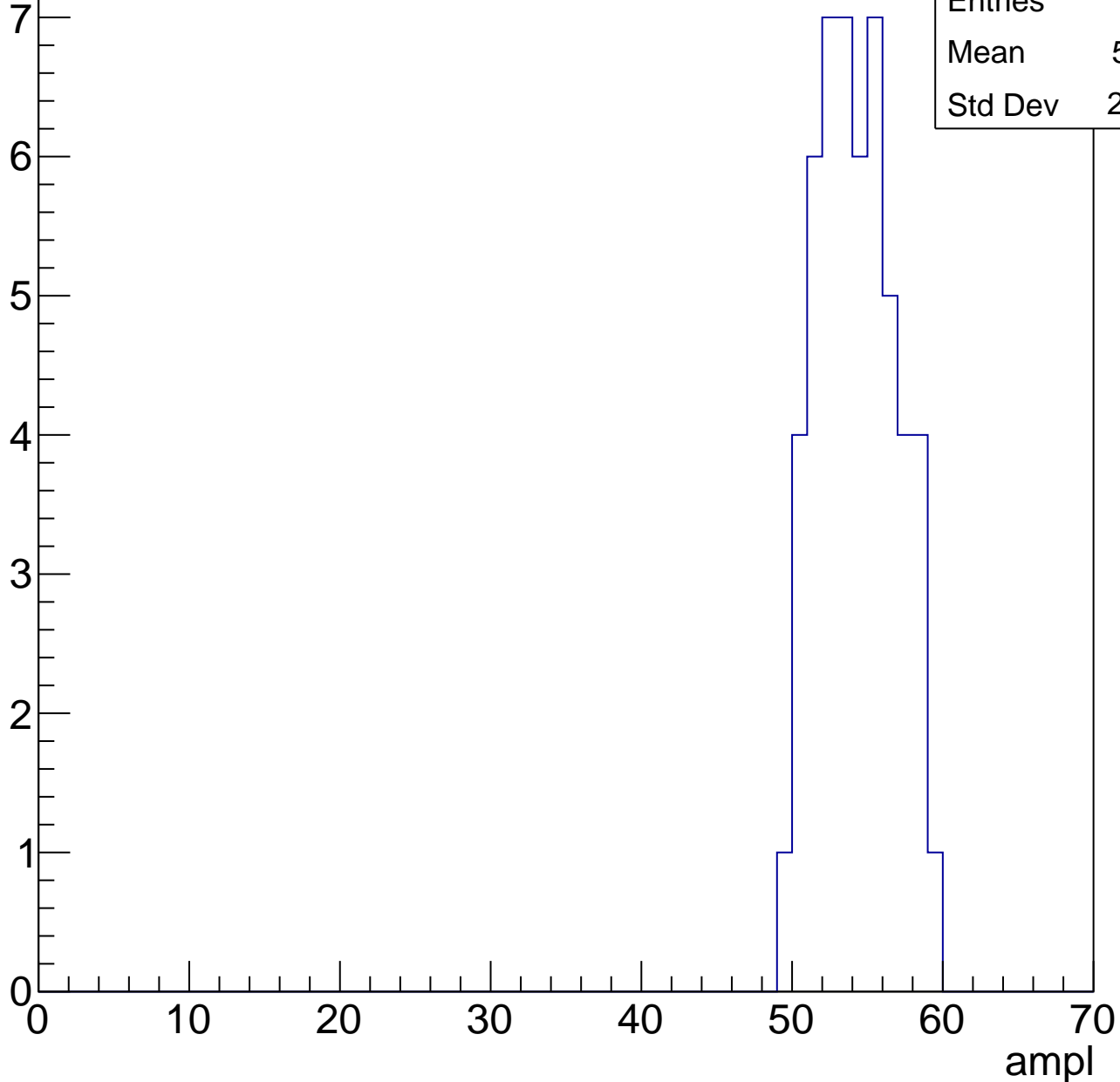
Entries	63
Mean	47.17
Std Dev	3.079



# B1L002S, U17-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	52
Mean	53.81
Std Dev	2.512

# B1L002S, U17-ch48, adc5

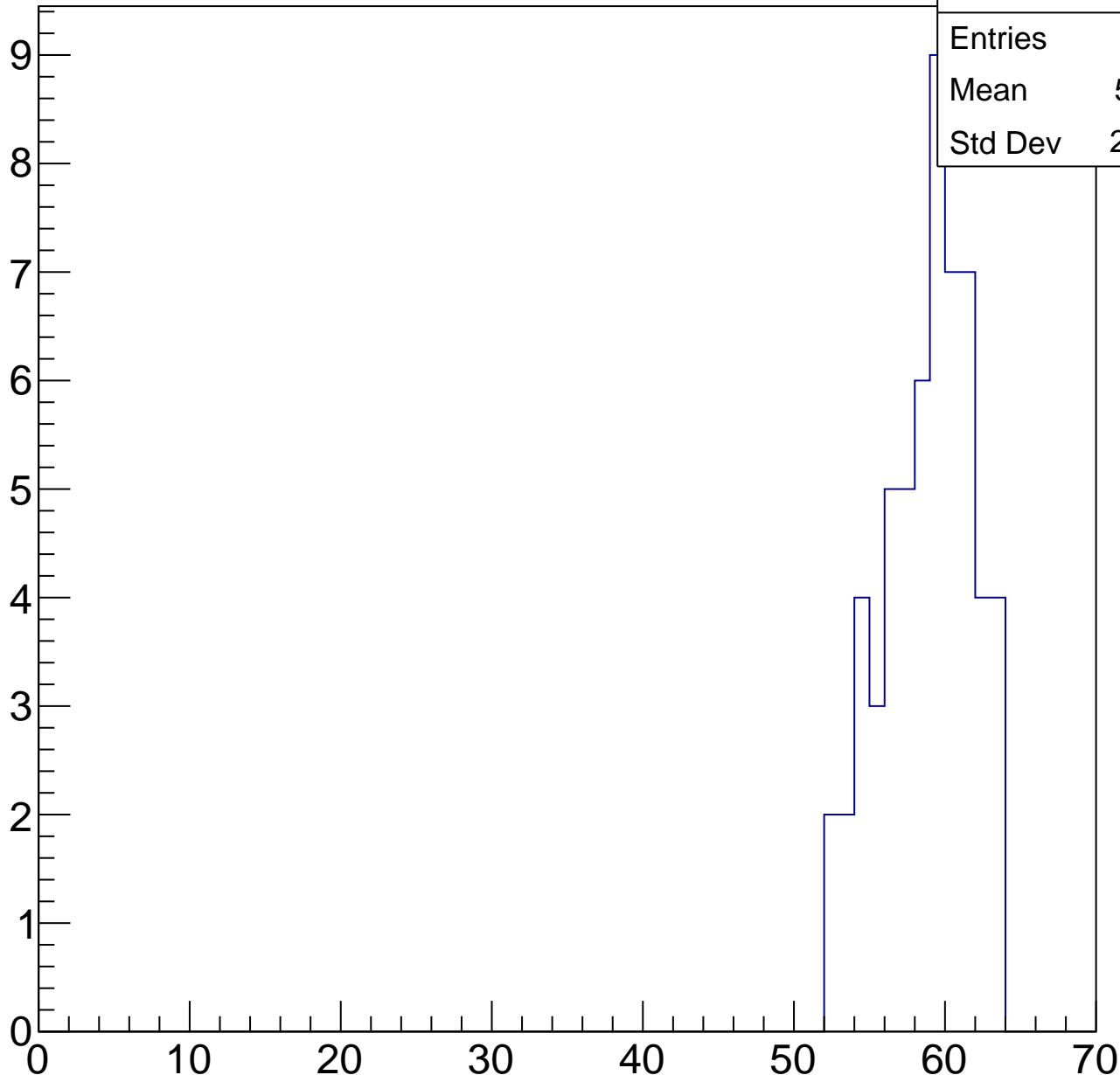
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.31
Std Dev	2.926

ampl

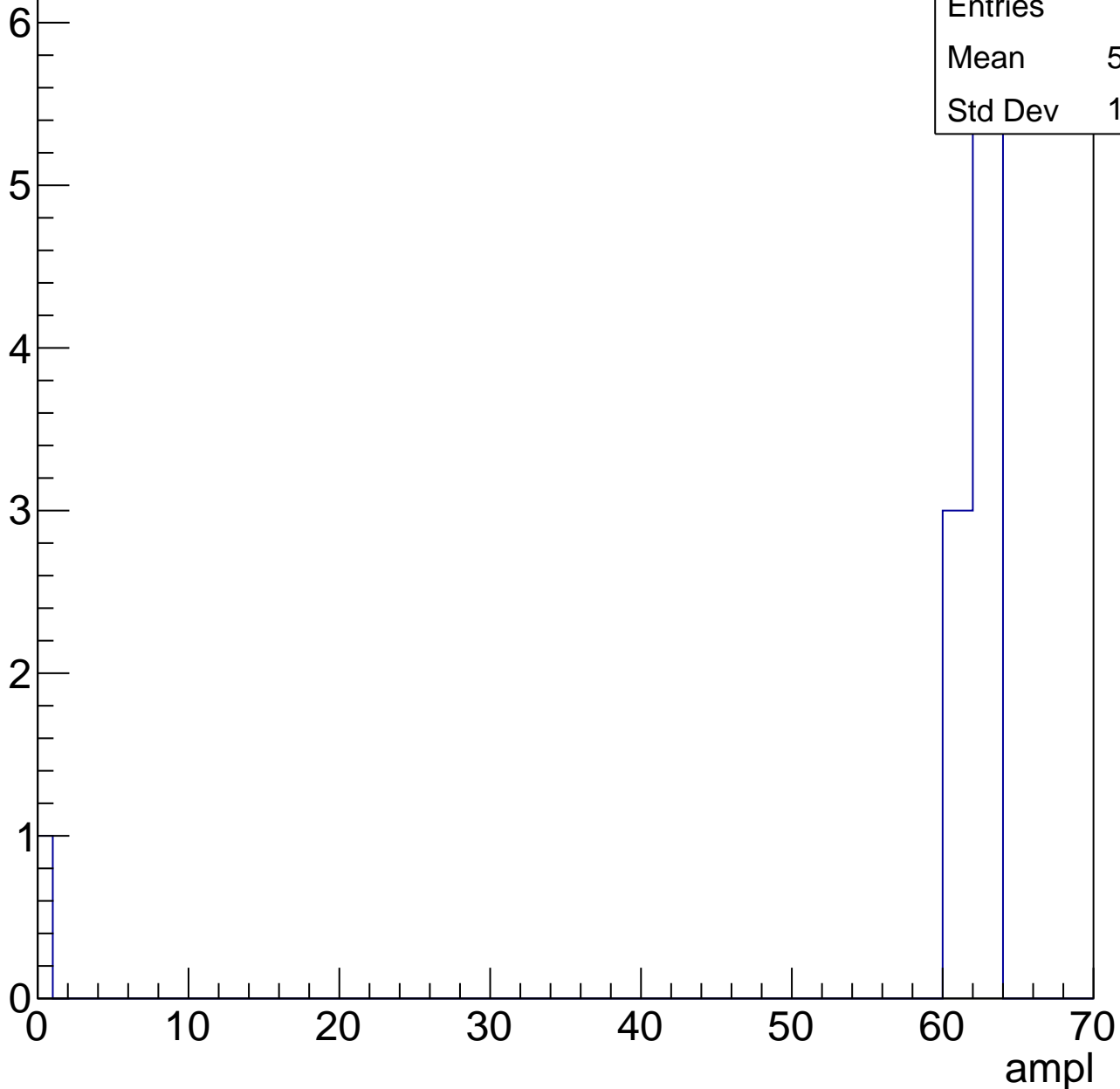


# B1L002S, U17-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.58
Std Dev	13.85





# B1L002S, U17-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch49, adc0

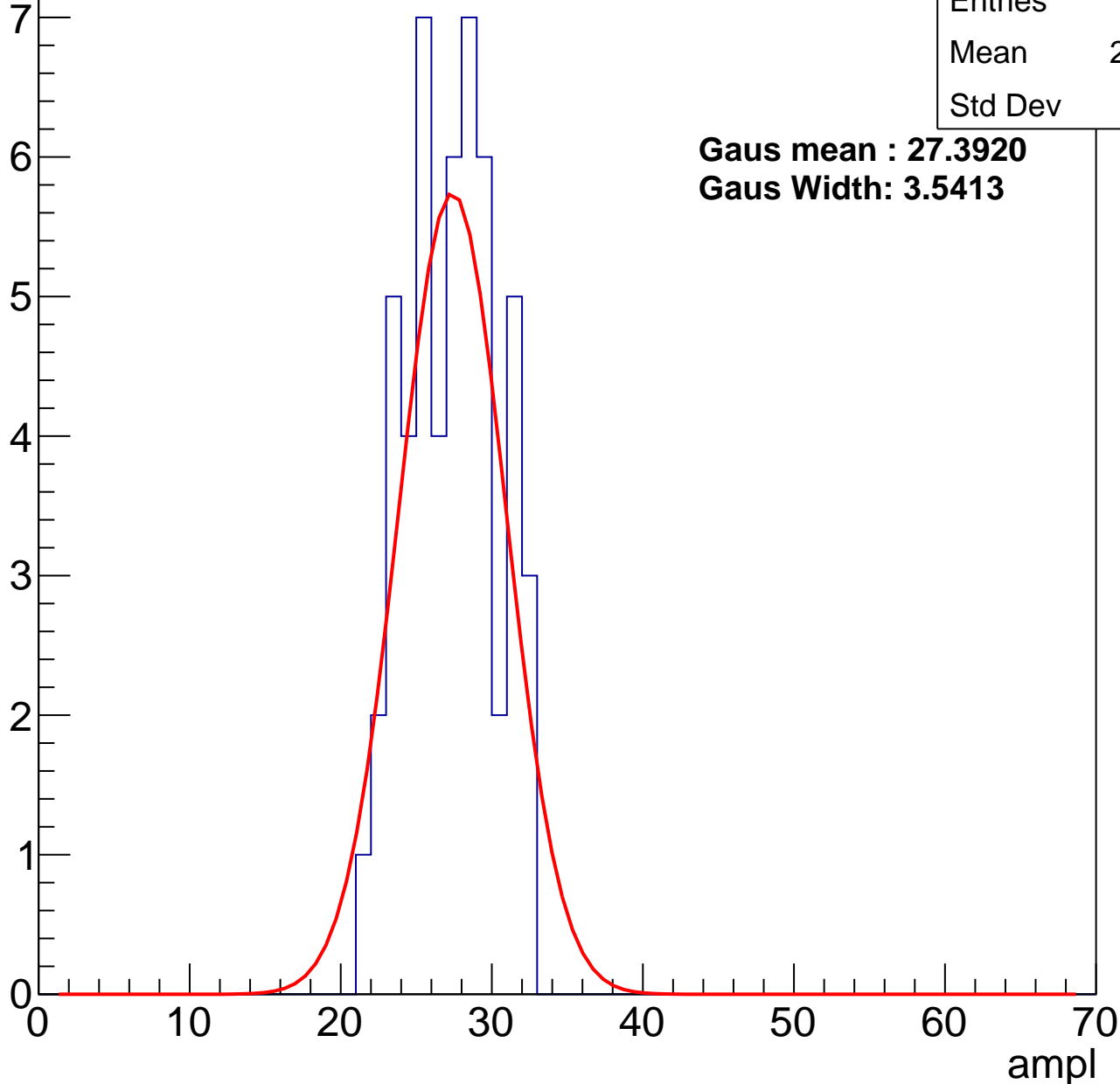
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	26.88
Std Dev	2.9

**Gaus mean : 27.3920**

**Gaus Width: 3.5413**



# B1L002S, U17-ch49, adc1

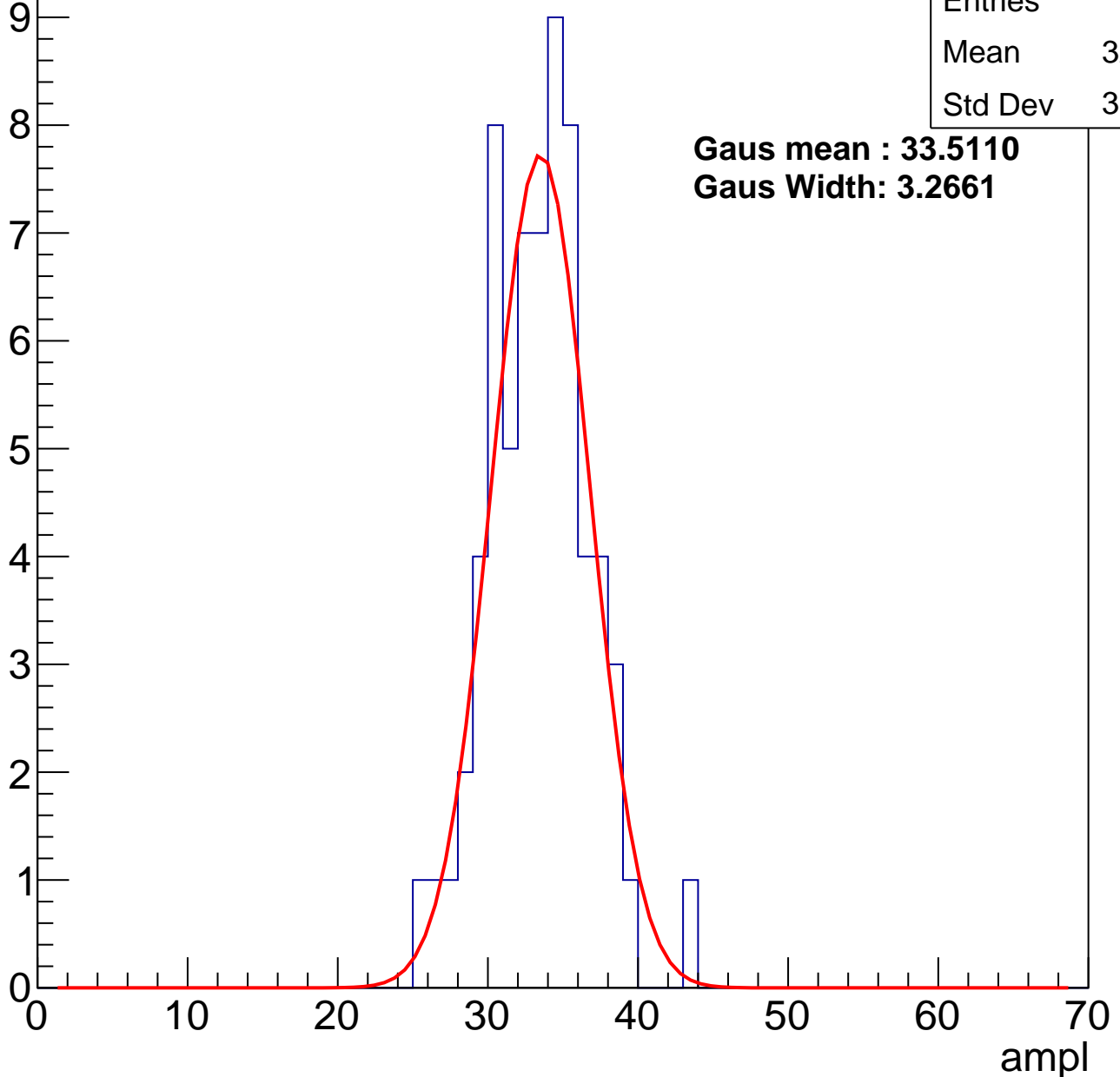
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	32.94
Std Dev	3.289

**Gaus mean : 33.5110**

**Gaus Width: 3.2661**



# B1L002S, U17-ch49, adc2

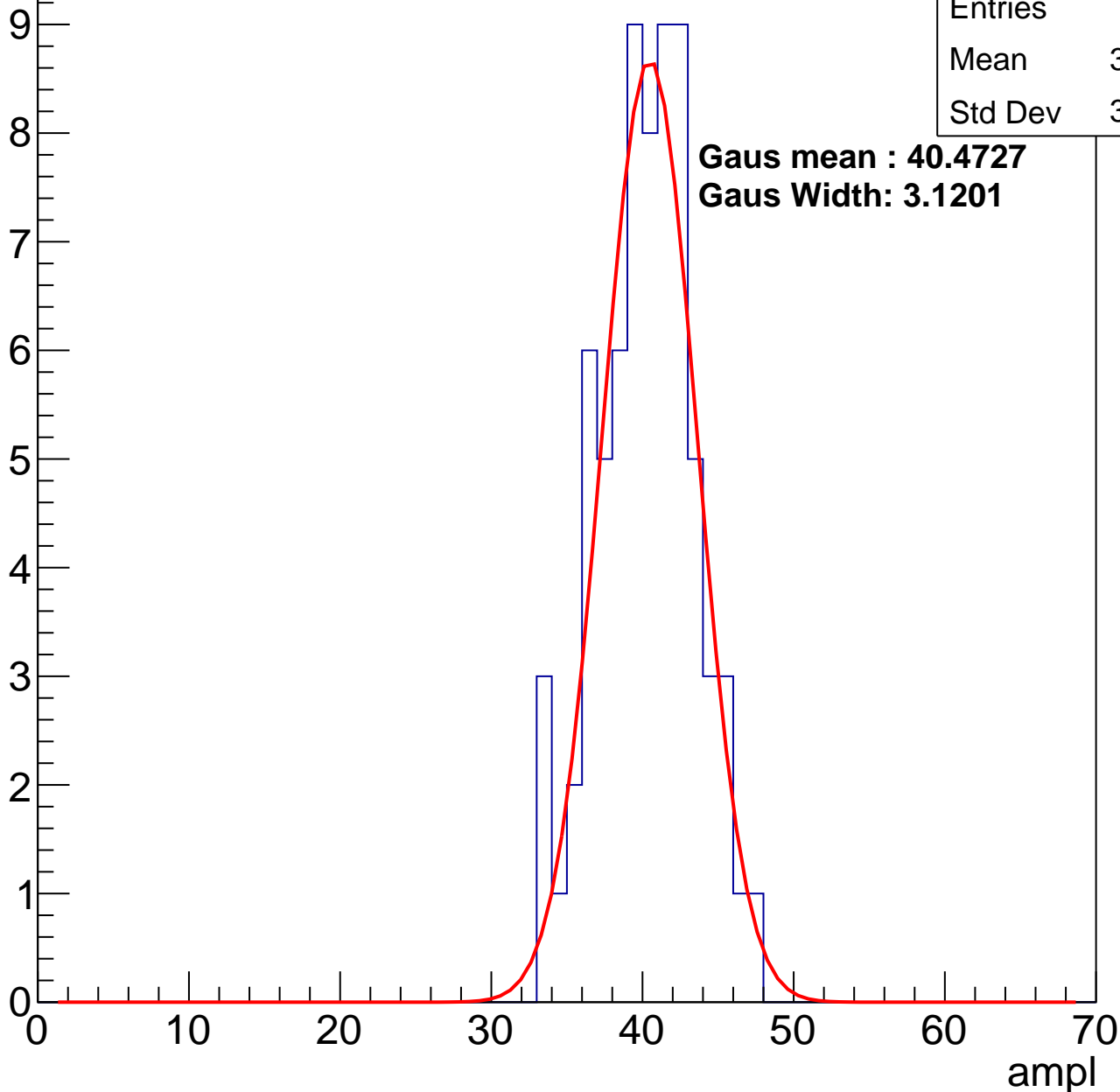
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	39.79
Std Dev	3.144

**Gaus mean : 40.4727**

**Gaus Width: 3.1201**

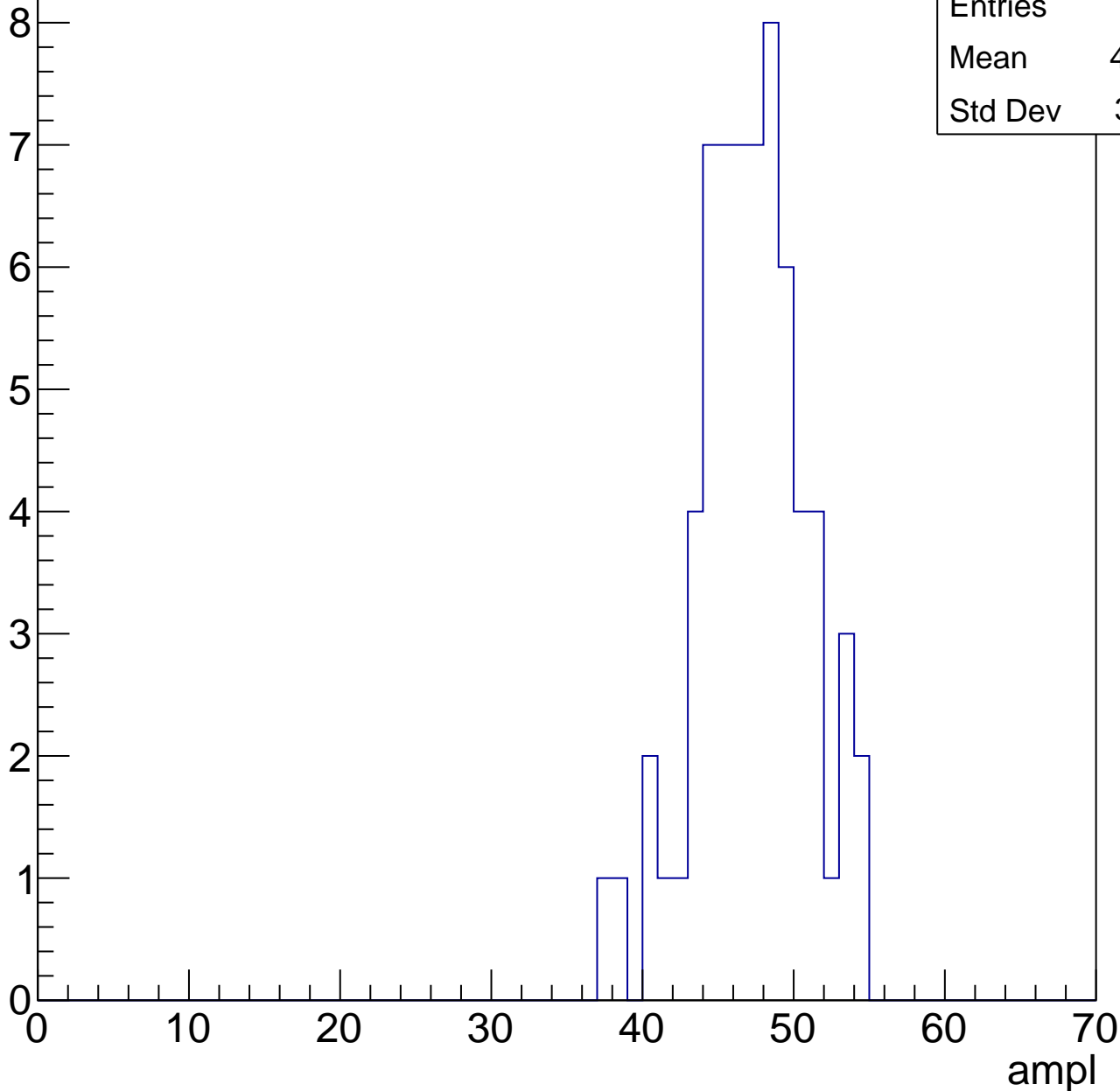


# B1L002S, U17-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.74
Std Dev	3.611

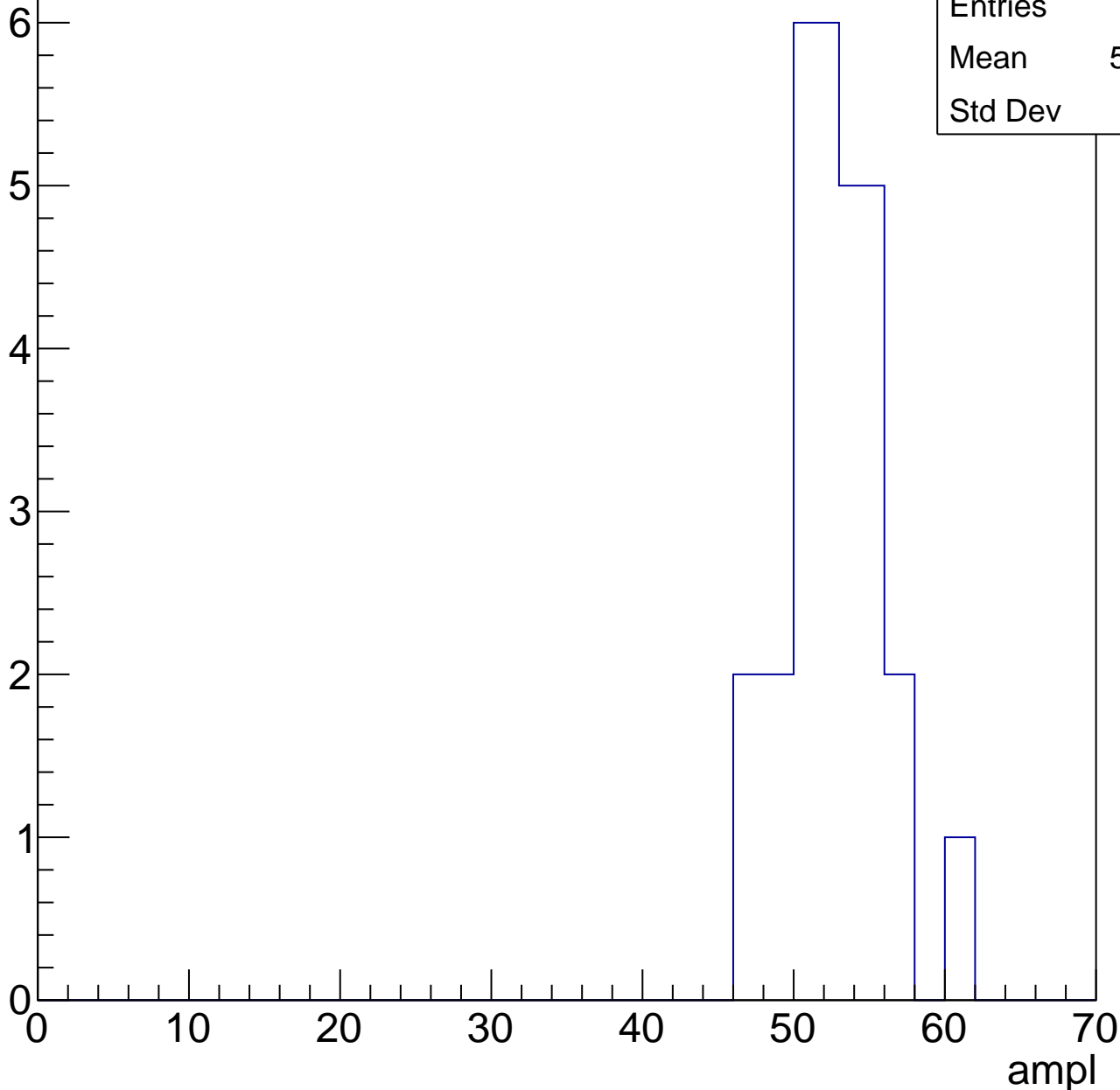


# B1L002S, U17-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

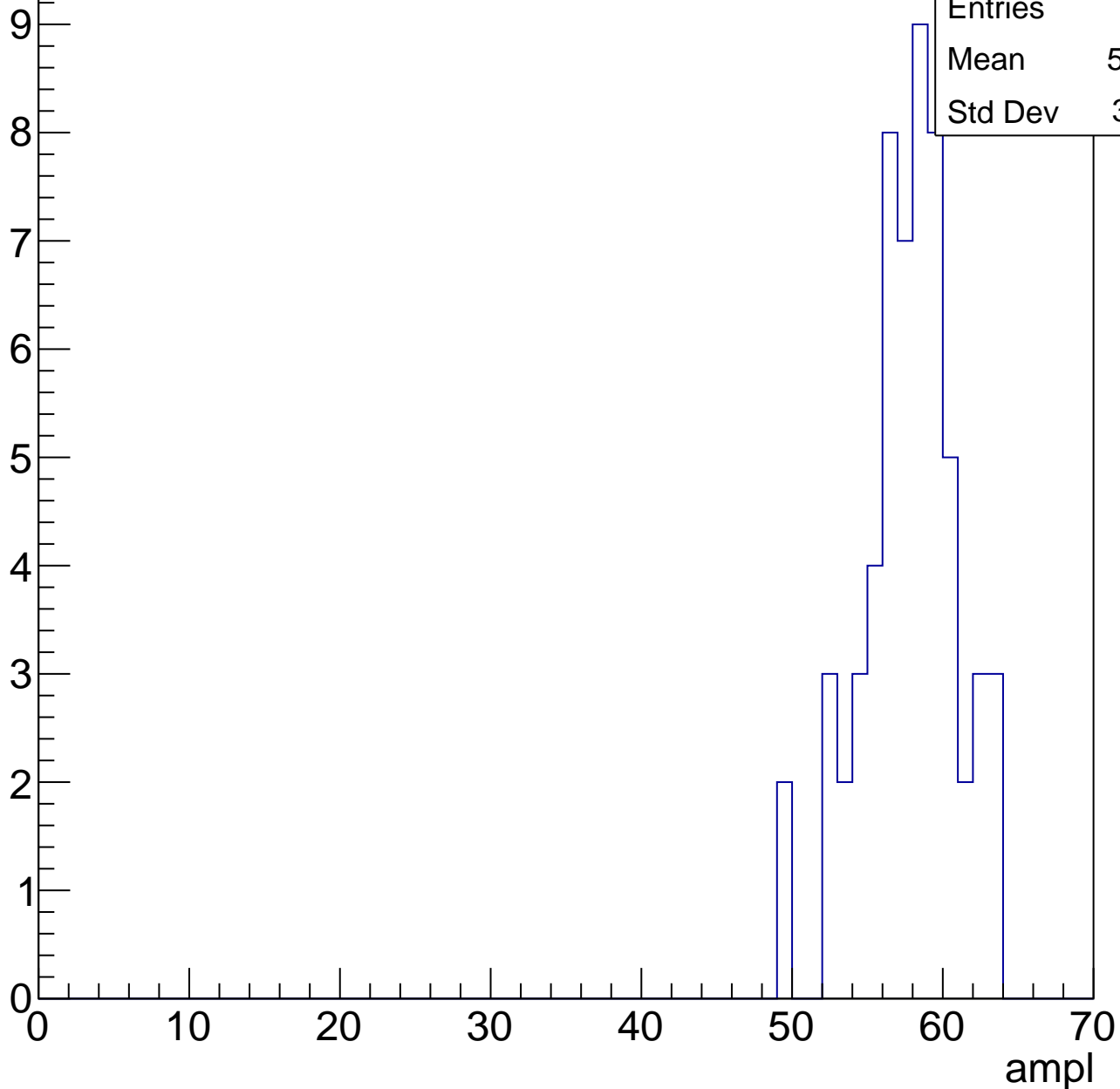
Entries	47
Mean	52.23
Std Dev	3.25



# B1L002S, U17-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



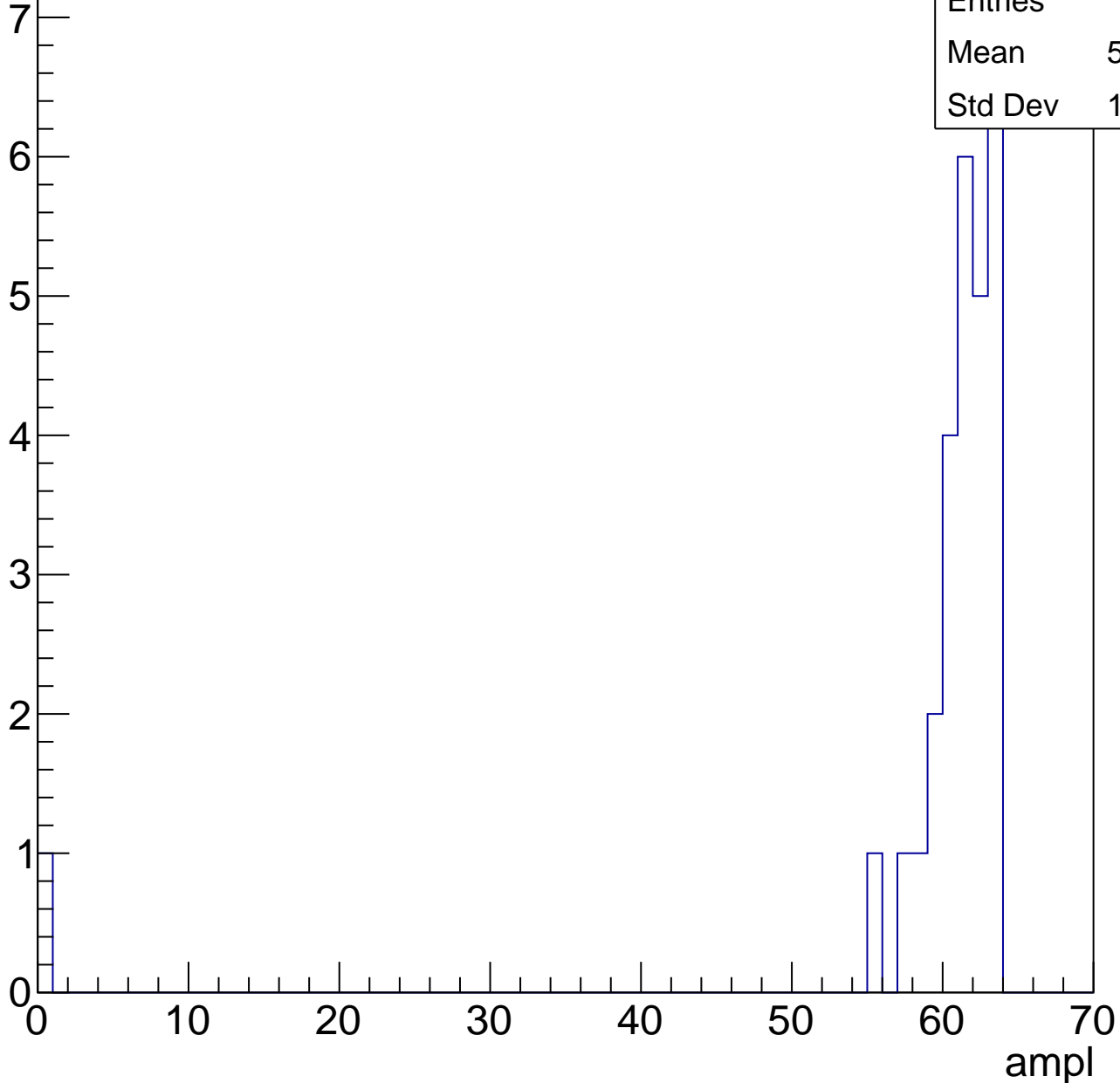
Entries	59
Mean	57.29
Std Dev	3.141

# B1L002S, U17-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.75
Std Dev	11.47





# B1L002S, U17-ch49, adc7

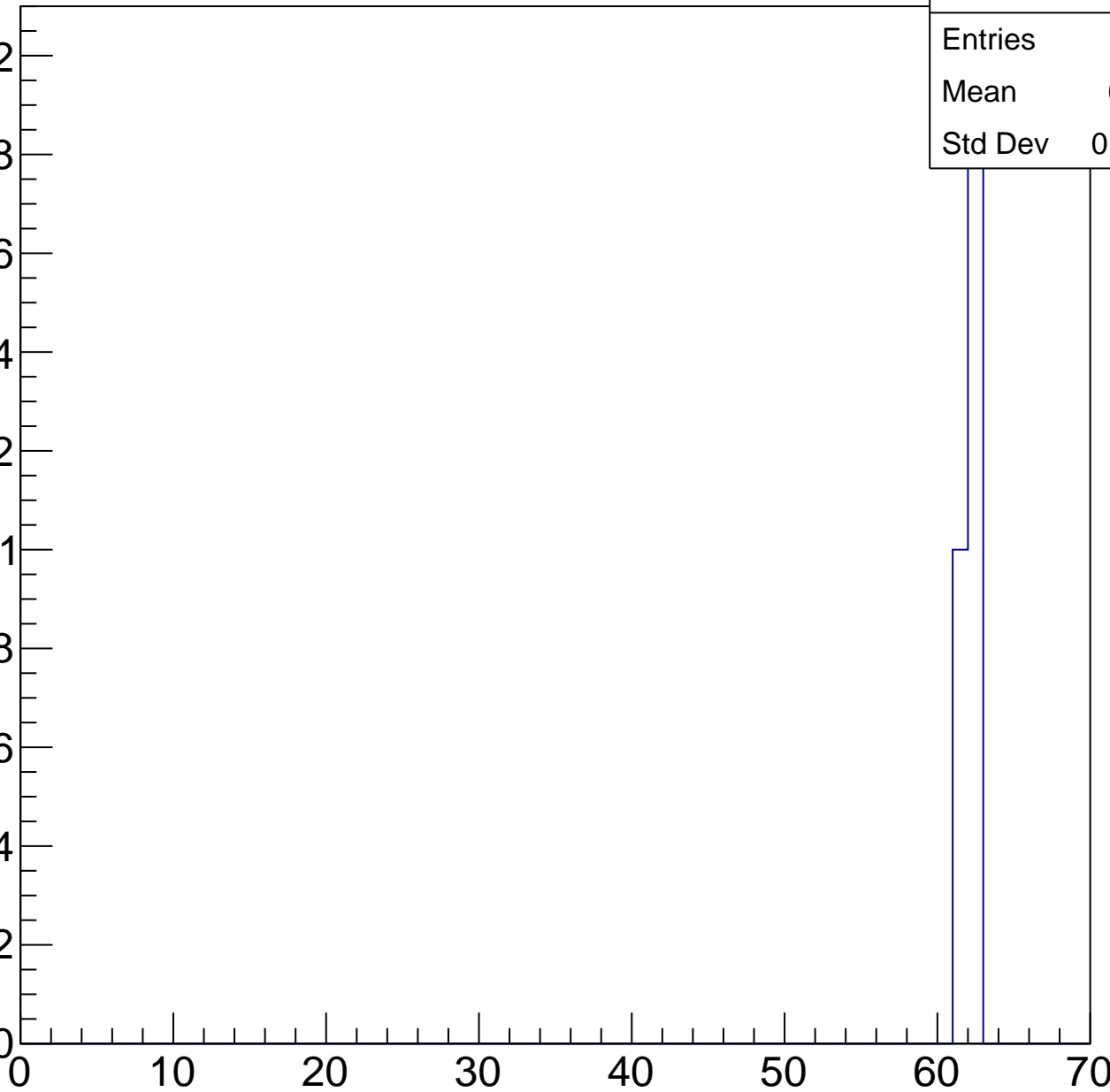
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl



# B1L002S, U17-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	57
Mean	28.09
Std Dev	3.108

**Gaus mean : 29.0469**

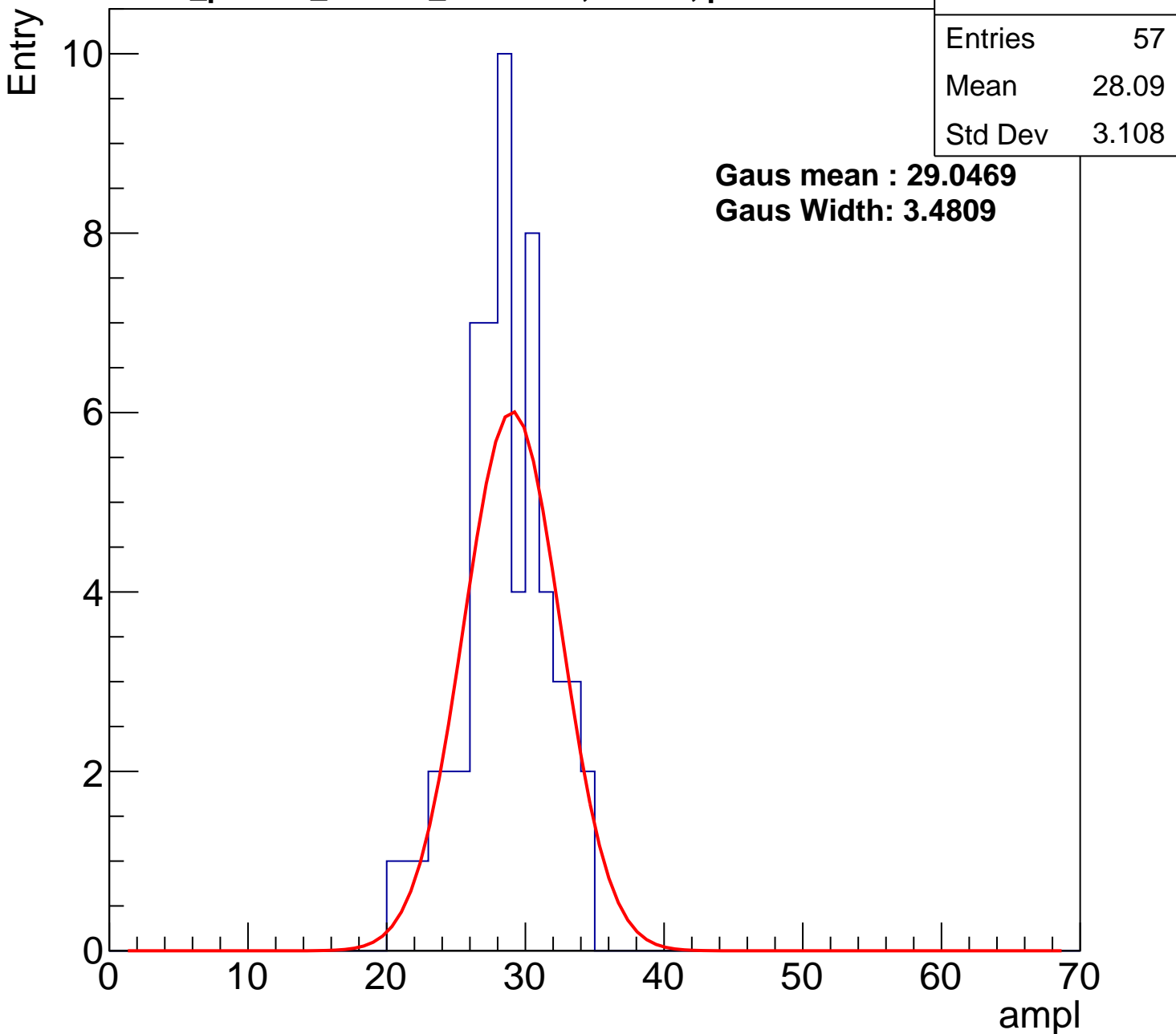
**Gaus Width: 3.4809**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch50, adc1

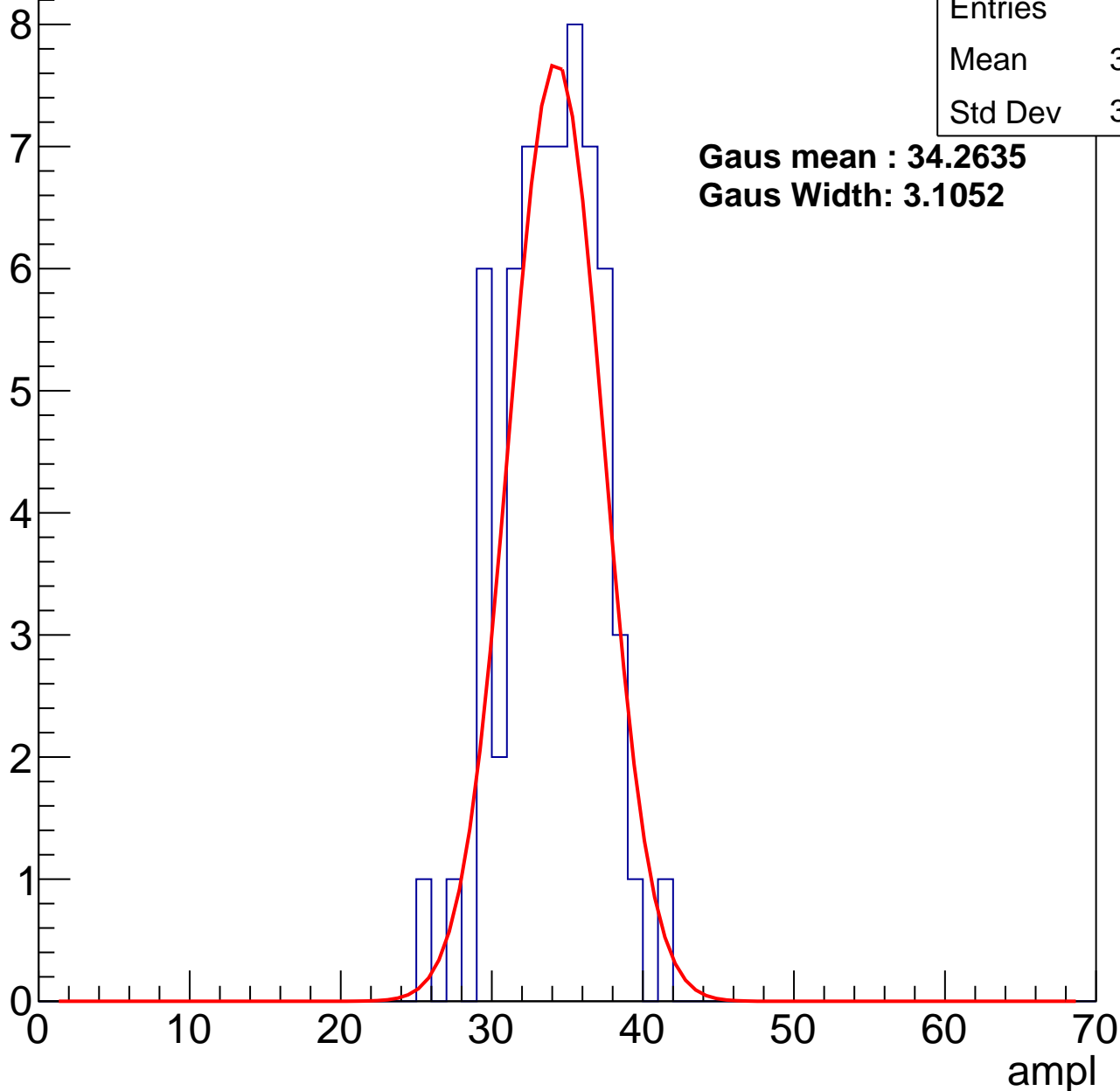
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.54
Std Dev	3.085

**Gaus mean : 34.2635**

**Gaus Width: 3.1052**



# B1L002S, U17-ch50, adc2

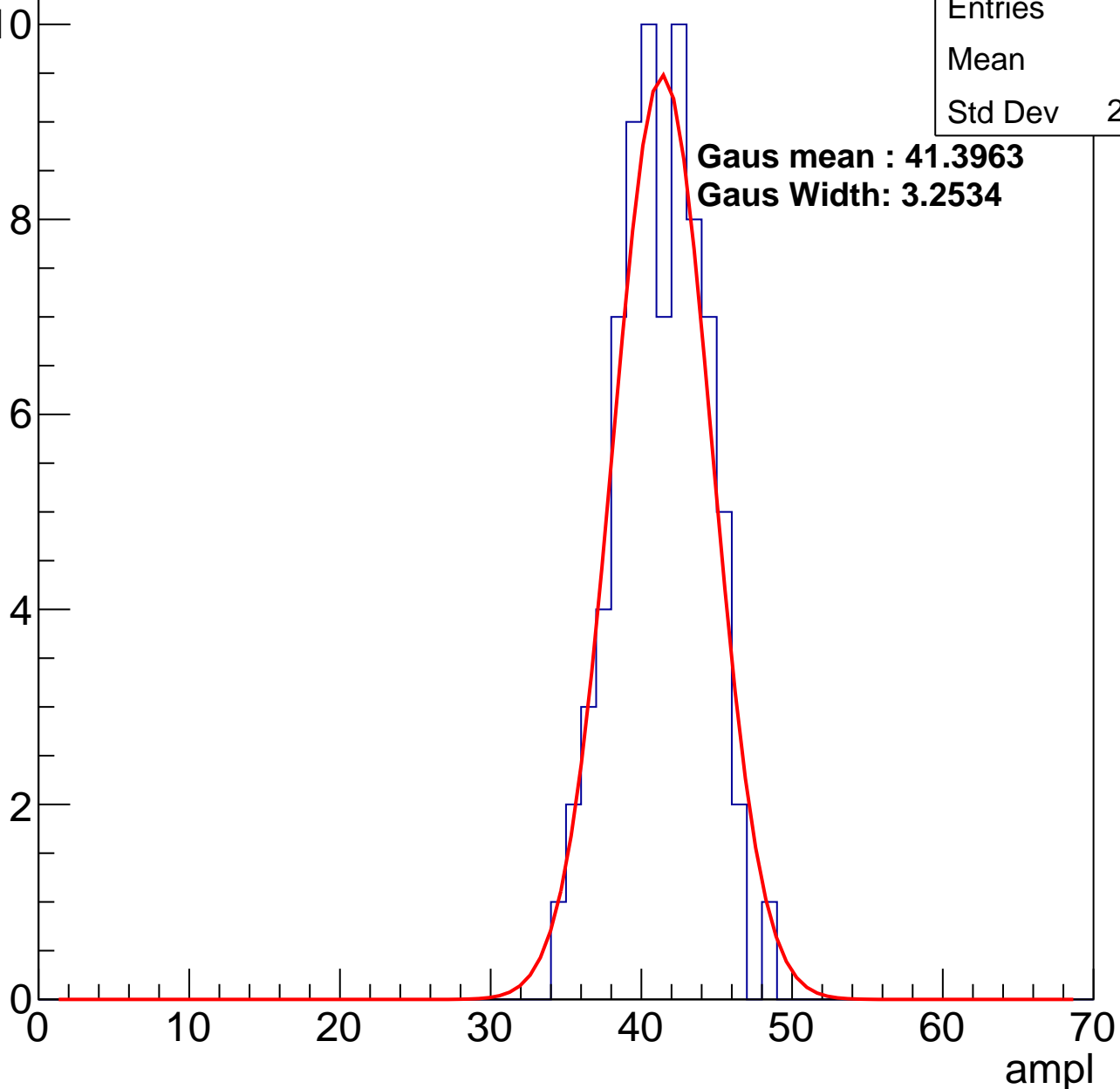
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	40.8
Std Dev	2.925

**Gaus mean : 41.3963**

**Gaus Width: 3.2534**

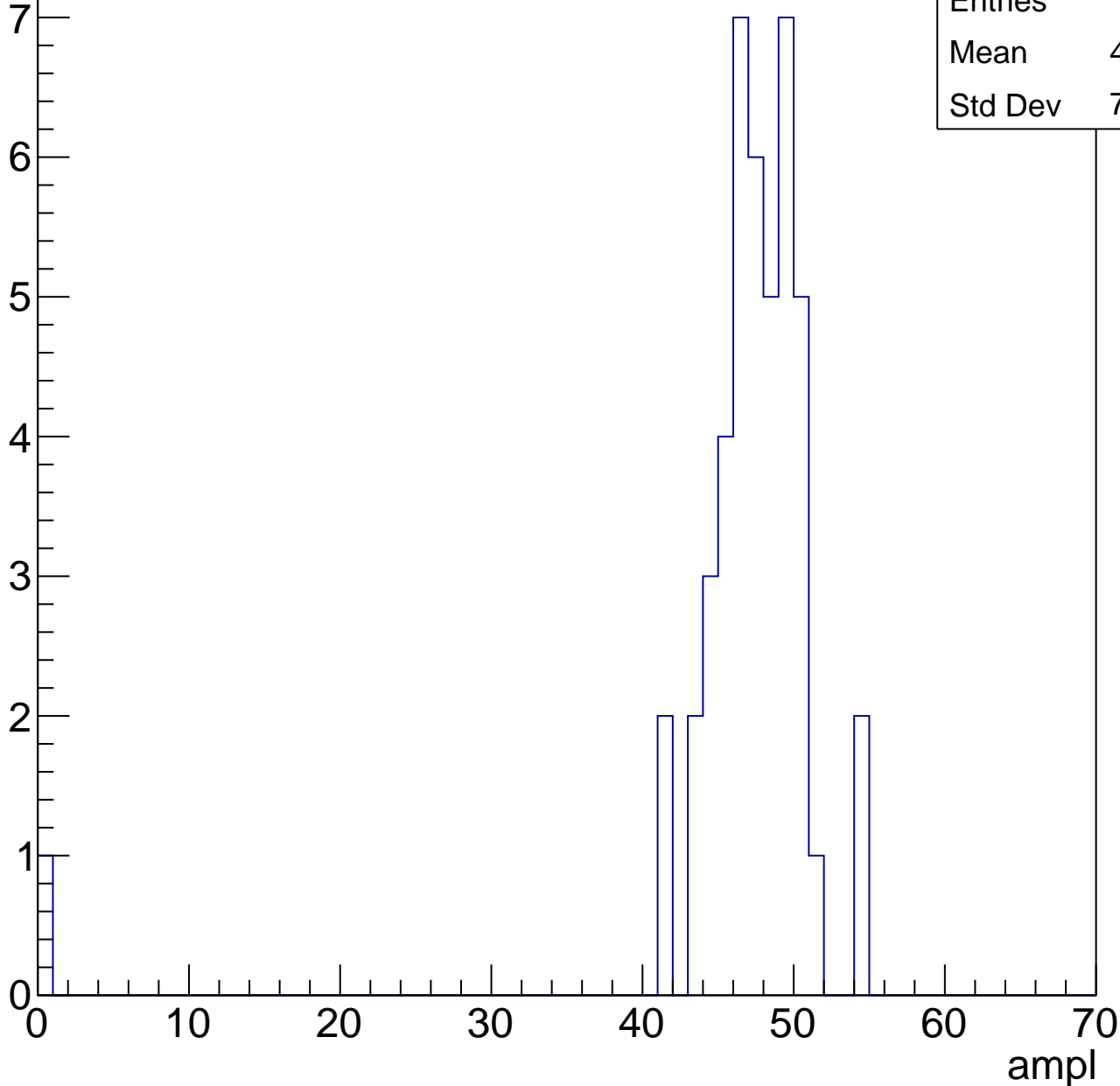


# B1L002S, U17-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	46.13
Std Dev	7.485

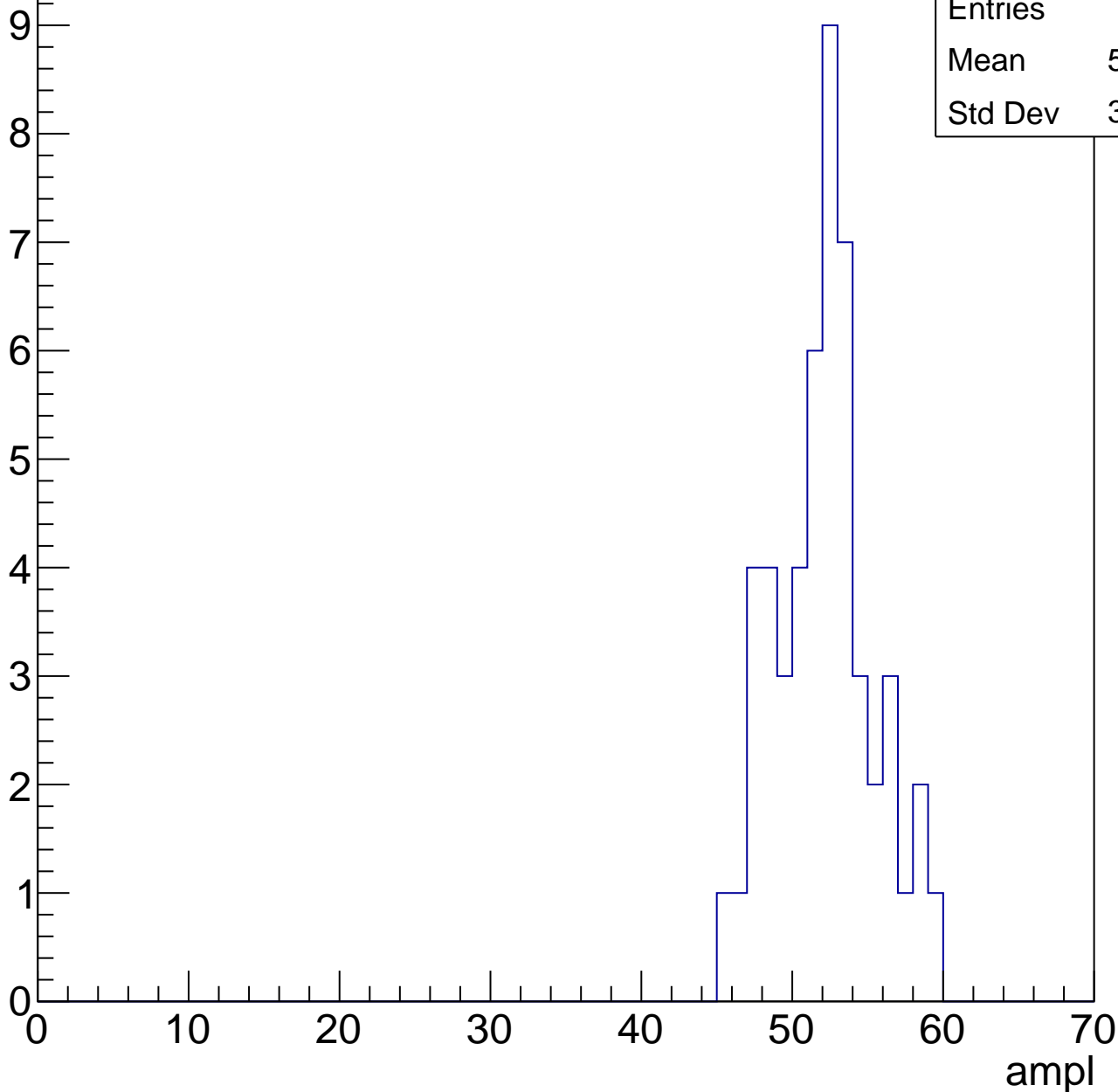


# B1L002S, U17-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

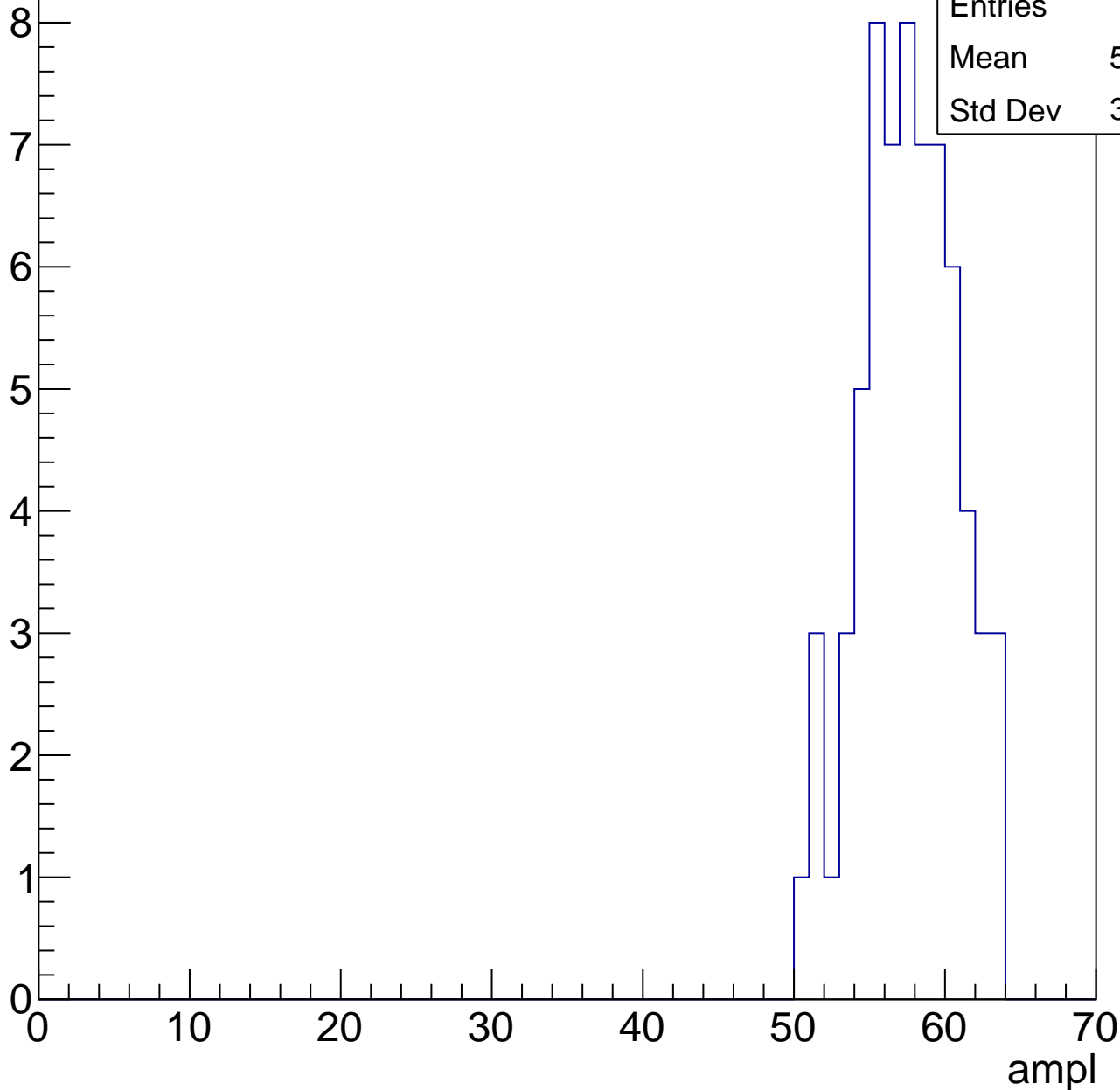
Entries	51
Mean	51.67
Std Dev	3.203



# B1L002S, U17-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

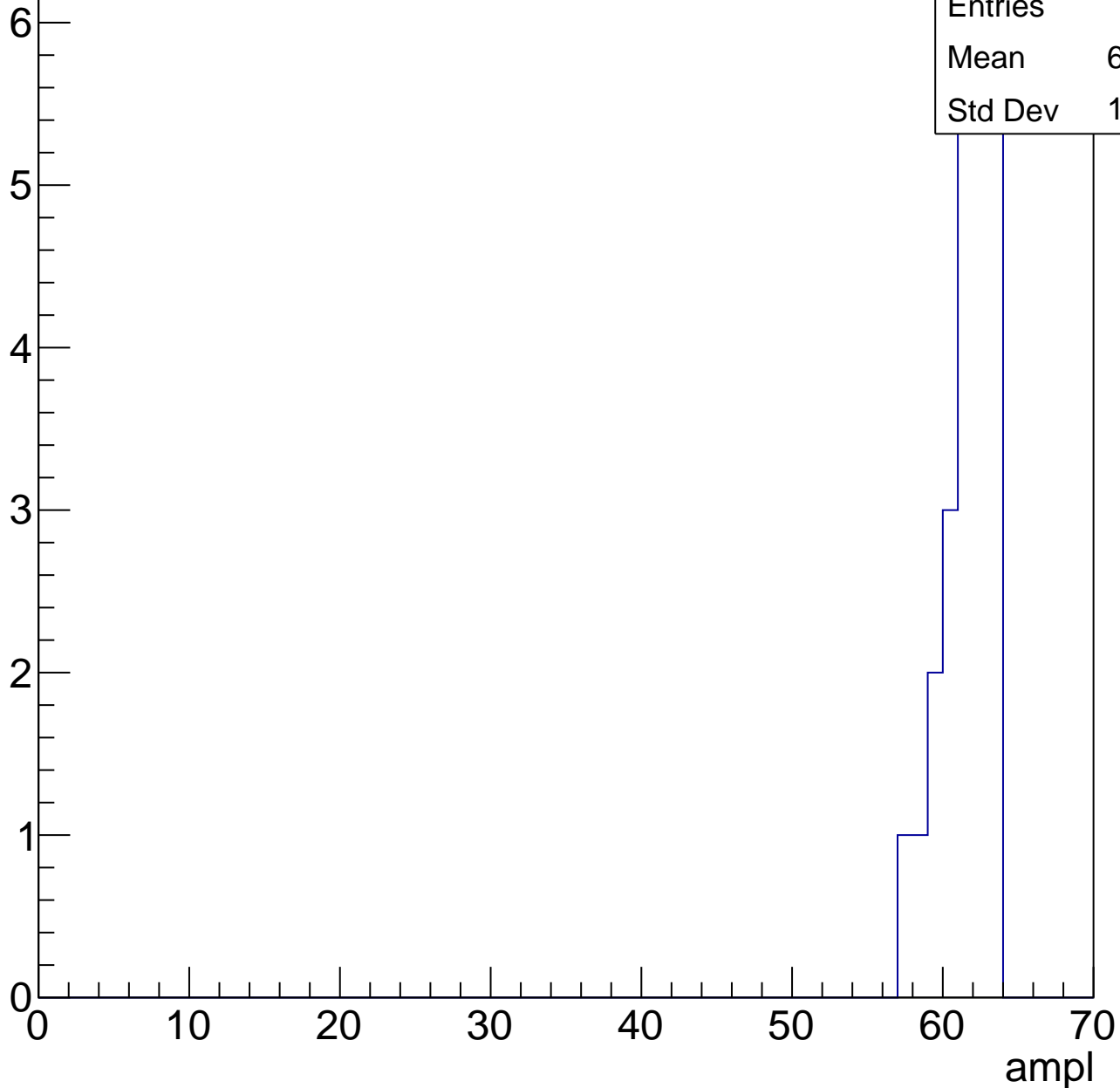


Entries	66
Mean	57.12
Std Dev	3.136

# B1L002S, U17-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

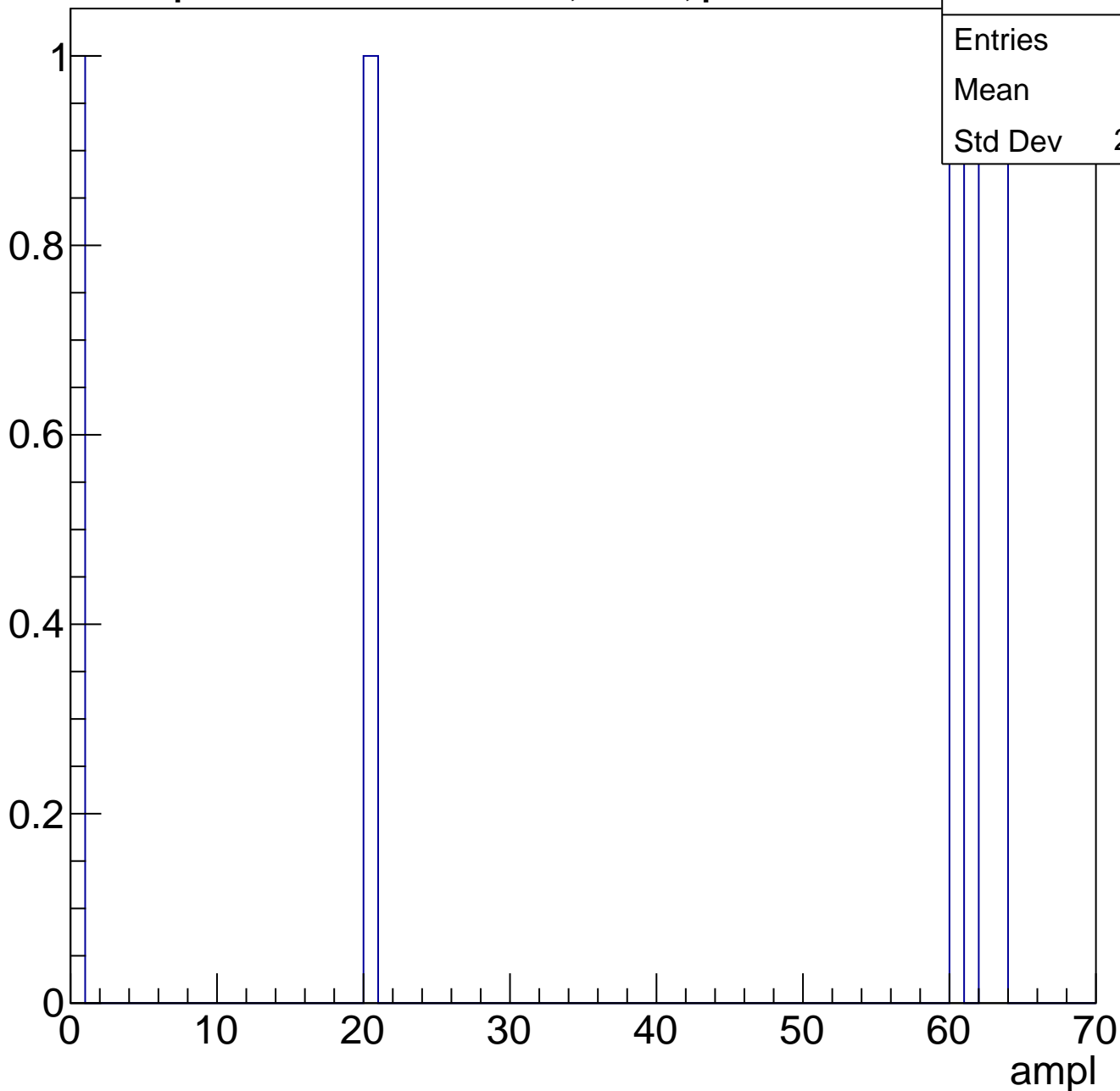




# B1L002S, U17-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch51, adc0

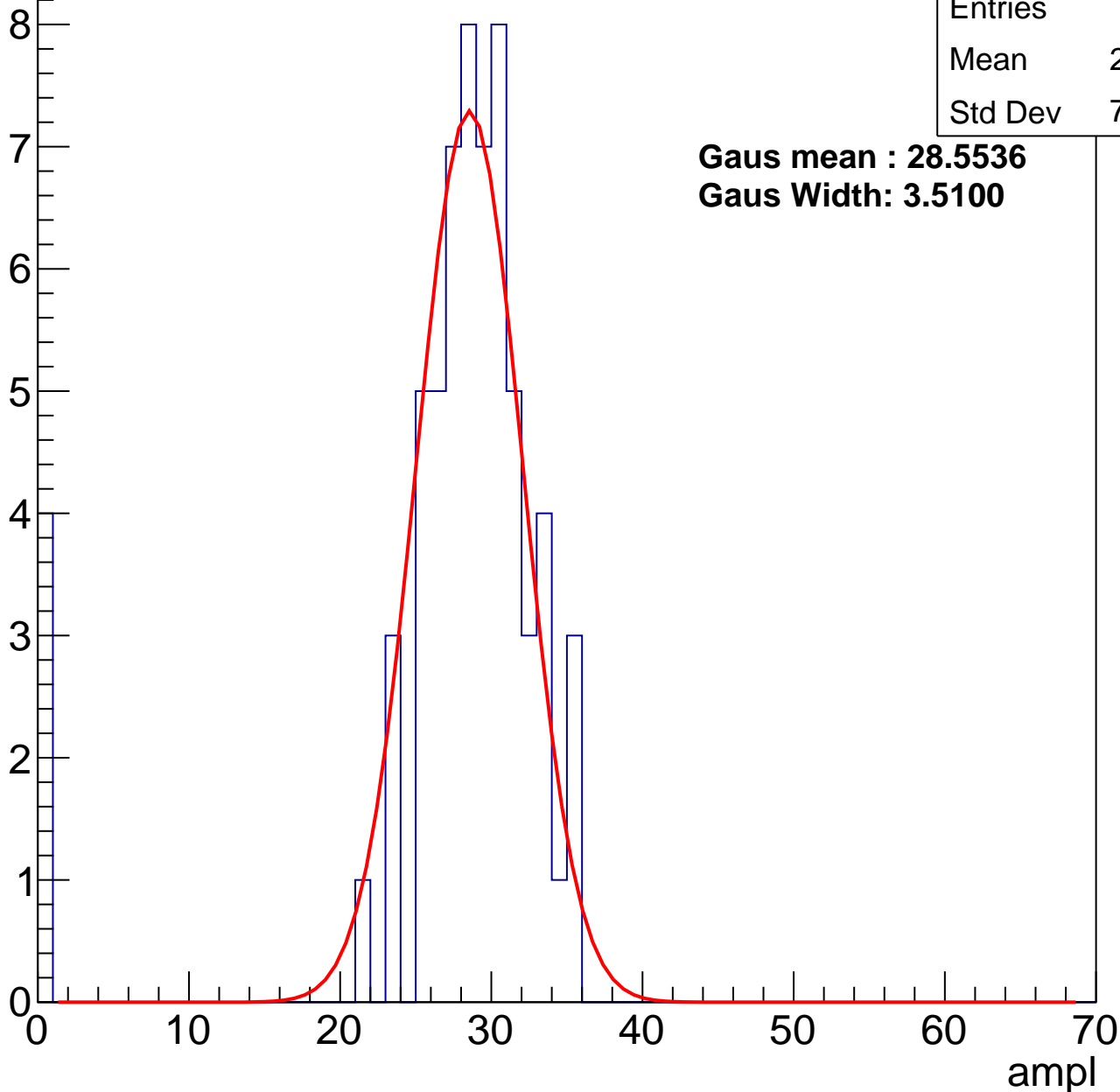
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	26.92
Std Dev	7.575

**Gaus mean : 28.5536**

**Gaus Width: 3.5100**



# B1L002S, U17-ch51, adc1

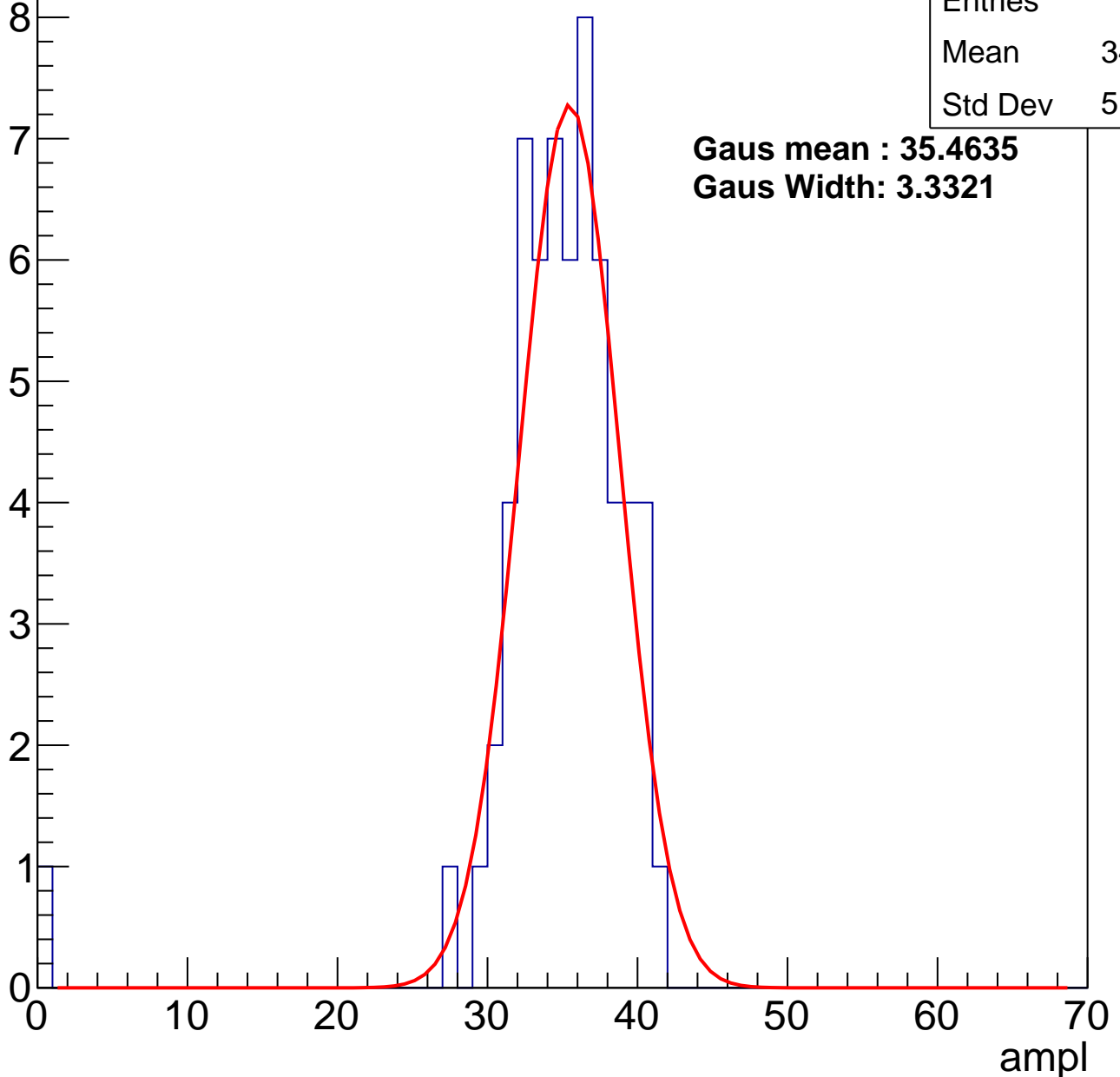
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.34
Std Dev	5.349

**Gaus mean : 35.4635**

**Gaus Width: 3.3321**



# B1L002S, U17-ch51, adc2

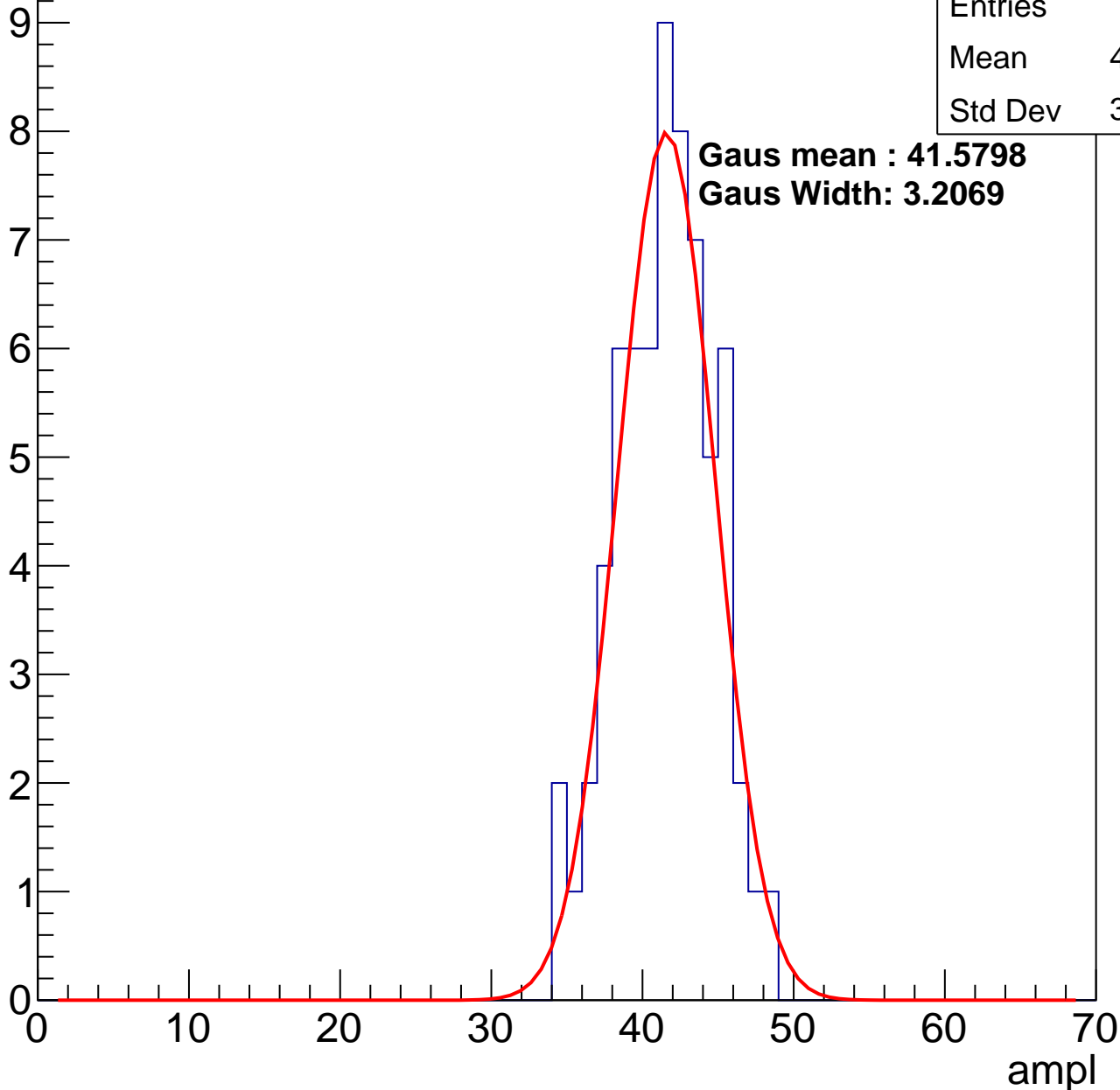
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.03
Std Dev	3.124

**Gaus mean : 41.5798**

**Gaus Width: 3.2069**



# B1L002S, U17-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	48.09
Std Dev	3.248

Entry

10

8

6

4

2

0

0

10

20

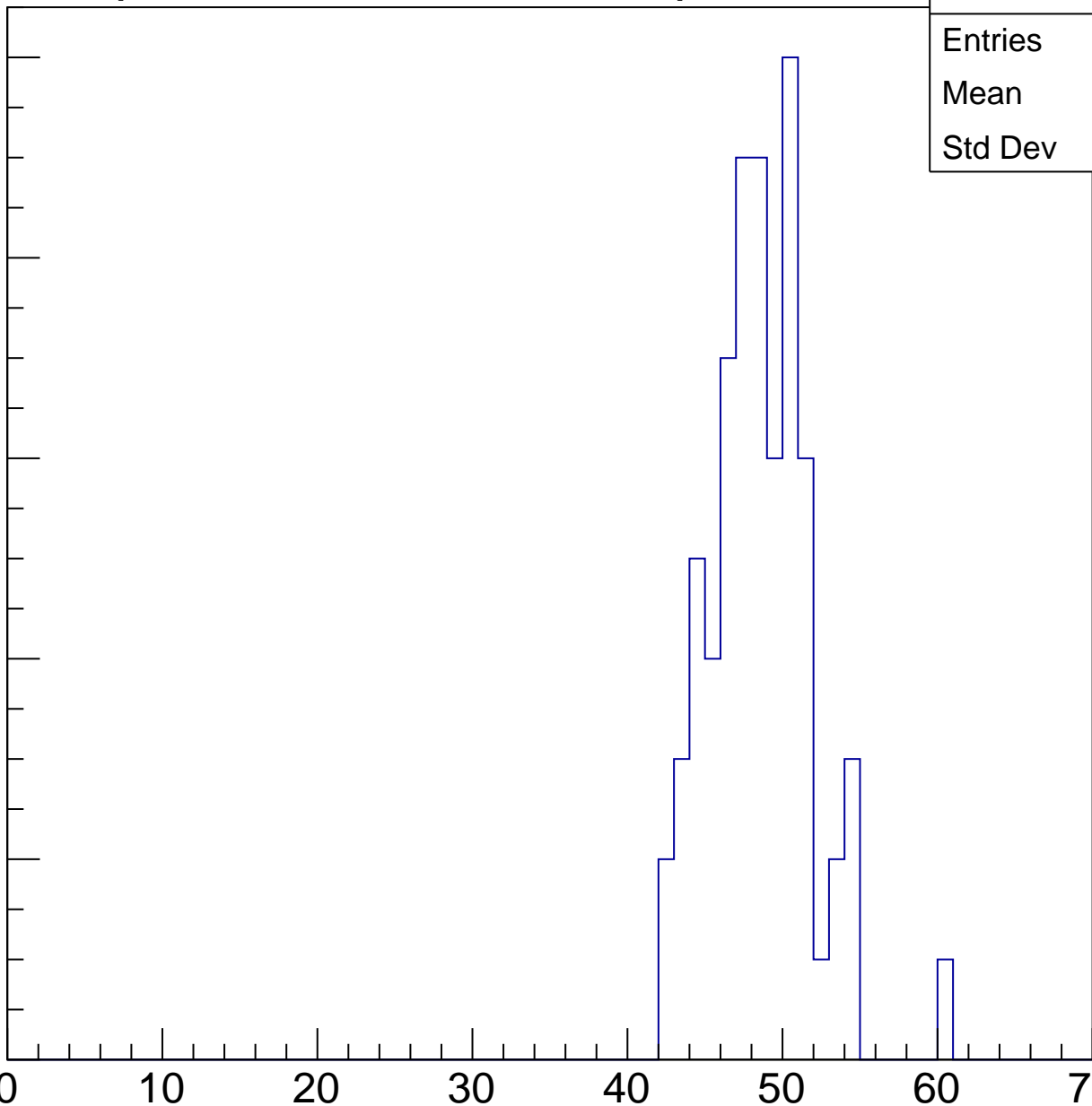
30

40

50

60

ampl

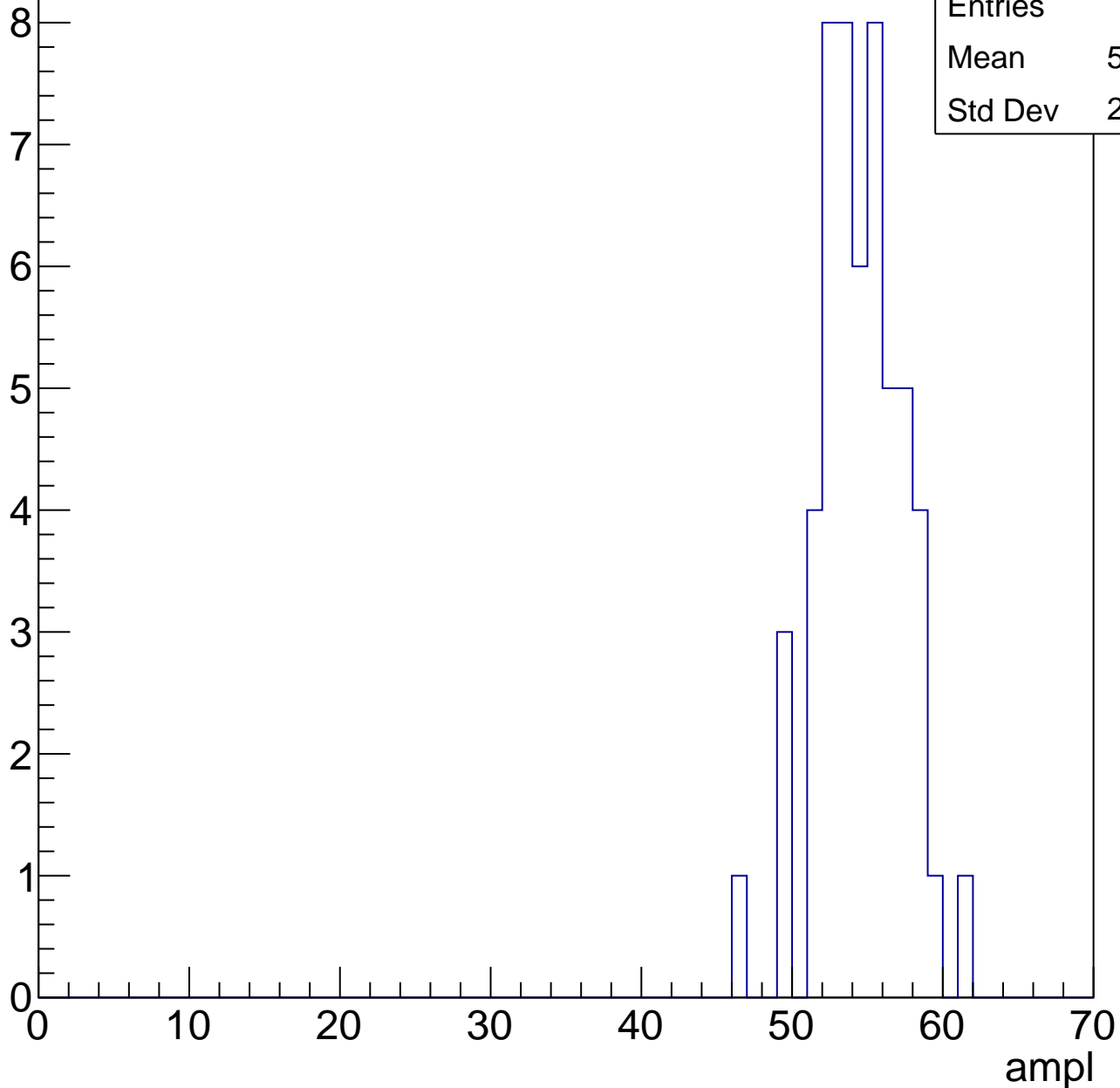


# B1L002S, U17-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	54.04
Std Dev	2.808

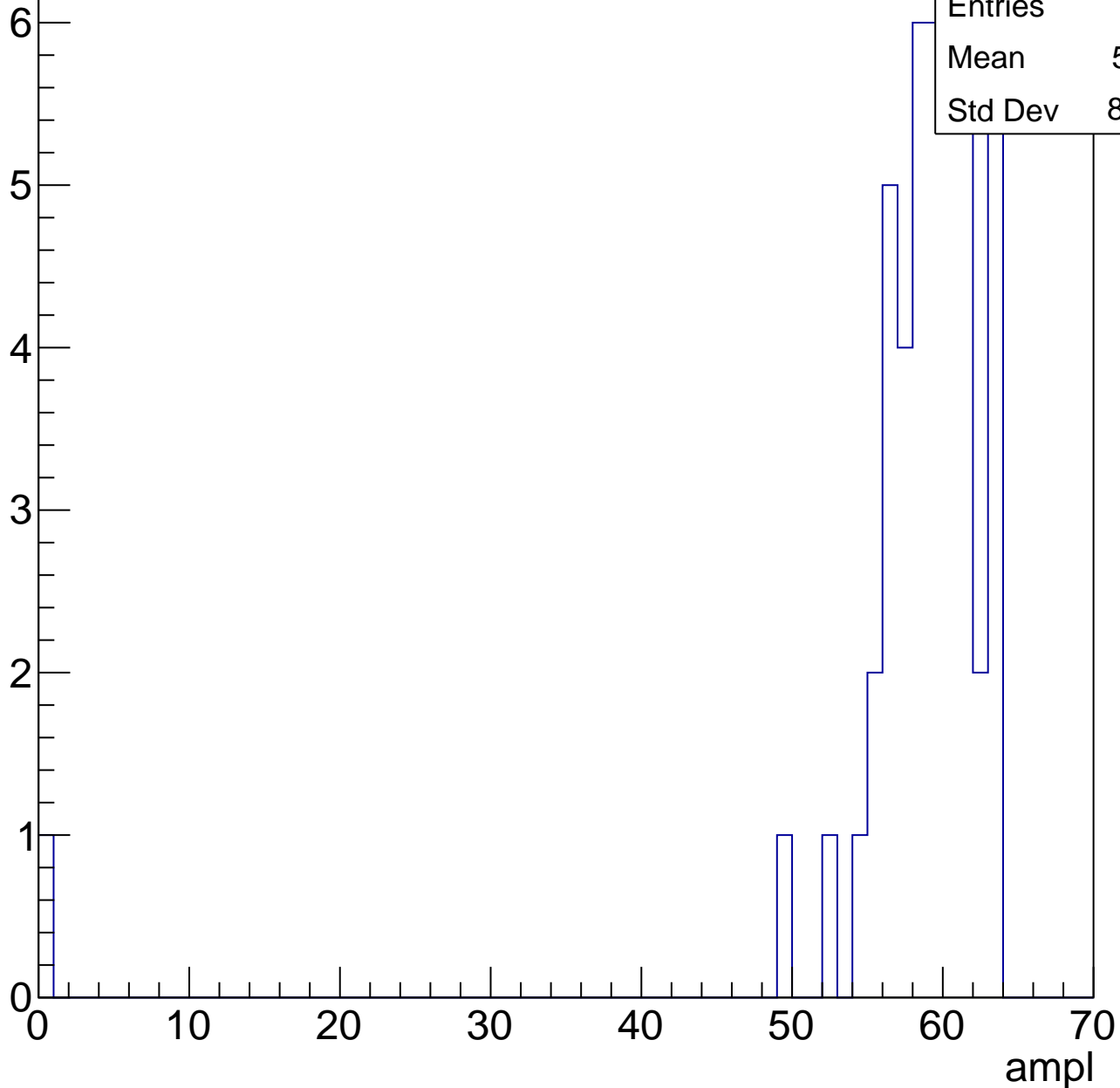


# B1L002S, U17-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.51
Std Dev	8.984

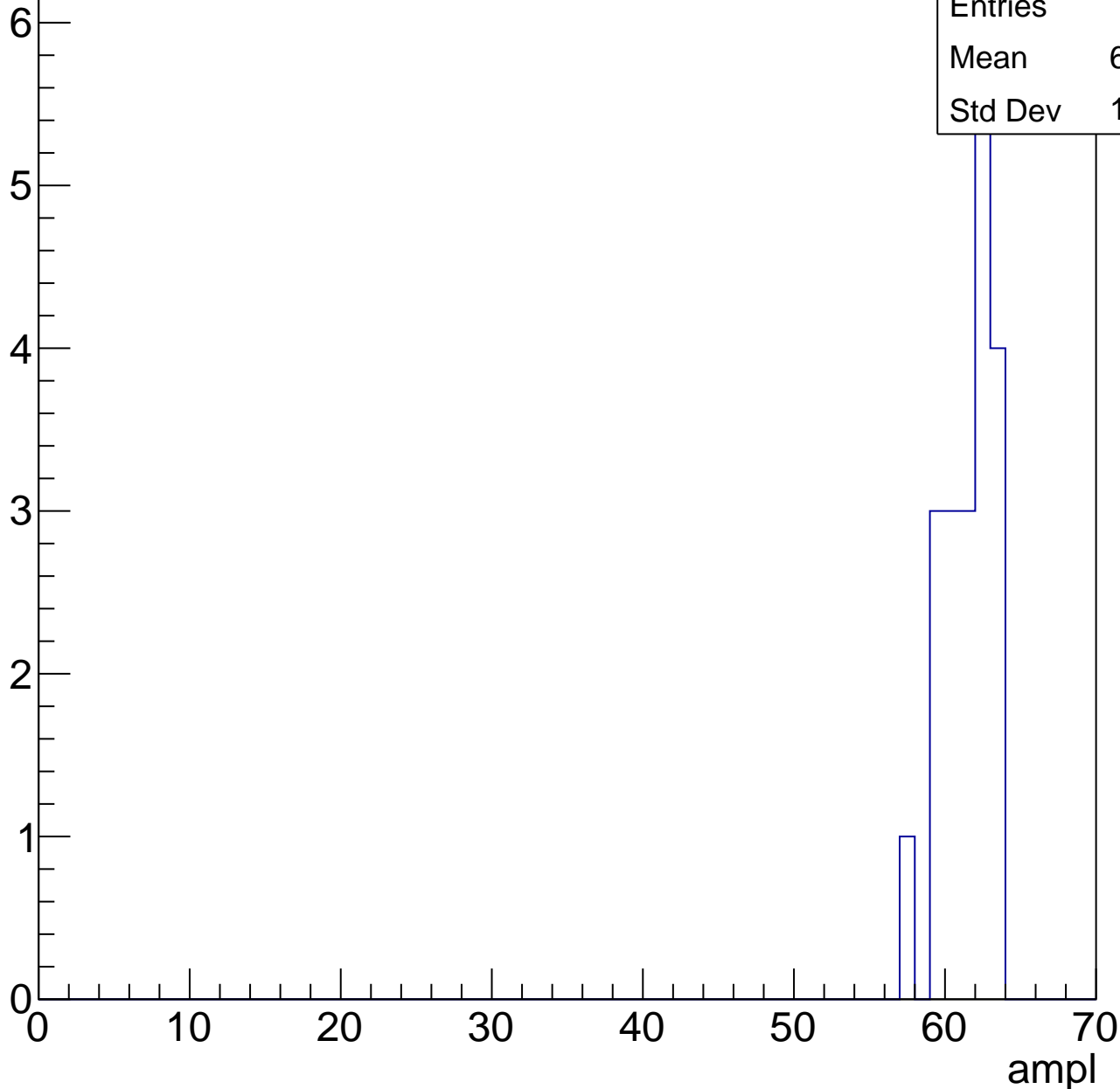


# B1L002S, U17-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.05
Std Dev	1.627

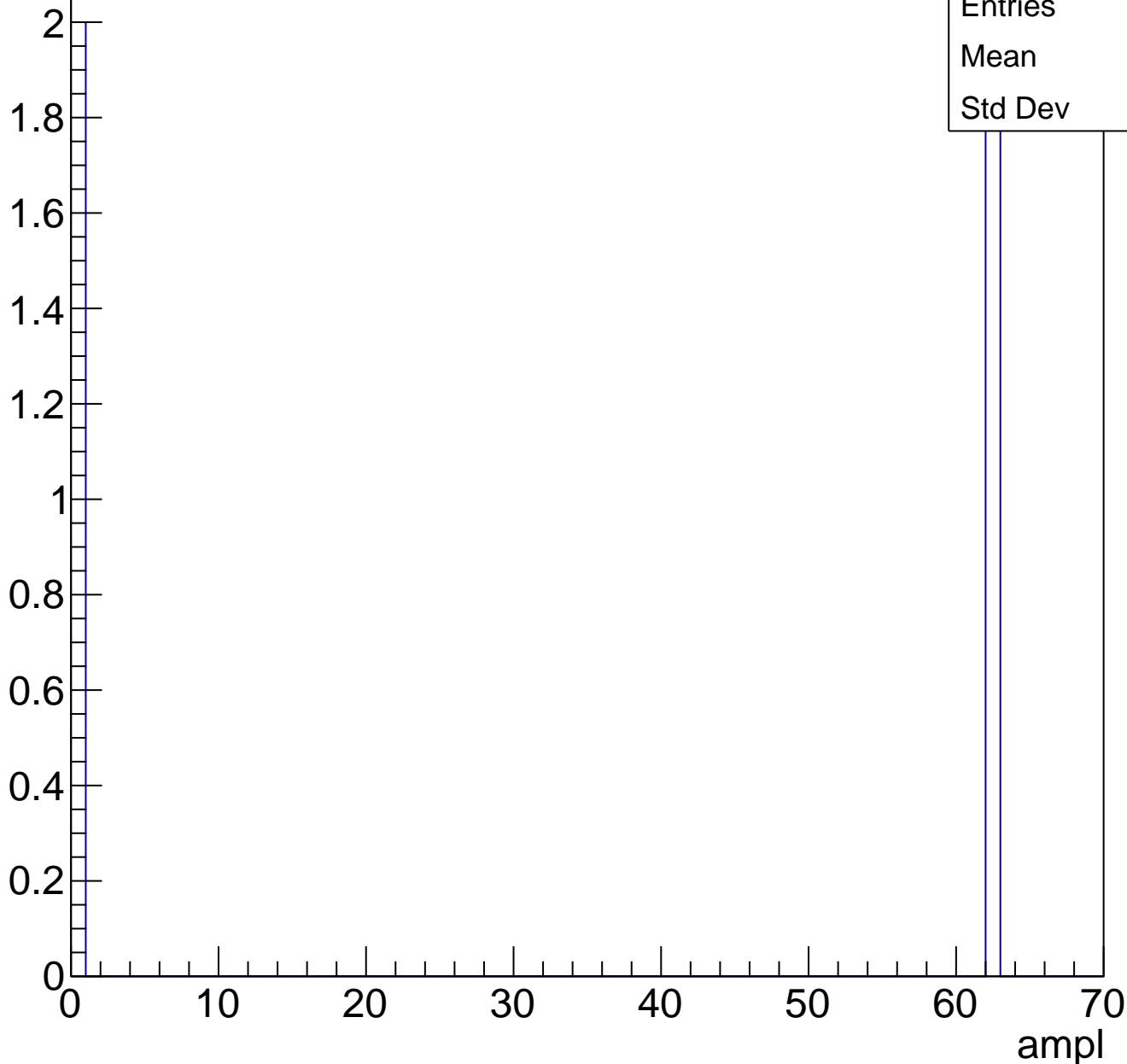




# B1L002S, U17-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch52, adc0

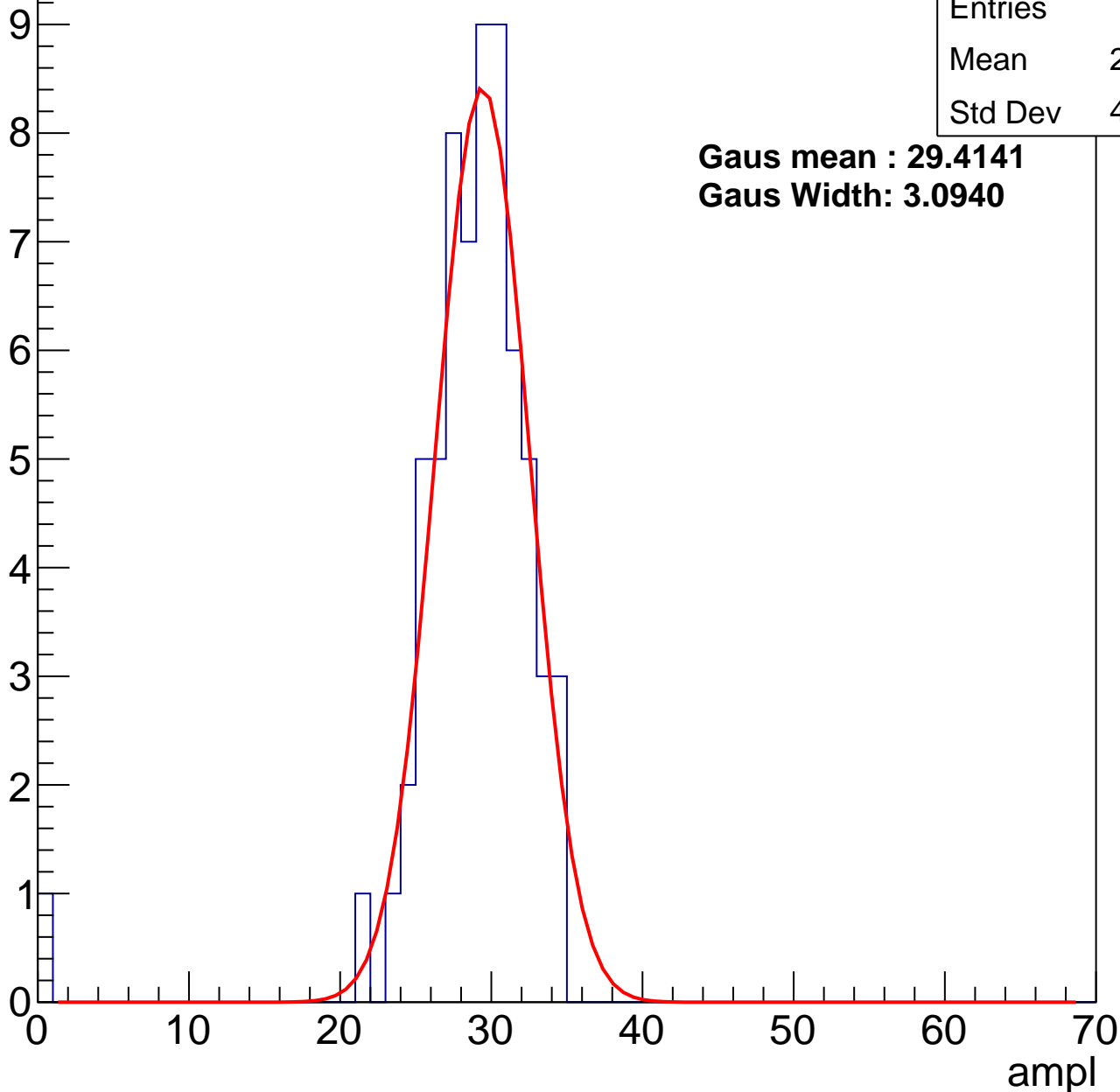
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.26
Std Dev	4.507

**Gaus mean : 29.4141**

**Gaus Width: 3.0940**



# B1L002S, U17-ch52, adc1

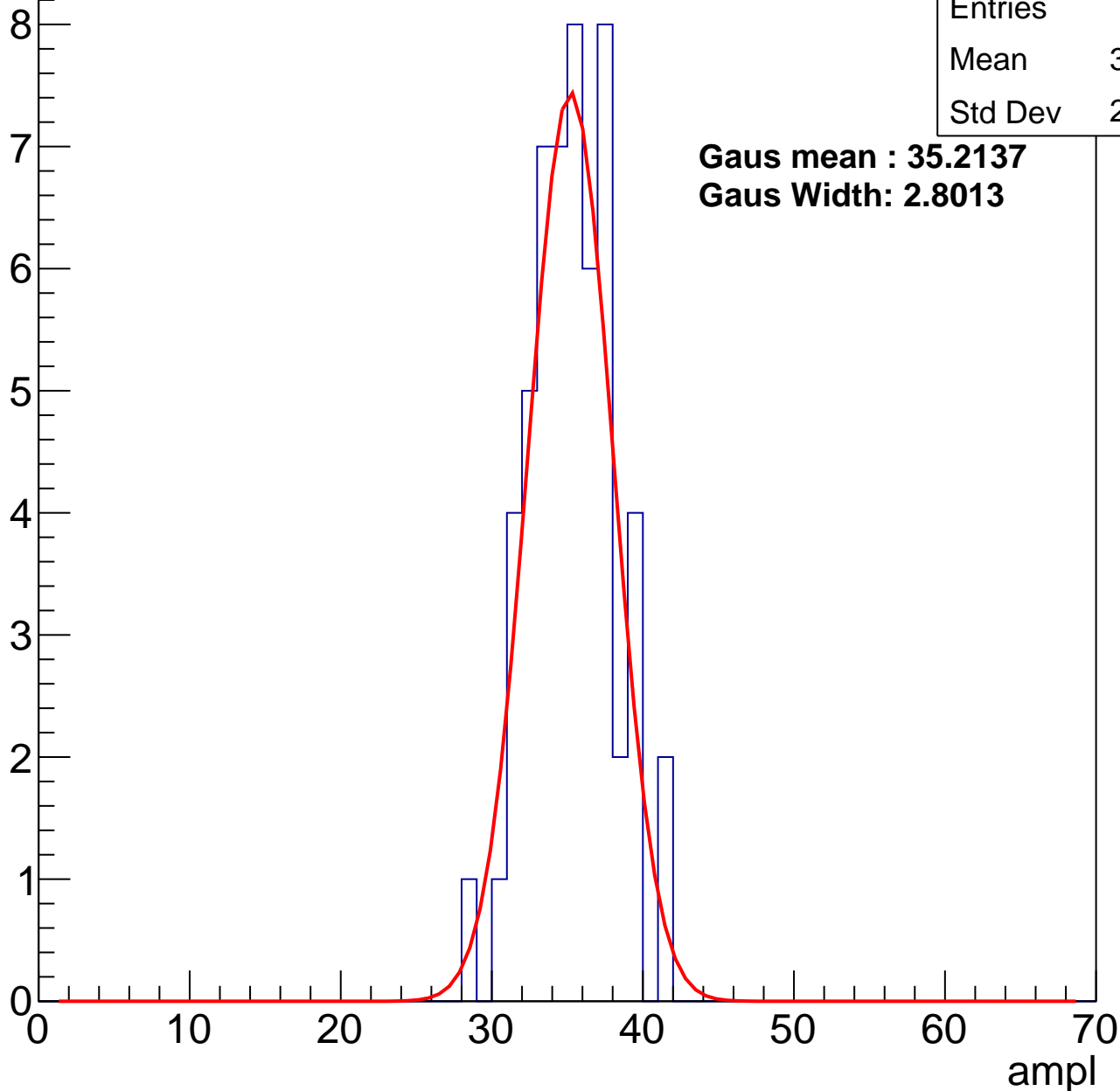
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.85
Std Dev	2.726

**Gaus mean : 35.2137**

**Gaus Width: 2.8013**



# B1L002S, U17-ch52, adc2

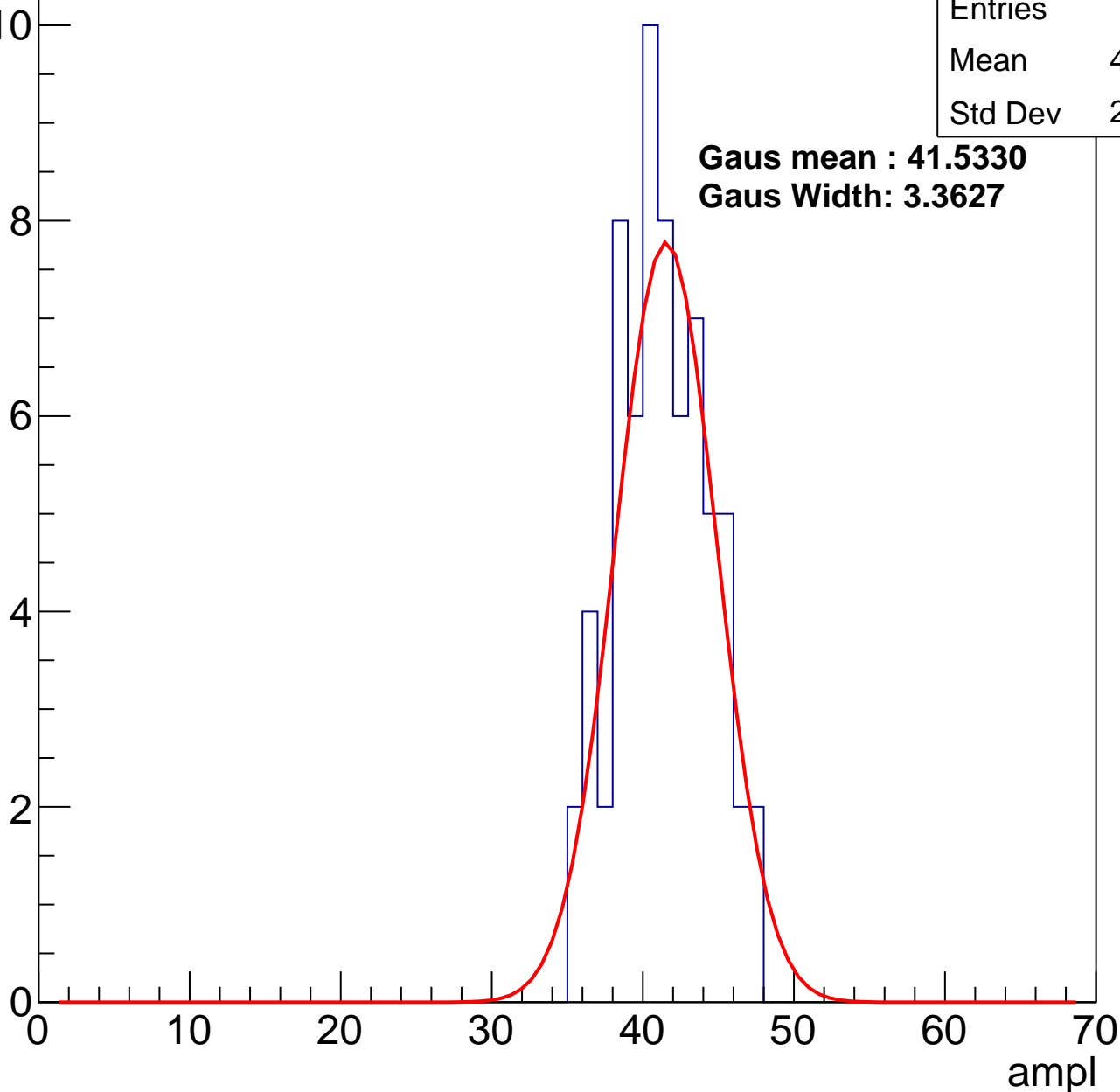
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.87
Std Dev	2.967

**Gaus mean : 41.5330**

**Gaus Width: 3.3627**

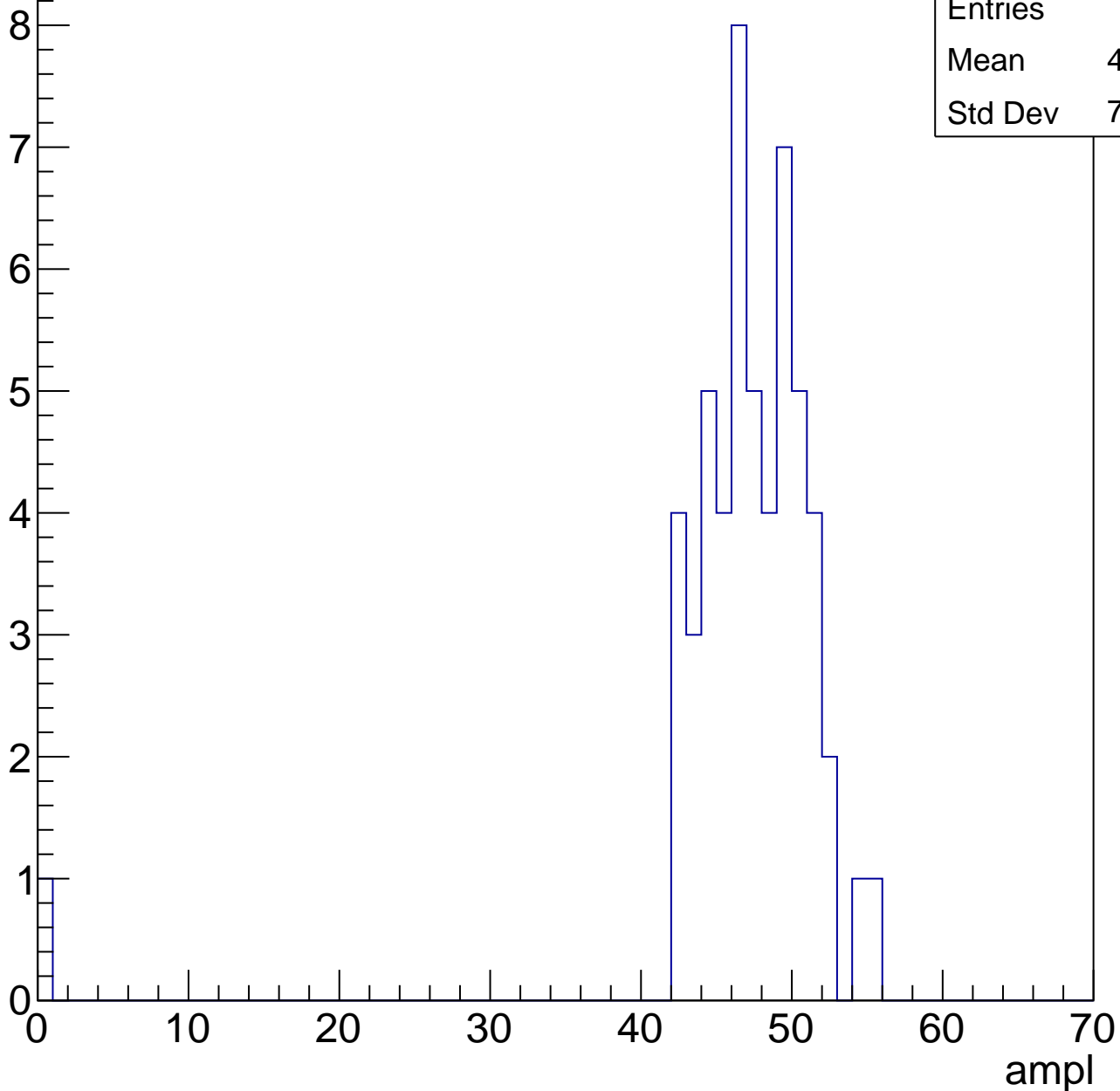


# B1L002S, U17-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	46.33
Std Dev	7.079

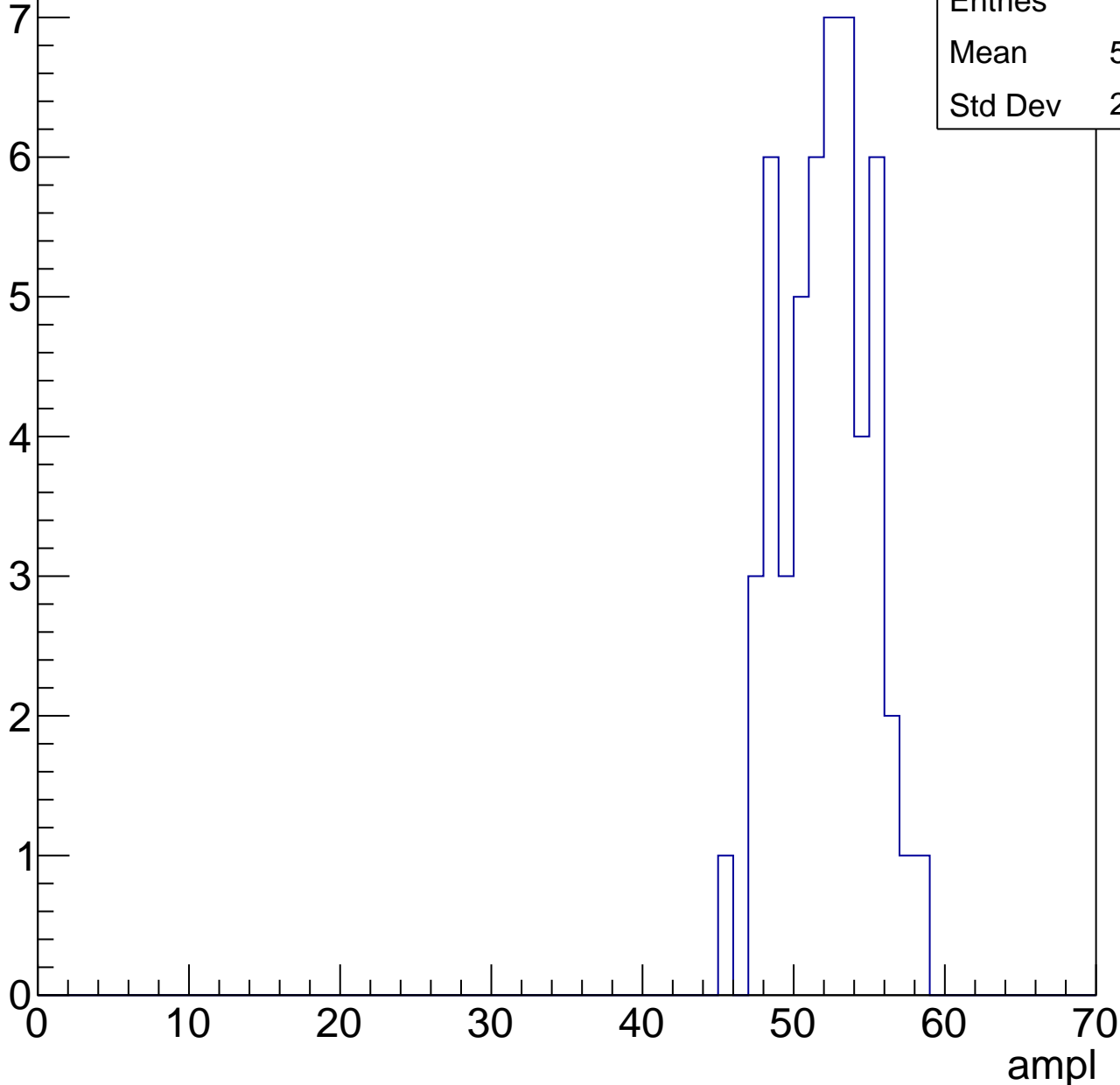


# B1L002S, U17-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.63
Std Dev	2.896

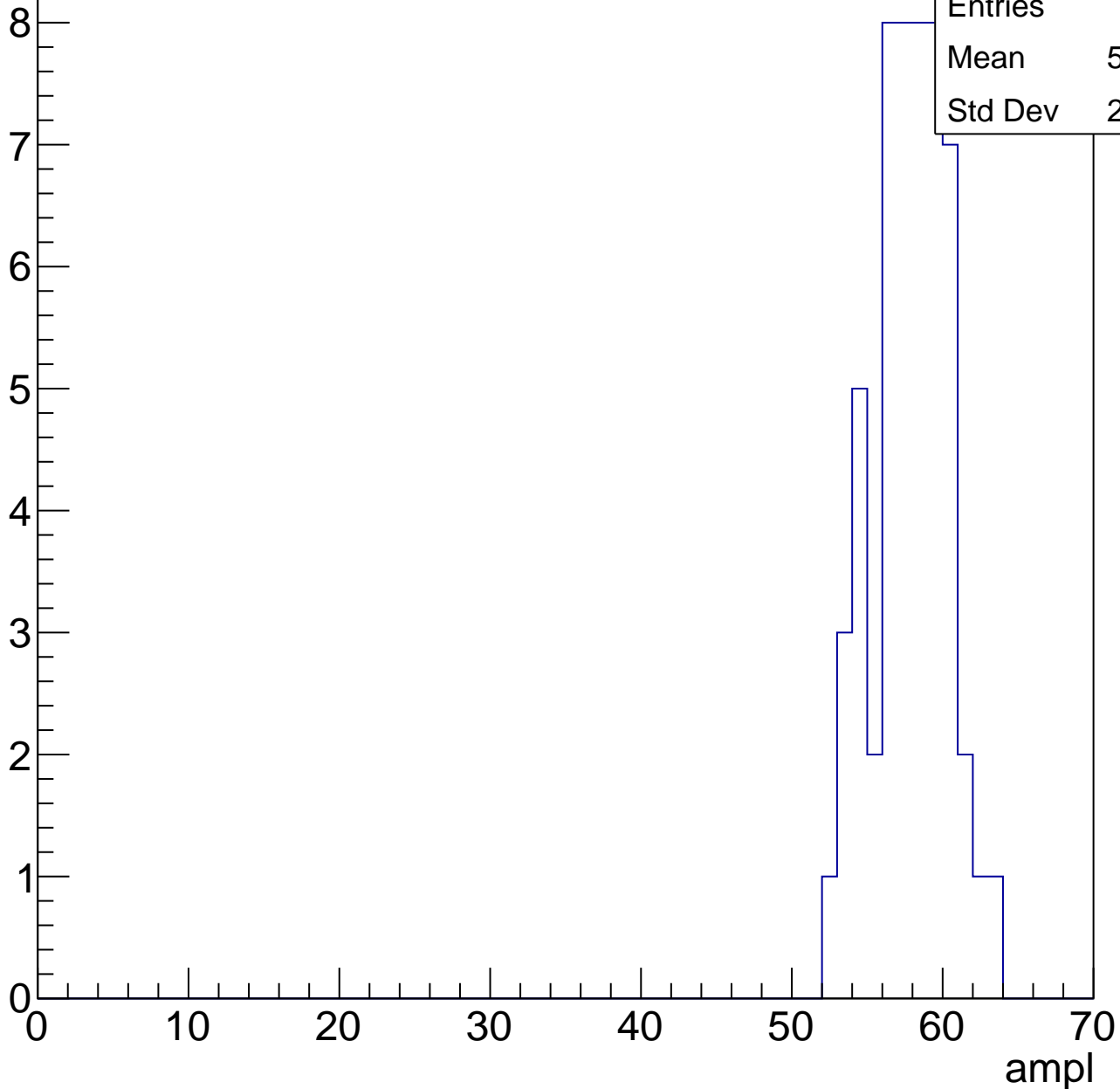


# B1L002S, U17-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.37
Std Dev	2.444

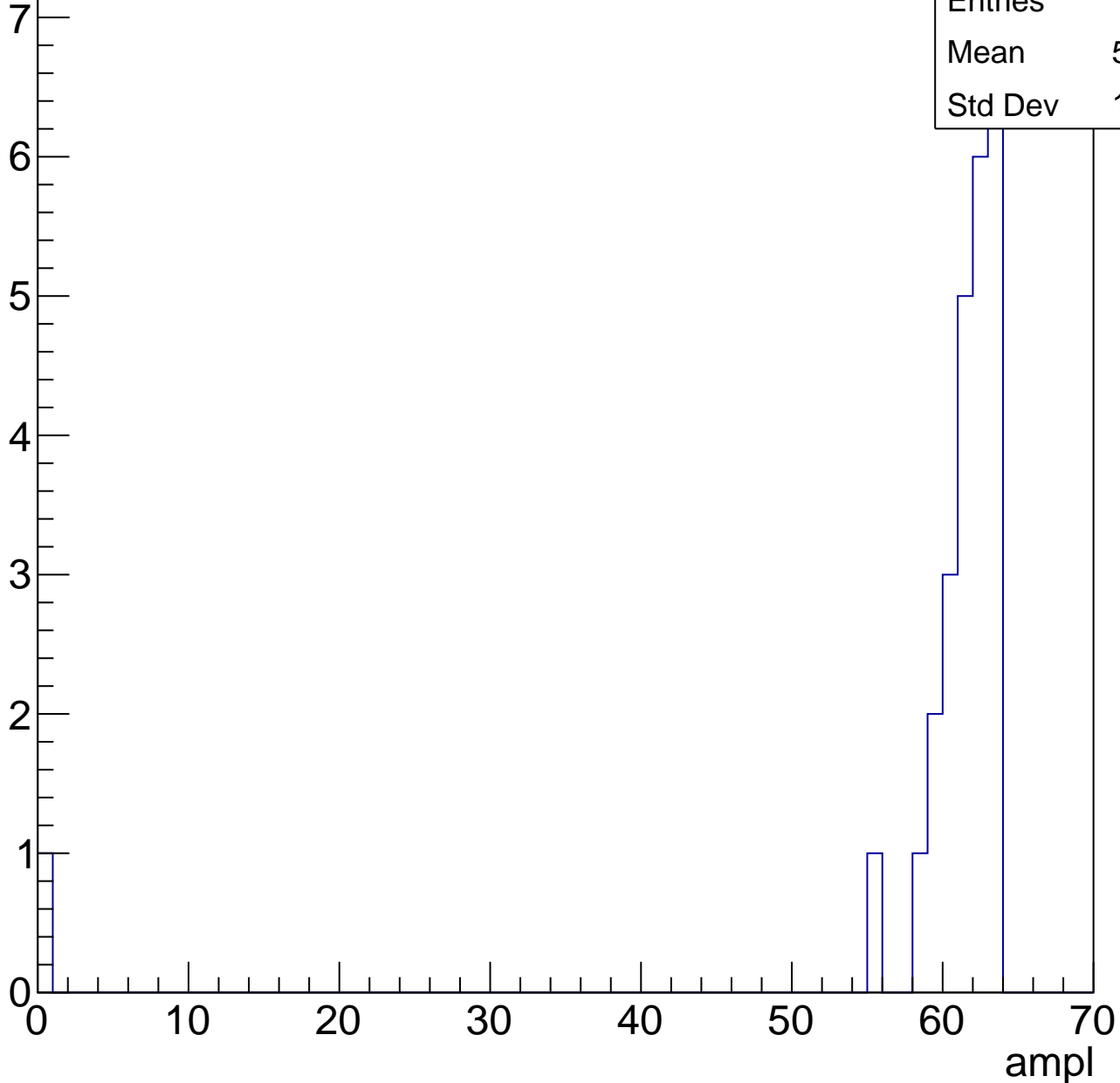


# B1L002S, U17-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.81
Std Dev	11.91

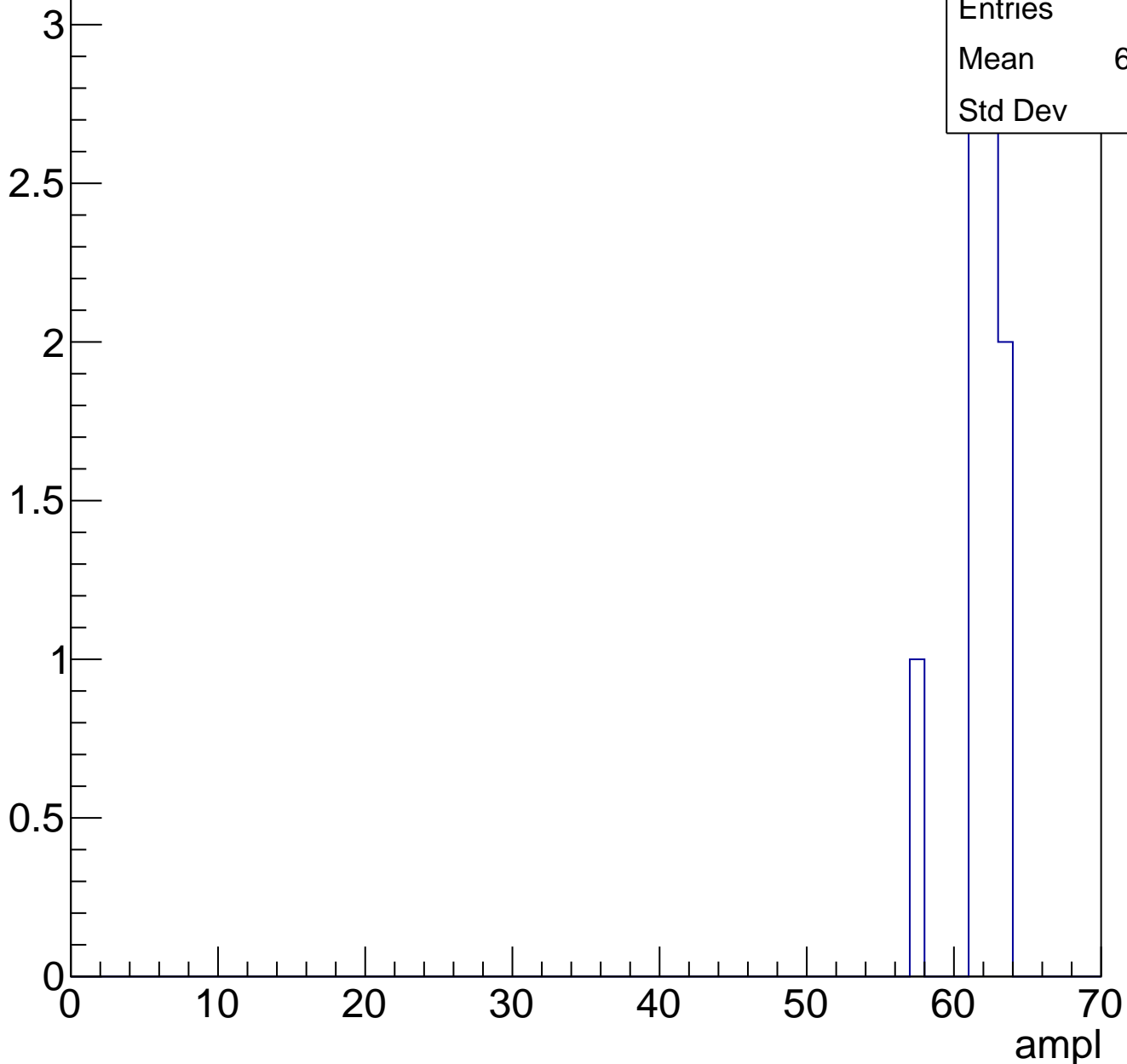




# B1L002S, U17-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch53, adc0

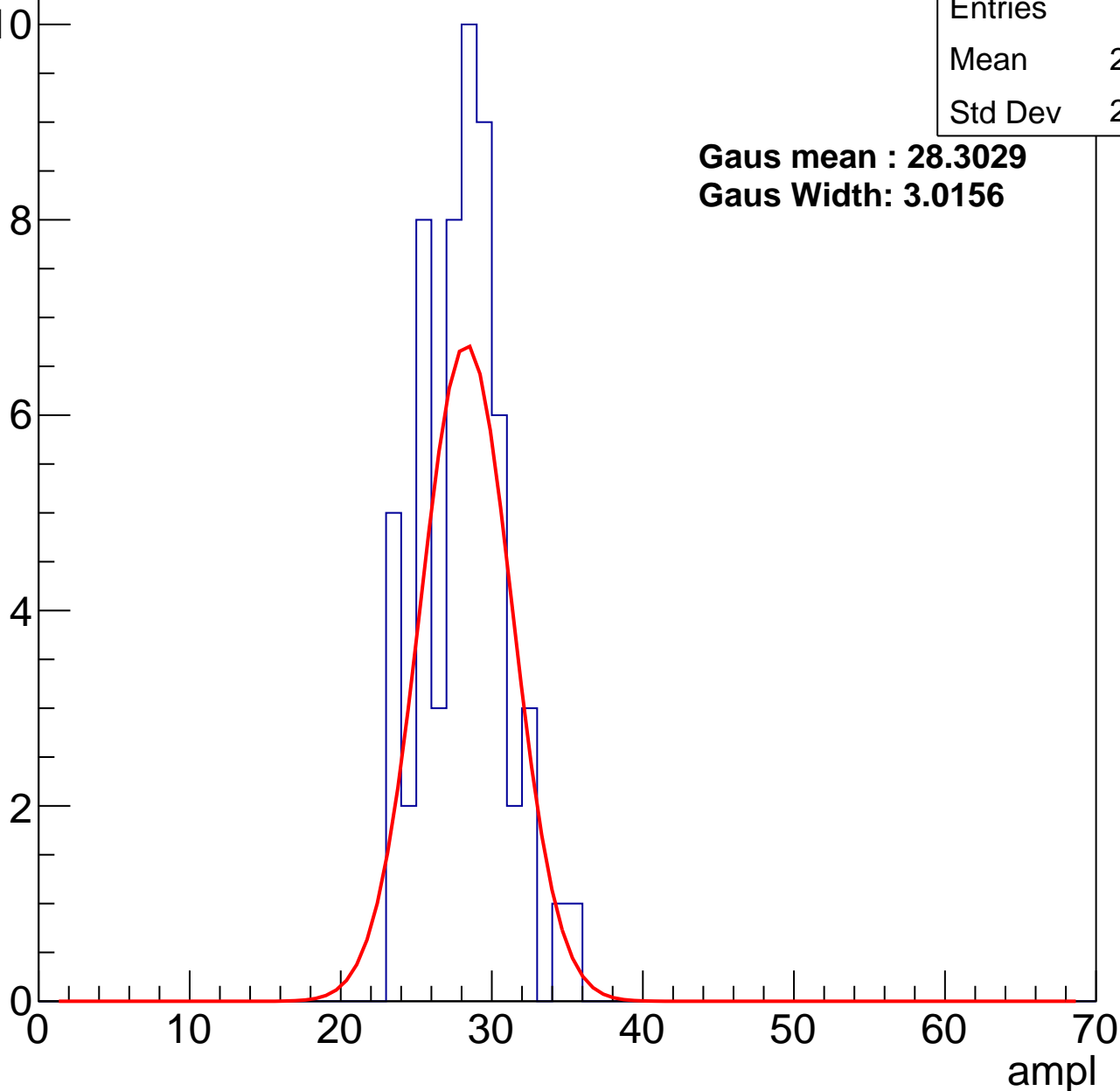
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.67
Std Dev	2.713

**Gaus mean : 28.3029**

**Gaus Width: 3.0156**



# B1L002S, U17-ch53, adc1

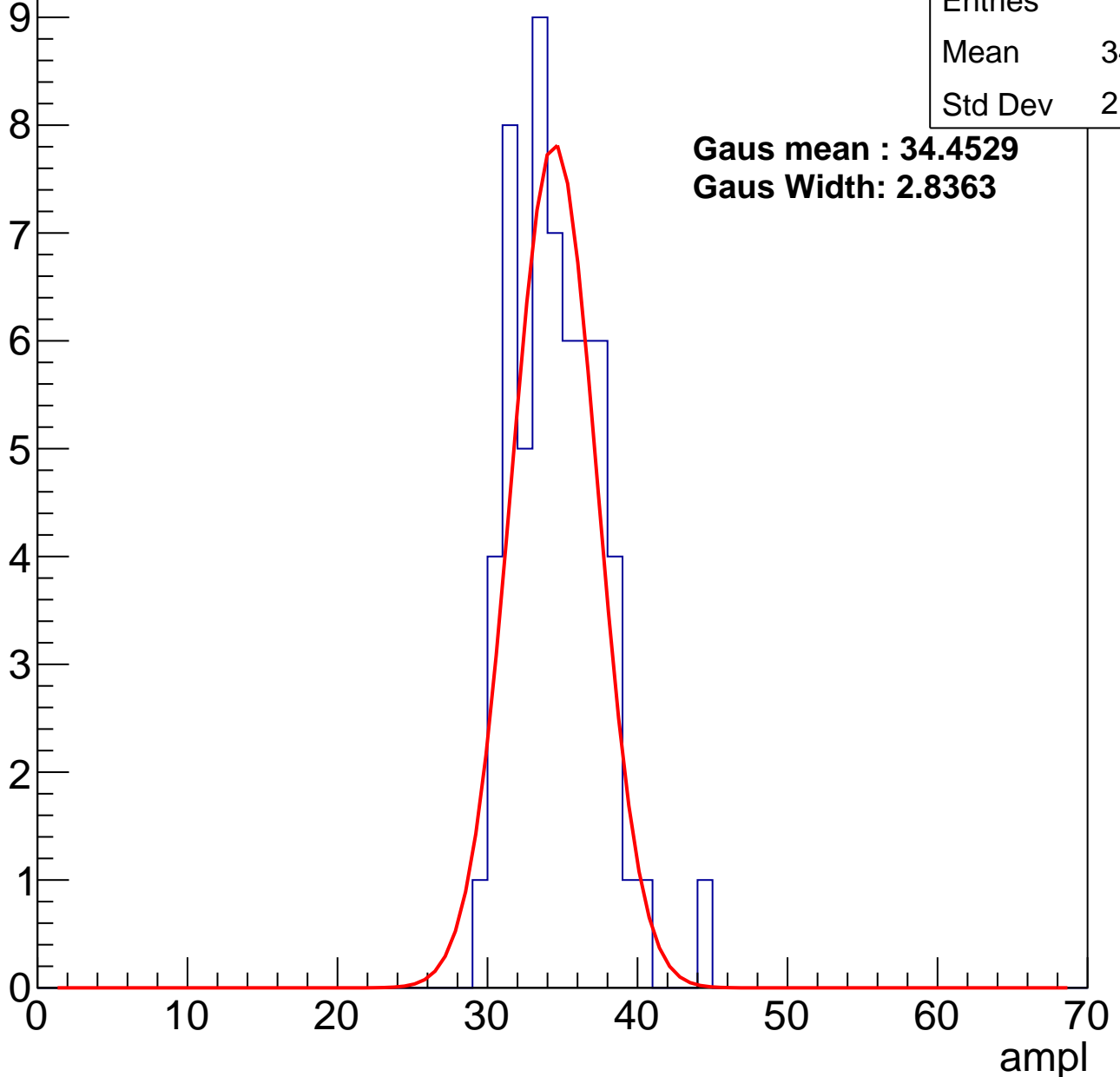
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.15
Std Dev	2.904

**Gaus mean : 34.4529**

**Gaus Width: 2.8363**



# B1L002S, U17-ch53, adc2

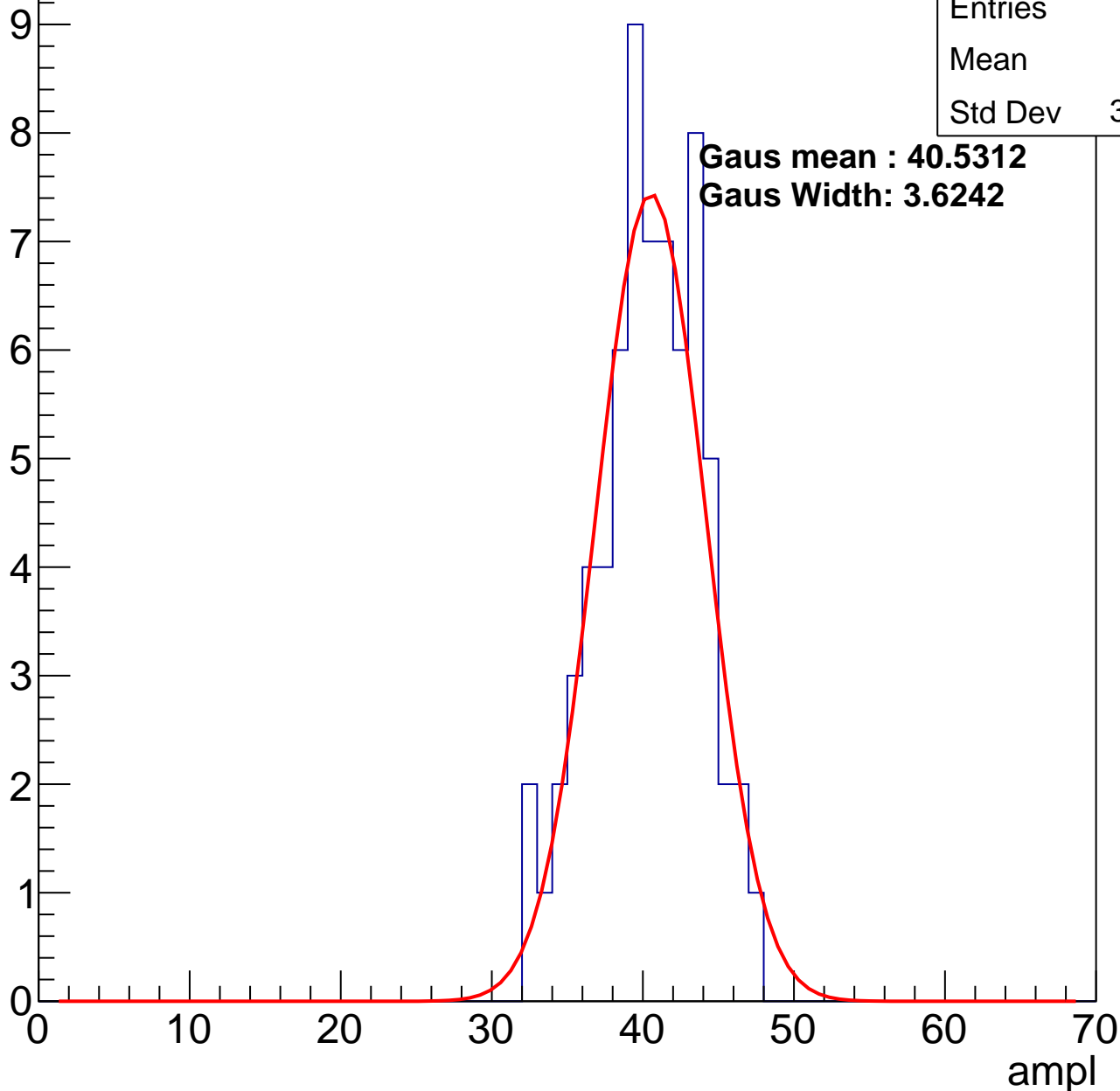
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.9
Std Dev	3.427

**Gaus mean : 40.5312**

**Gaus Width: 3.6242**

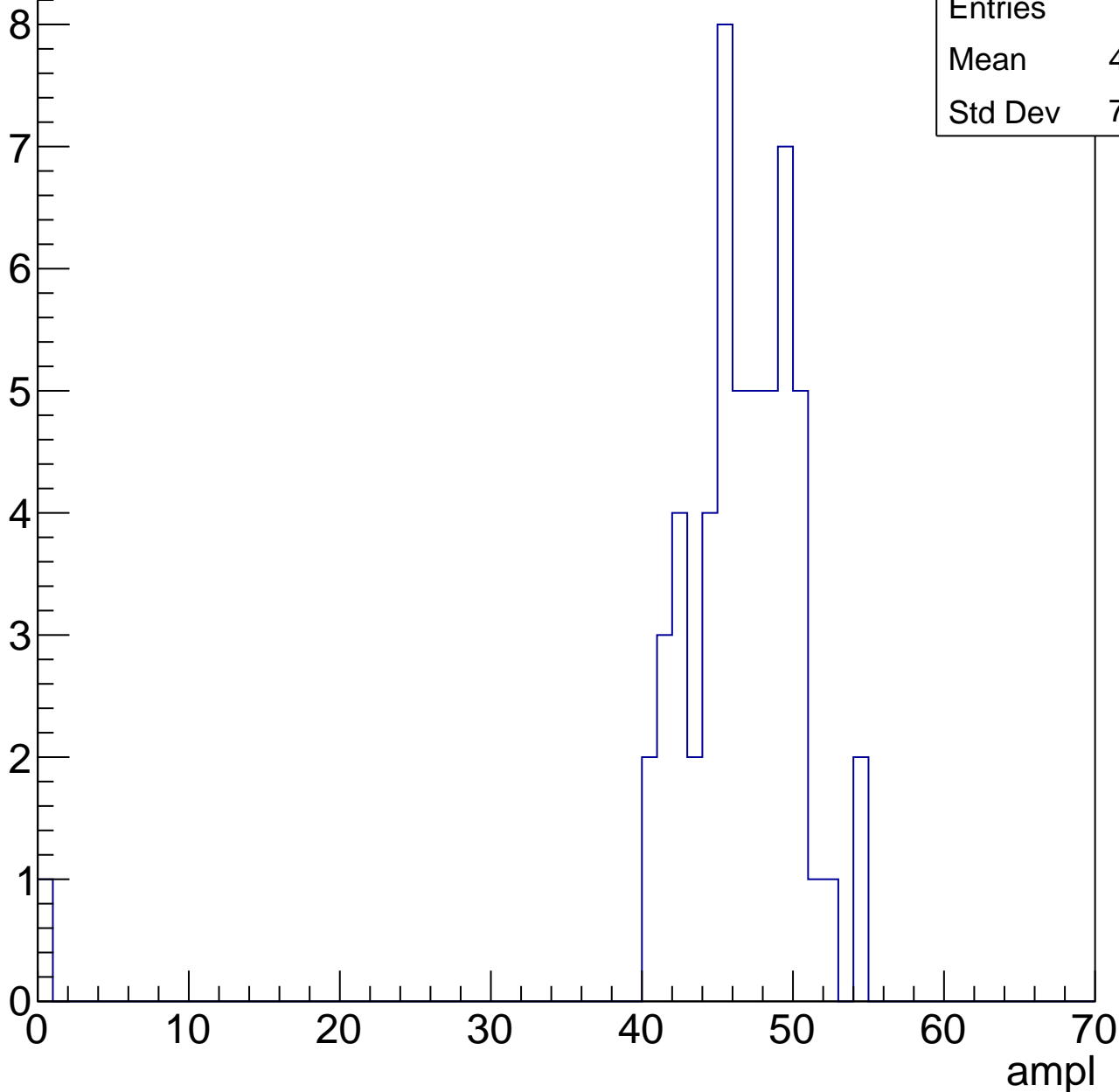


# B1L002S, U17-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	45.49
Std Dev	7.022

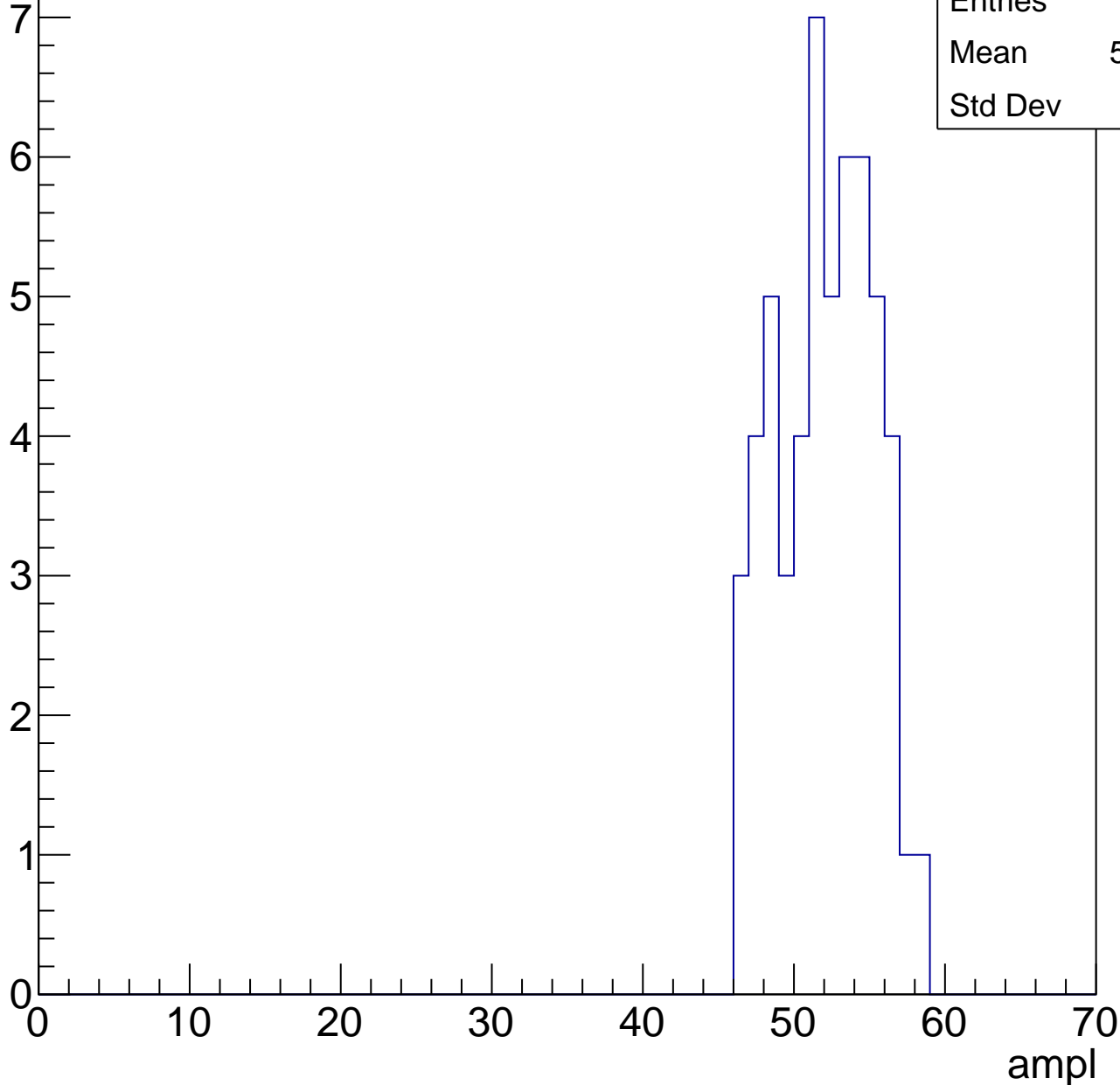


# B1L002S, U17-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.59
Std Dev	3.13

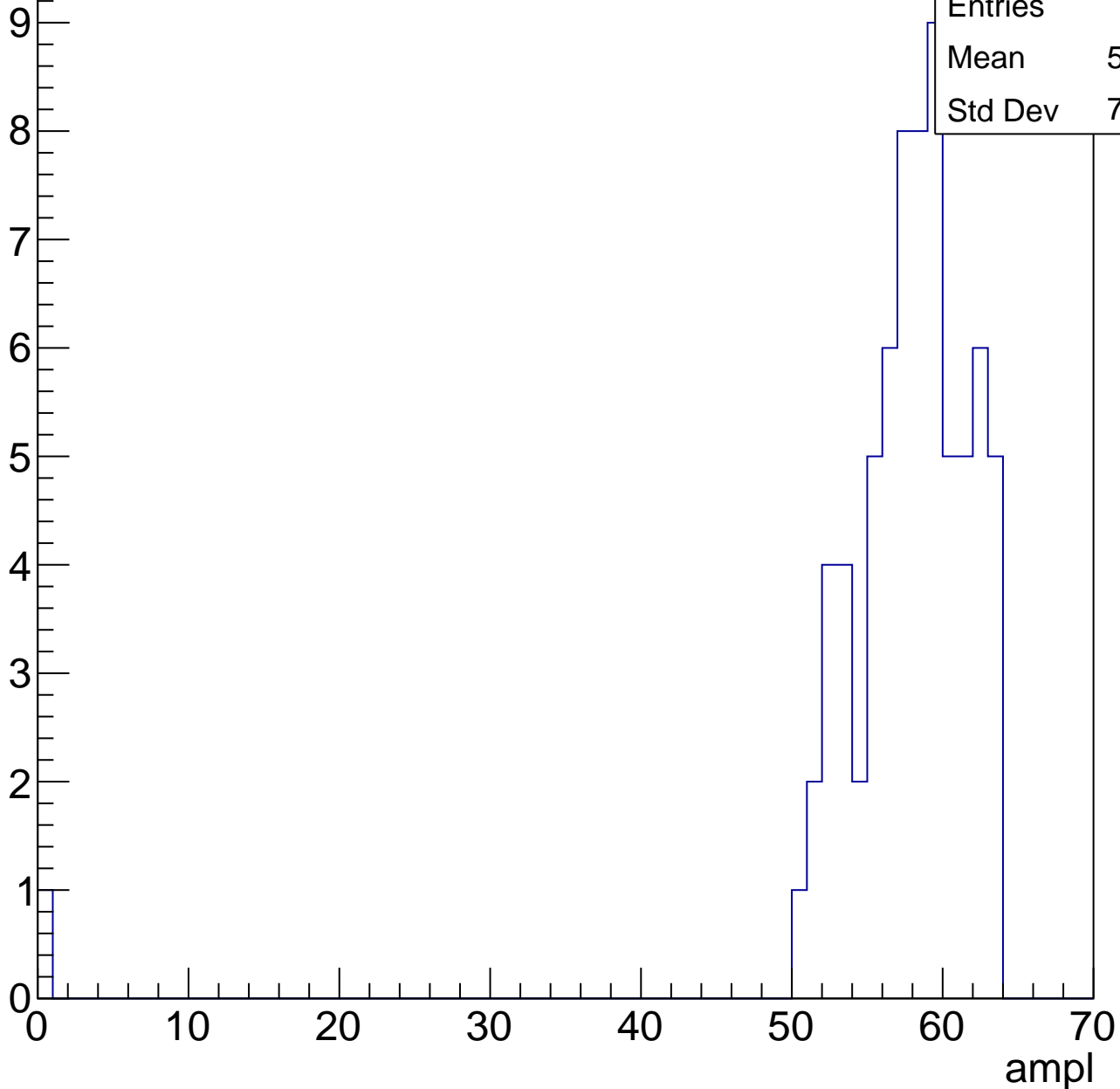


# B1L002S, U17-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

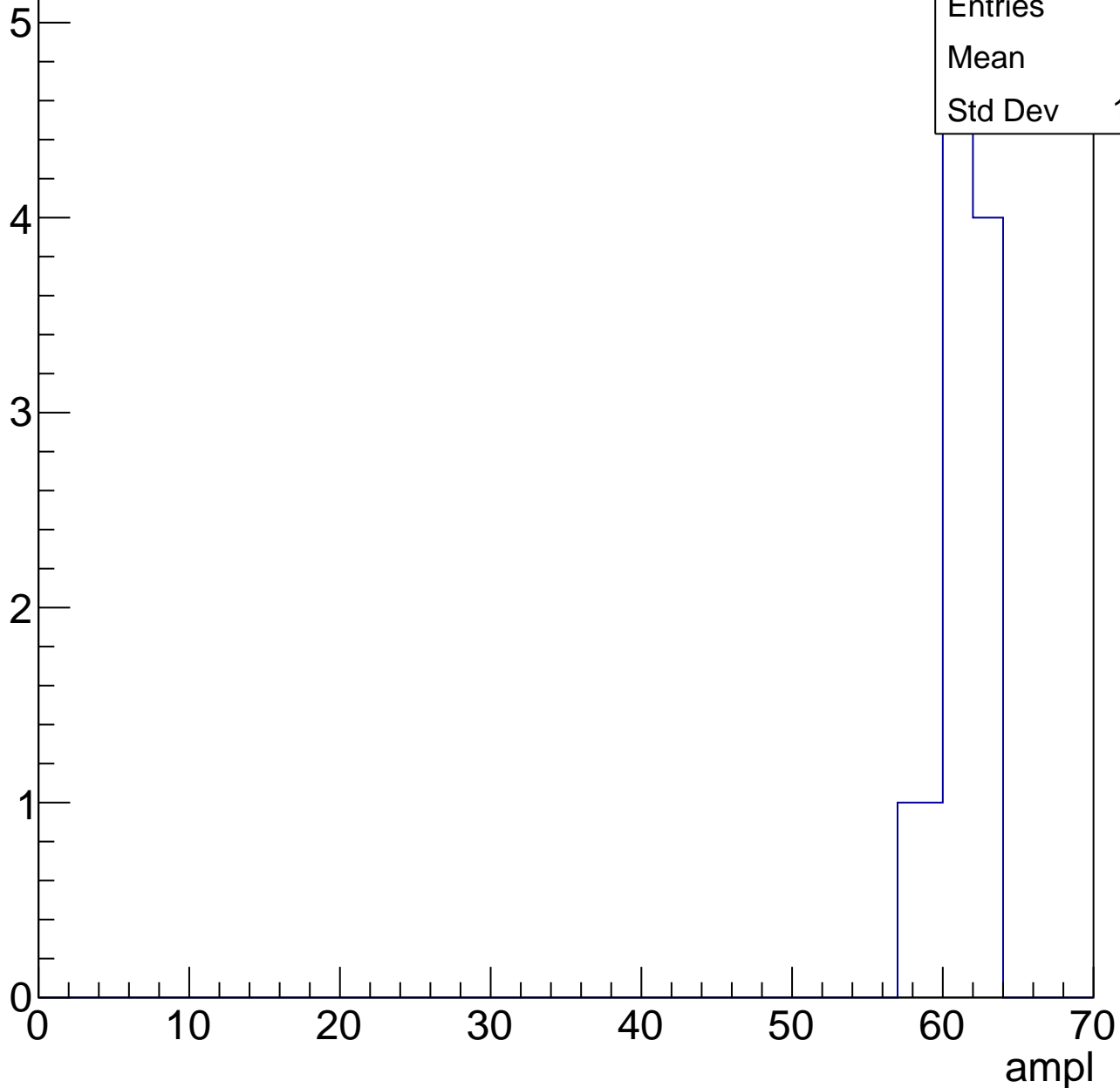
Entries	71
Mean	56.82
Std Dev	7.576



# B1L002S, U17-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

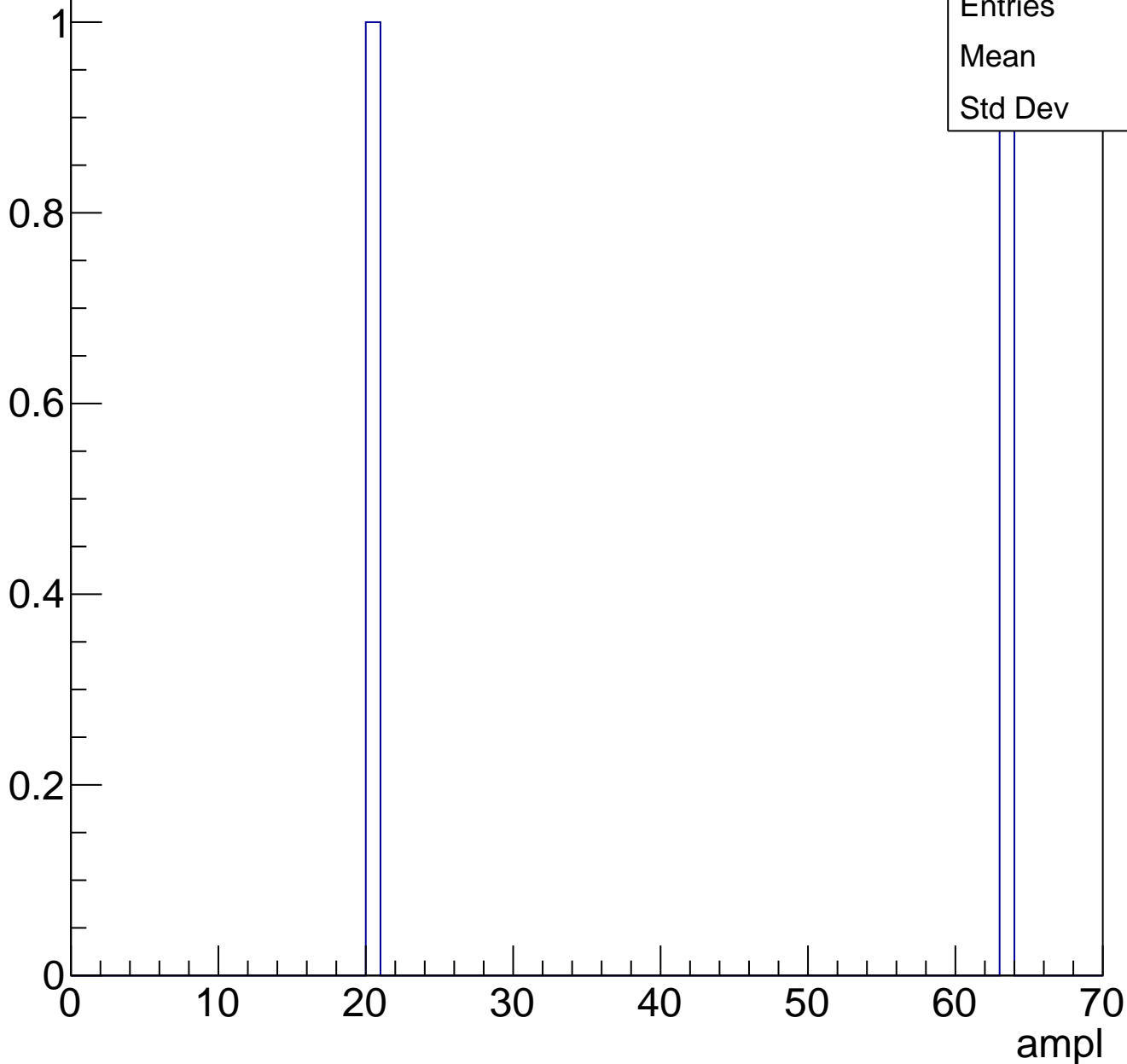




# B1L002S, U17-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch54, adc0

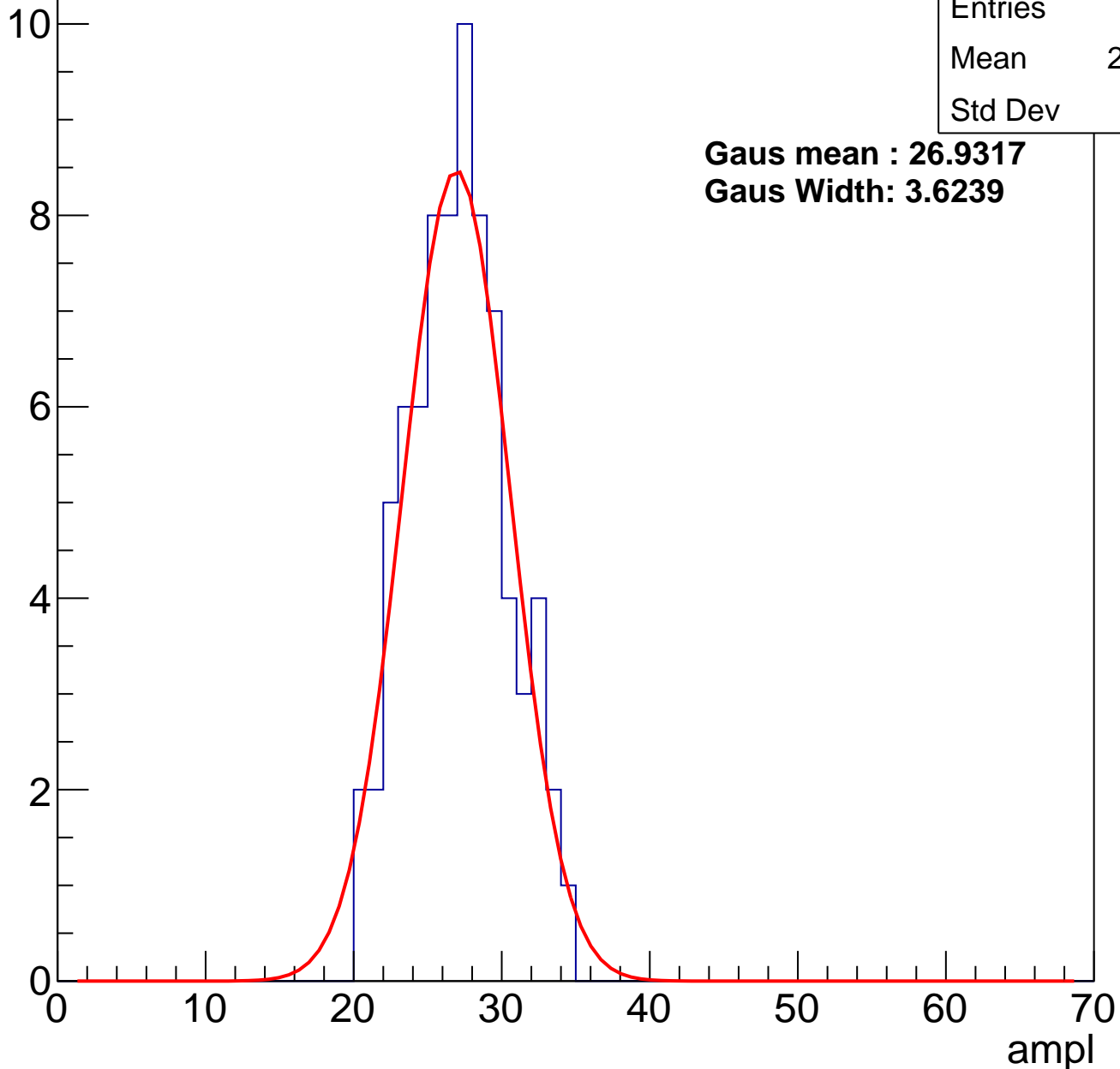
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	26.58
Std Dev	3.27

**Gaus mean : 26.9317**

**Gaus Width: 3.6239**

Entry



# B1L002S, U17-ch54, adc1

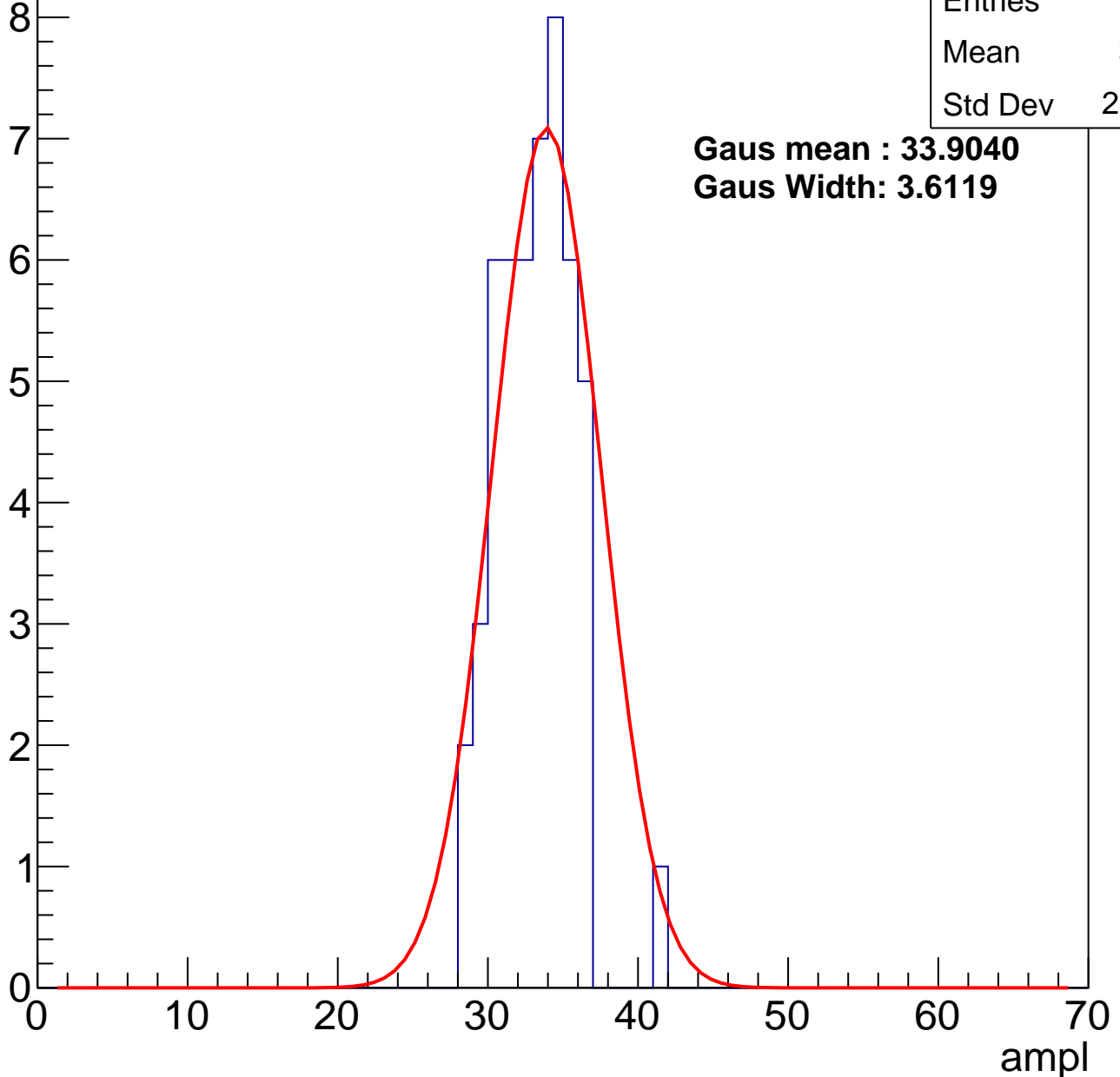
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	32.7
Std Dev	2.524

**Gaus mean : 33.9040**

**Gaus Width: 3.6119**



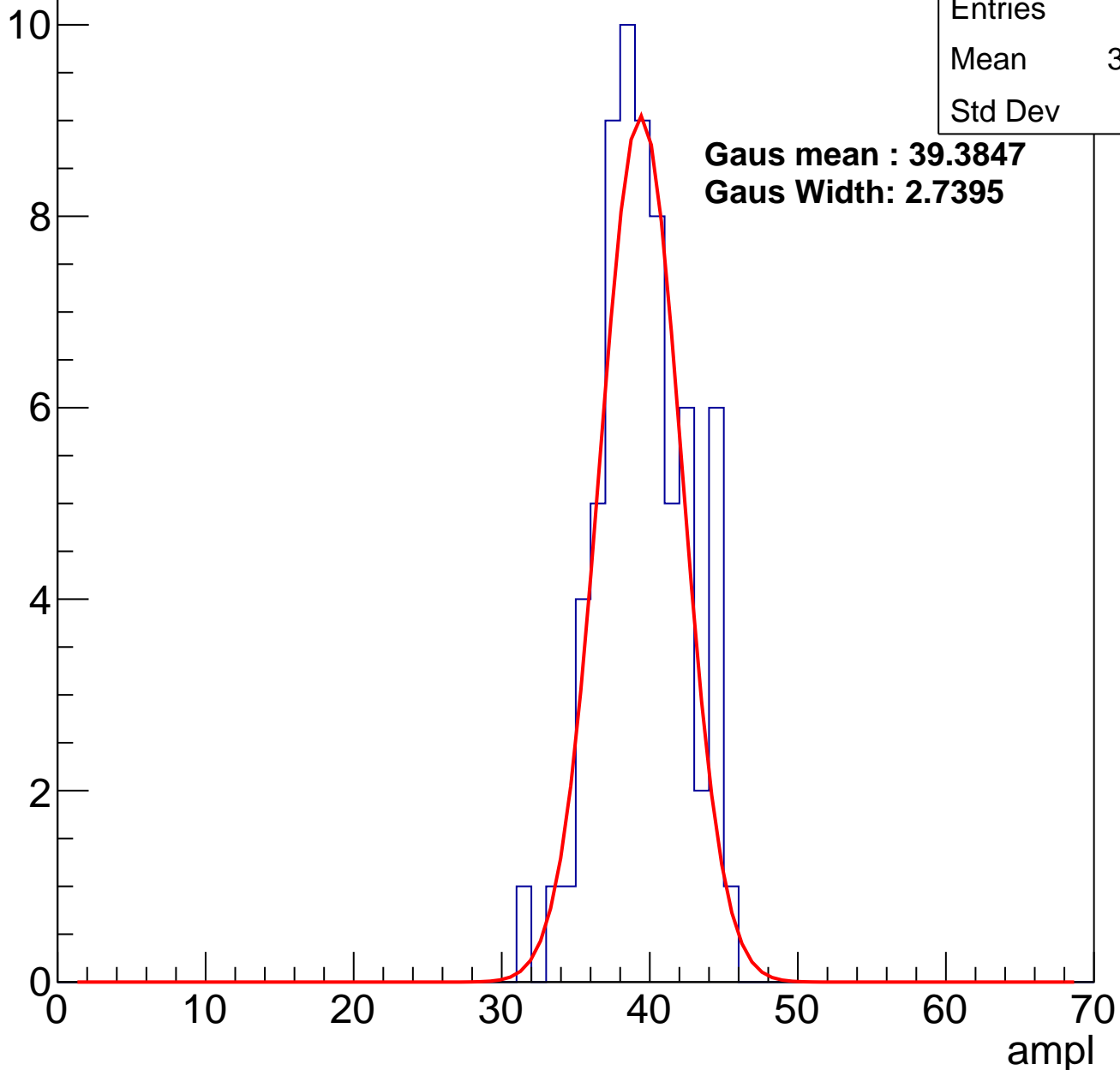
# B1L002S, U17-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	39.03
Std Dev	2.92

**Gaus mean : 39.3847**  
**Gaus Width: 2.7395**

Entry

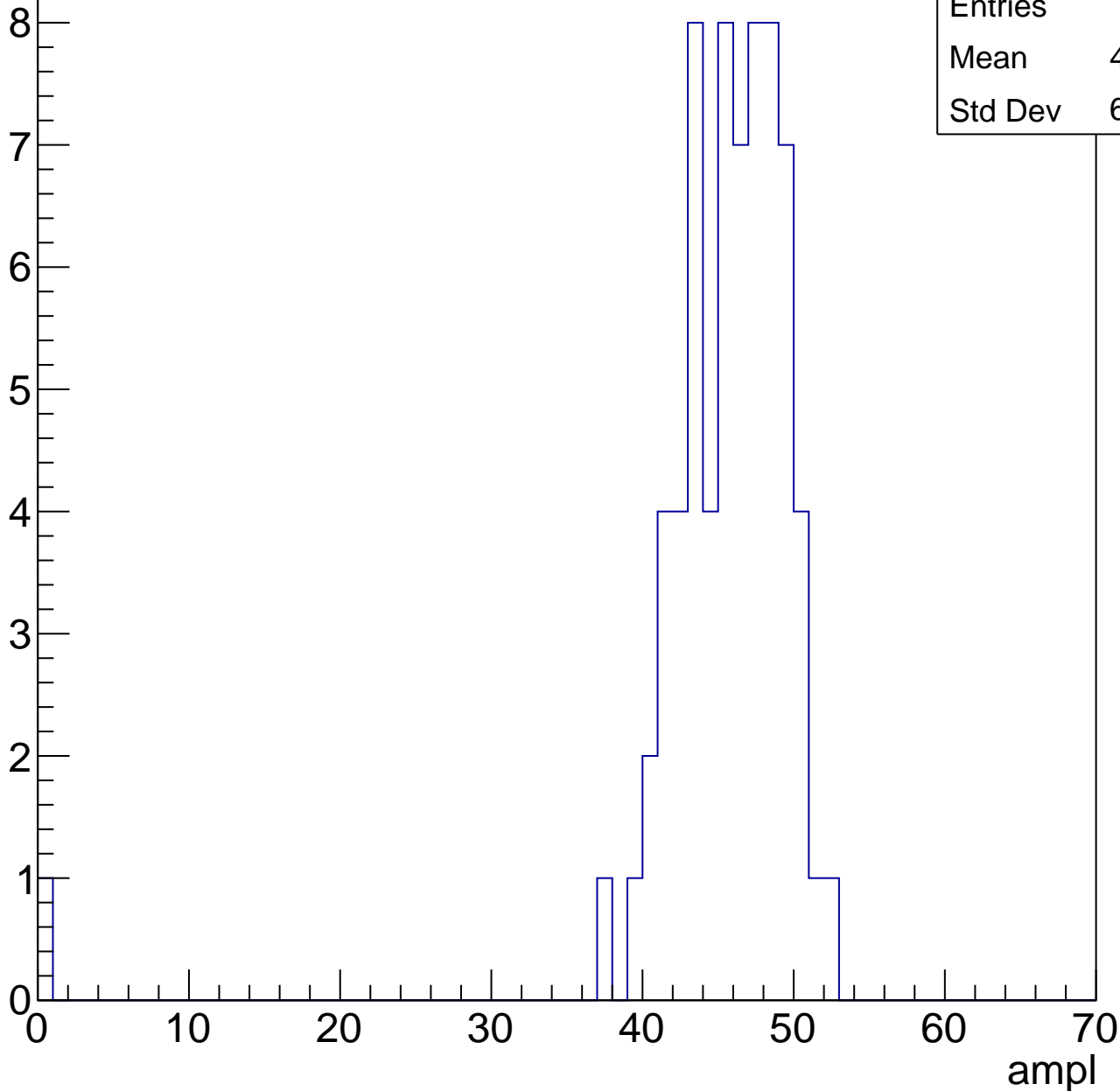


# B1L002S, U17-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	44.87
Std Dev	6.276

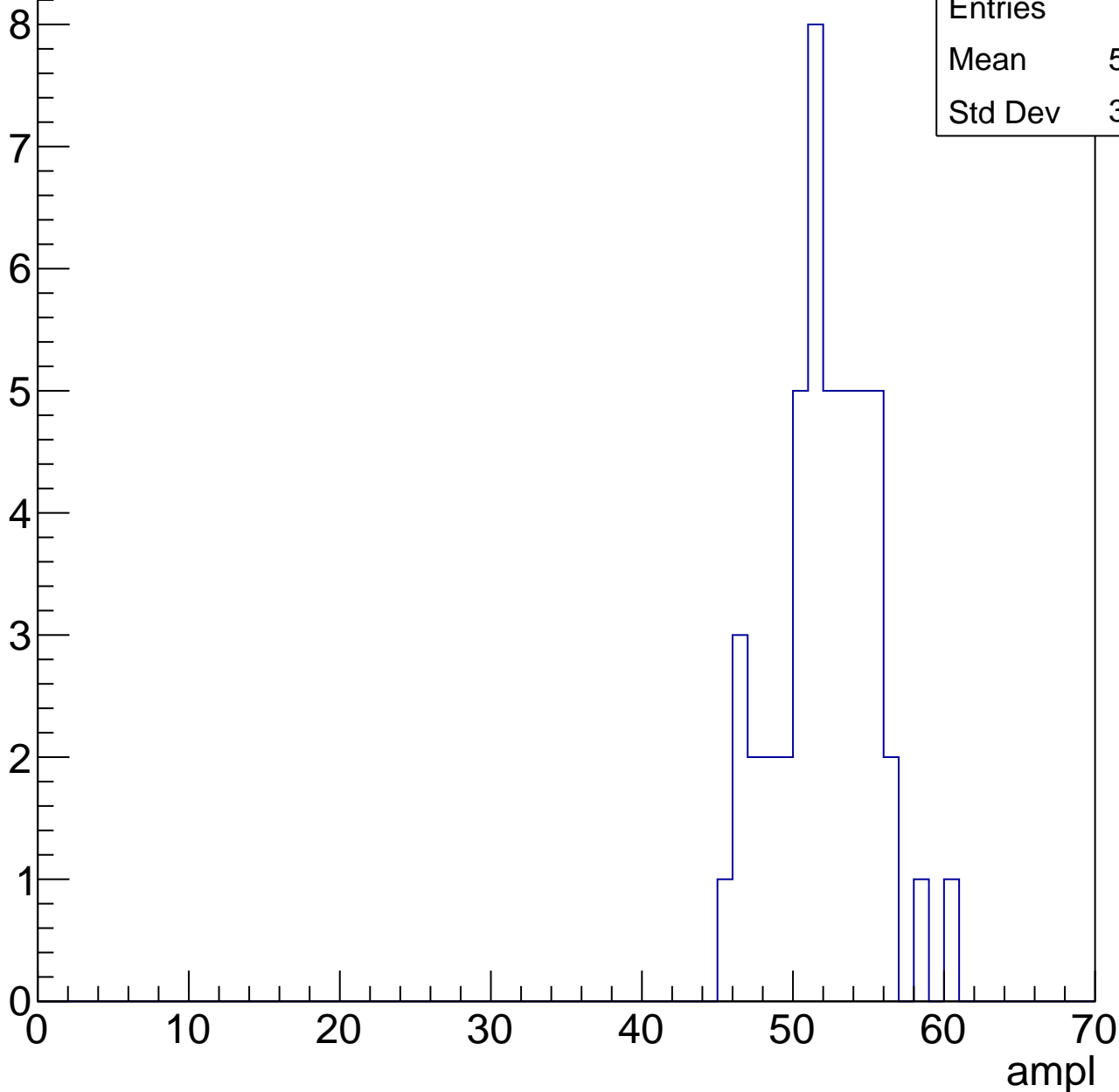


# B1L002S, U17-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	51.68
Std Dev	3.203

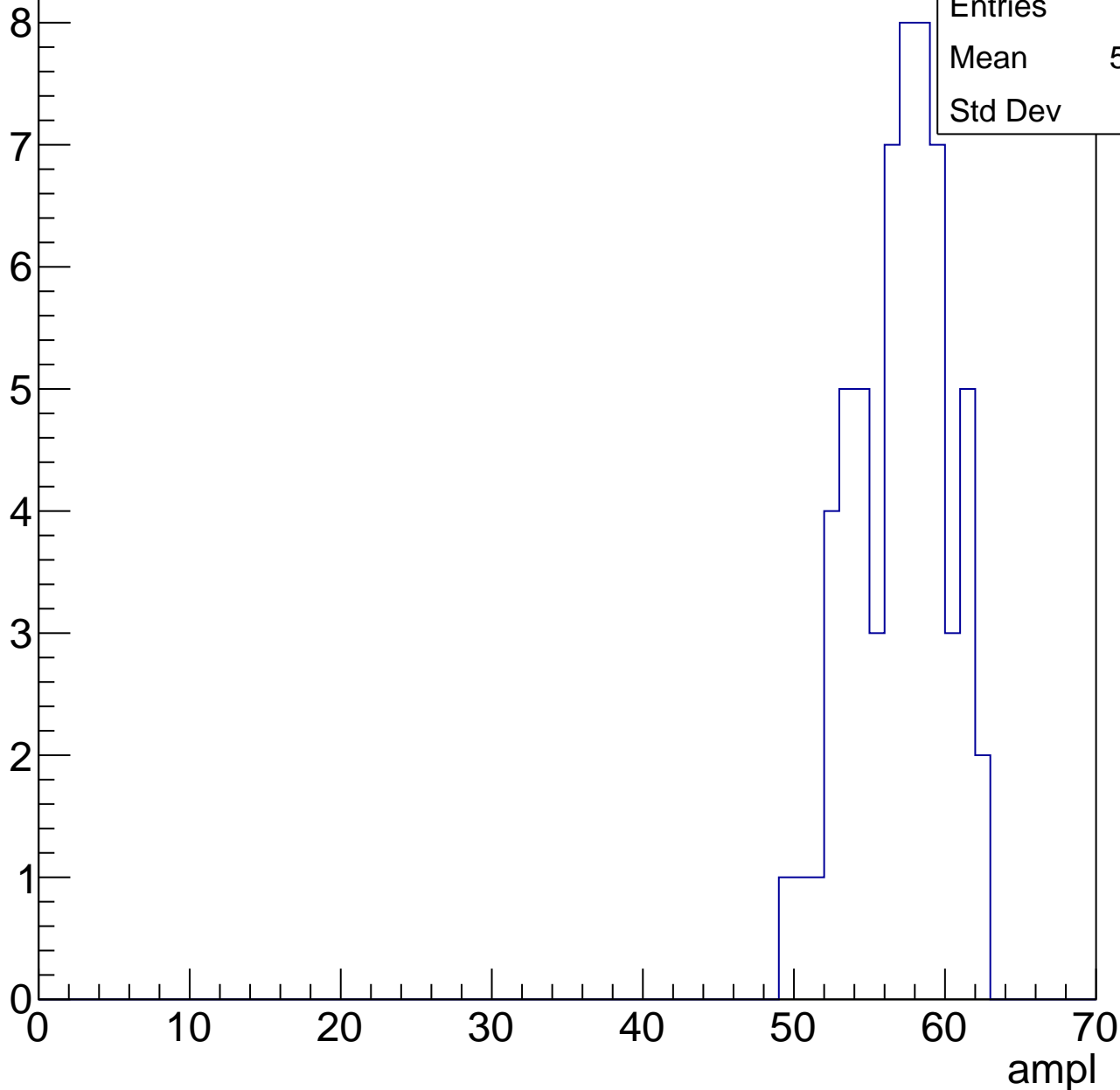


# B1L002S, U17-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.53
Std Dev	3.09

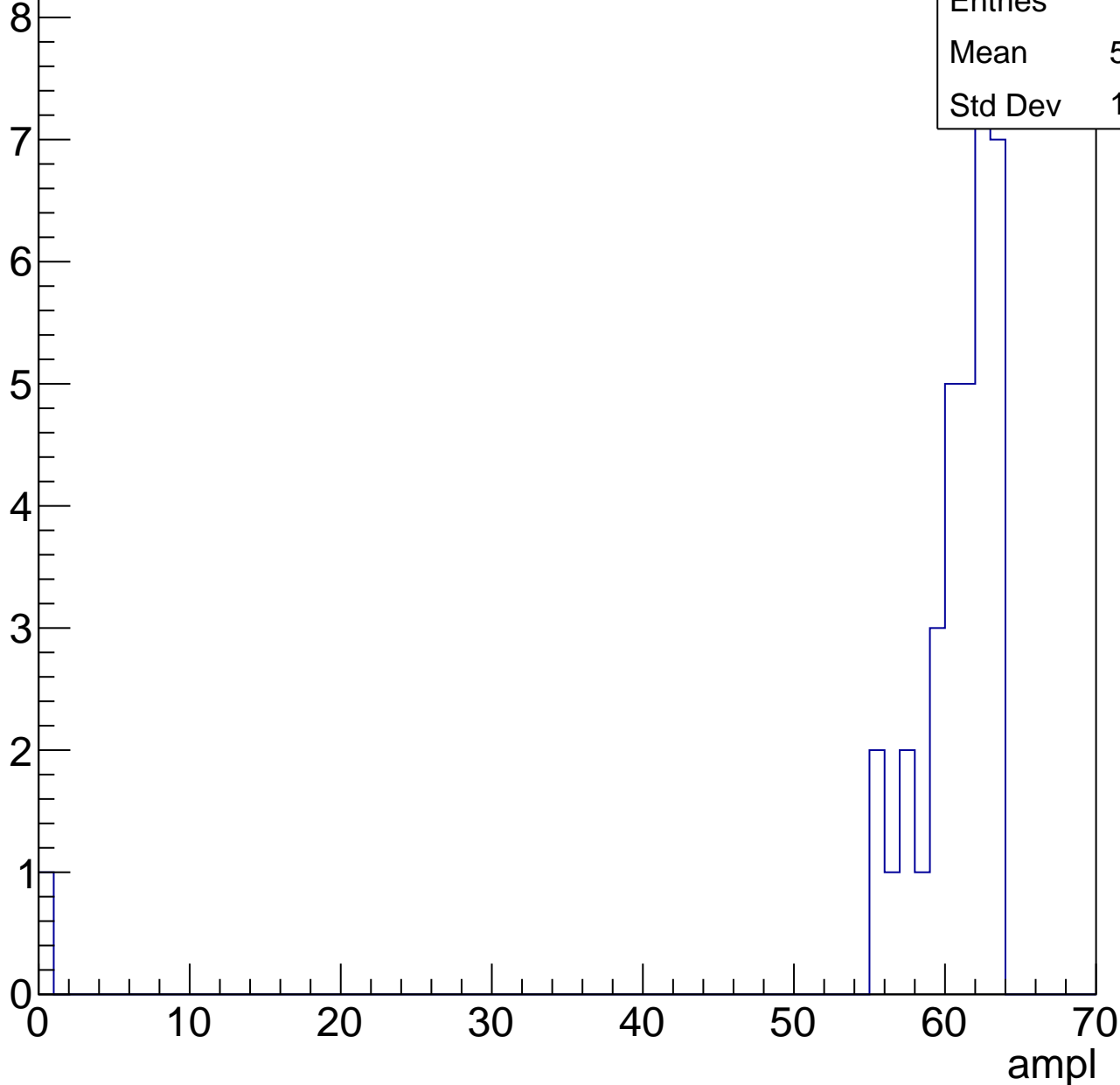


# B1L002S, U17-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.77
Std Dev	10.33

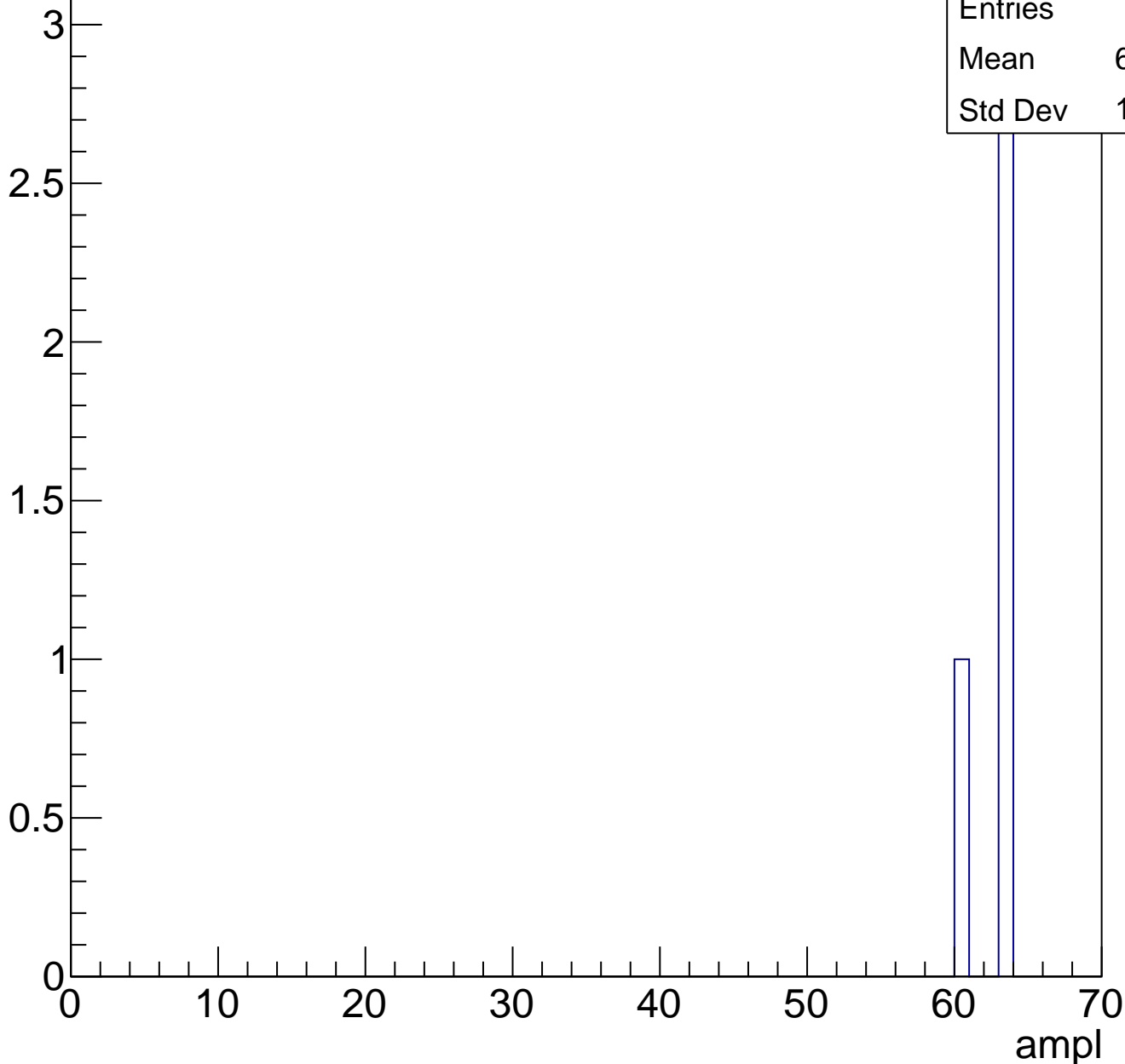




# B1L002S, U17-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch55, adc0

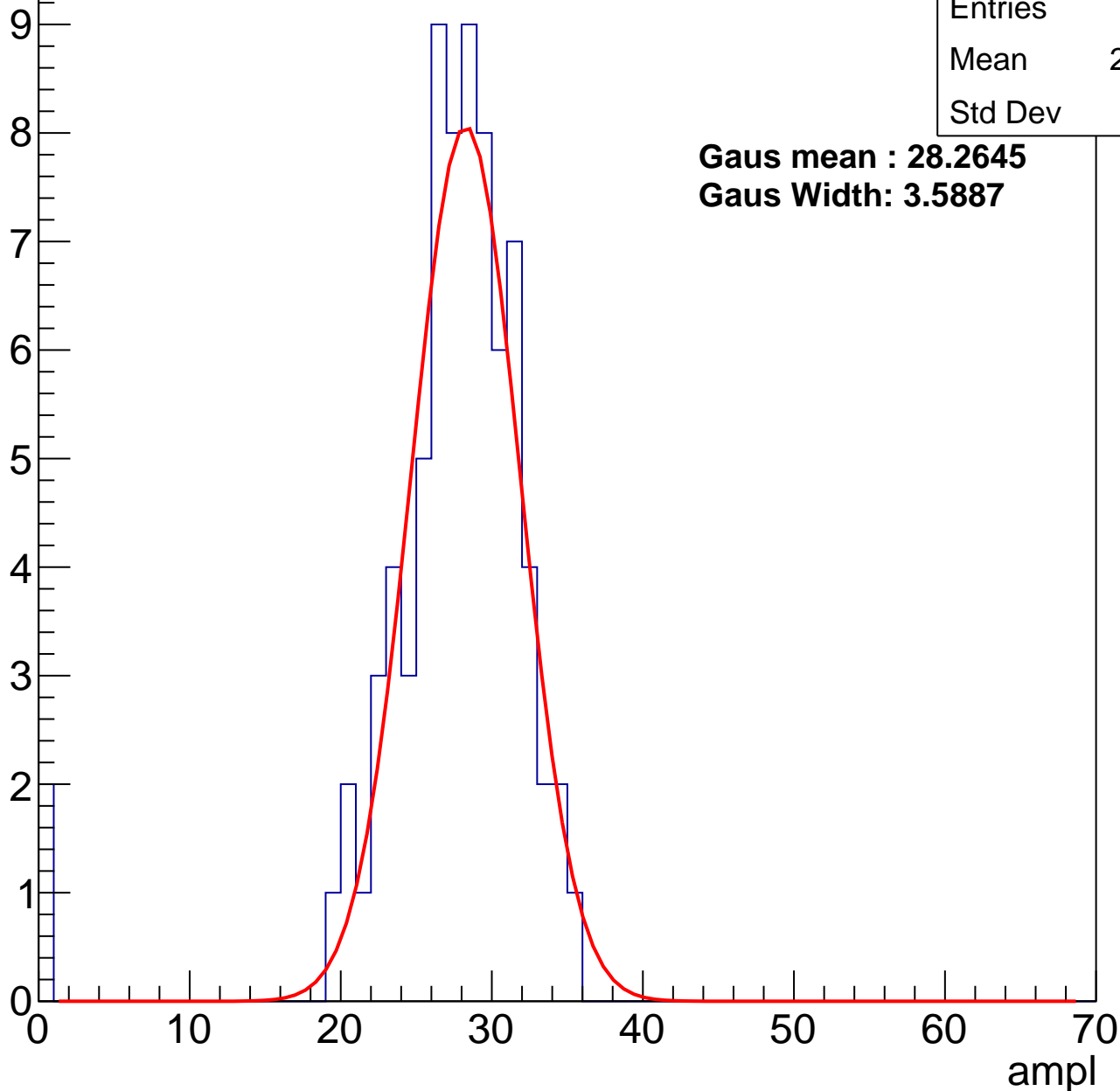
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	26.79
Std Dev	5.57

**Gaus mean : 28.2645**

**Gaus Width: 3.5887**



# B1L002S, U17-ch55, adc1

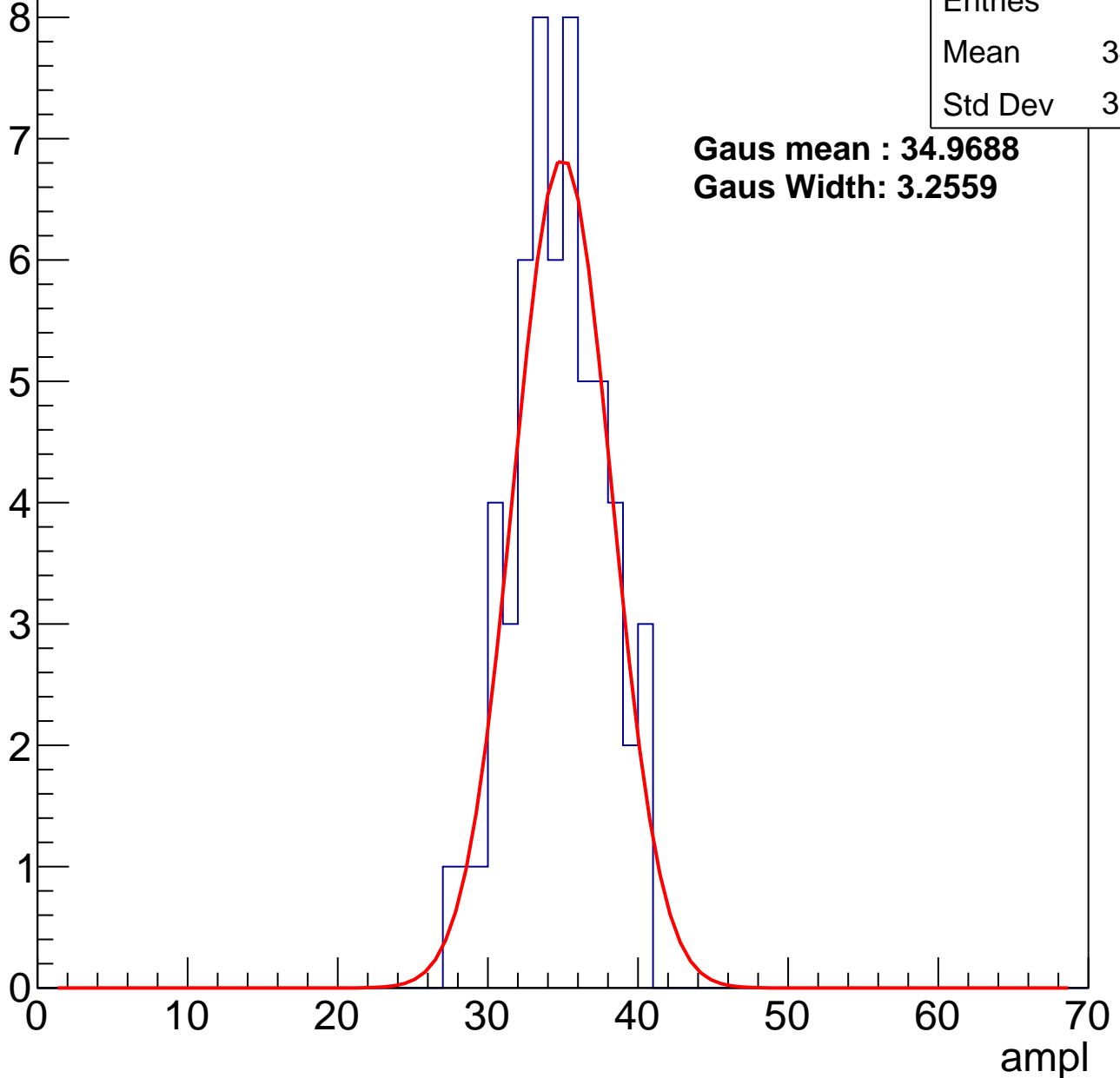
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.25
Std Dev	3.034

**Gaus mean : 34.9688**

**Gaus Width: 3.2559**



# B1L002S, U17-ch55, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	40.08
Std Dev	2.795

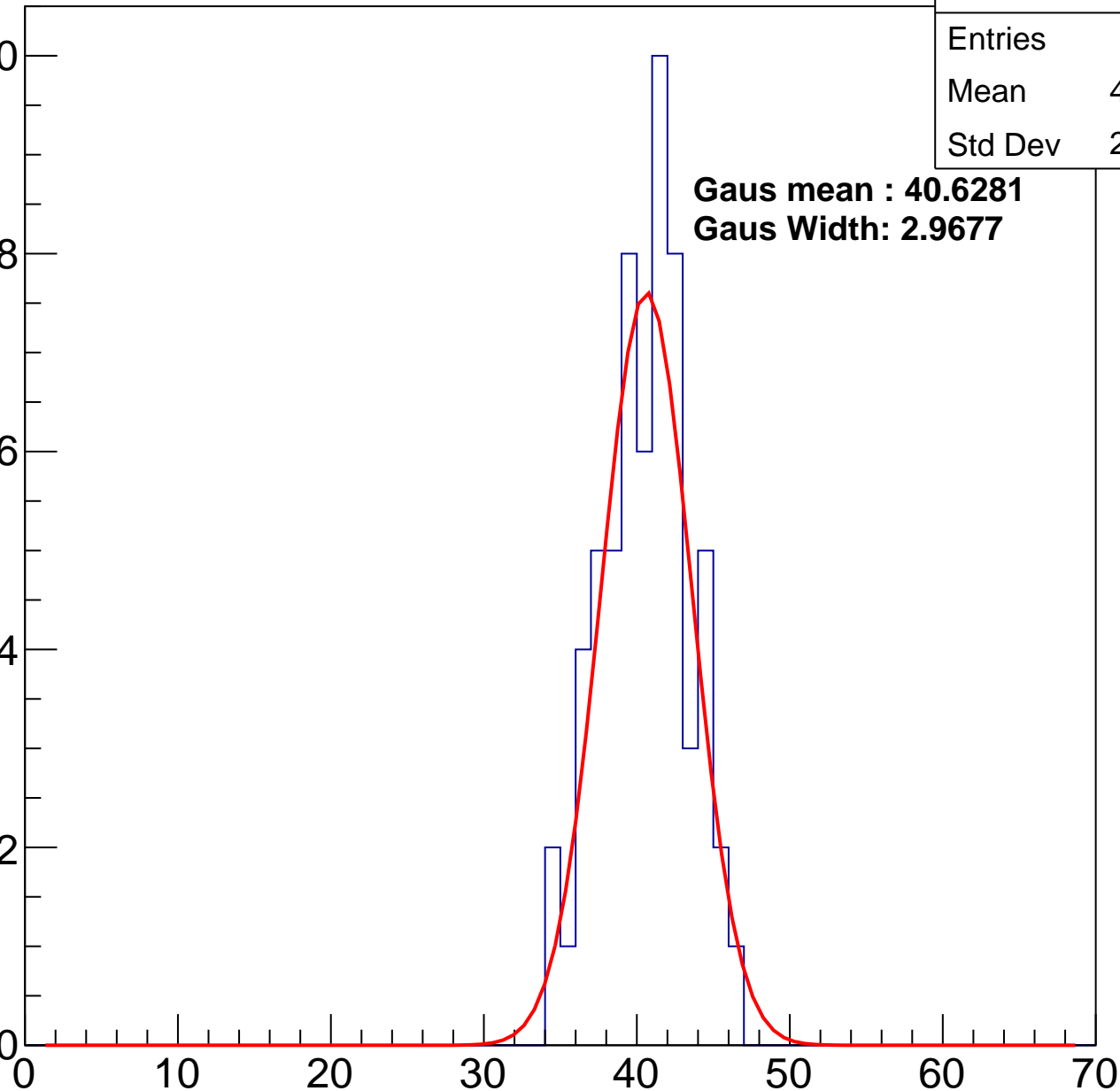
**Gaus mean : 40.6281**

**Gaus Width: 2.9677**

Entry

10  
8  
6  
4  
2  
0

ampl

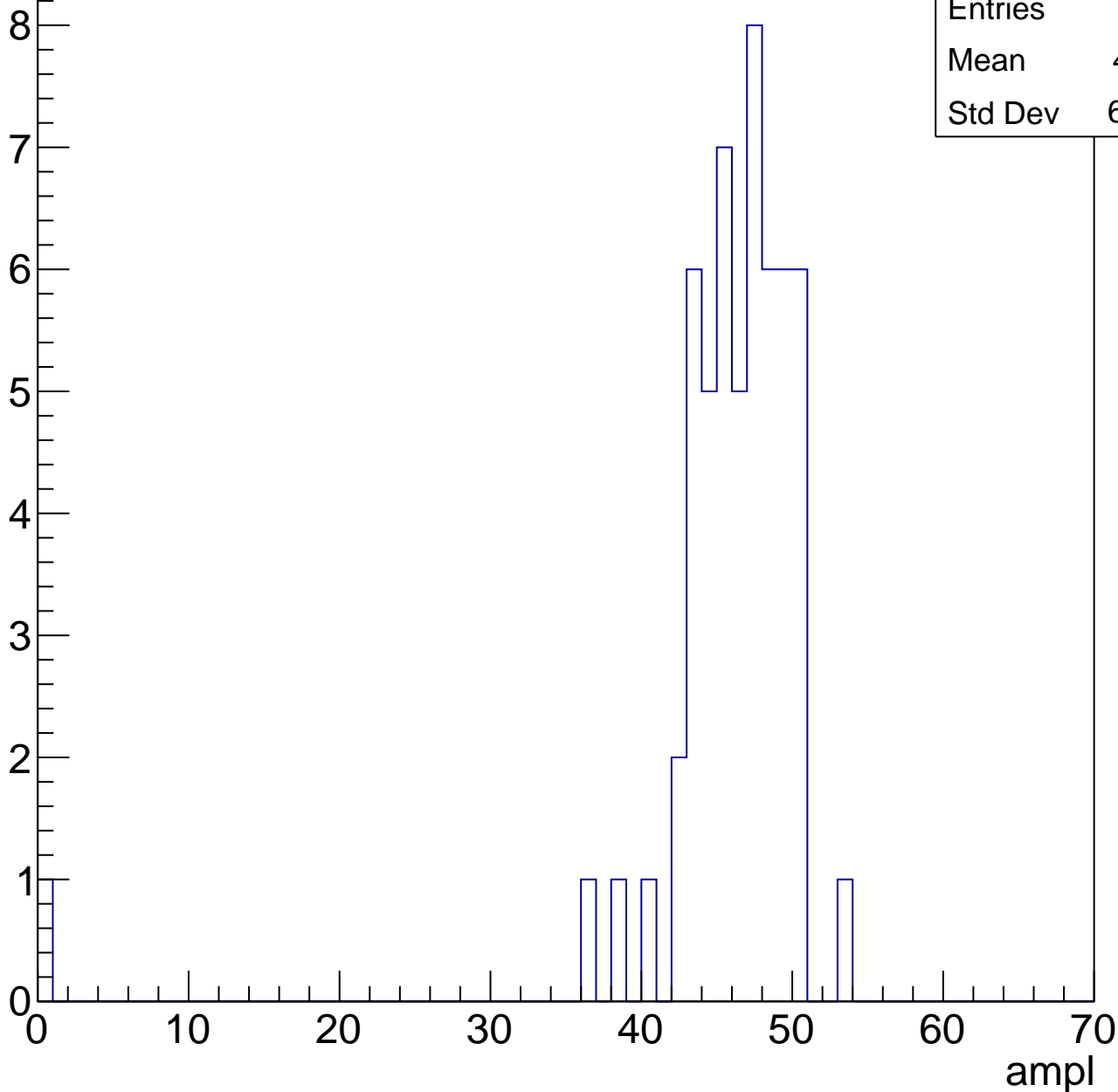


# B1L002S, U17-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	45.21
Std Dev	6.847

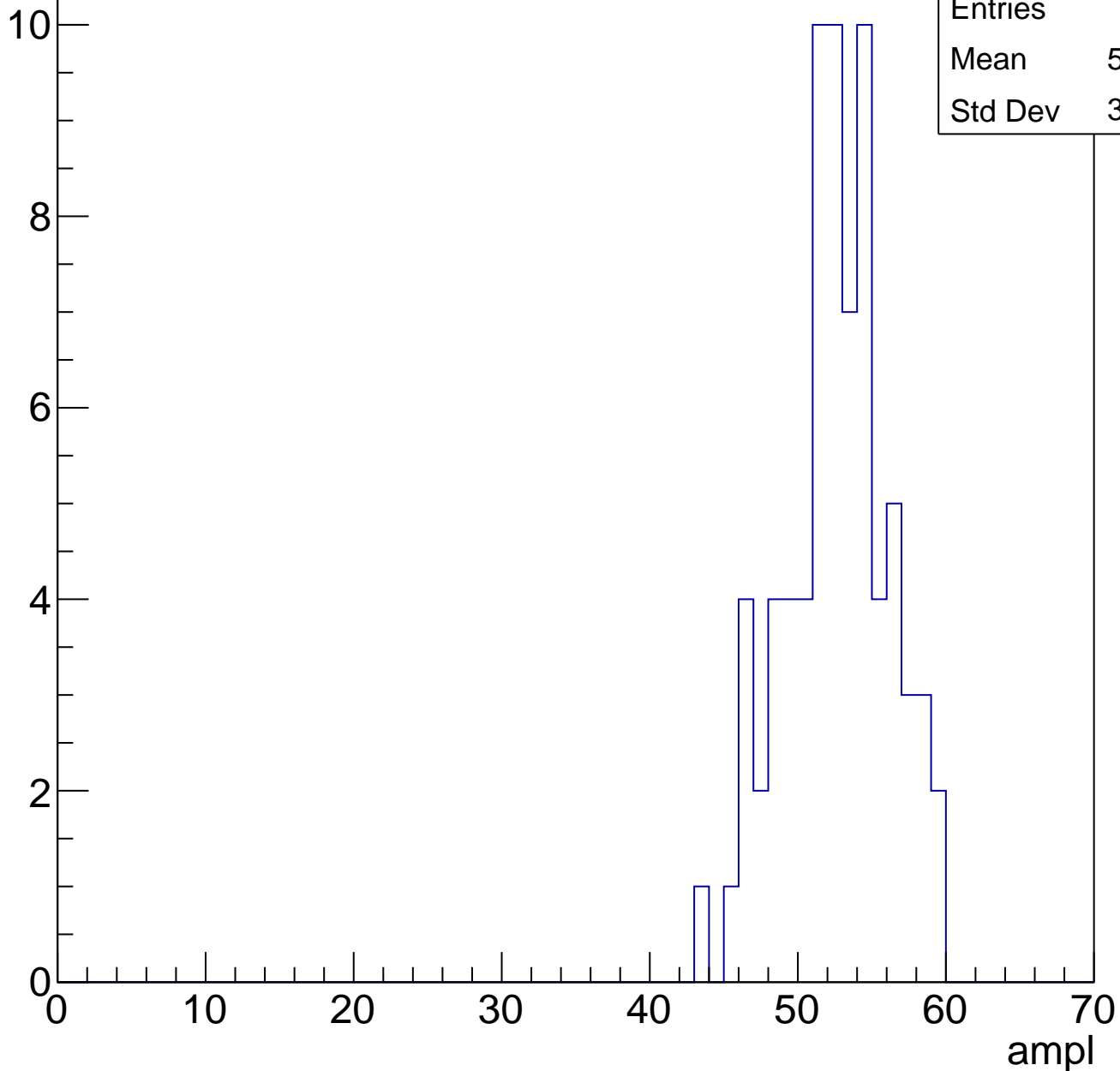


# B1L002S, U17-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	52.14
Std Dev	3.473

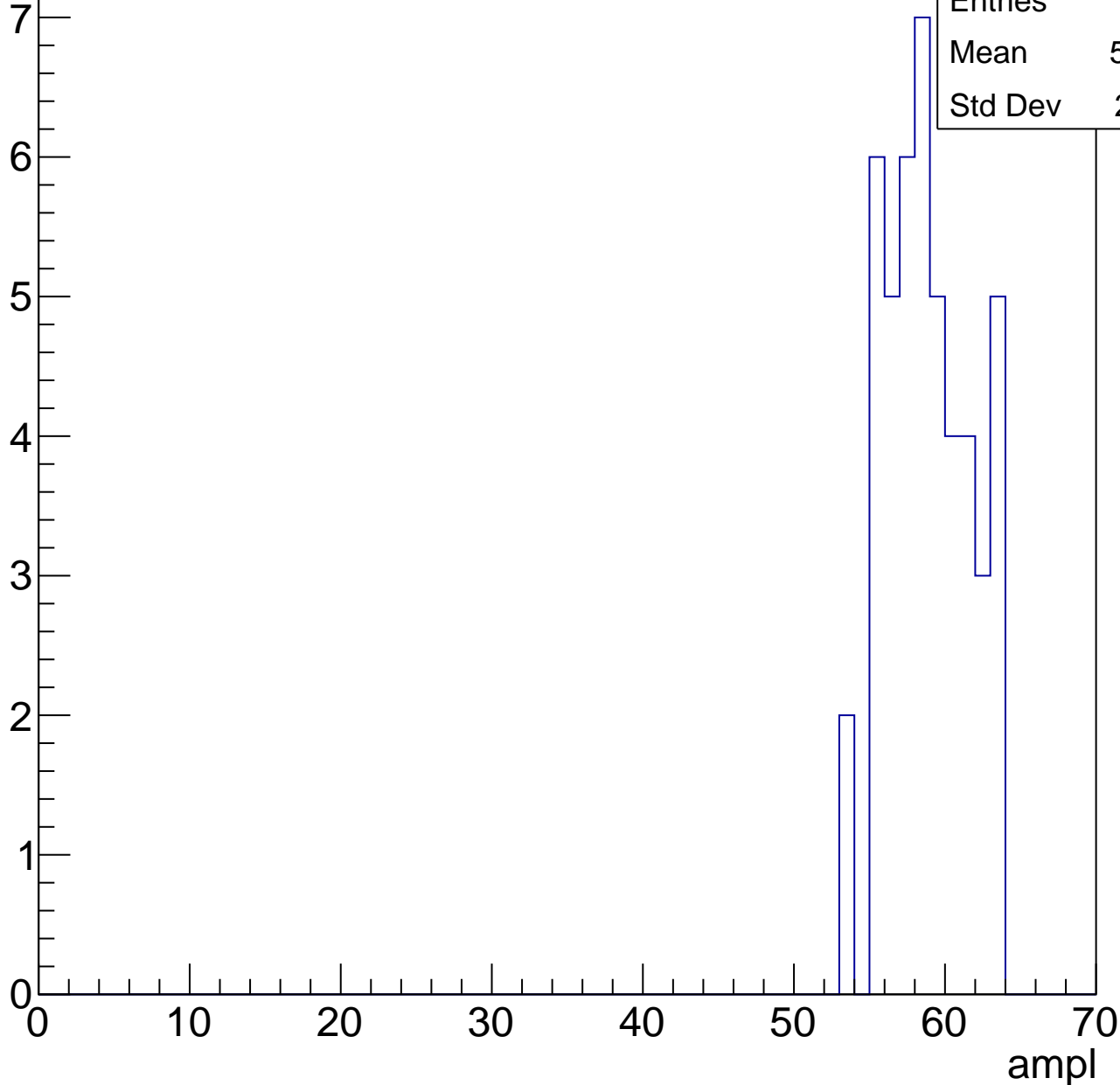
Entry



# B1L002S, U17-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

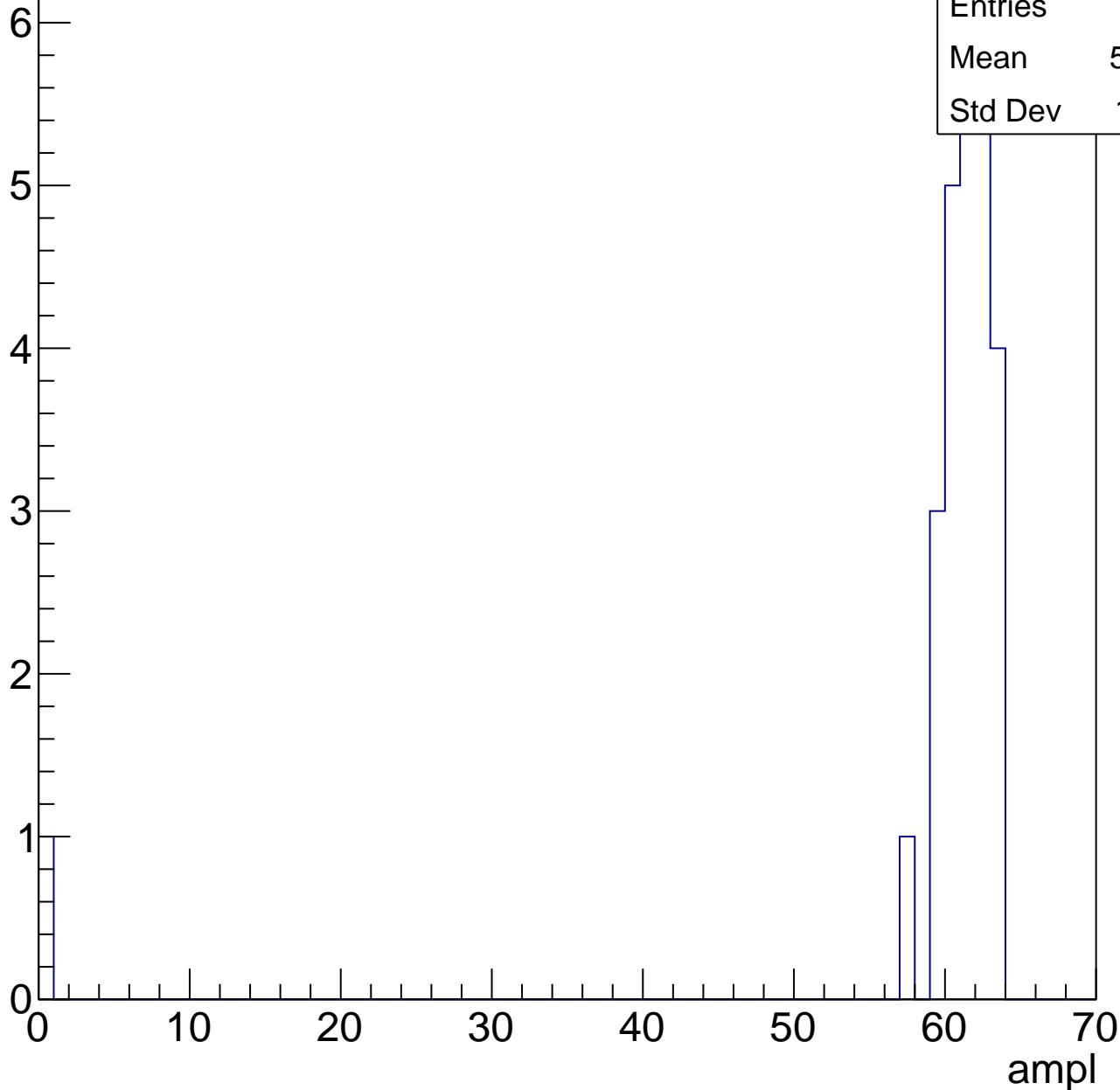


# B1L002S, U17-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.62
Std Dev	11.81

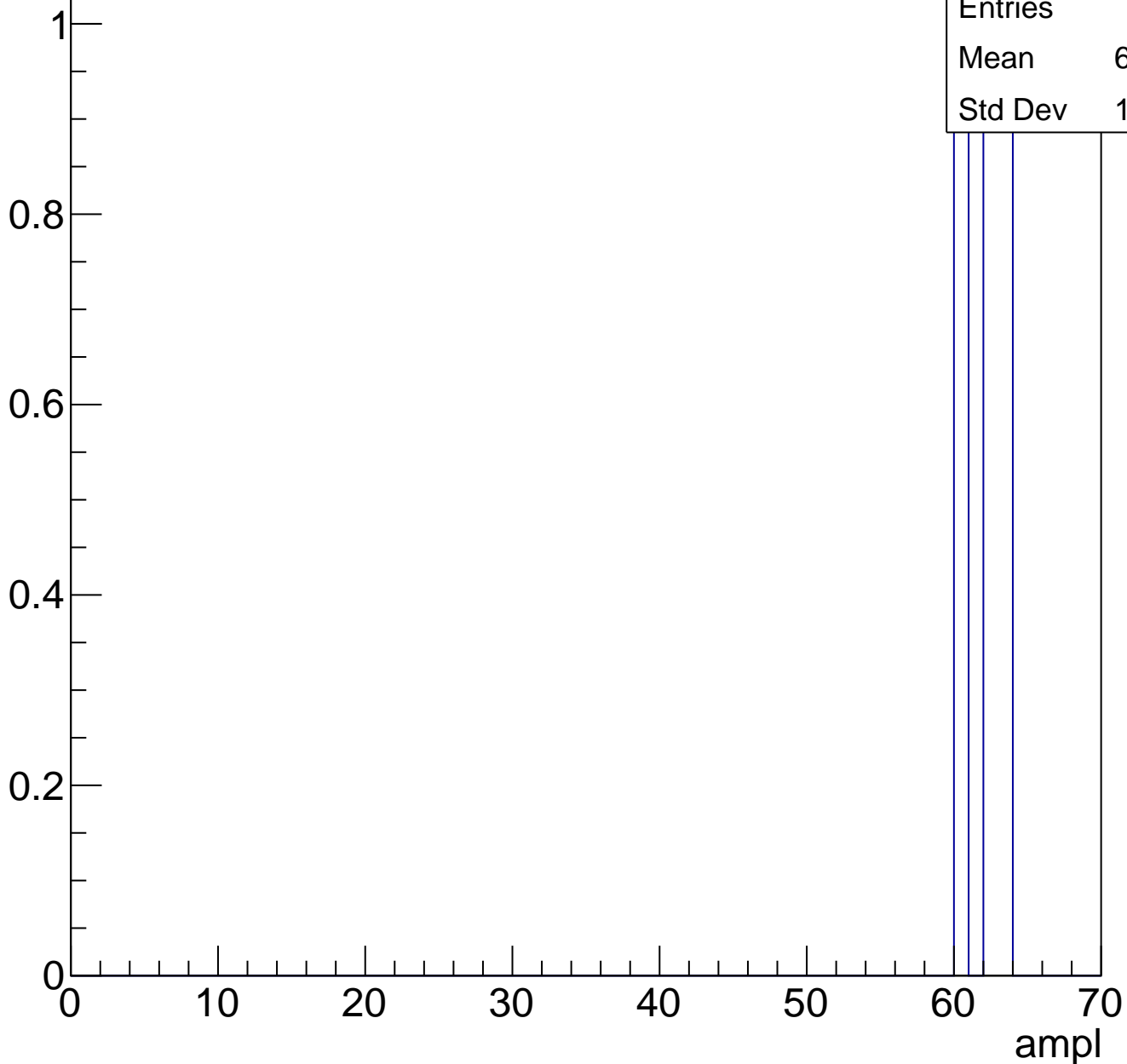




# B1L002S, U17-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch56, adc0

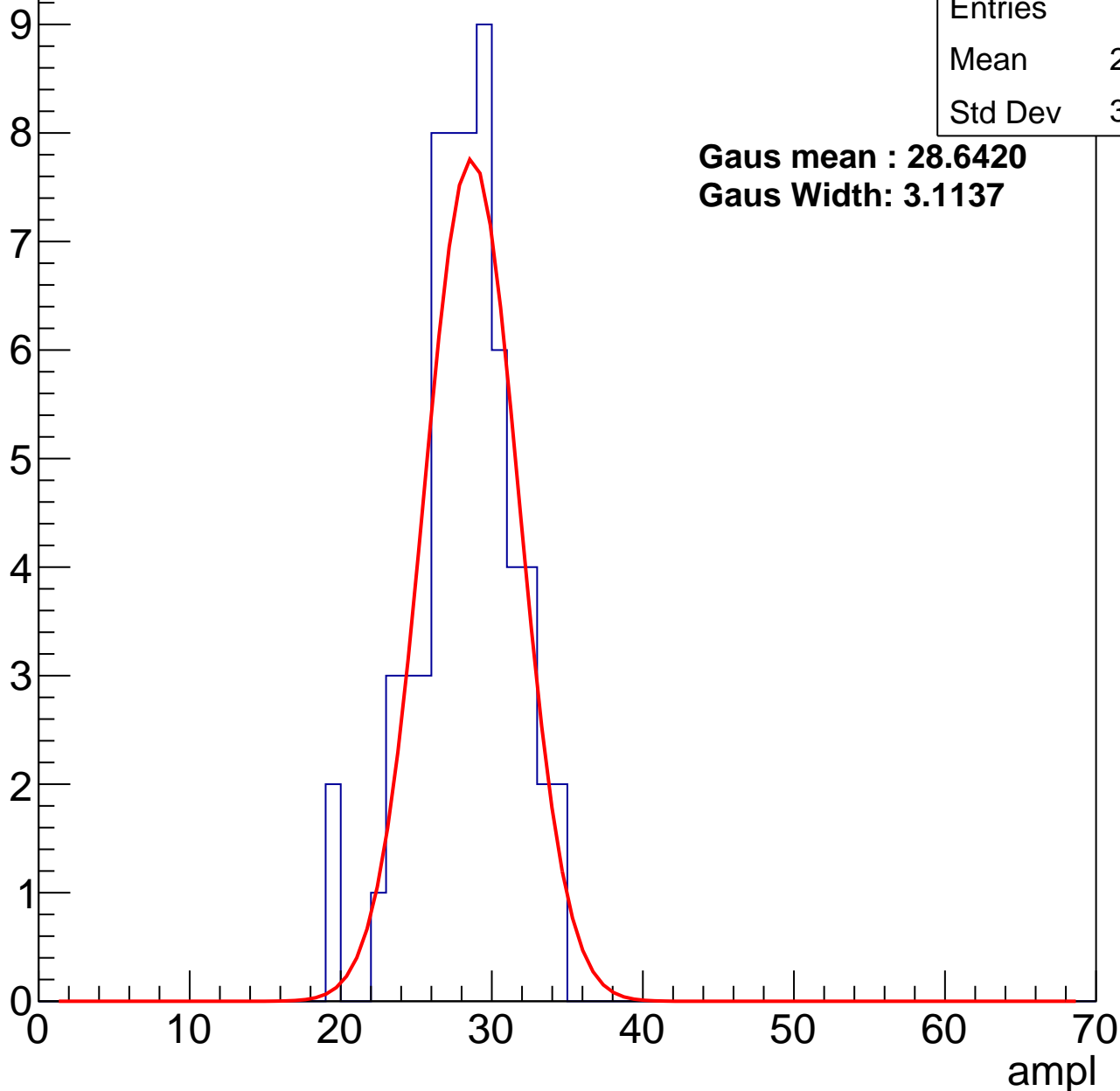
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.79
Std Dev	3.188

**Gaus mean : 28.6420**

**Gaus Width: 3.1137**



# B1L002S, U17-ch56, adc1

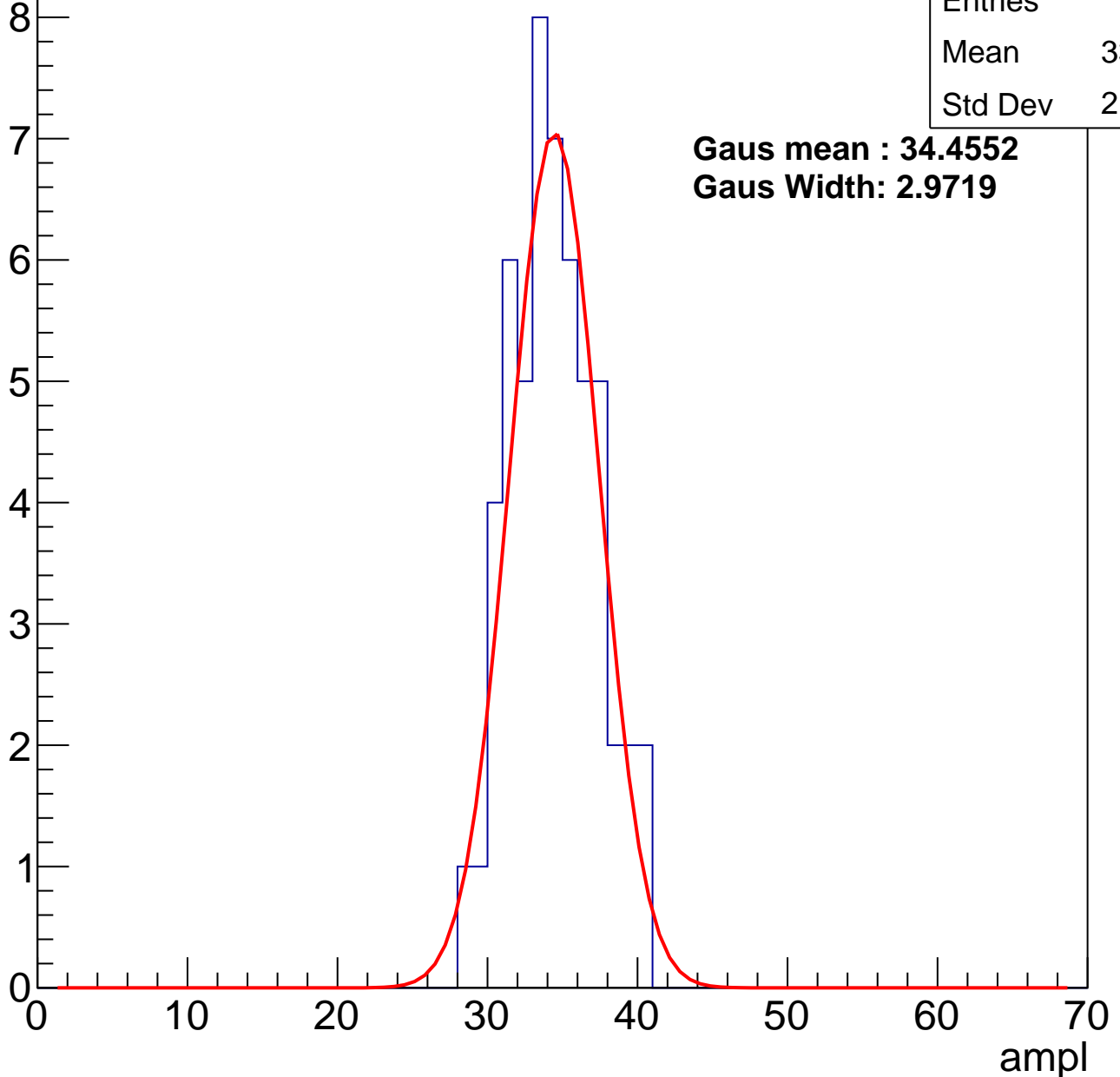
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	33.96
Std Dev	2.828

**Gaus mean : 34.4552**

**Gaus Width: 2.9719**



# B1L002S, U17-ch56, adc2

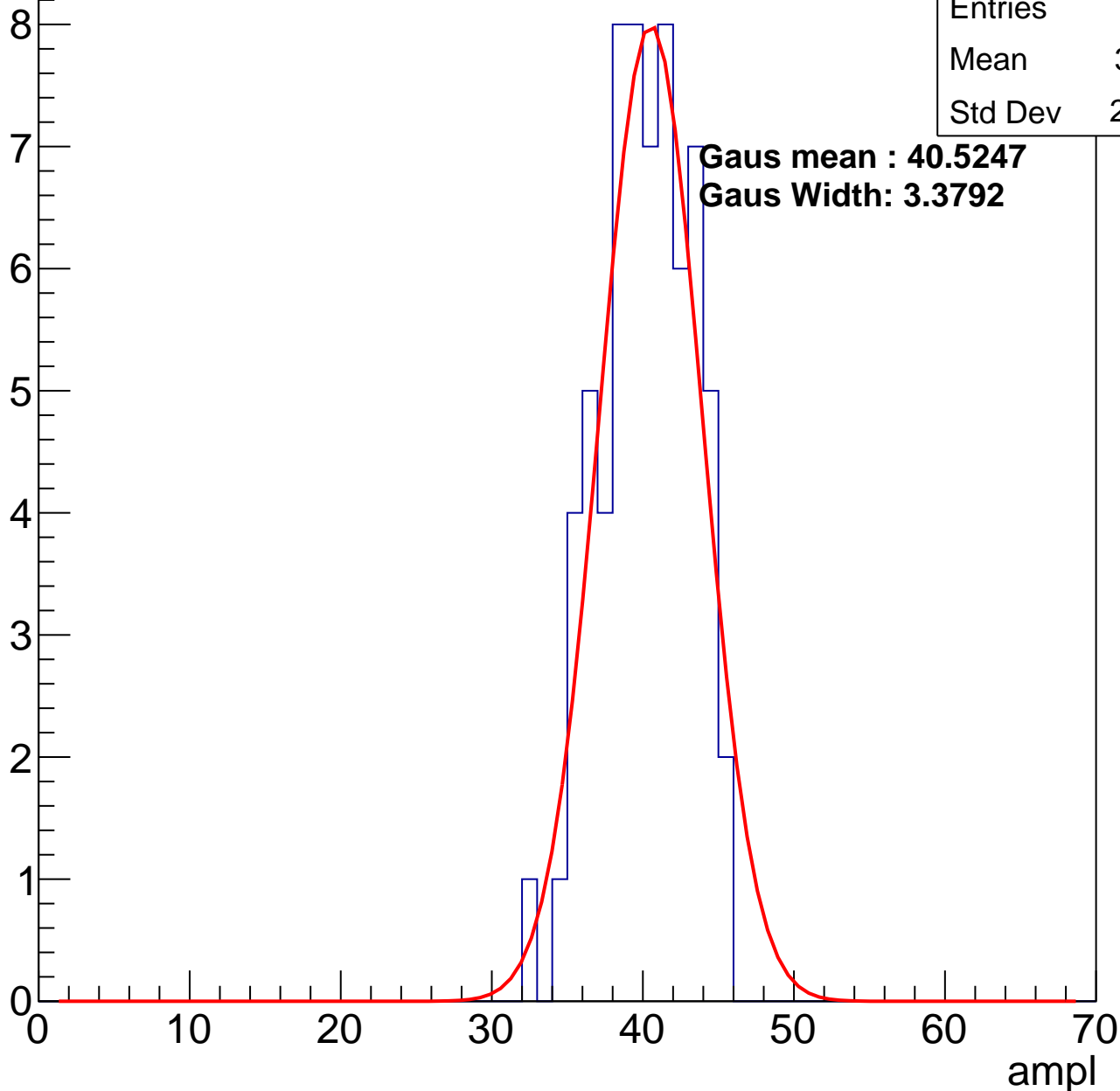
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	39.71
Std Dev	2.953

**Gaus mean : 40.5247**

**Gaus Width: 3.3792**

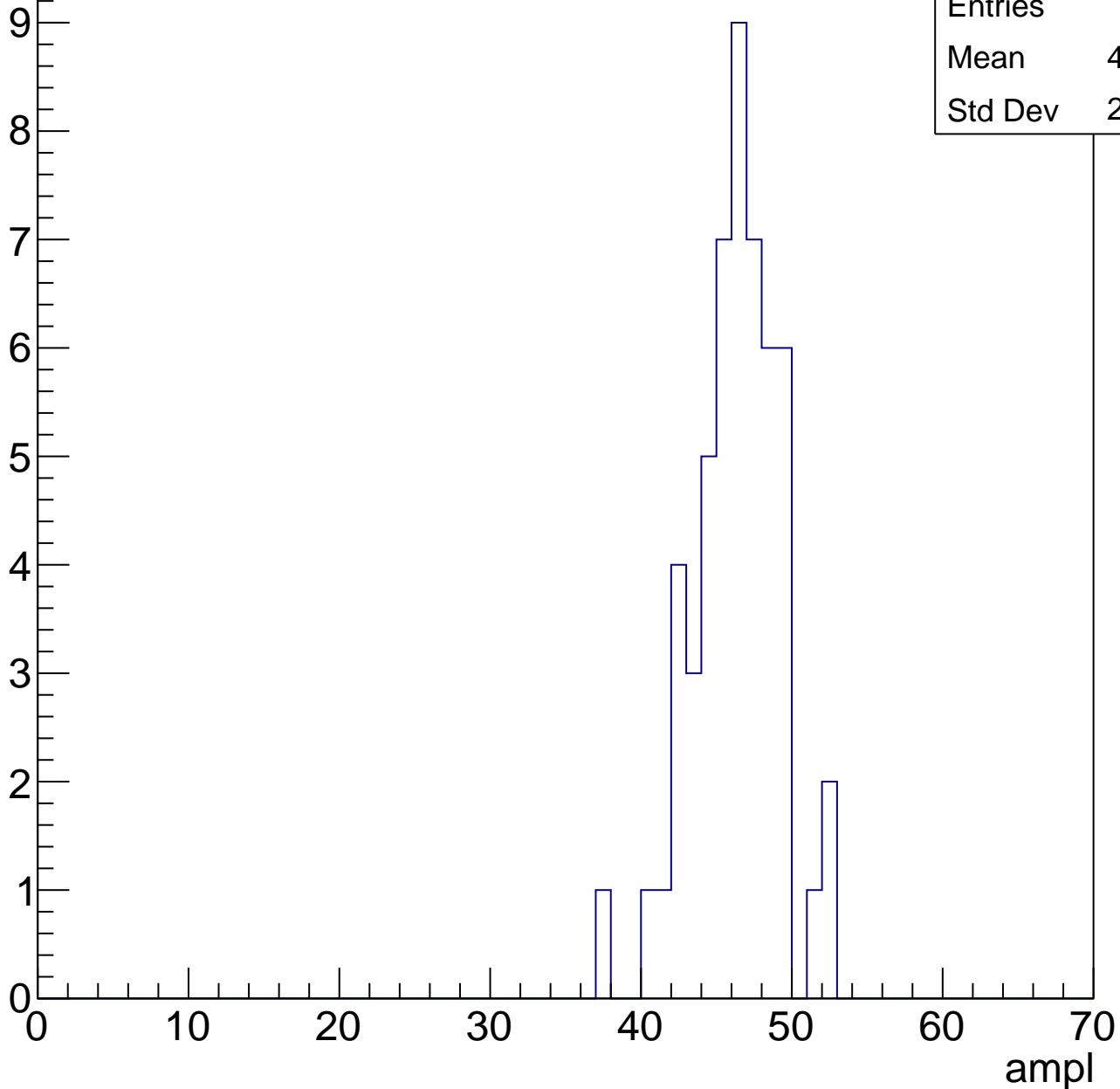


# B1L002S, U17-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	45.85
Std Dev	2.884

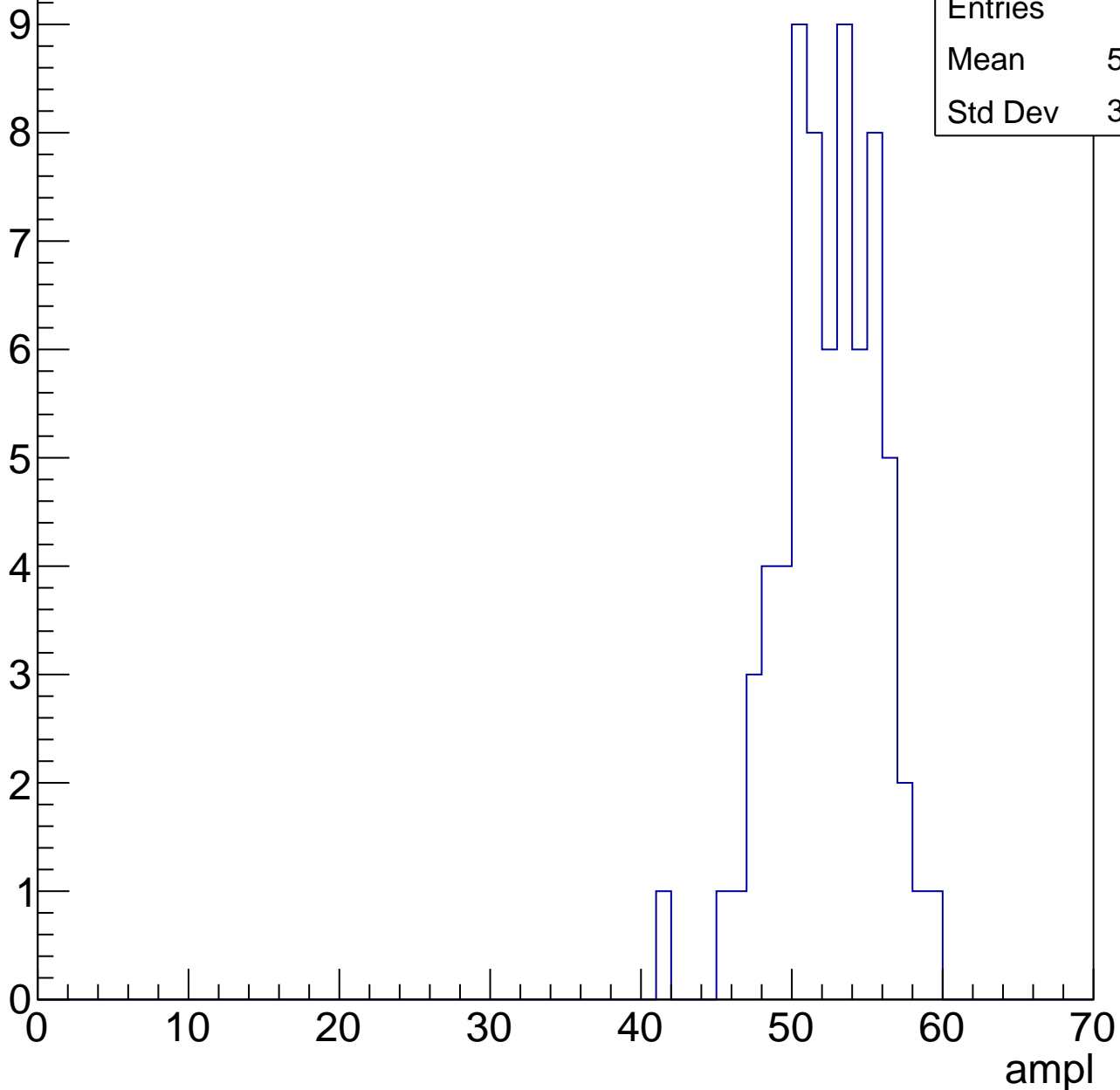


# B1L002S, U17-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	51.93
Std Dev	3.285

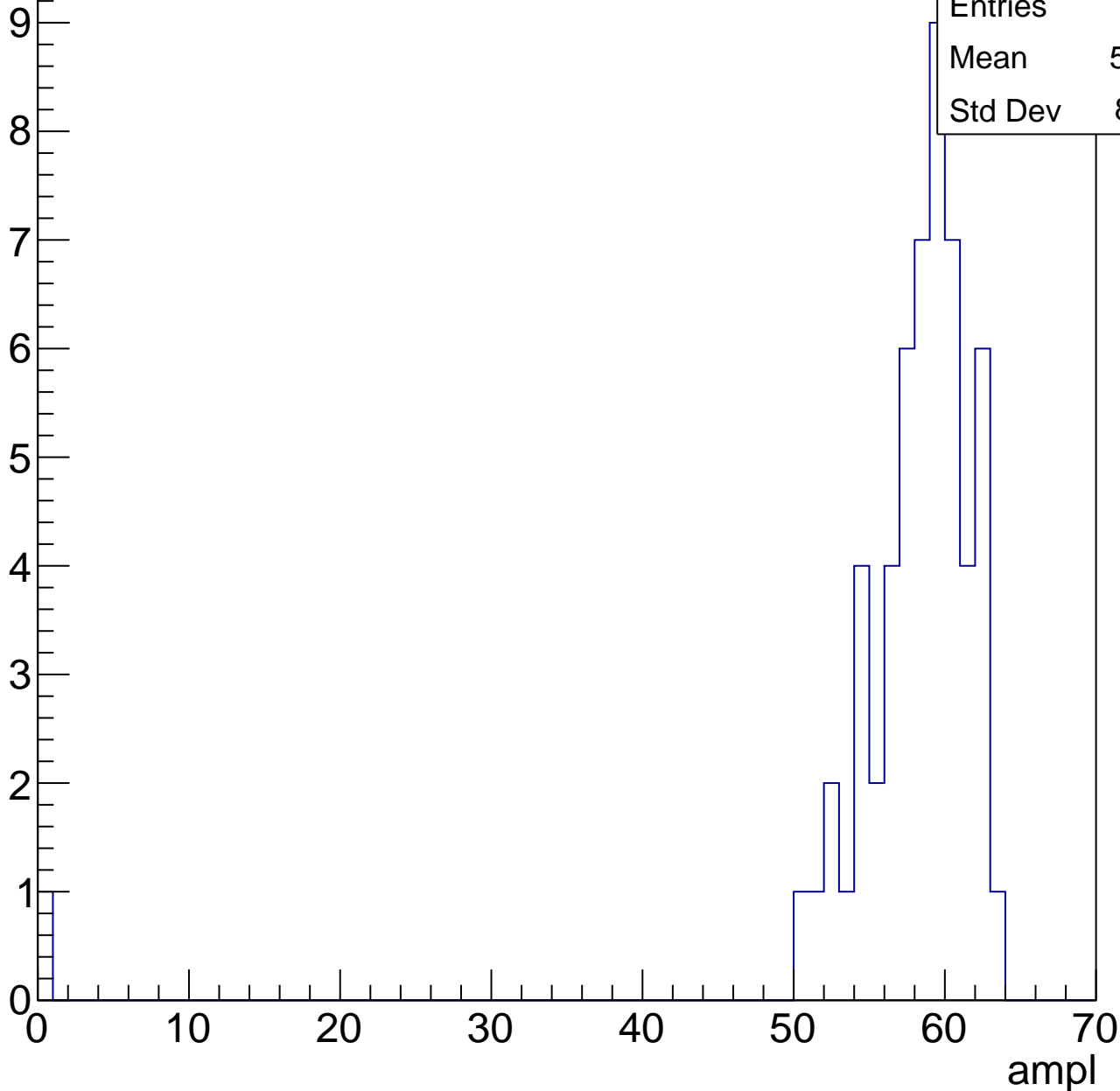


# B1L002S, U17-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.89
Std Dev	8.241

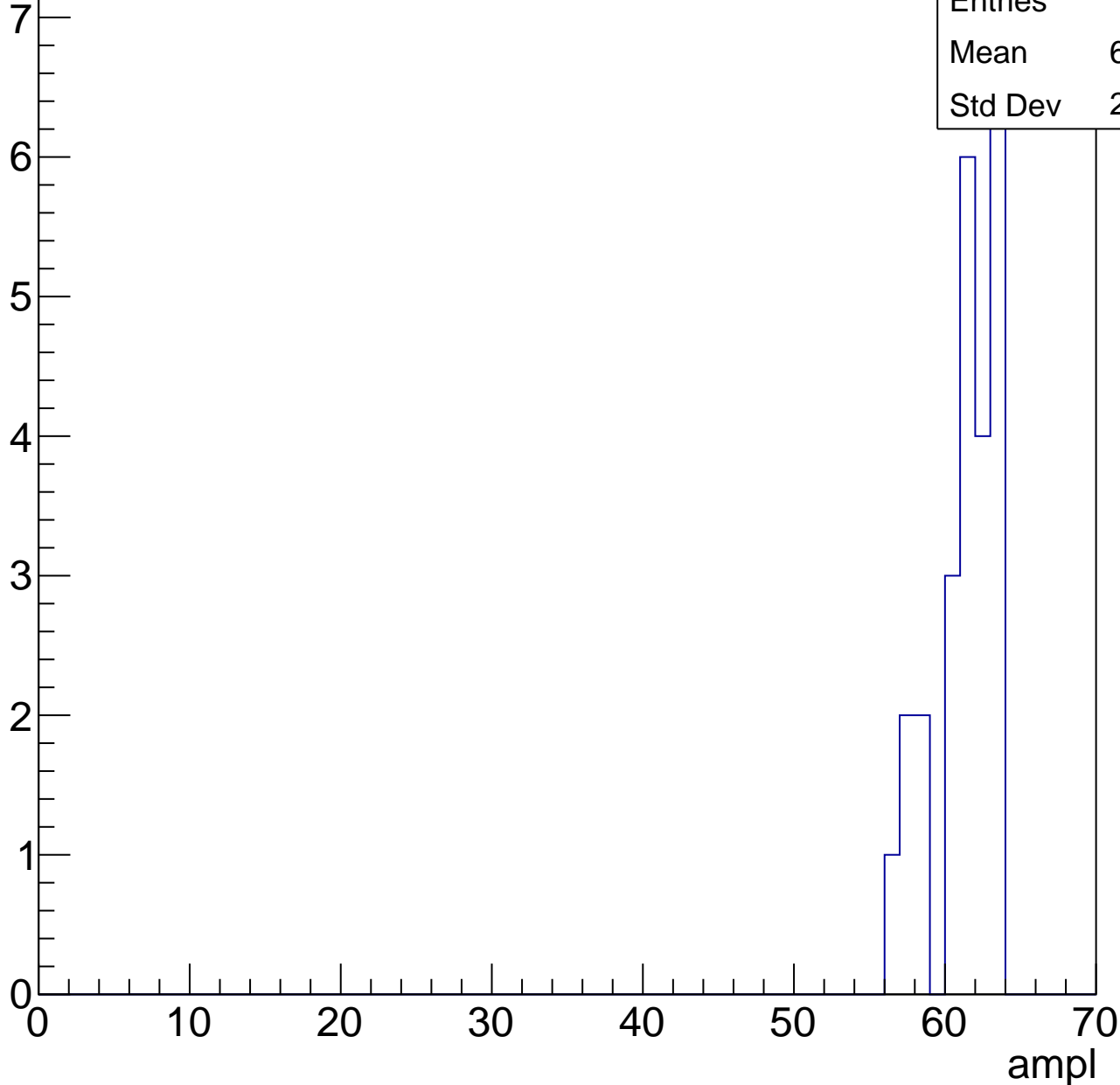


# B1L002S, U17-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	60.84
Std Dev	2.092





# B1L002S, U17-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch57, adc0

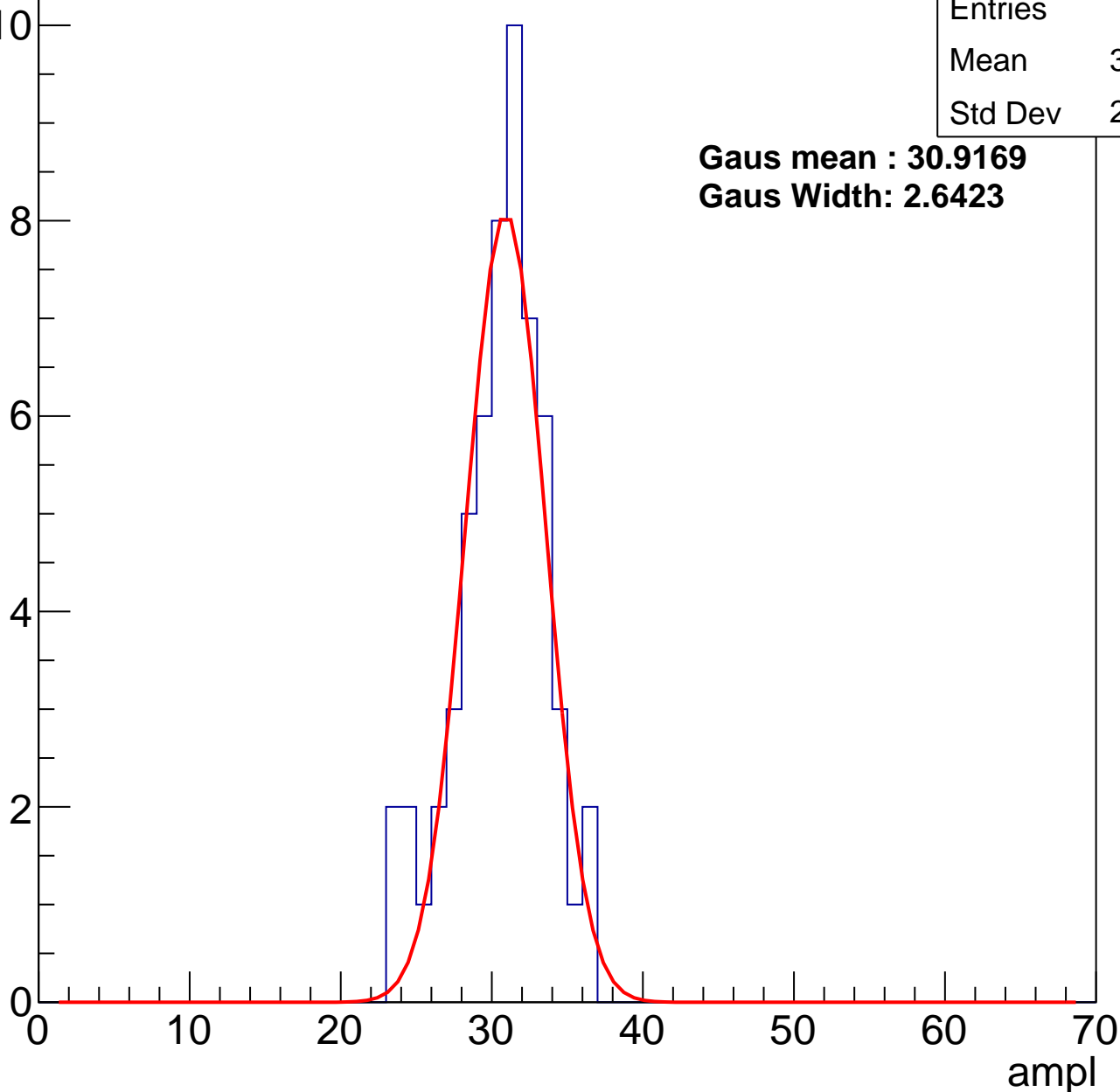
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	30.12
Std Dev	2.983

**Gaus mean : 30.9169**

**Gaus Width: 2.6423**



# B1L002S, U17-ch57, adc1

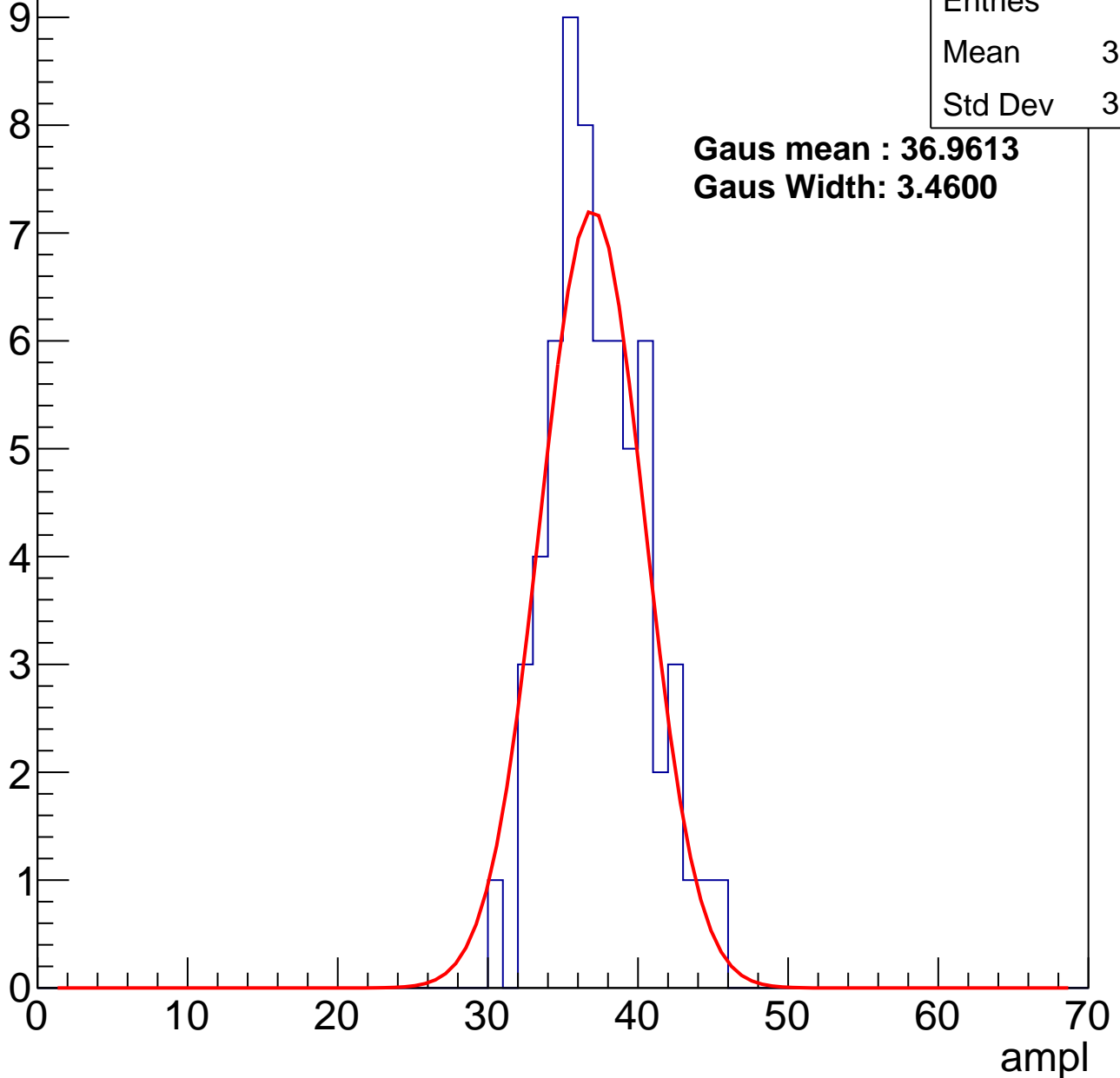
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	36.94
Std Dev	3.167

**Gaus mean : 36.9613**

**Gaus Width: 3.4600**



# B1L002S, U17-ch57, adc2

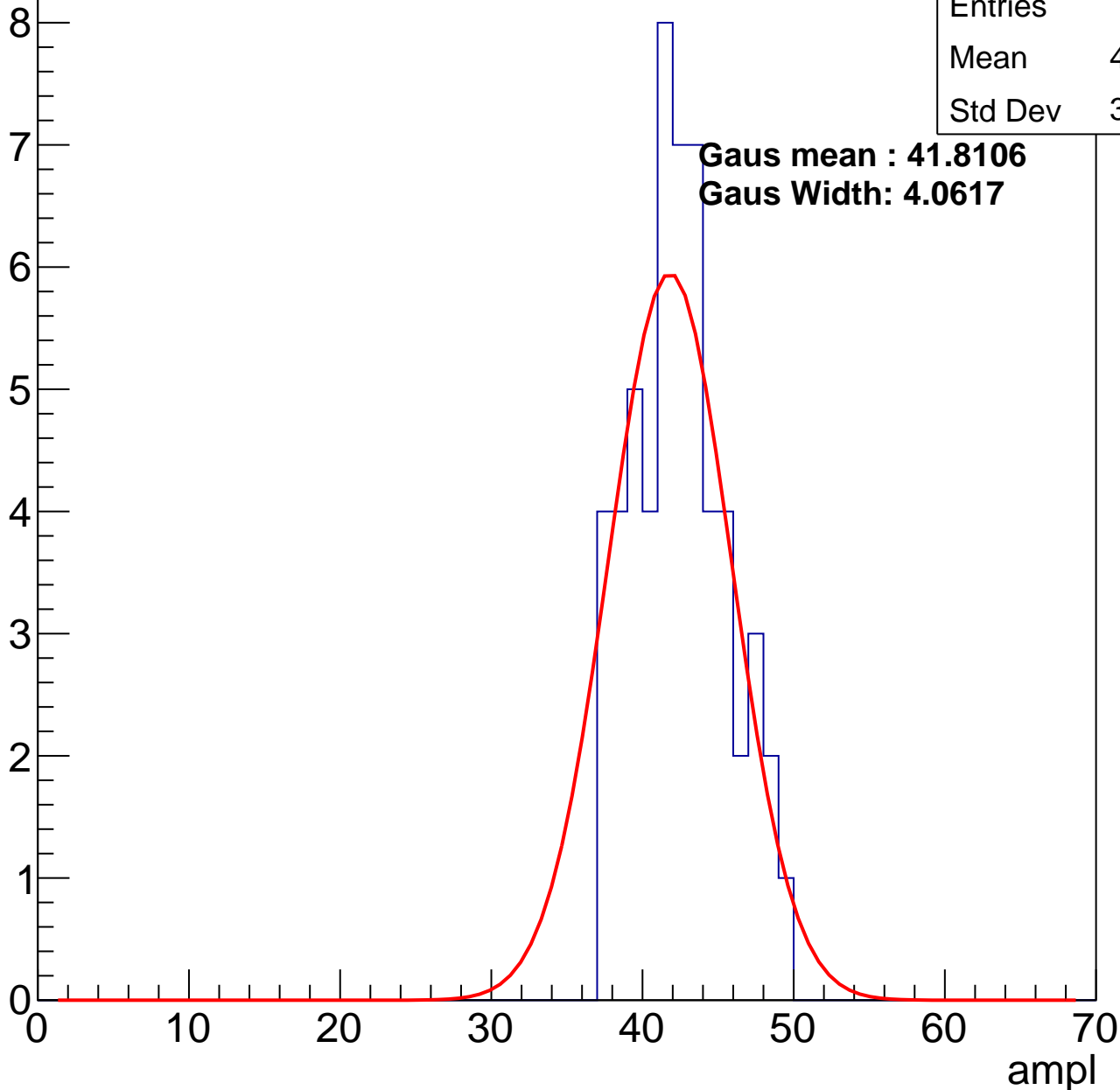
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	42.04
Std Dev	3.075

**Gaus mean : 41.8106**

**Gaus Width: 4.0617**

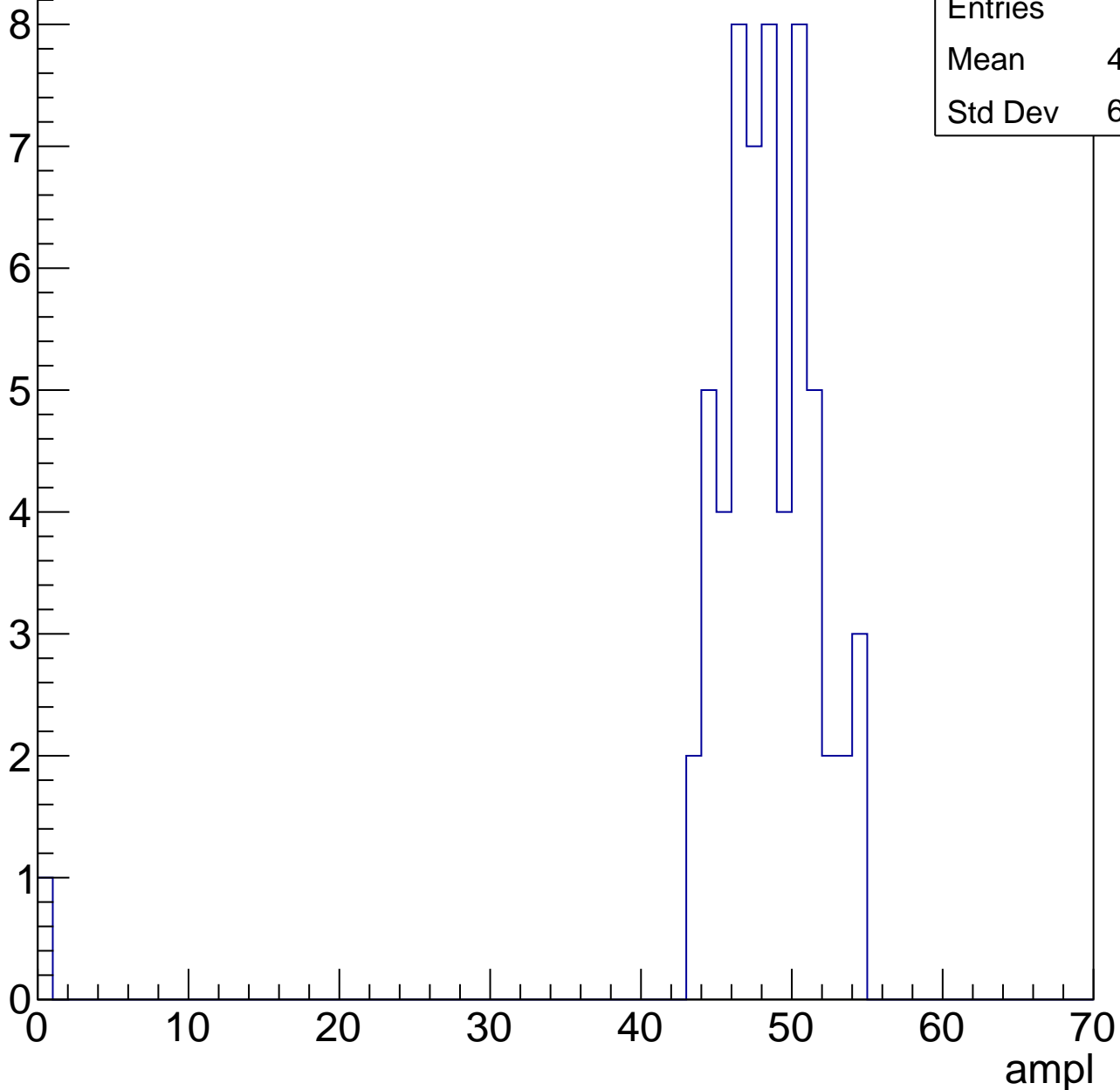


# B1L002S, U17-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

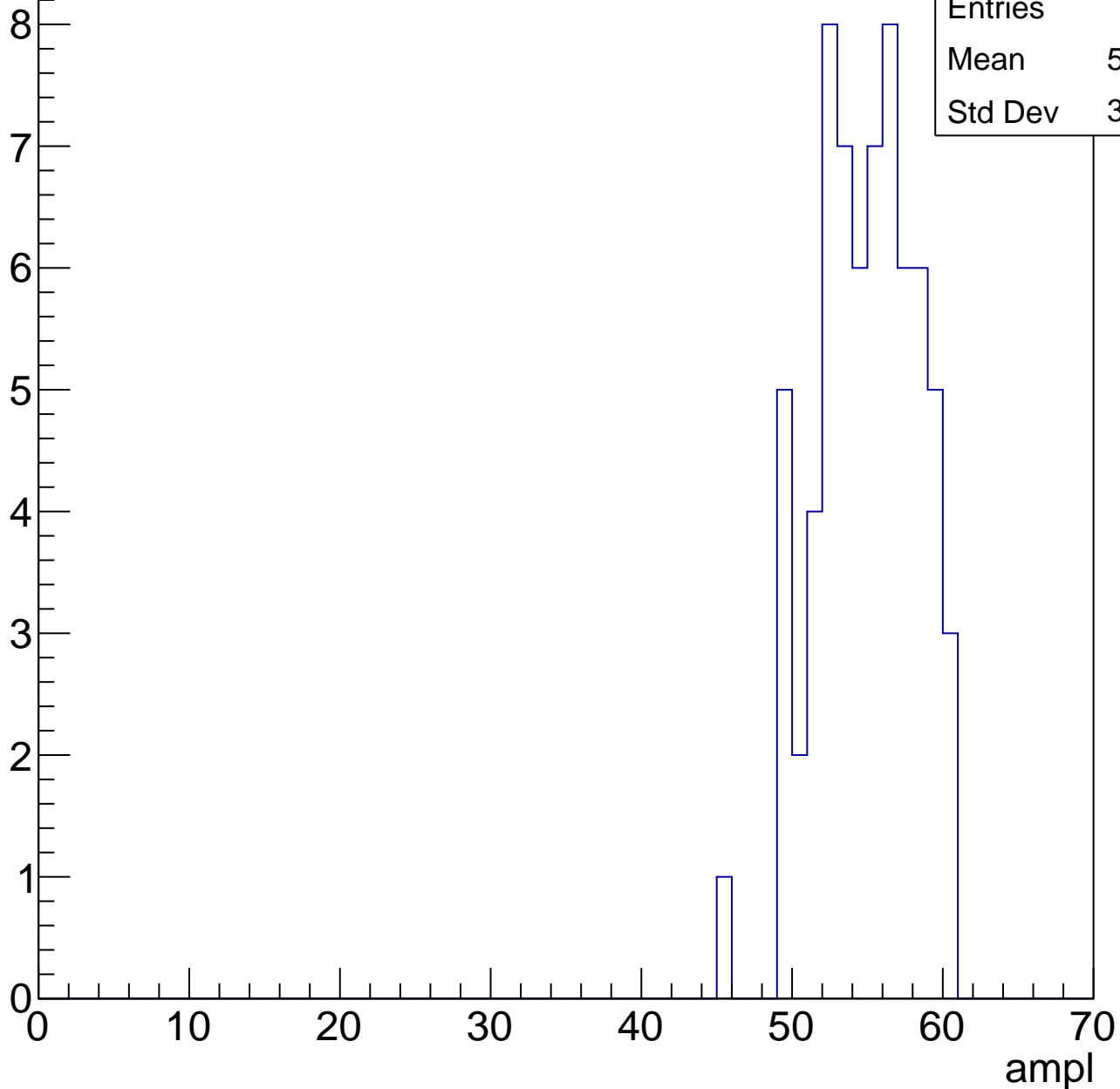
Entries	59
Mean	47.29
Std Dev	6.827



# B1L002S, U17-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



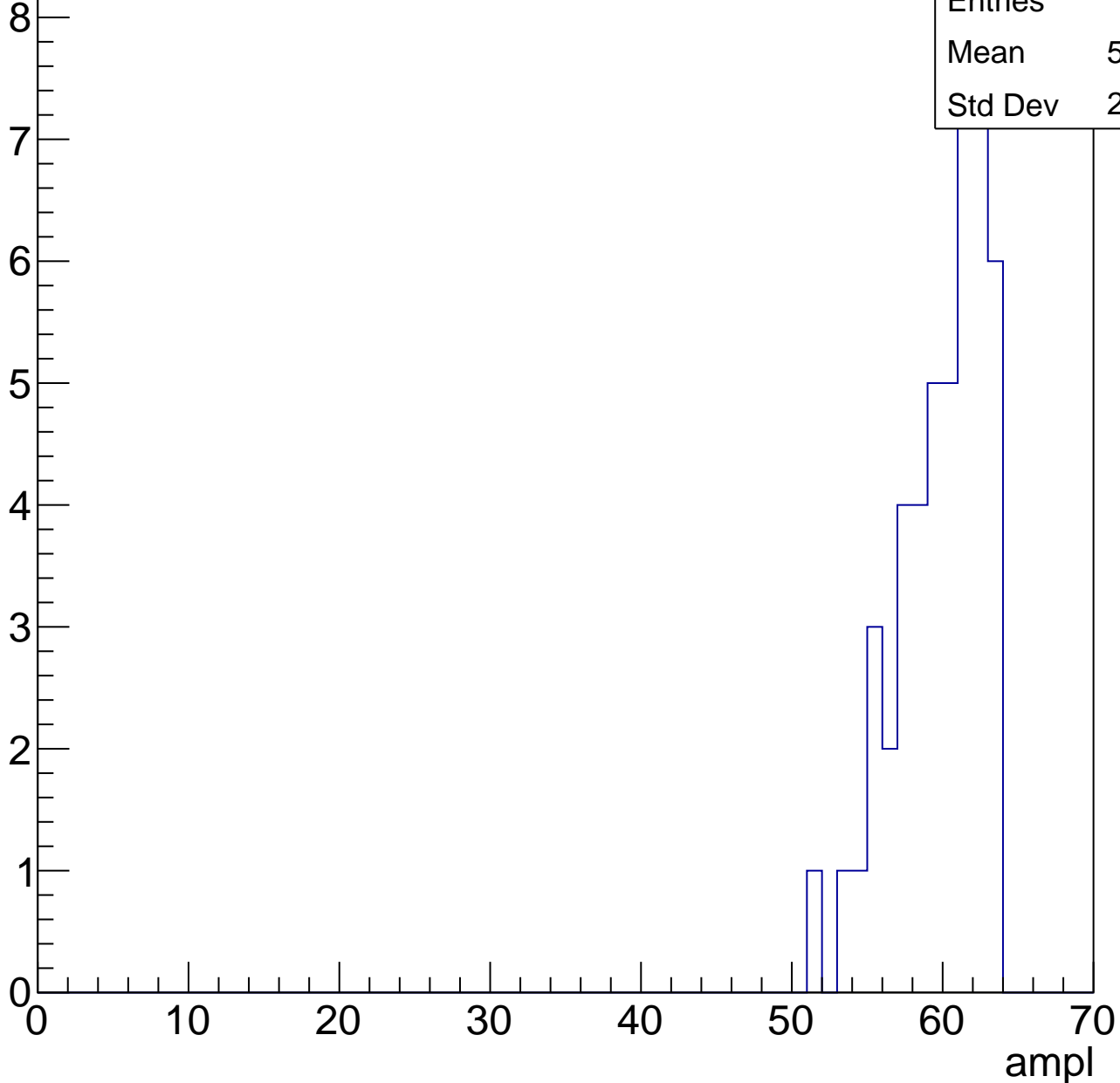
Entries	68
Mean	54.46
Std Dev	3.256

# B1L002S, U17-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

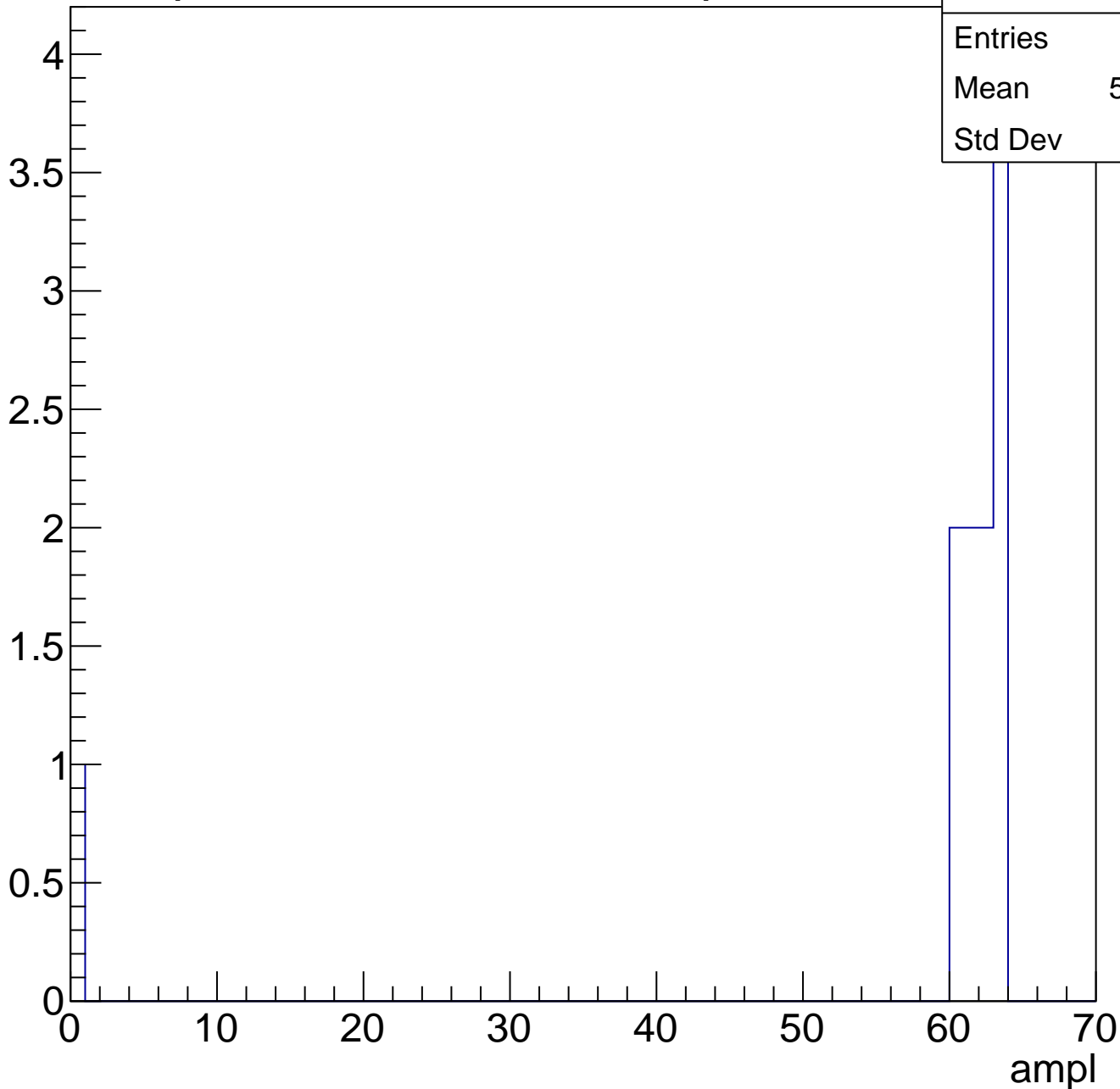
Entries	48
Mean	59.42
Std Dev	2.914



# B1L002S, U17-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U17-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch58, adc0

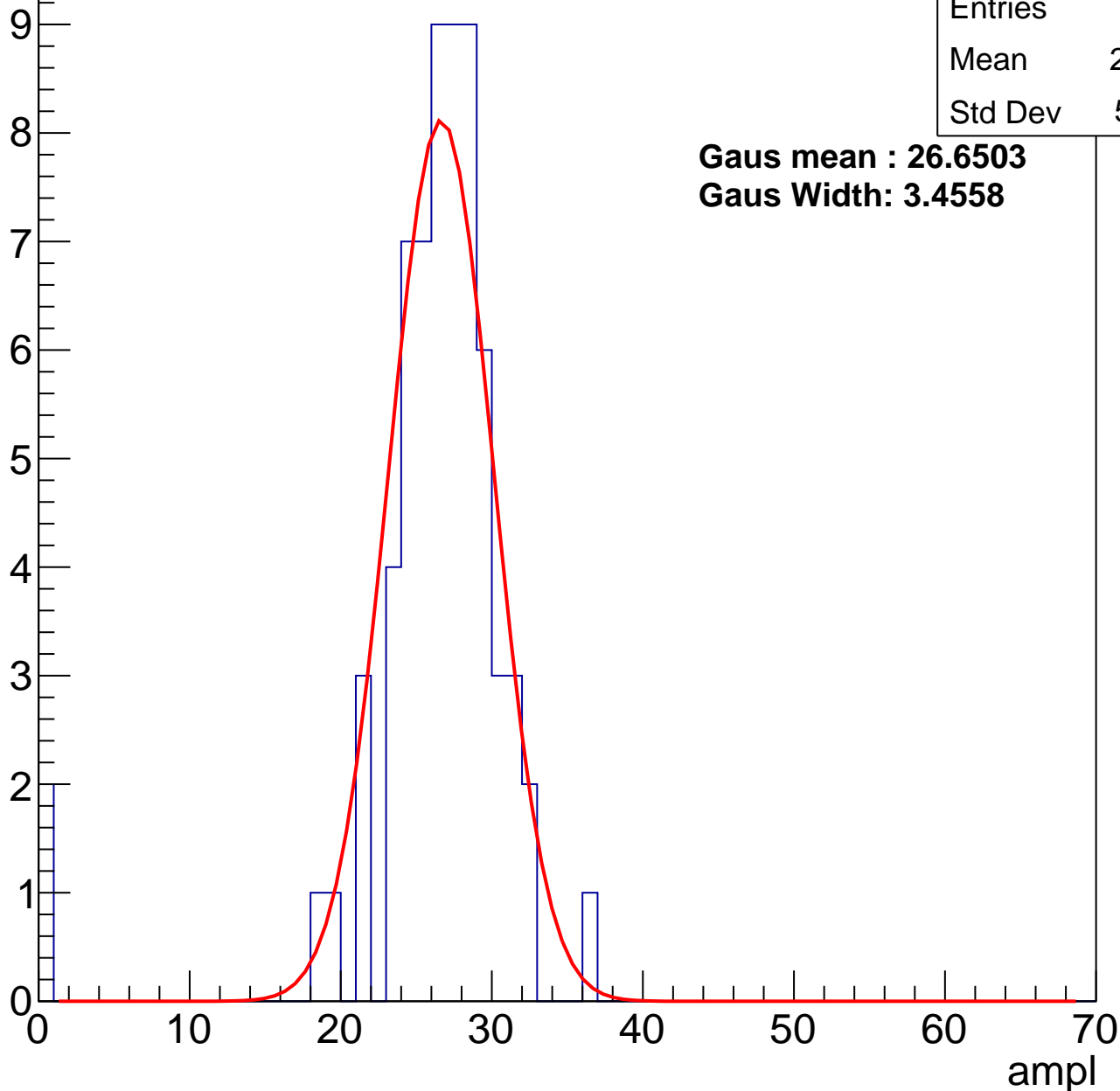
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	25.69
Std Dev	5.461

**Gaus mean : 26.6503**

**Gaus Width: 3.4558**



# B1L002S, U17-ch58, adc1

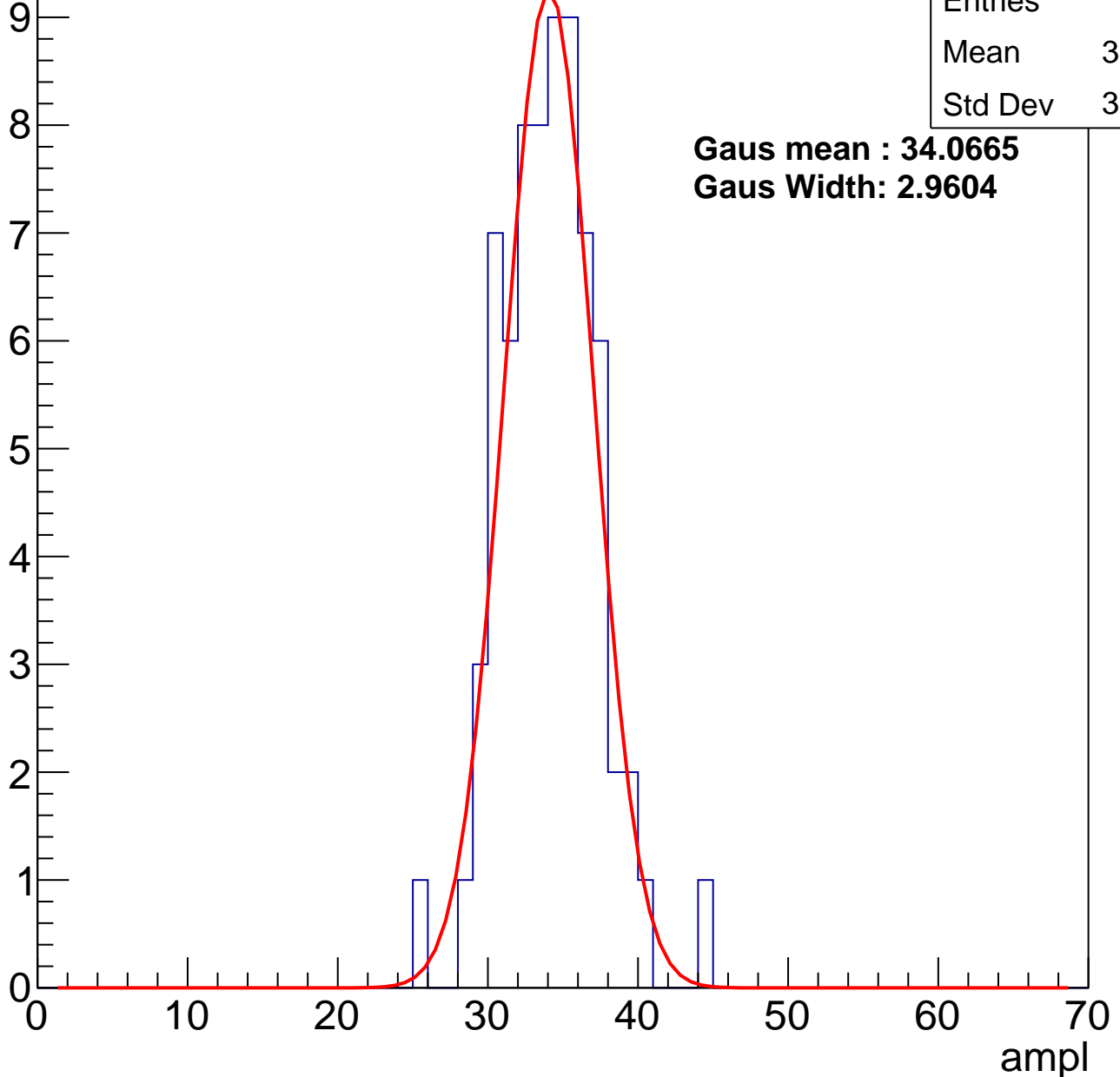
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	33.65
Std Dev	3.136

**Gaus mean : 34.0665**

**Gaus Width: 2.9604**



# B1L002S, U17-ch58, adc2

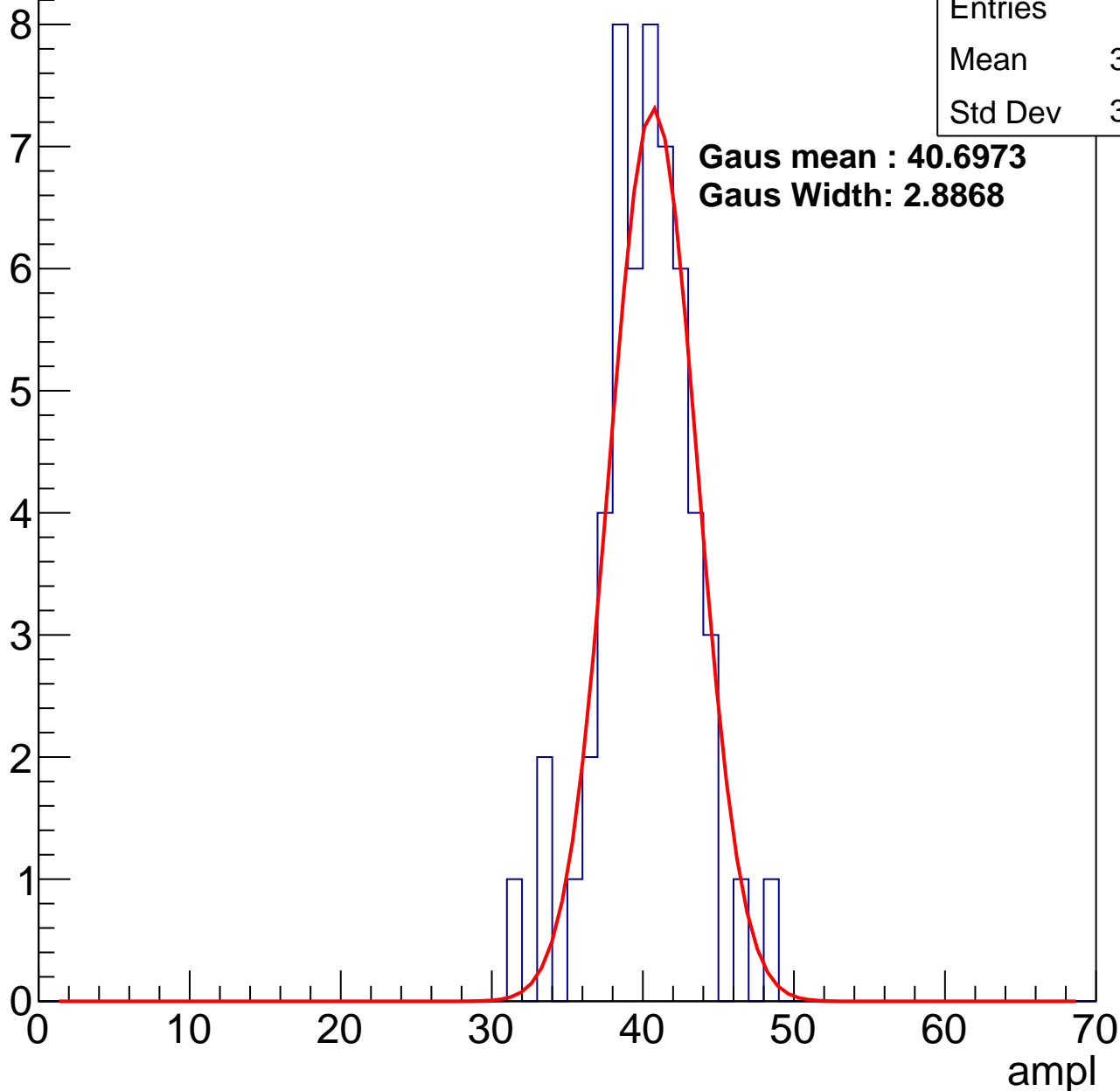
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	39.76
Std Dev	3.109

**Gaus mean : 40.6973**

**Gaus Width: 2.8868**



# B1L002S, U17-ch58, adc3

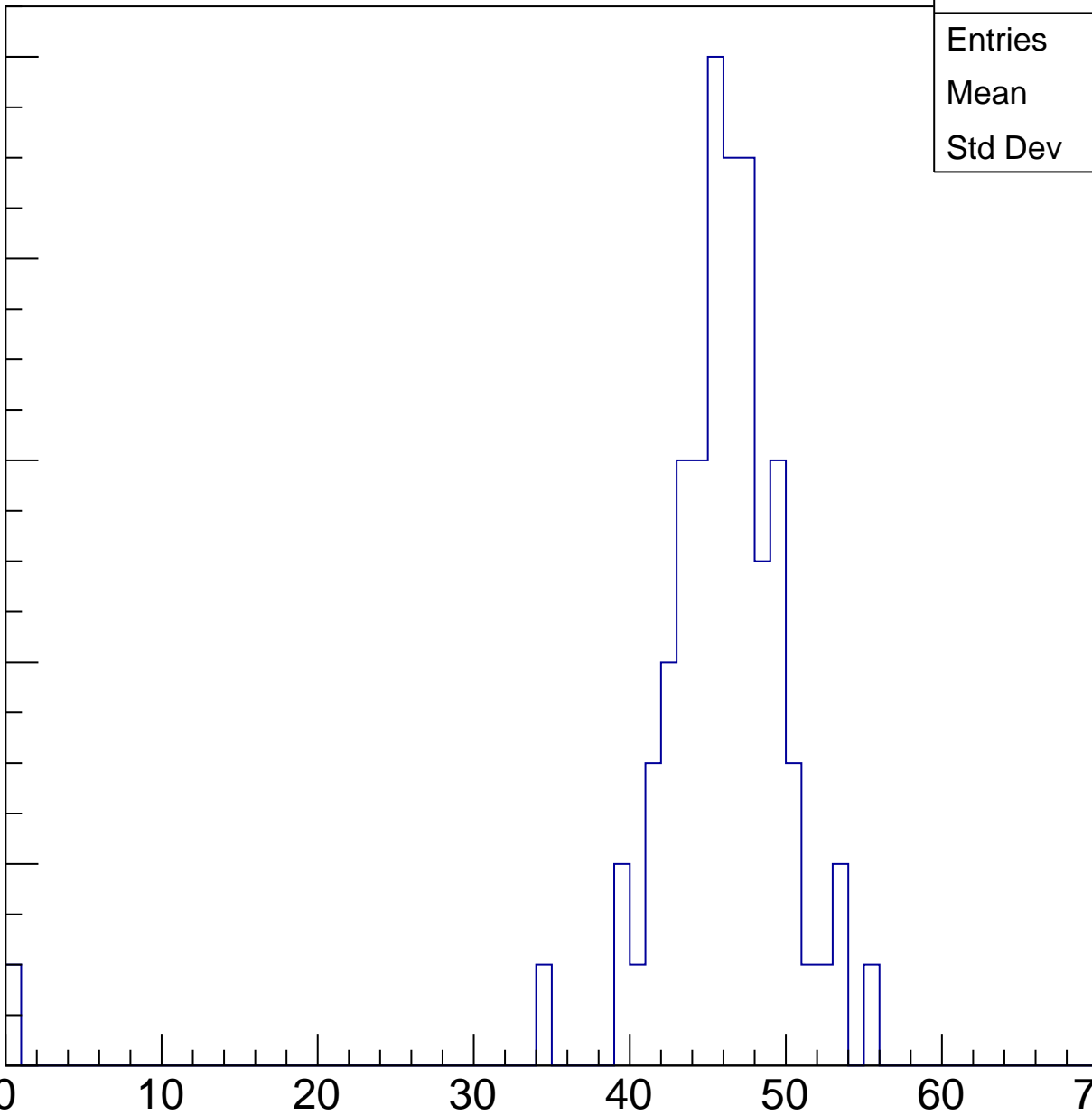
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	45.07
Std Dev	6.425

Entry

10  
8  
6  
4  
2  
0

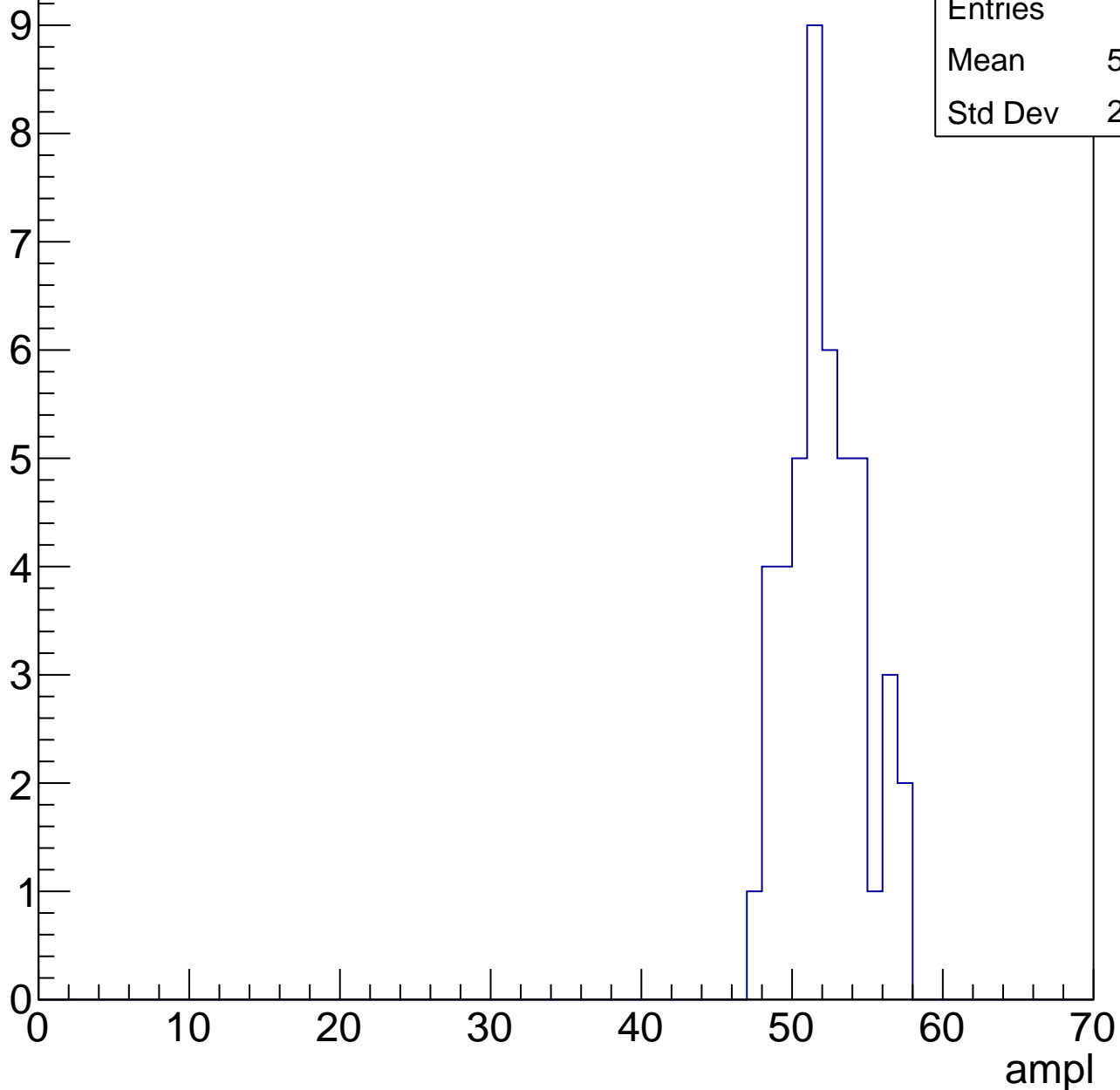
ampl



# B1L002S, U17-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	45
Mean	51.73
Std Dev	2.507

# B1L002S, U17-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	56.95
Std Dev	2.956

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

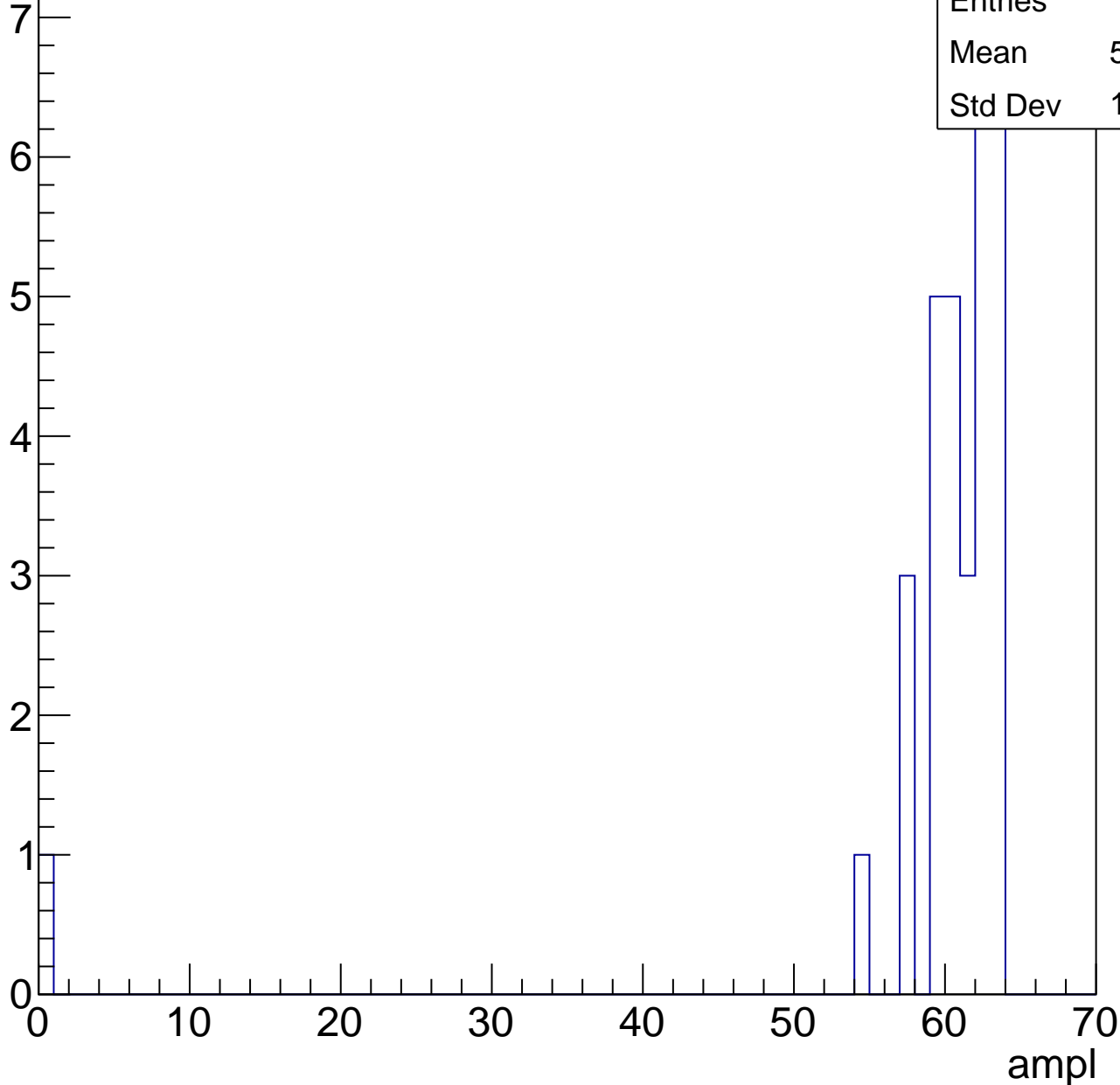
70

# B1L002S, U17-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	58.69
Std Dev	10.76

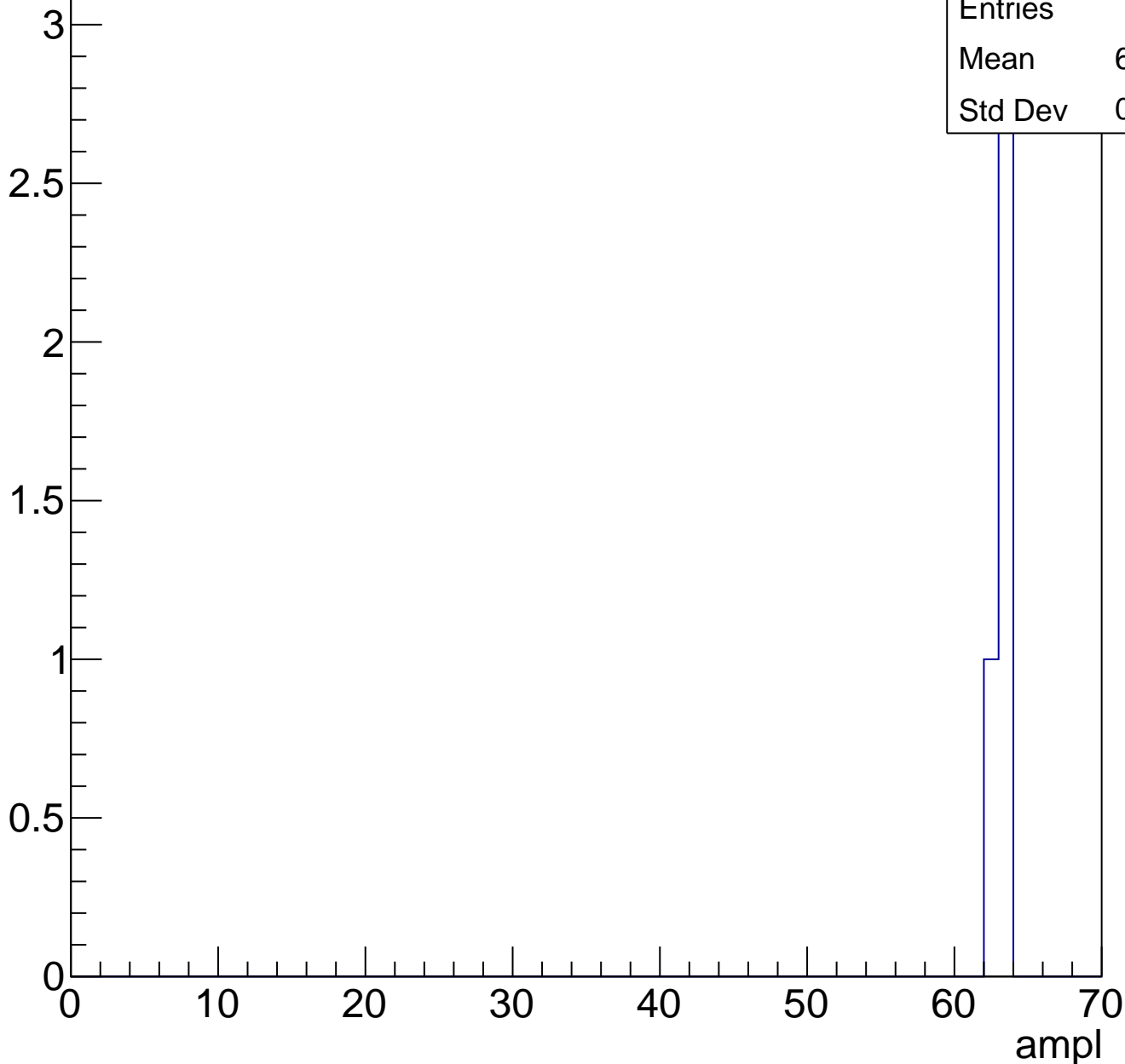




# B1L002S, U17-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch59, adc0

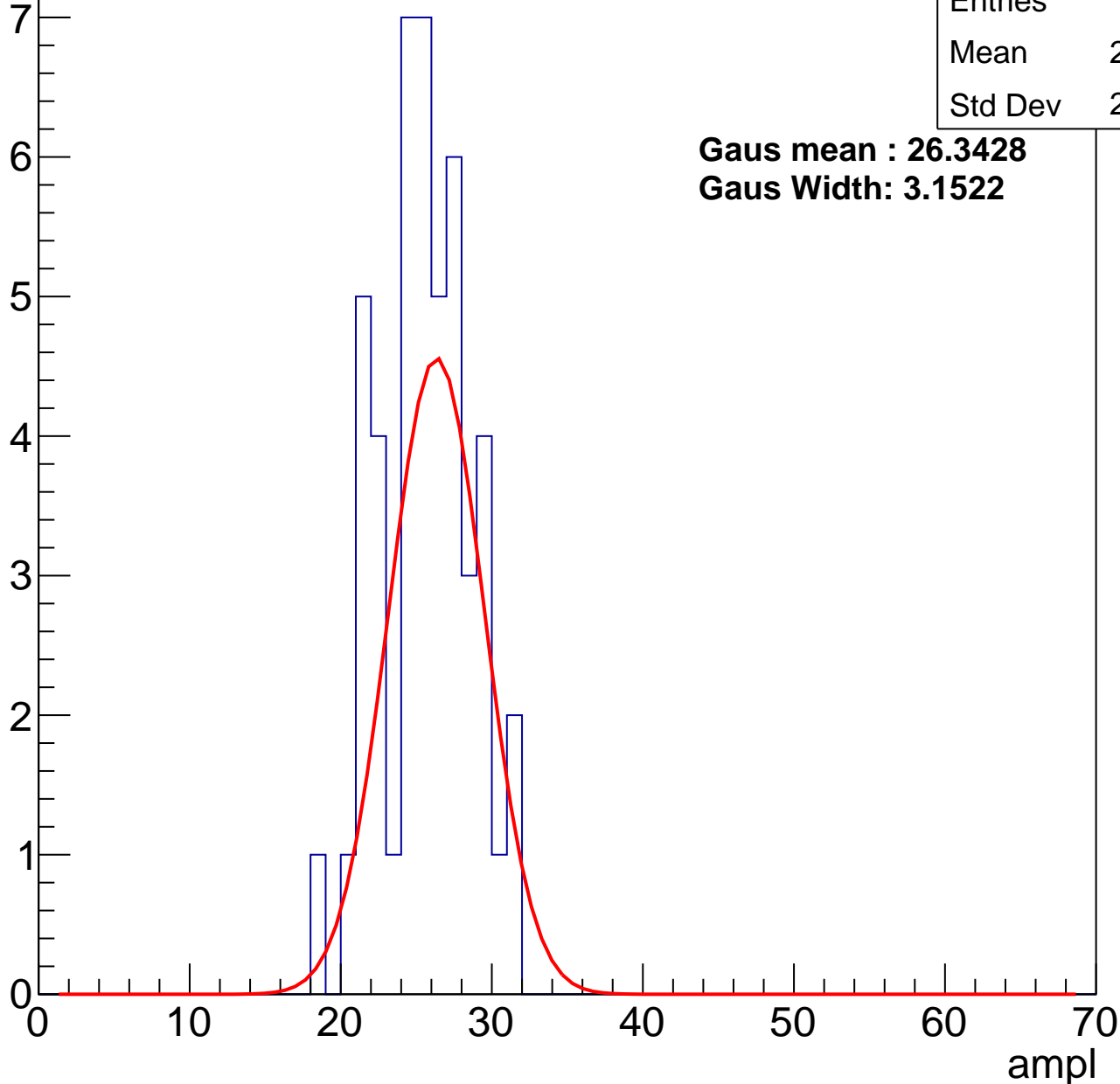
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	25.13
Std Dev	2.979

**Gaus mean : 26.3428**

**Gaus Width: 3.1522**



# B1L002S, U17-ch59, adc1

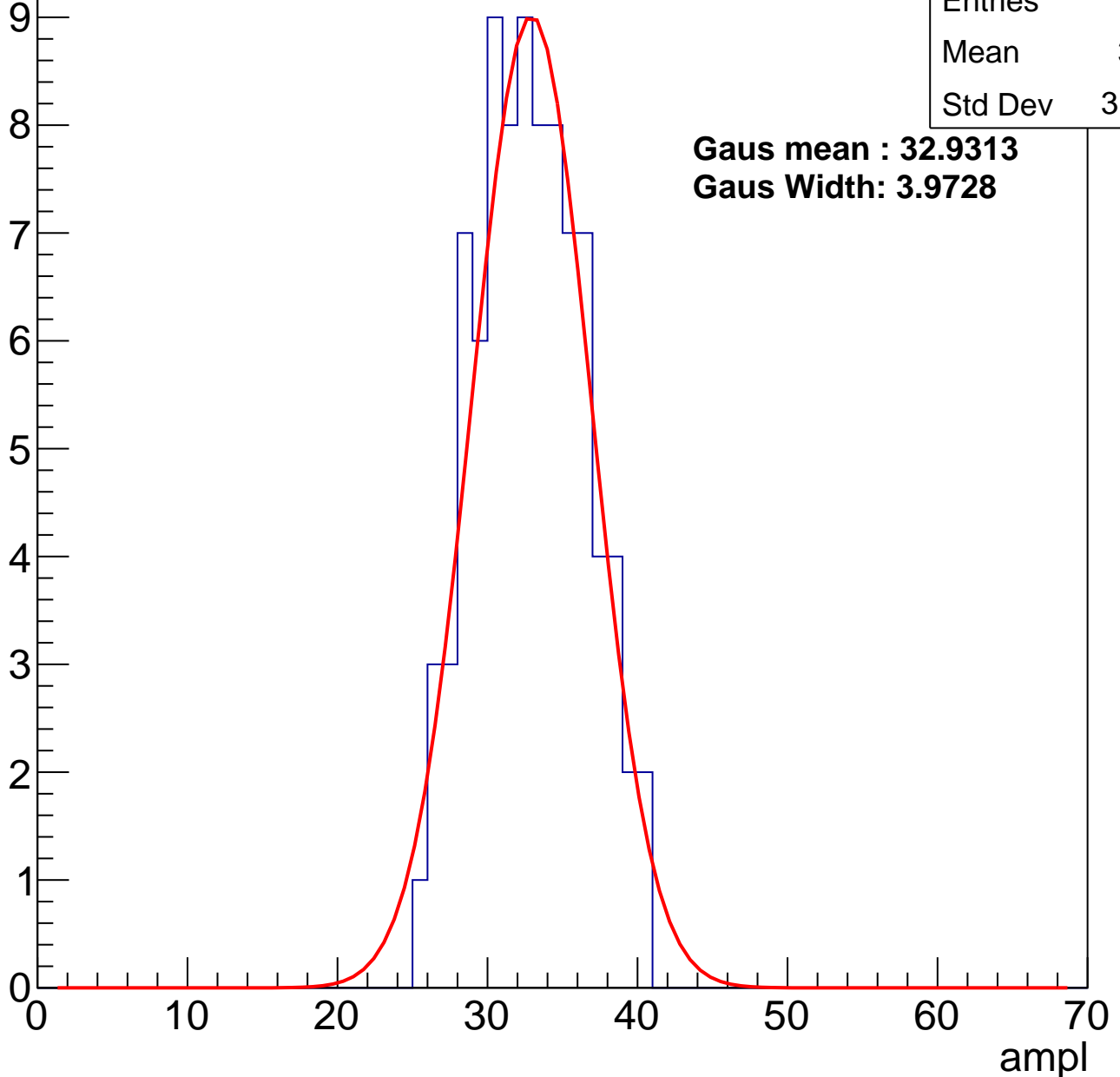
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	88
Mean	32.4
Std Dev	3.556

**Gaus mean : 32.9313**

**Gaus Width: 3.9728**



# B1L002S, U17-ch59, adc2

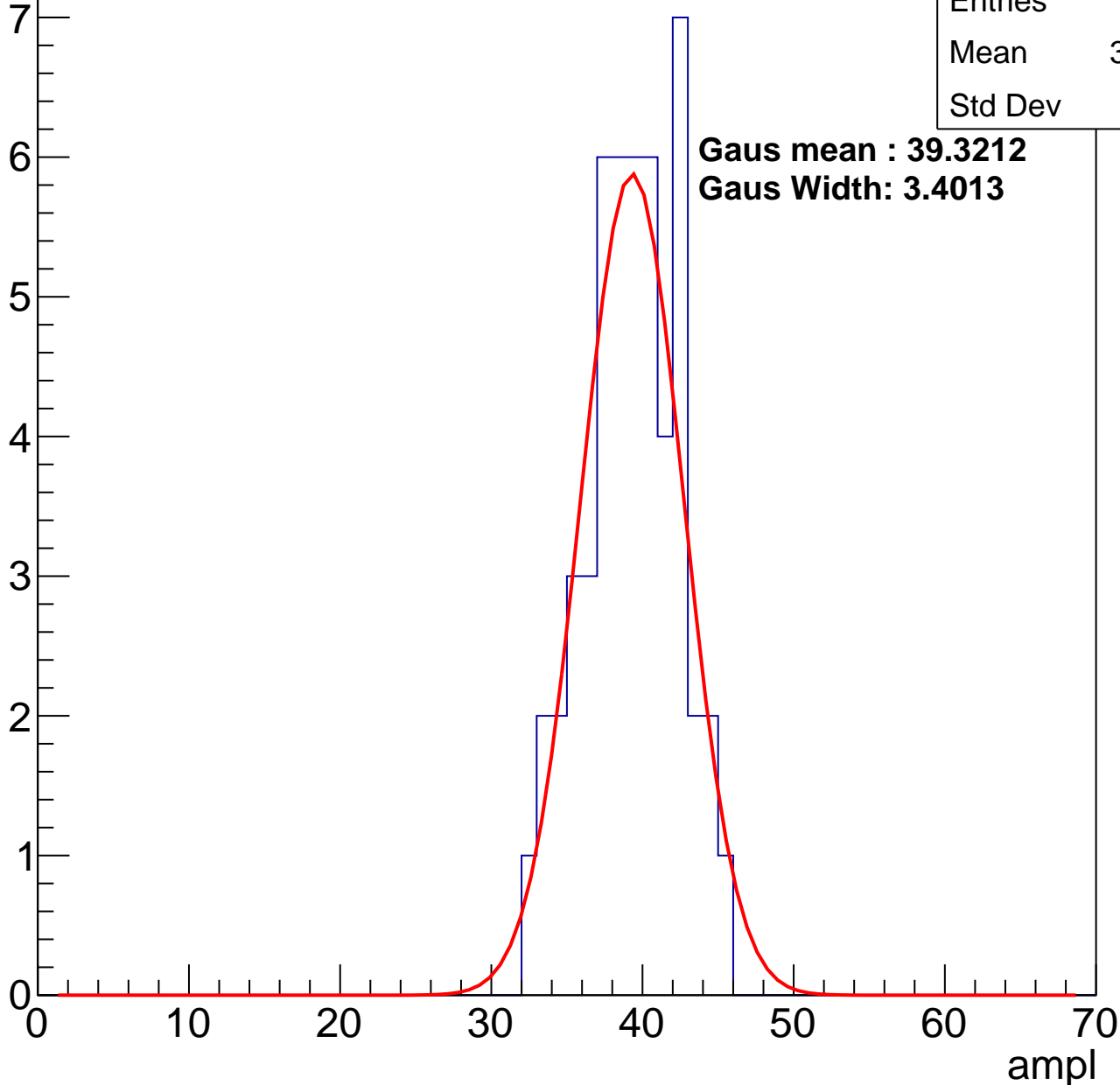
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	38.82
Std Dev	3.06

**Gaus mean : 39.3212**

**Gaus Width: 3.4013**

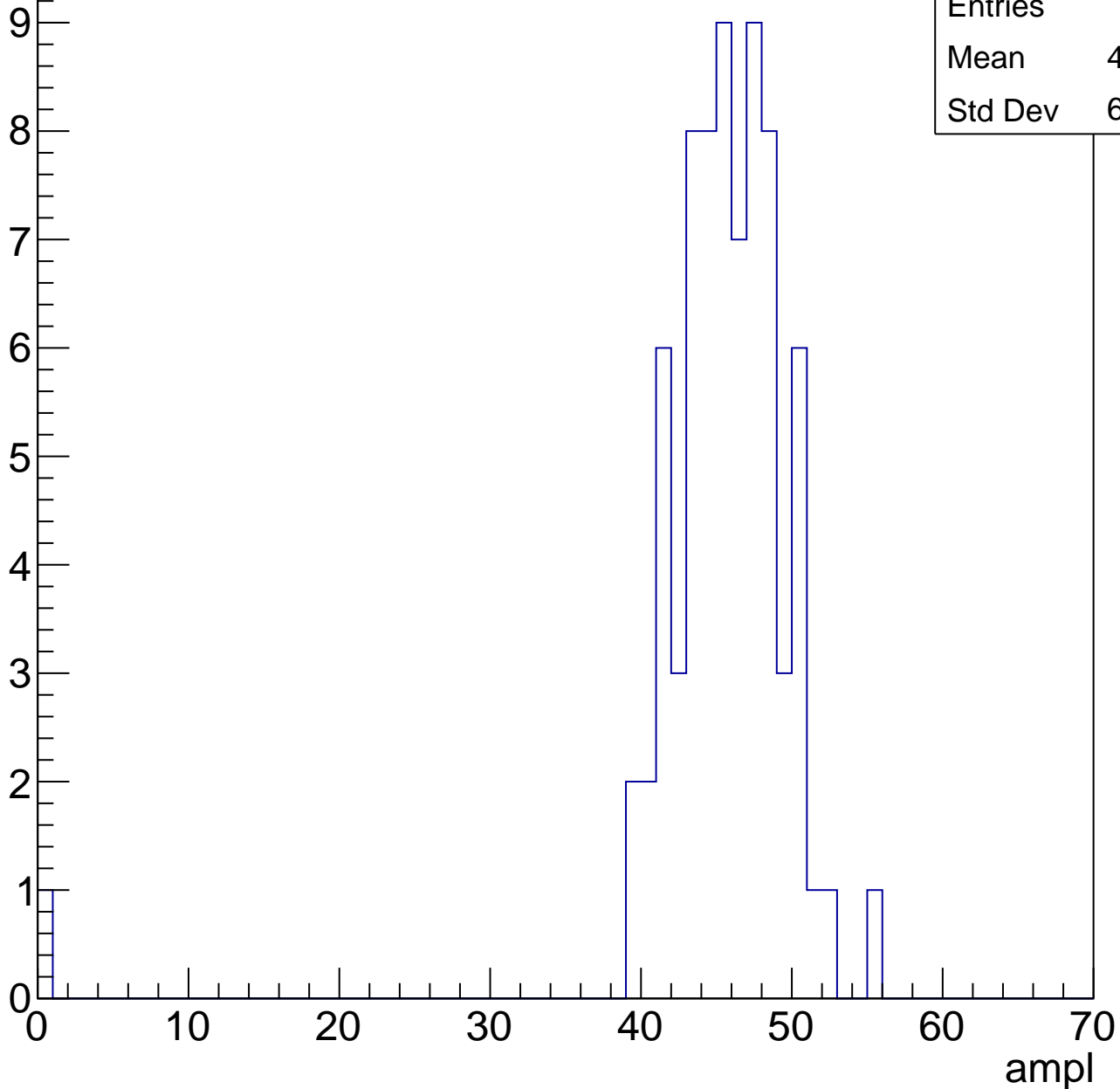


# B1L002S, U17-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	44.87
Std Dev	6.117

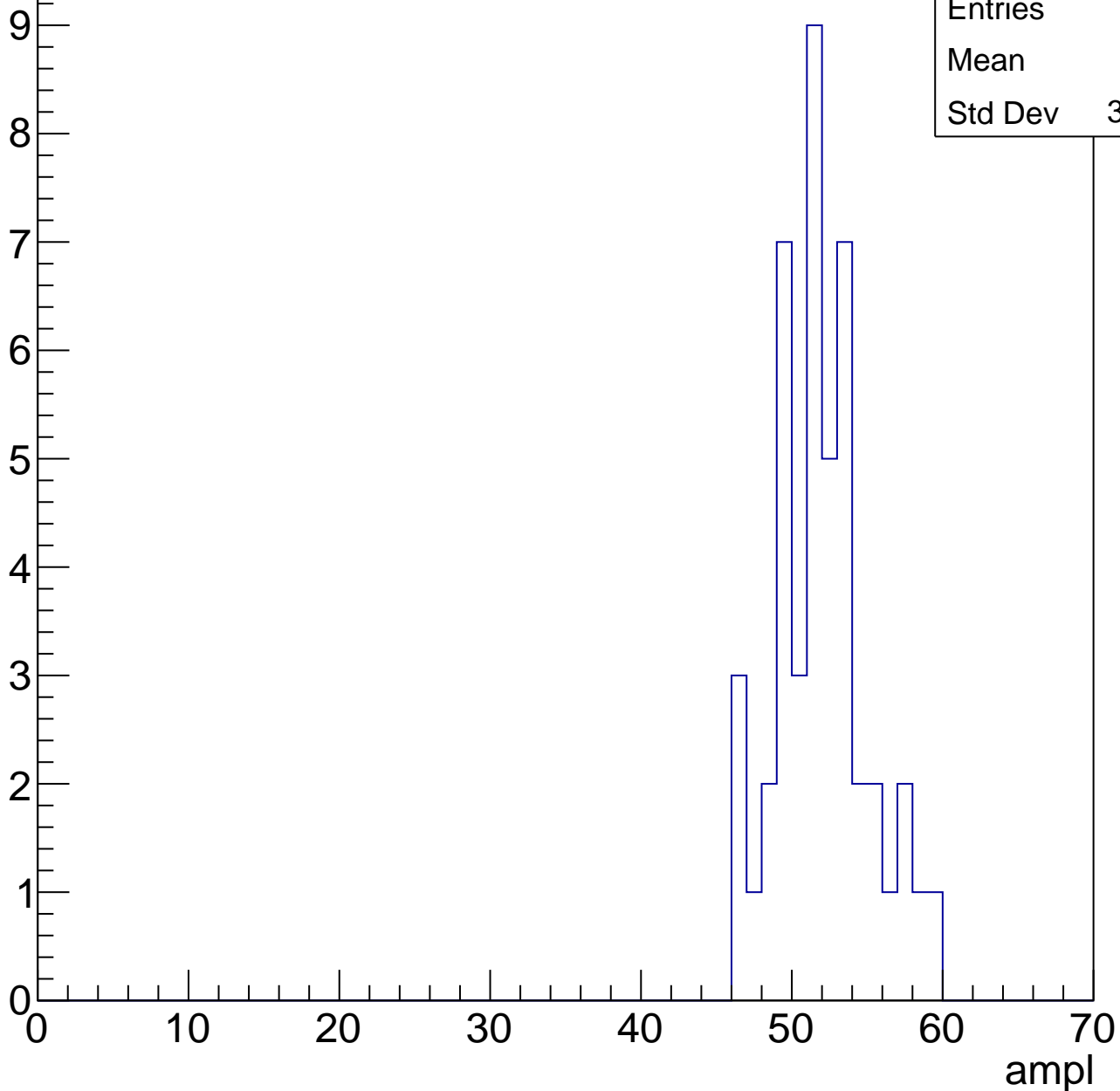


# B1L002S, U17-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

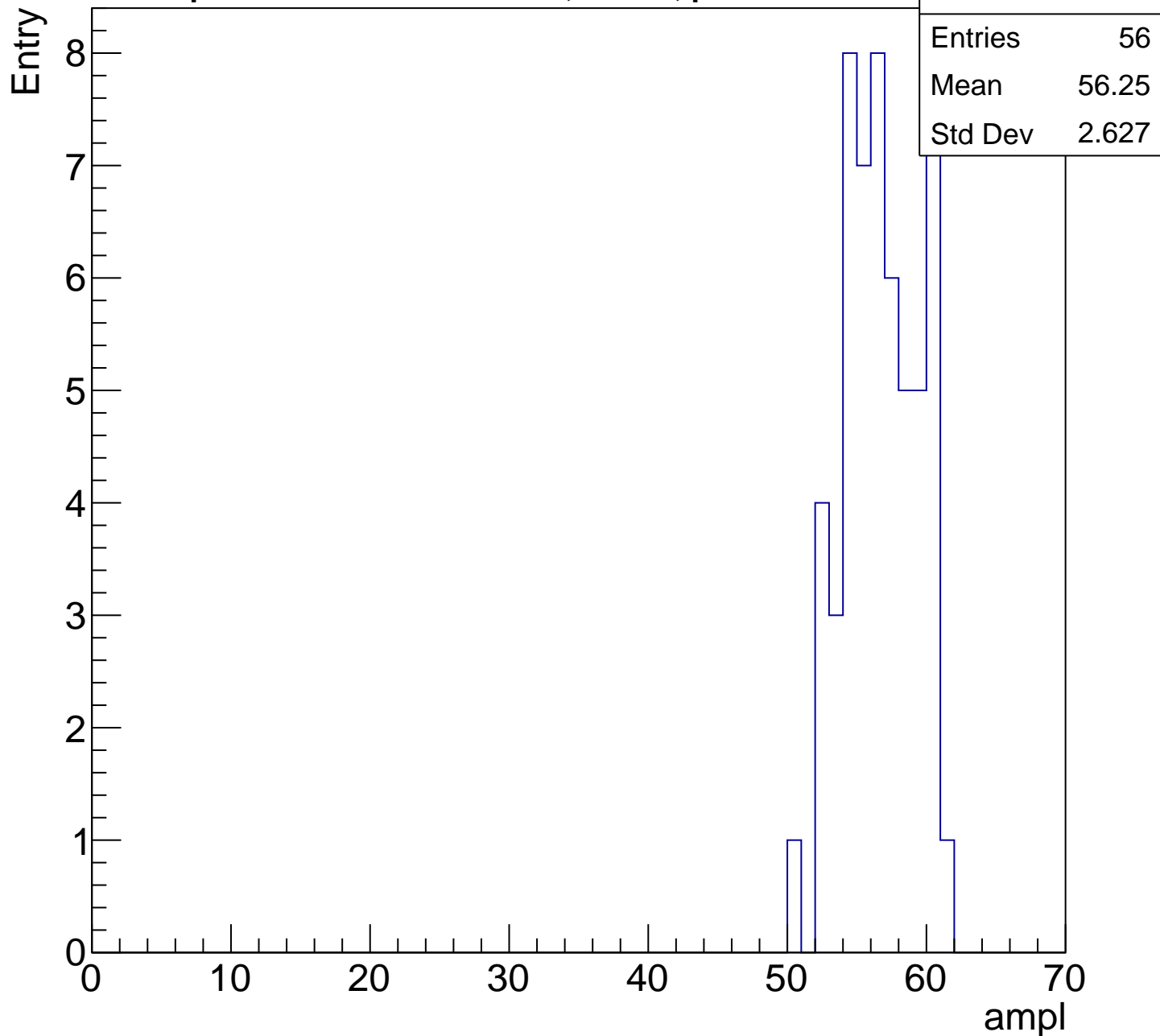
Entry

Entries	46
Mean	51.5
Std Dev	3.027



# B1L002S, U17-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

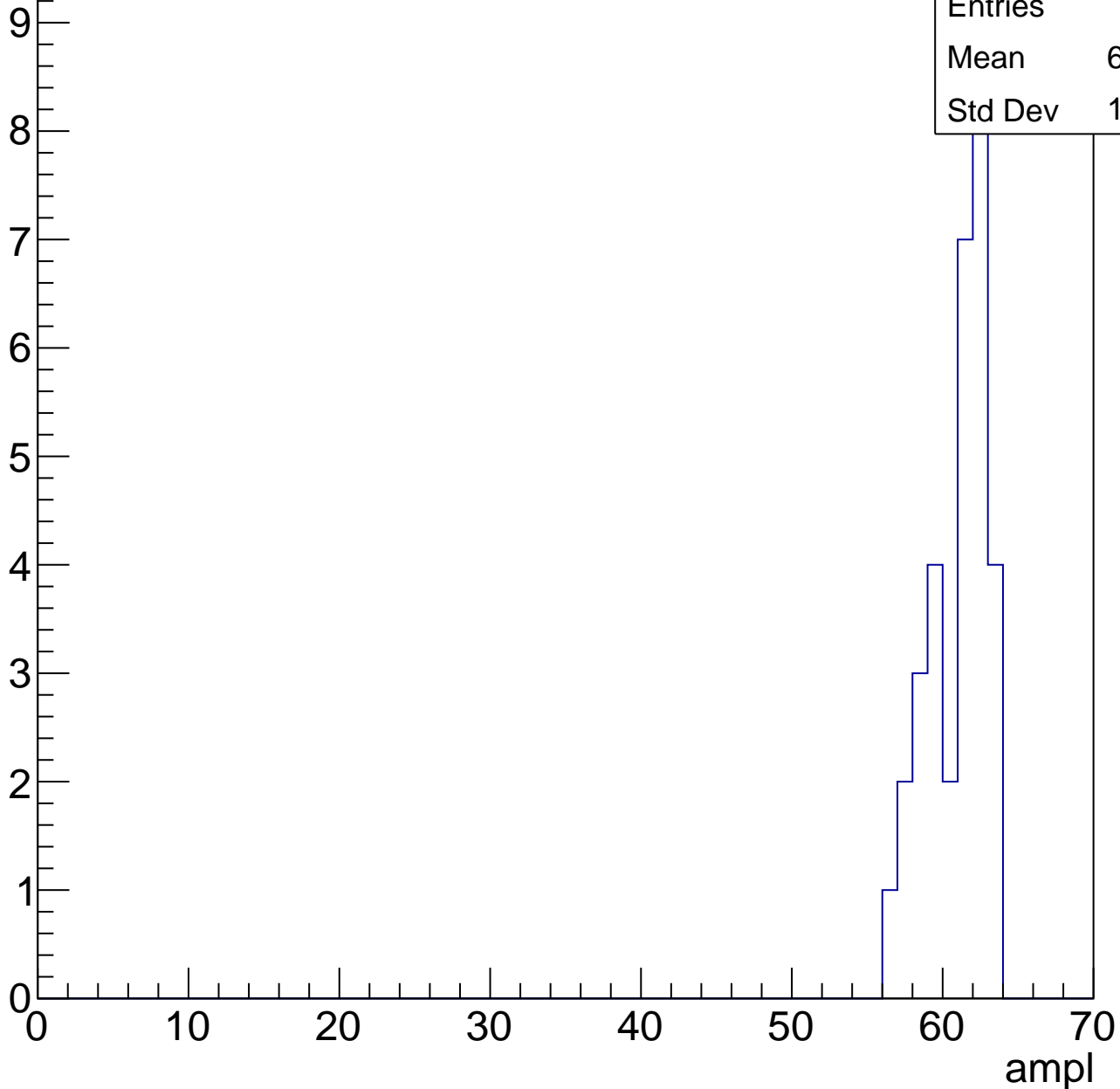


# B1L002S, U17-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	60.53
Std Dev	1.936



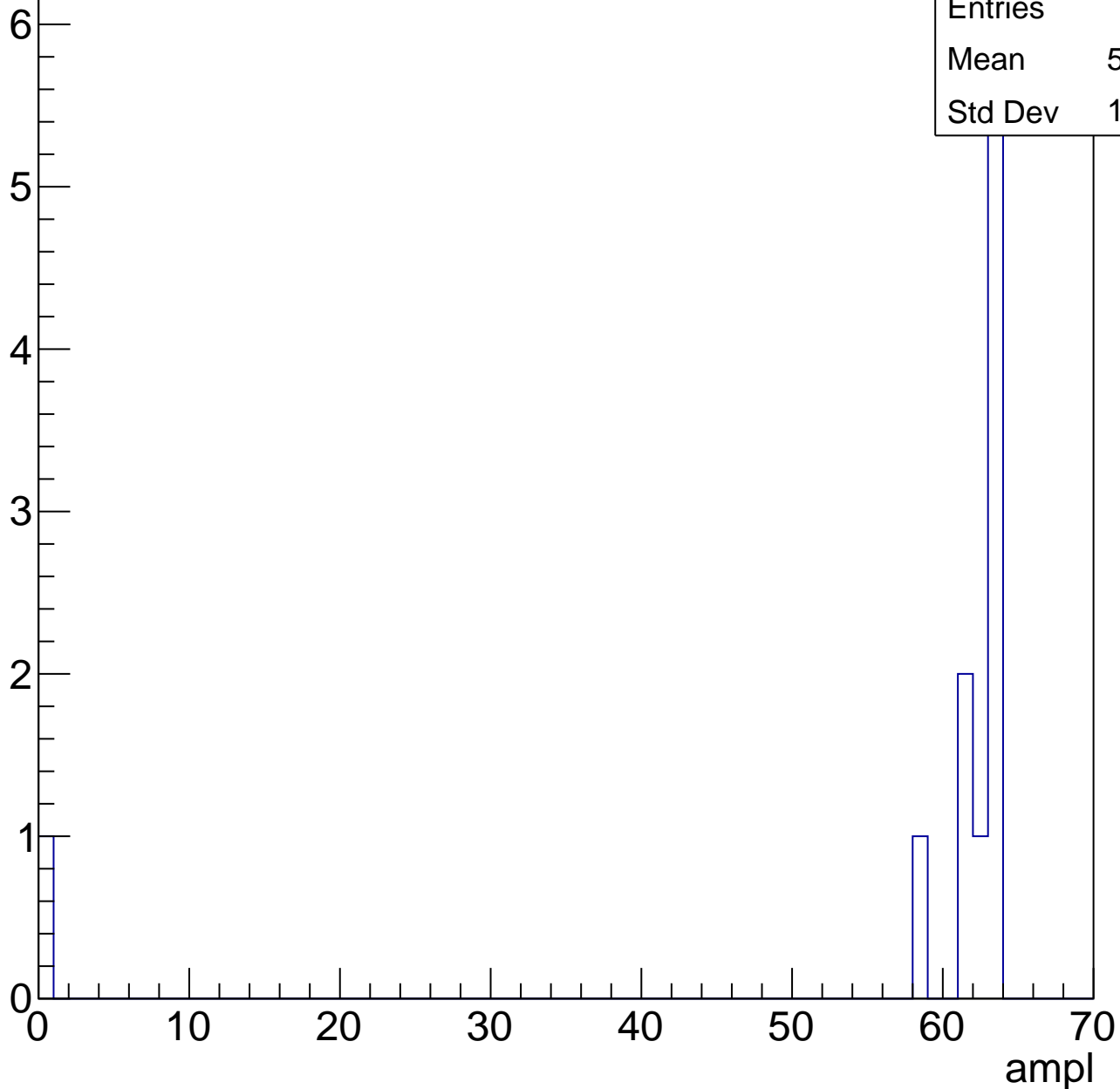


# B1L002S, U17-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	56.36
Std Dev	17.88



# B1L002S, U17-ch60, adc0

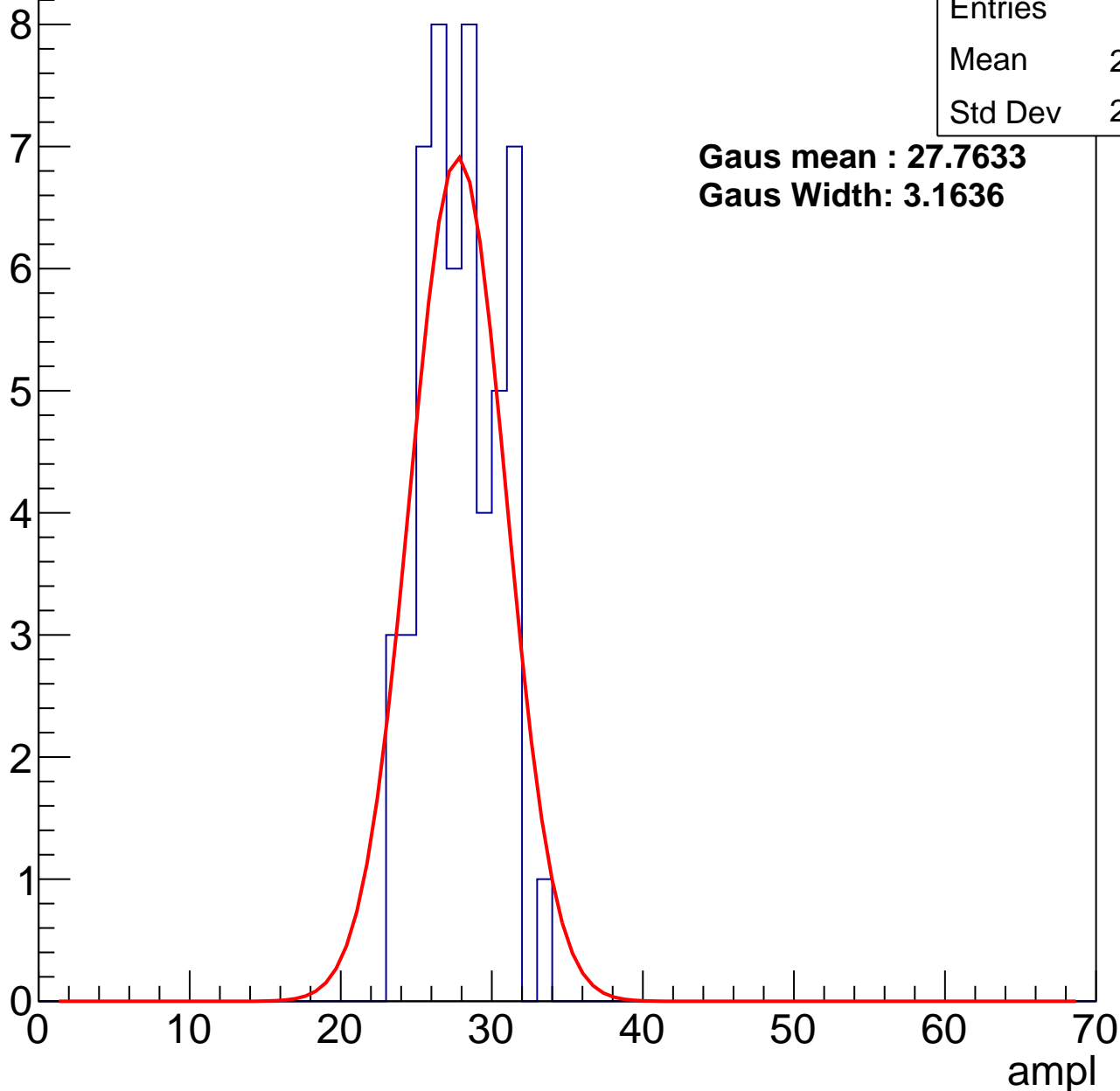
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.42
Std Dev	2.476

**Gaus mean : 27.7633**

**Gaus Width: 3.1636**



# B1L002S, U17-ch60, adc1

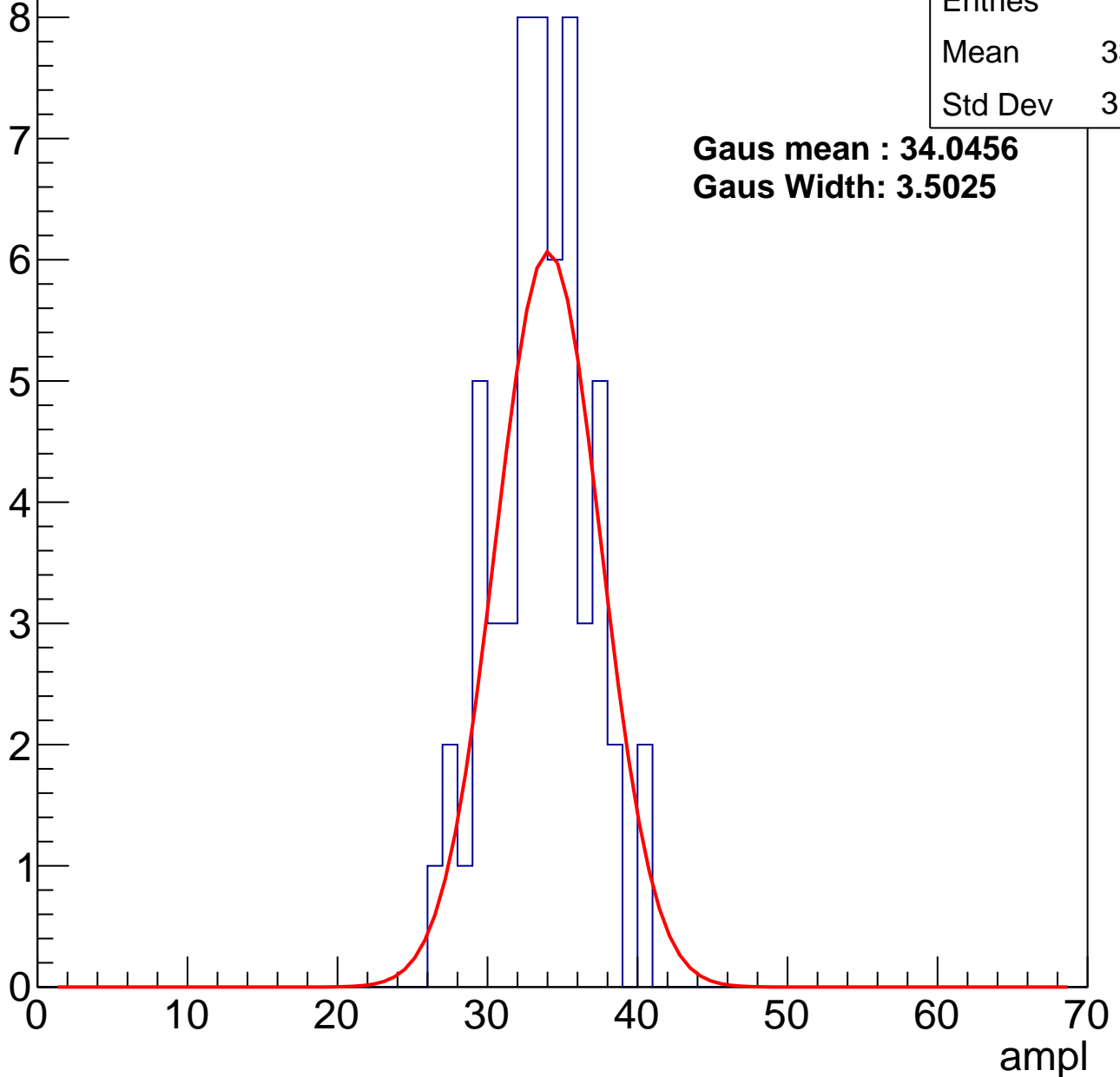
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.14
Std Dev	3.148

**Gaus mean : 34.0456**

**Gaus Width: 3.5025**



# B1L002S, U17-ch60, adc2

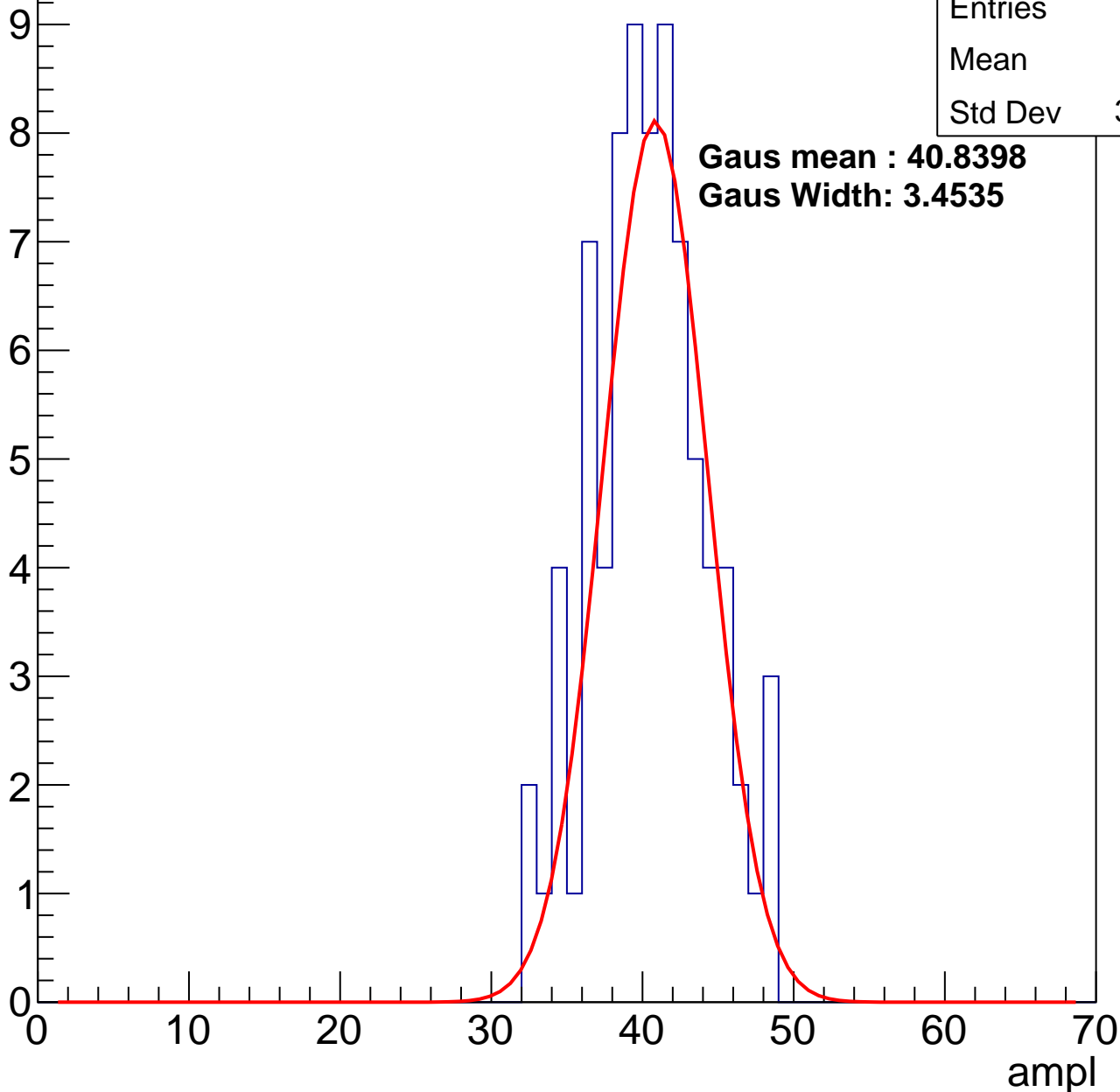
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	40
Std Dev	3.721

**Gaus mean : 40.8398**

**Gaus Width: 3.4535**

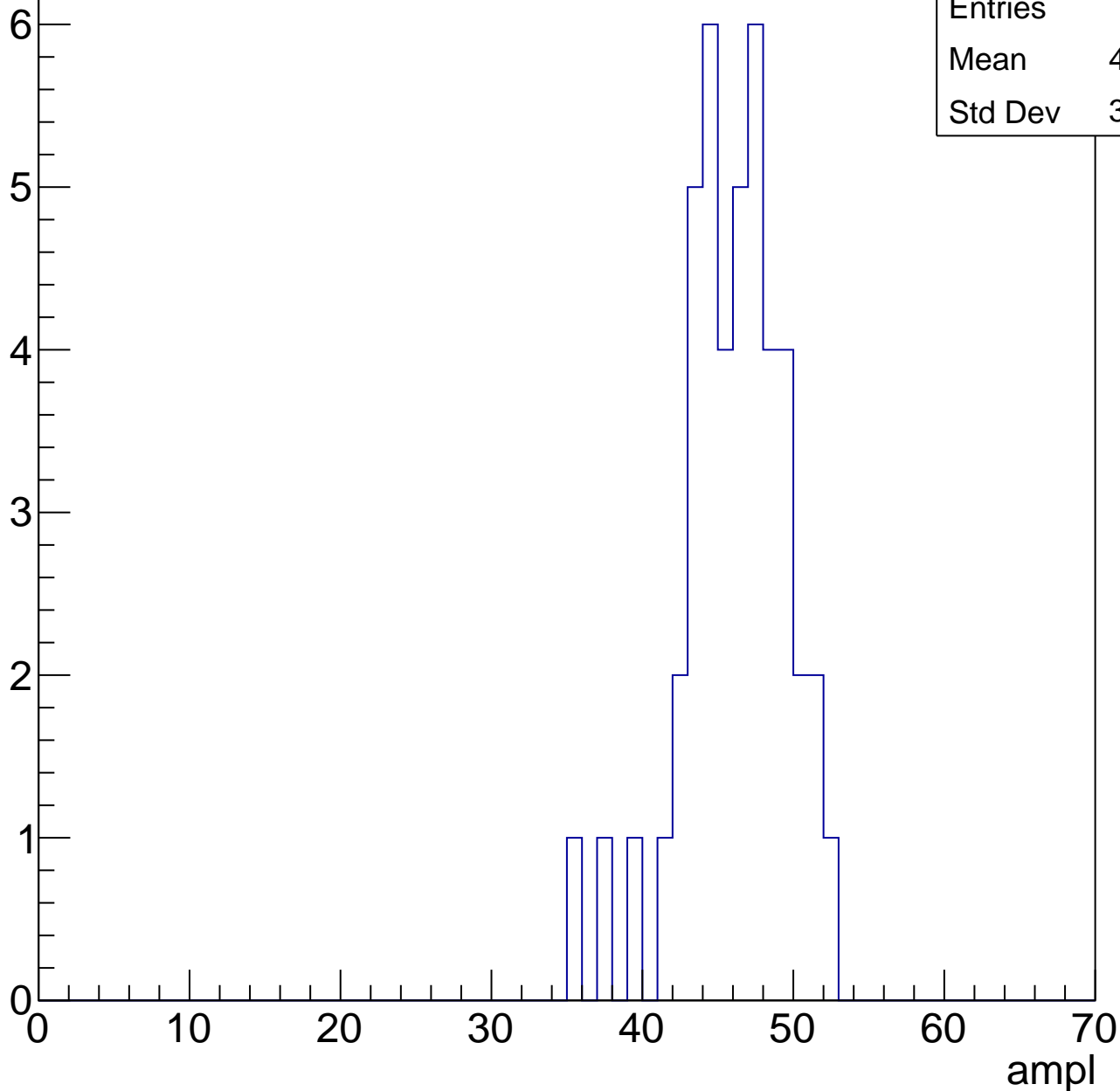


# B1L002S, U17-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

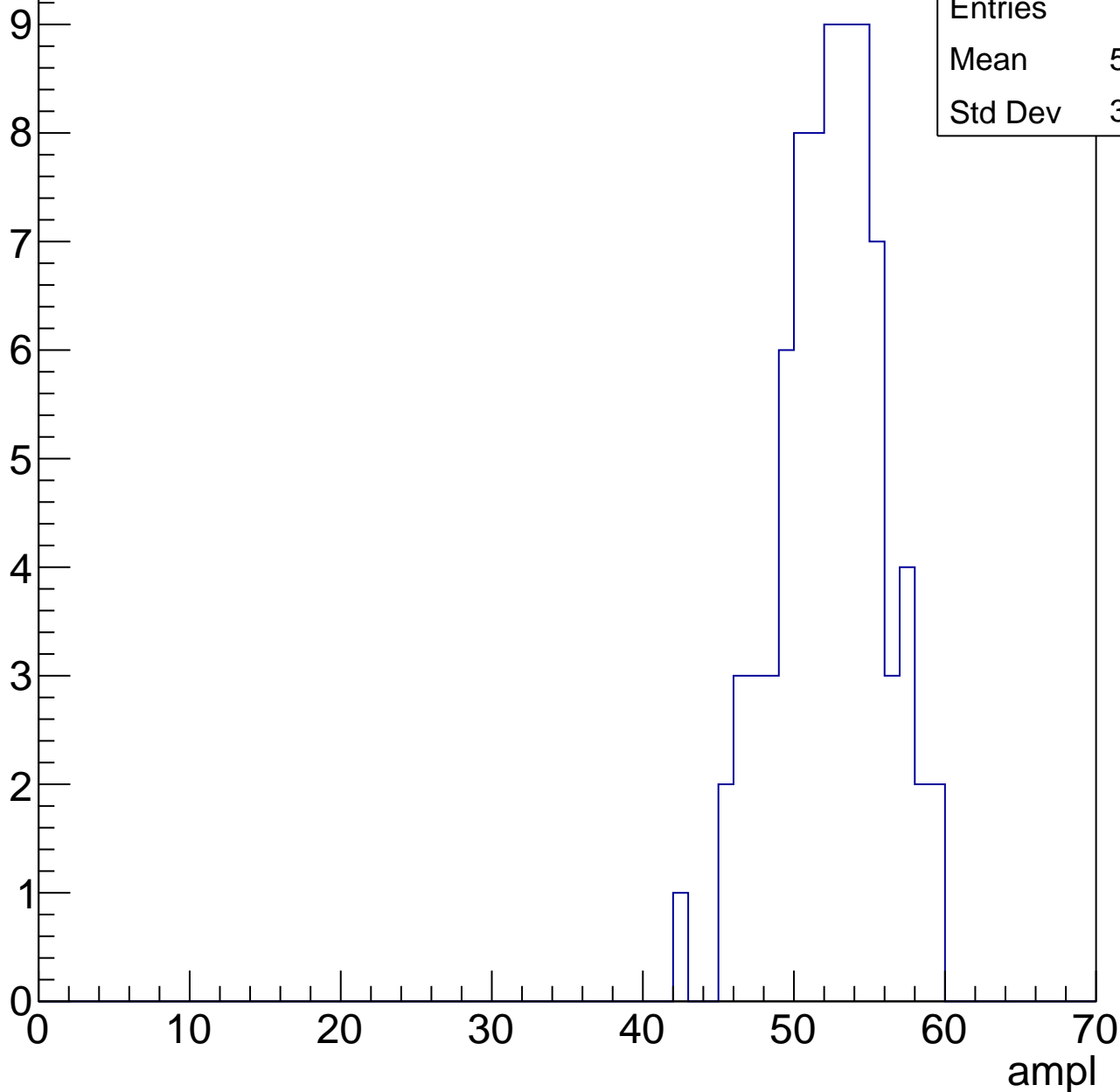
Entries	45
Mean	45.53
Std Dev	3.487



# B1L002S, U17-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

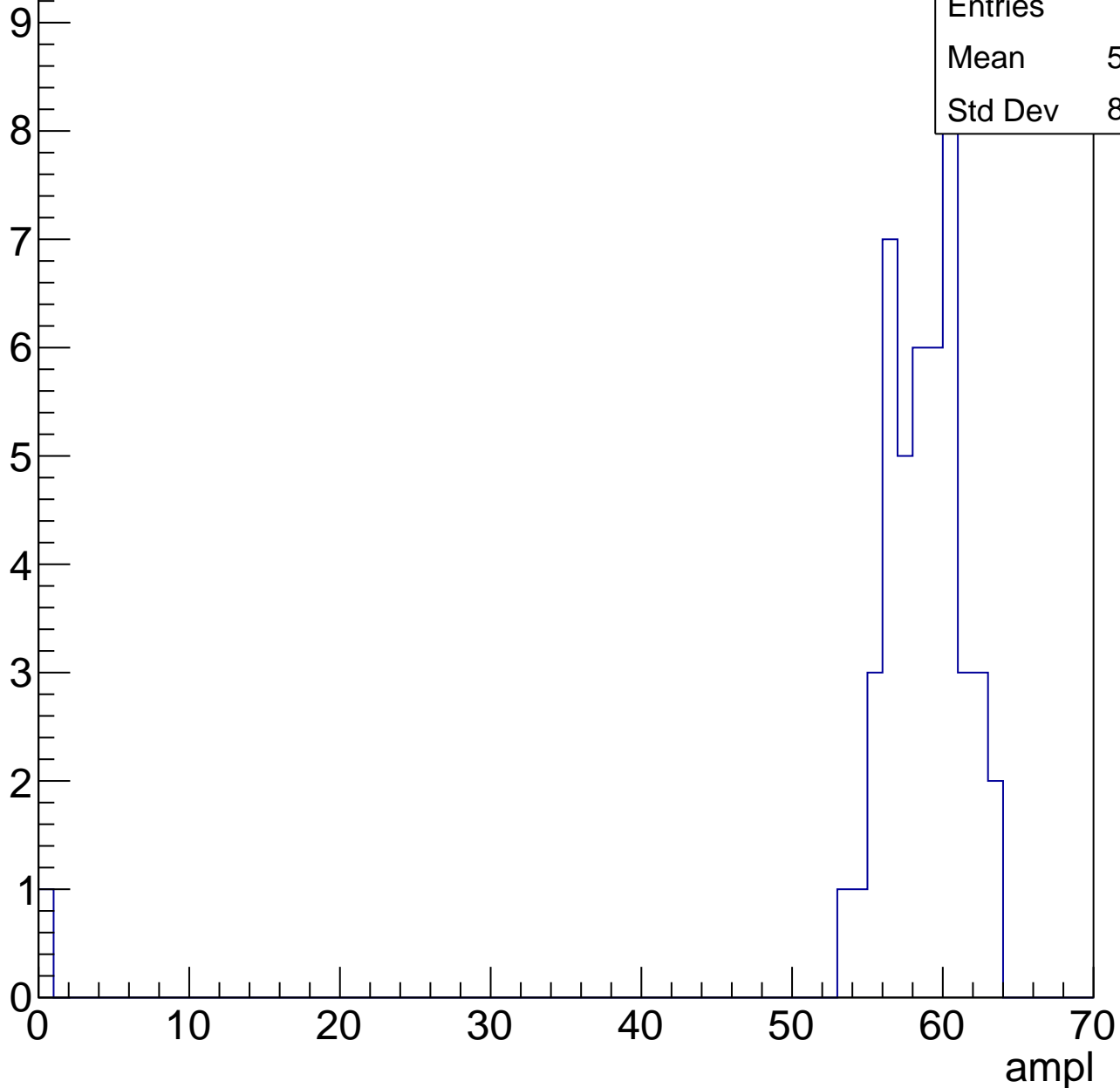


Entries	79
Mean	51.94
Std Dev	3.465

# B1L002S, U17-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

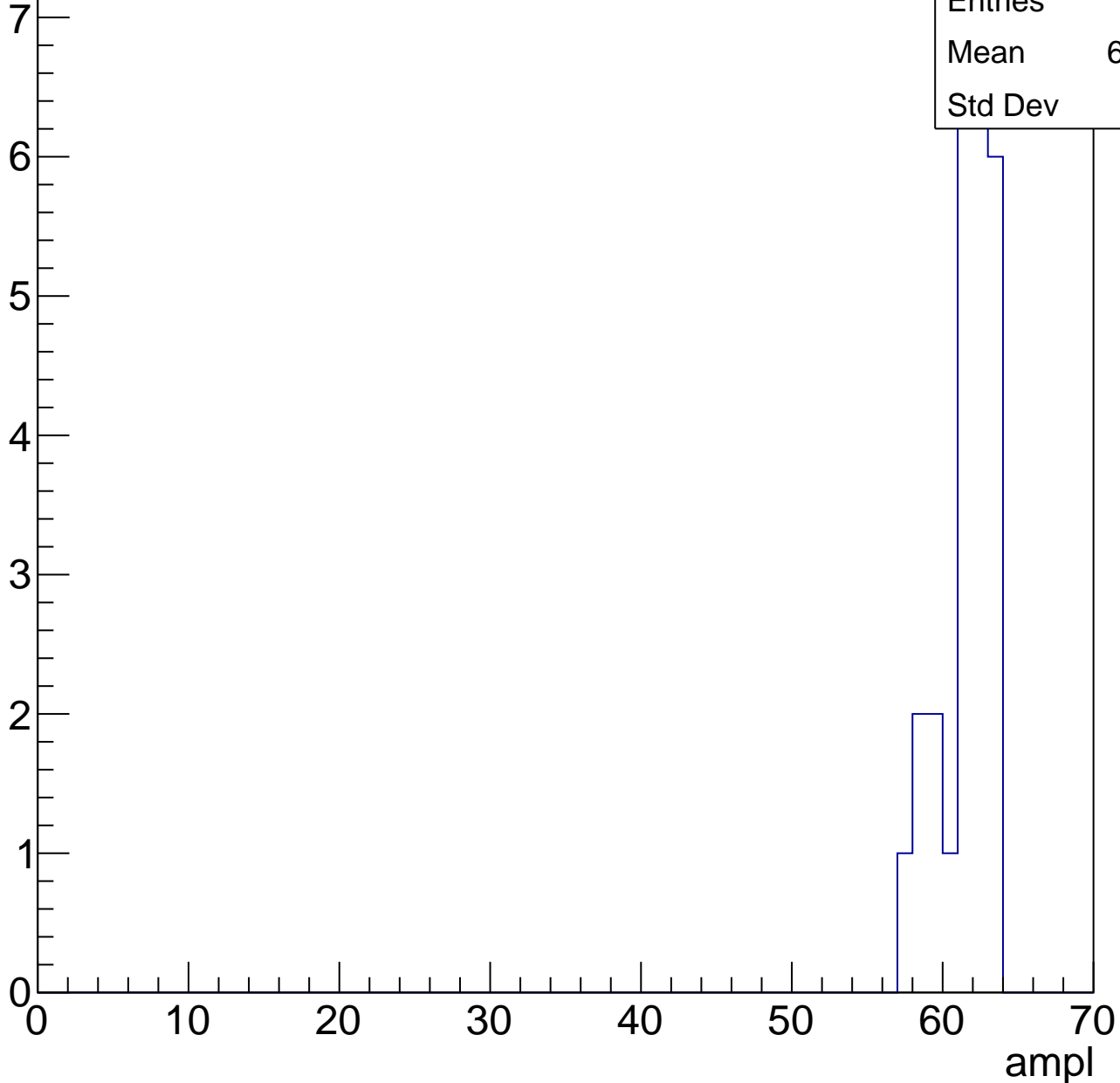


# B1L002S, U17-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.15
Std Dev	1.68

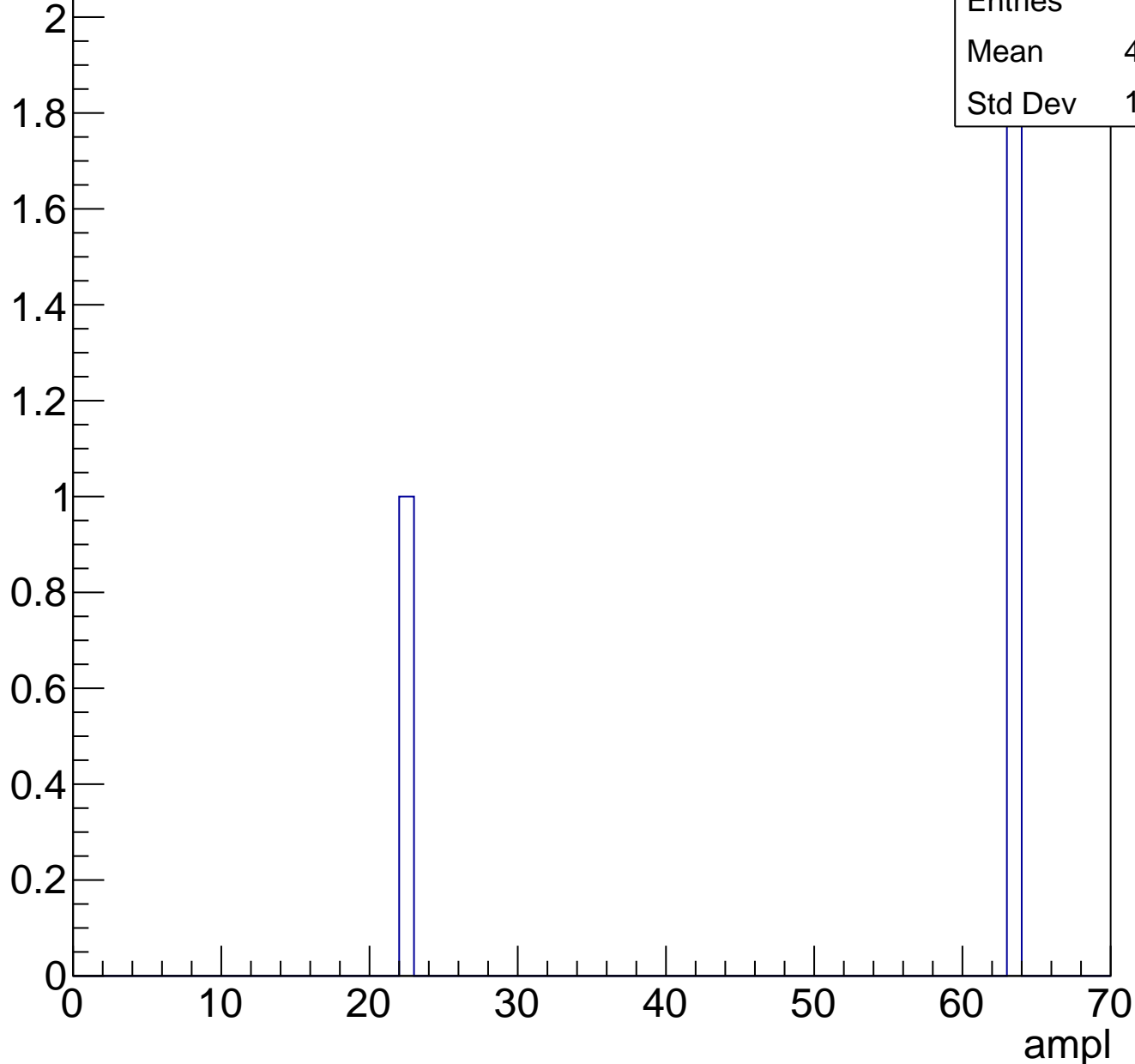




# B1L002S, U17-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	49.33
Std Dev	19.33

# B1L002S, U17-ch61, adc0

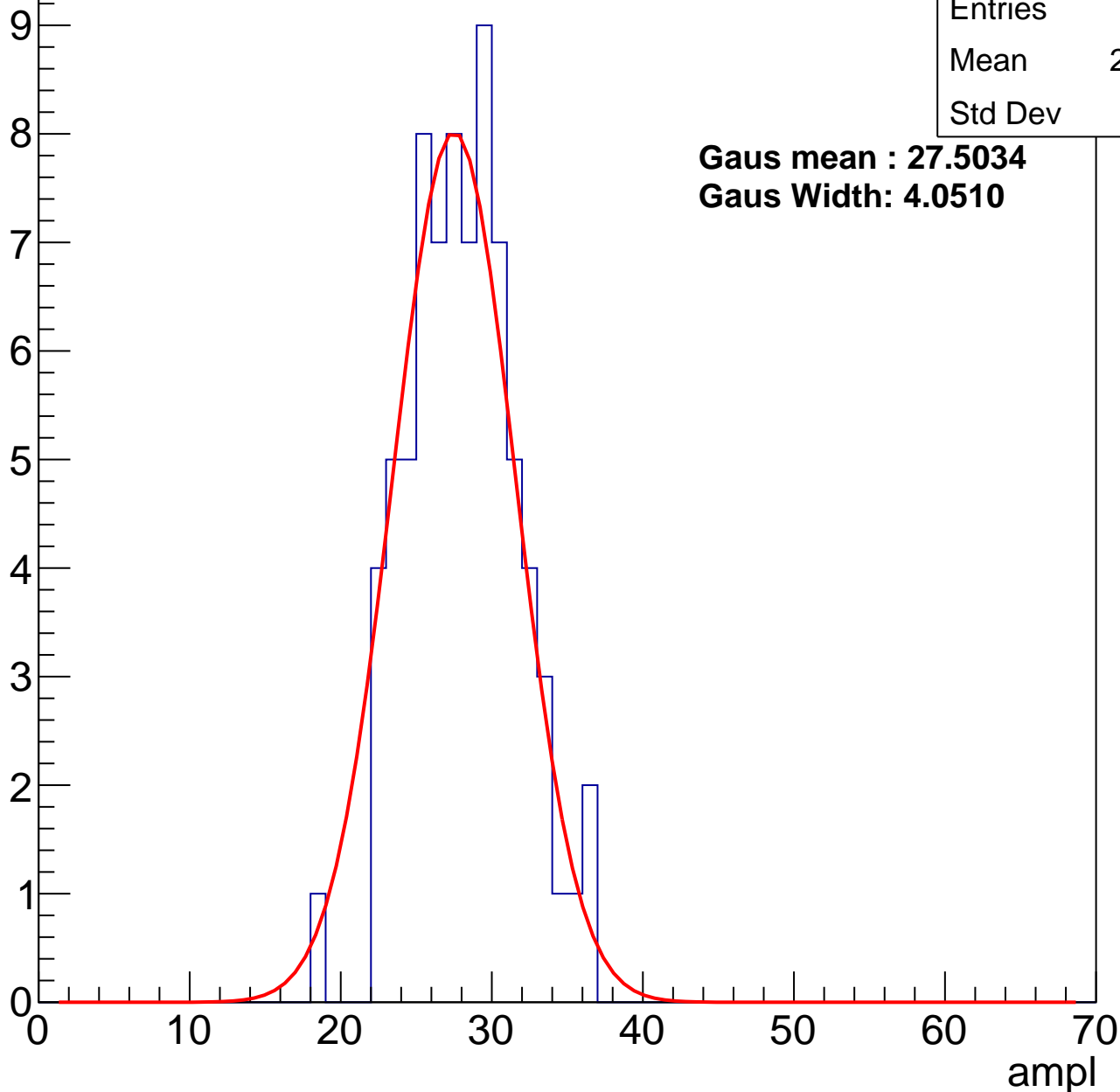
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	27.65
Std Dev	3.57

**Gaus mean : 27.5034**

**Gaus Width: 4.0510**



# B1L002S, U17-ch61, adc1

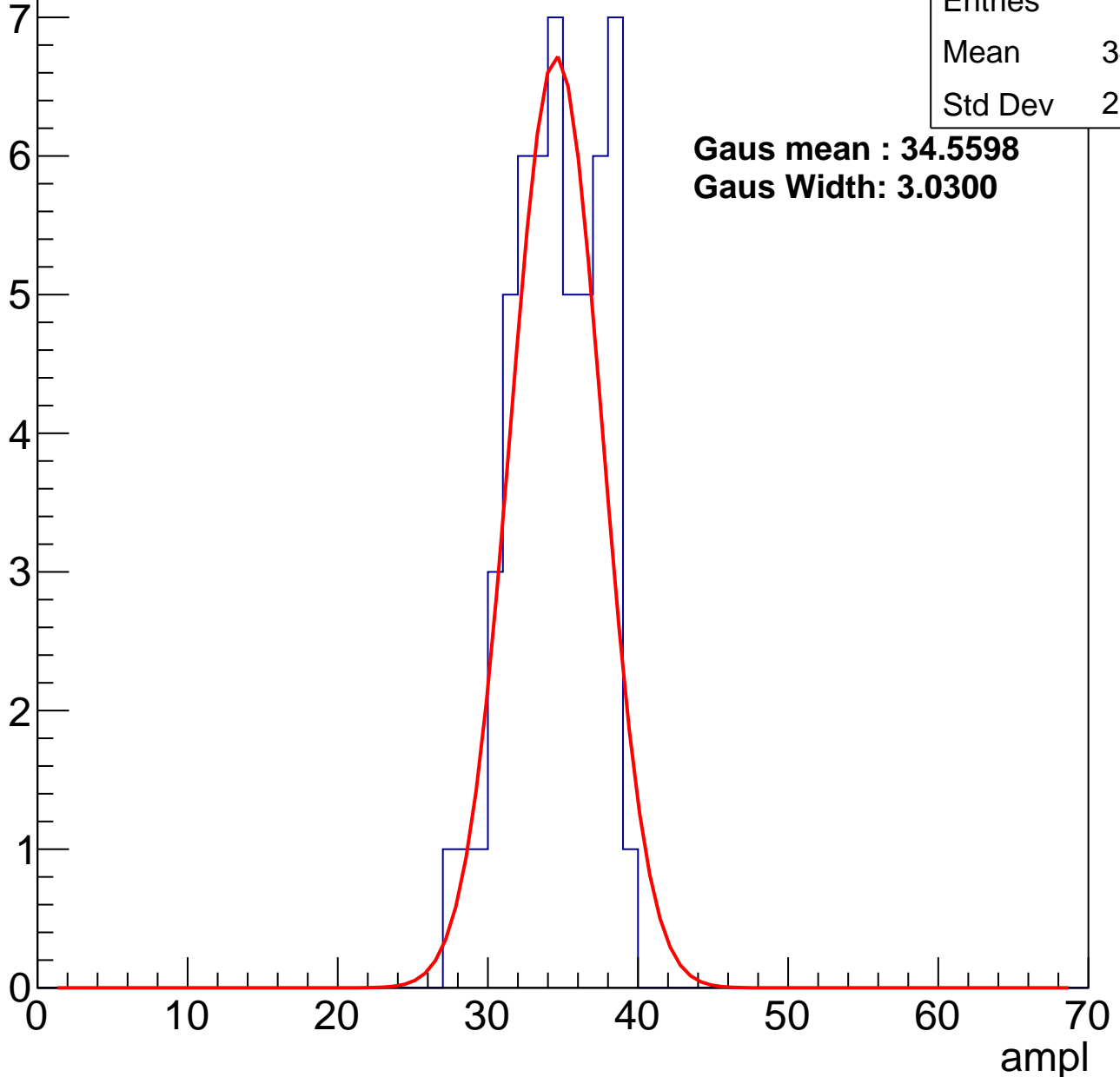
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.06
Std Dev	2.883

**Gaus mean : 34.5598**

**Gaus Width: 3.0300**



# B1L002S, U17-ch61, adc2

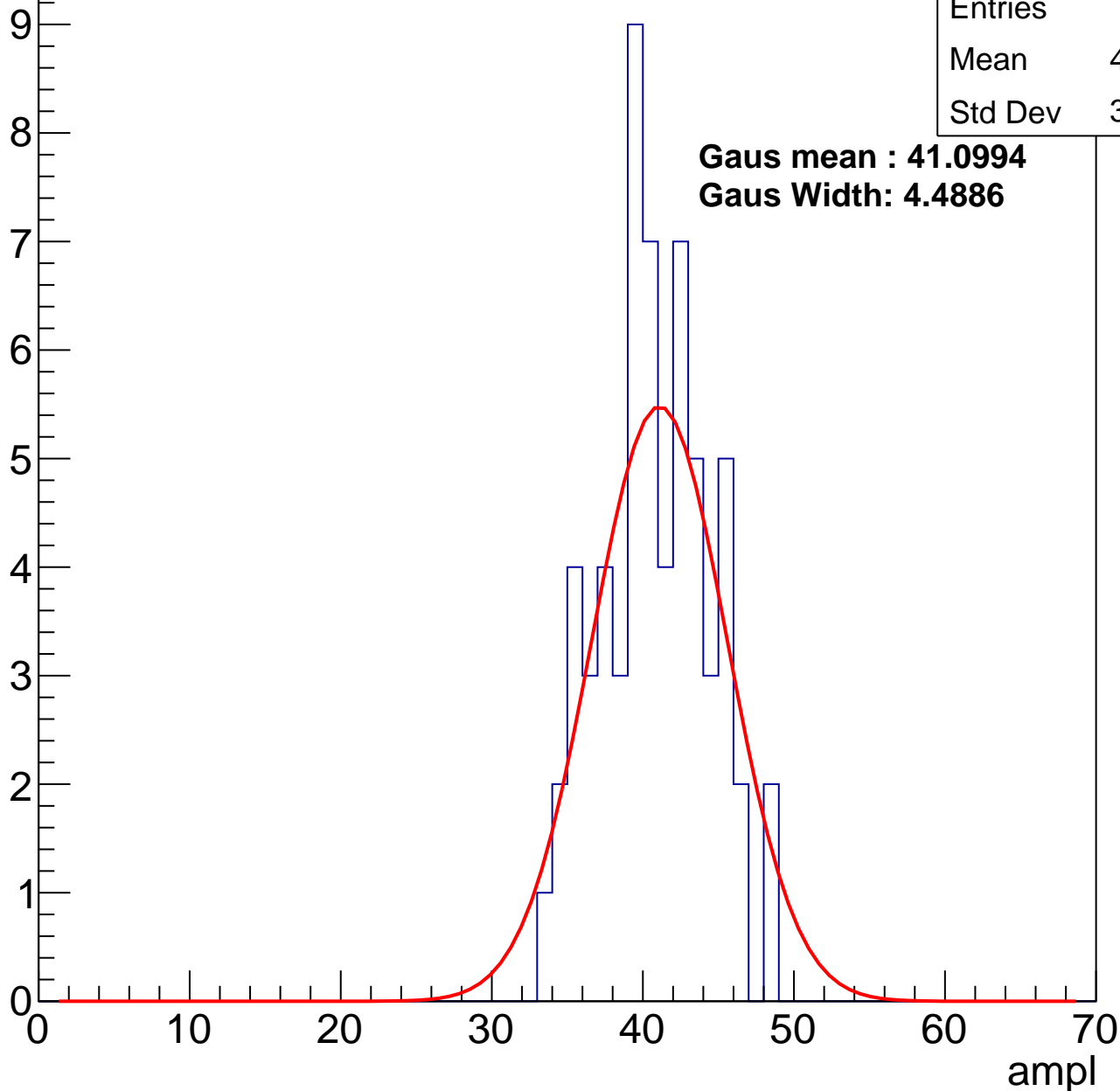
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.33
Std Dev	3.552

**Gaus mean : 41.0994**

**Gaus Width: 4.4886**

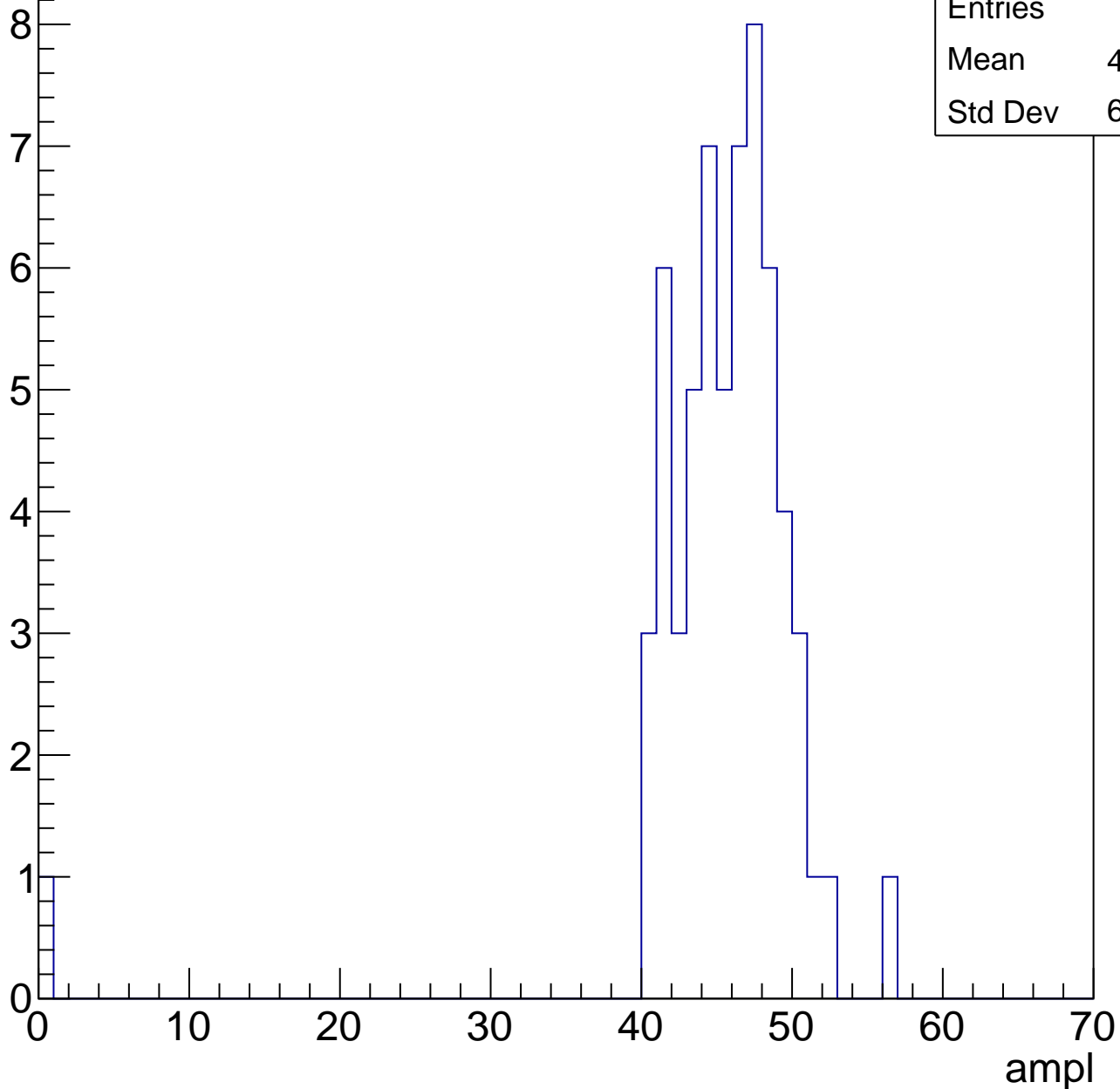


# B1L002S, U17-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	44.77
Std Dev	6.634

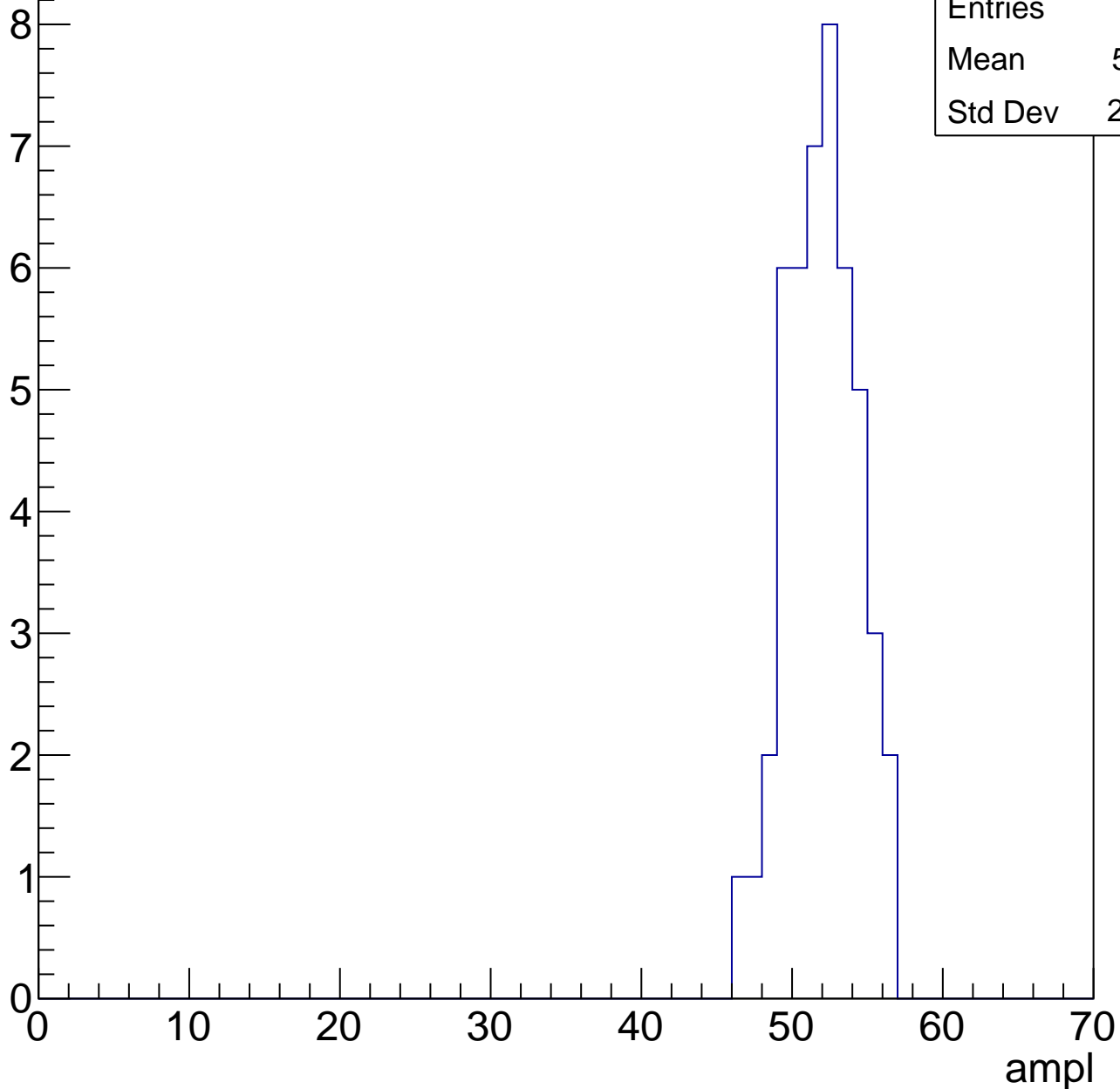


# B1L002S, U17-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

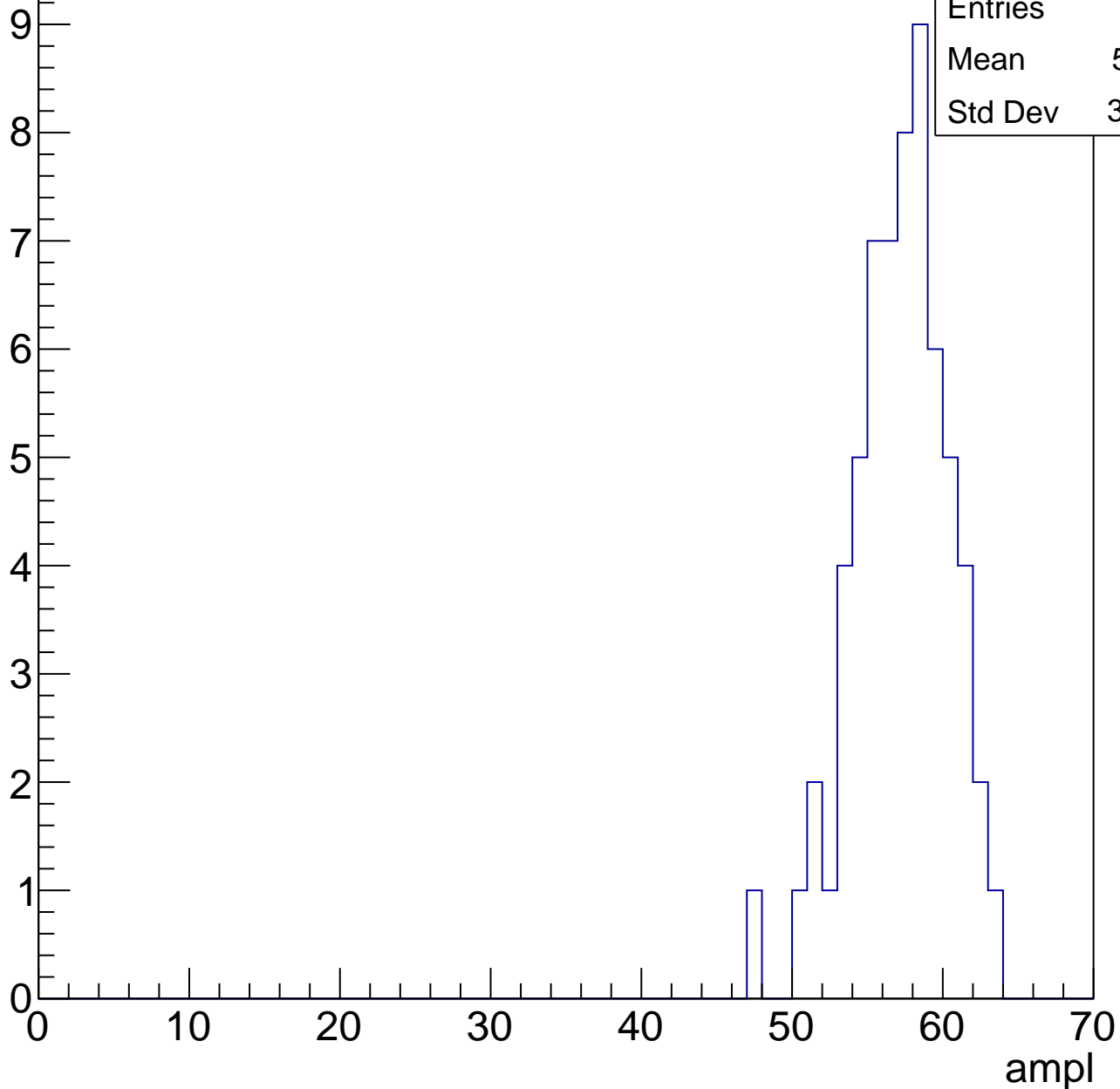
Entries	47
Mean	51.51
Std Dev	2.314



# B1L002S, U17-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

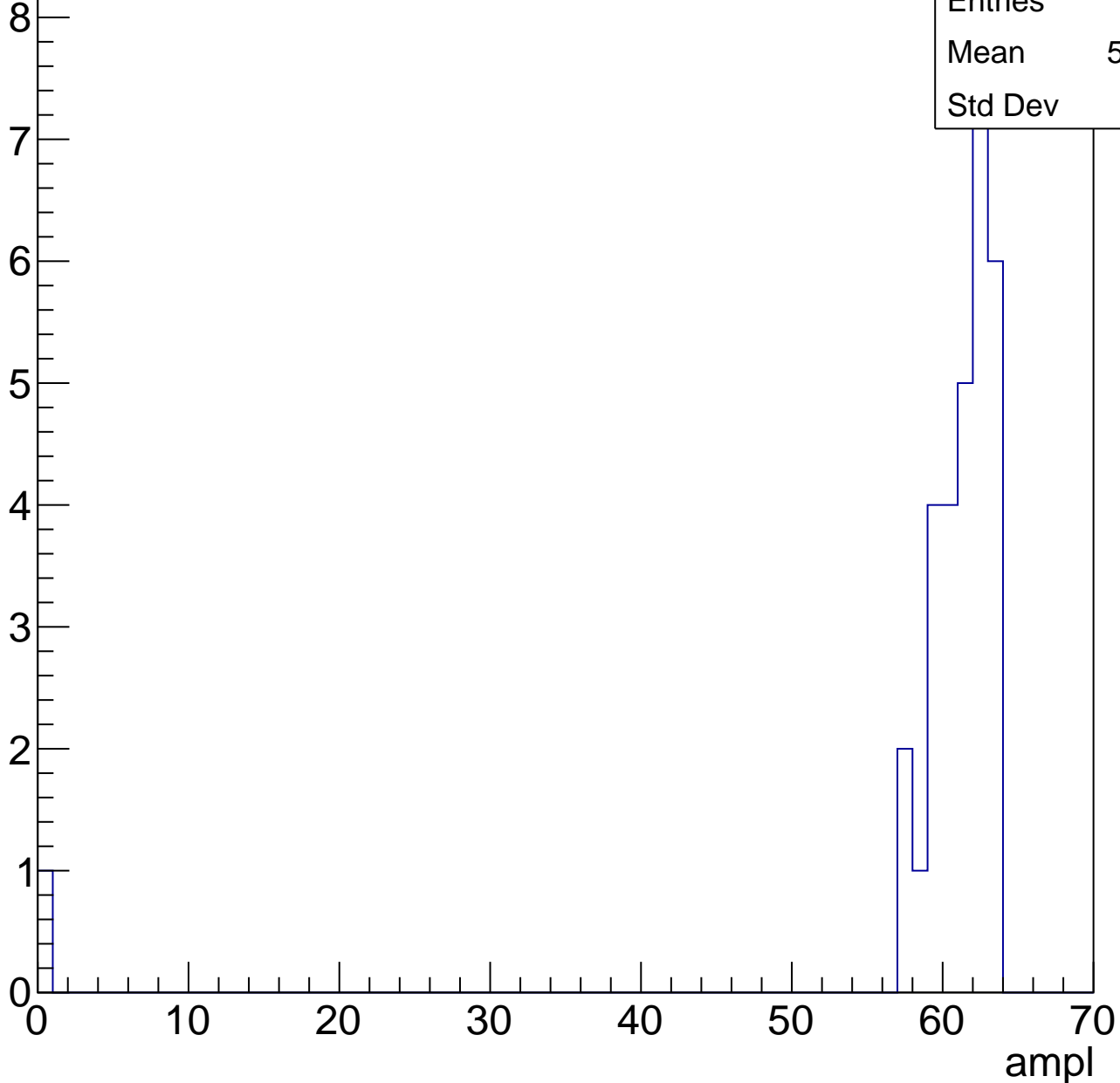


# B1L002S, U17-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.94
Std Dev	10.9

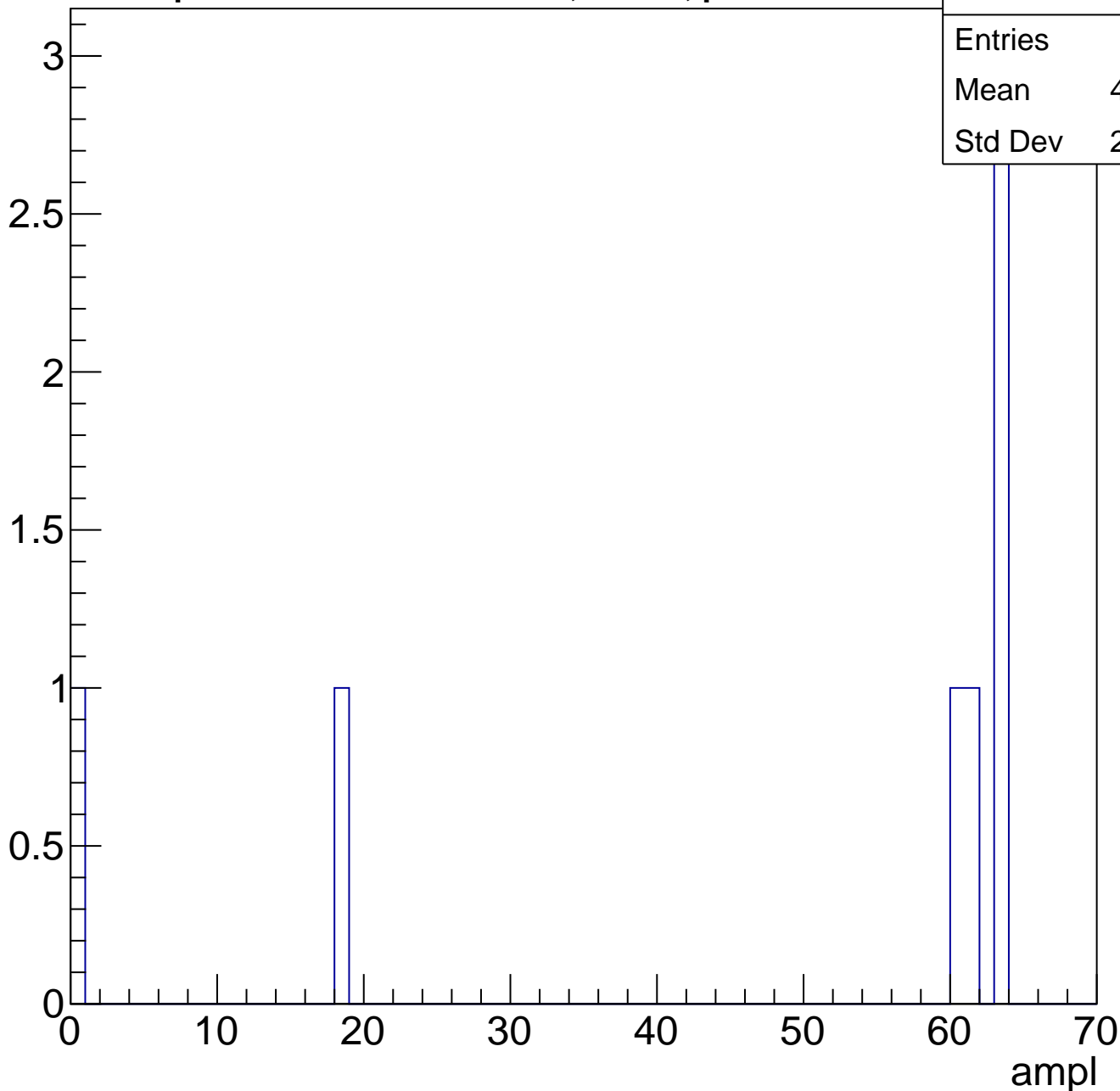




# B1L002S, U17-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch62, adc0

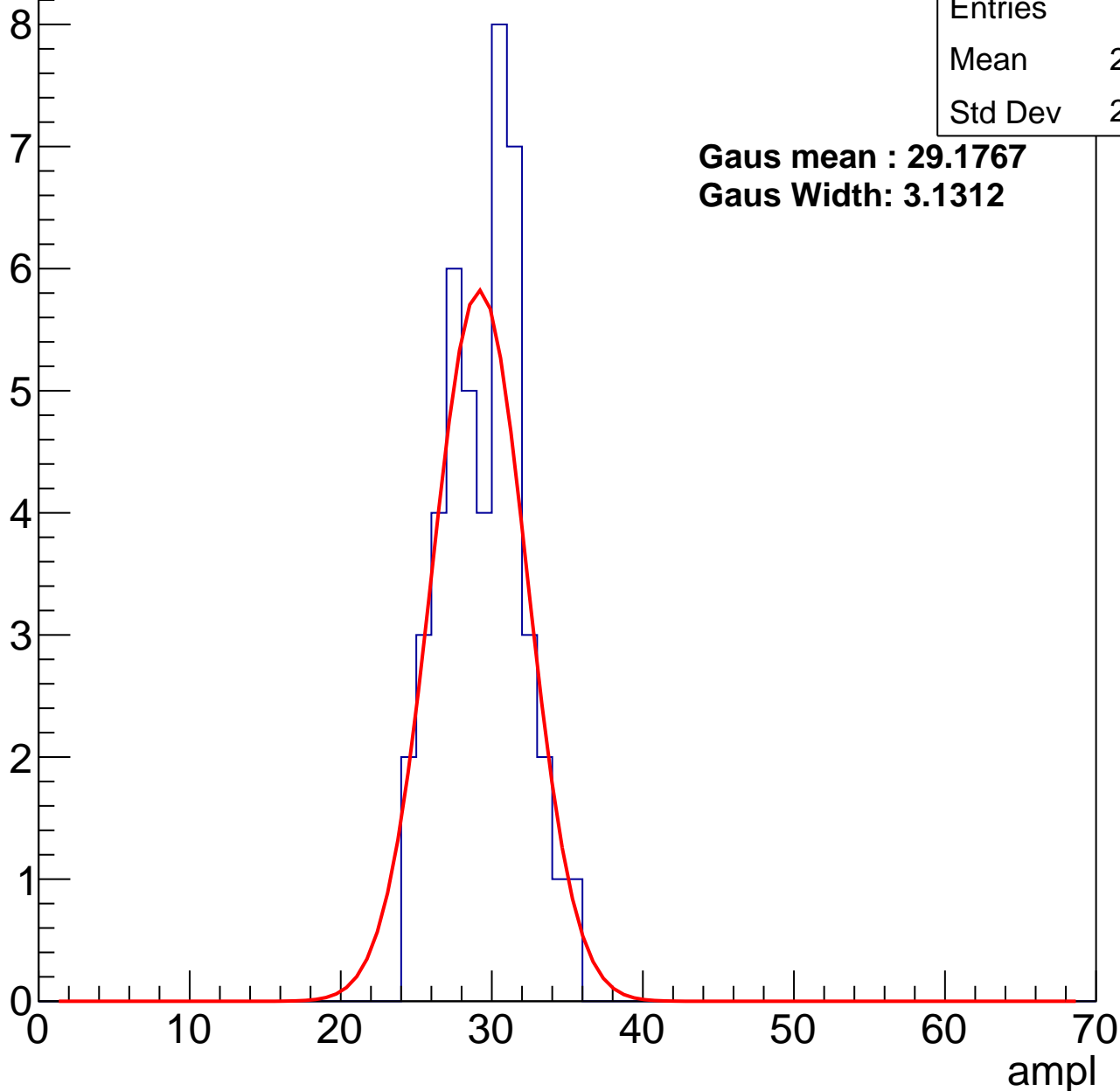
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	28.98
Std Dev	2.633

**Gaus mean : 29.1767**

**Gaus Width: 3.1312**



# B1L002S, U17-ch62, adc1

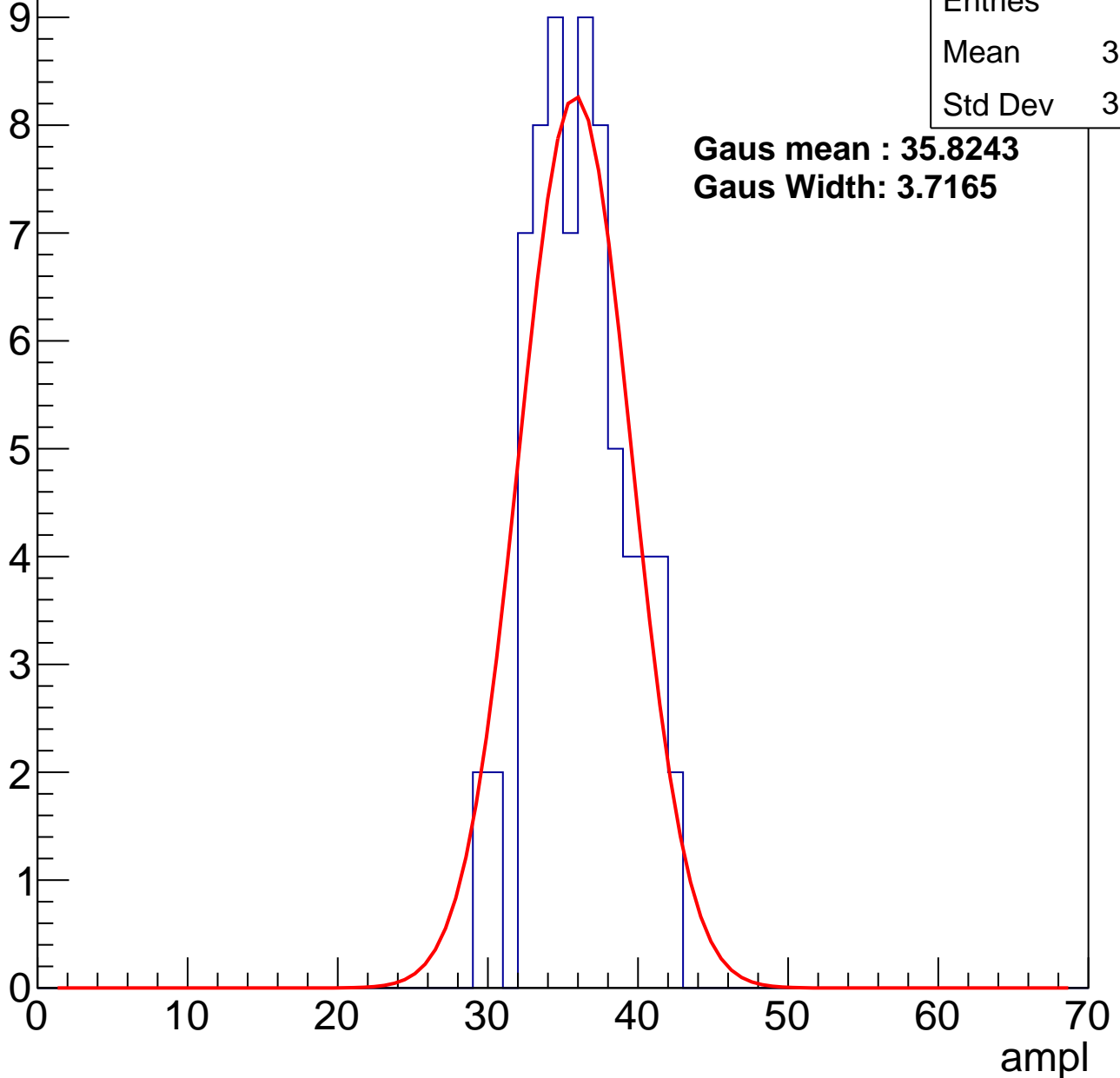
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	35.65
Std Dev	3.113

**Gaus mean : 35.8243**

**Gaus Width: 3.7165**



# B1L002S, U17-ch62, adc2

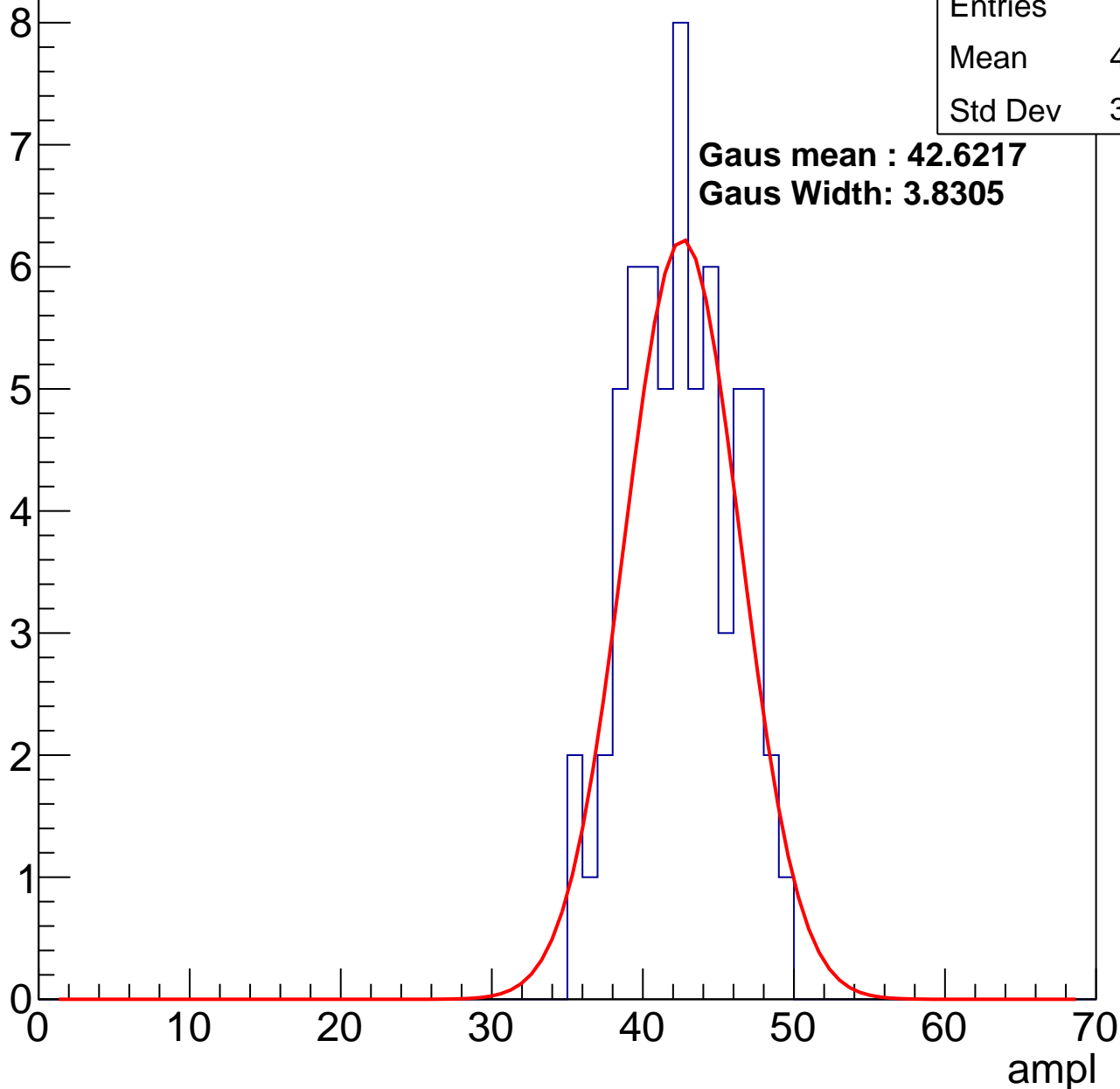
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	42.08
Std Dev	3.428

**Gaus mean : 42.6217**

**Gaus Width: 3.8305**

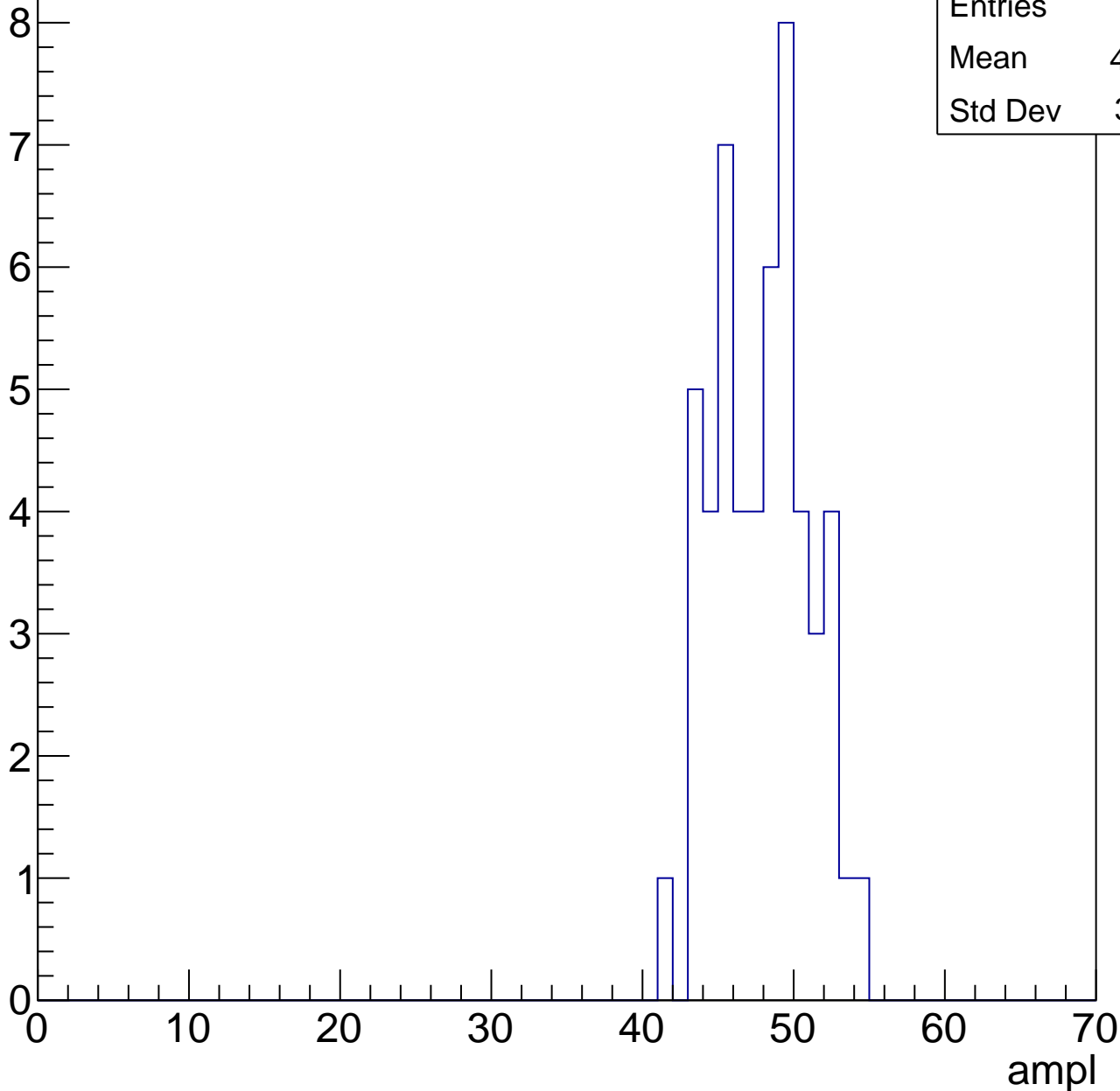


# B1L002S, U17-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	47.44
Std Dev	3.041

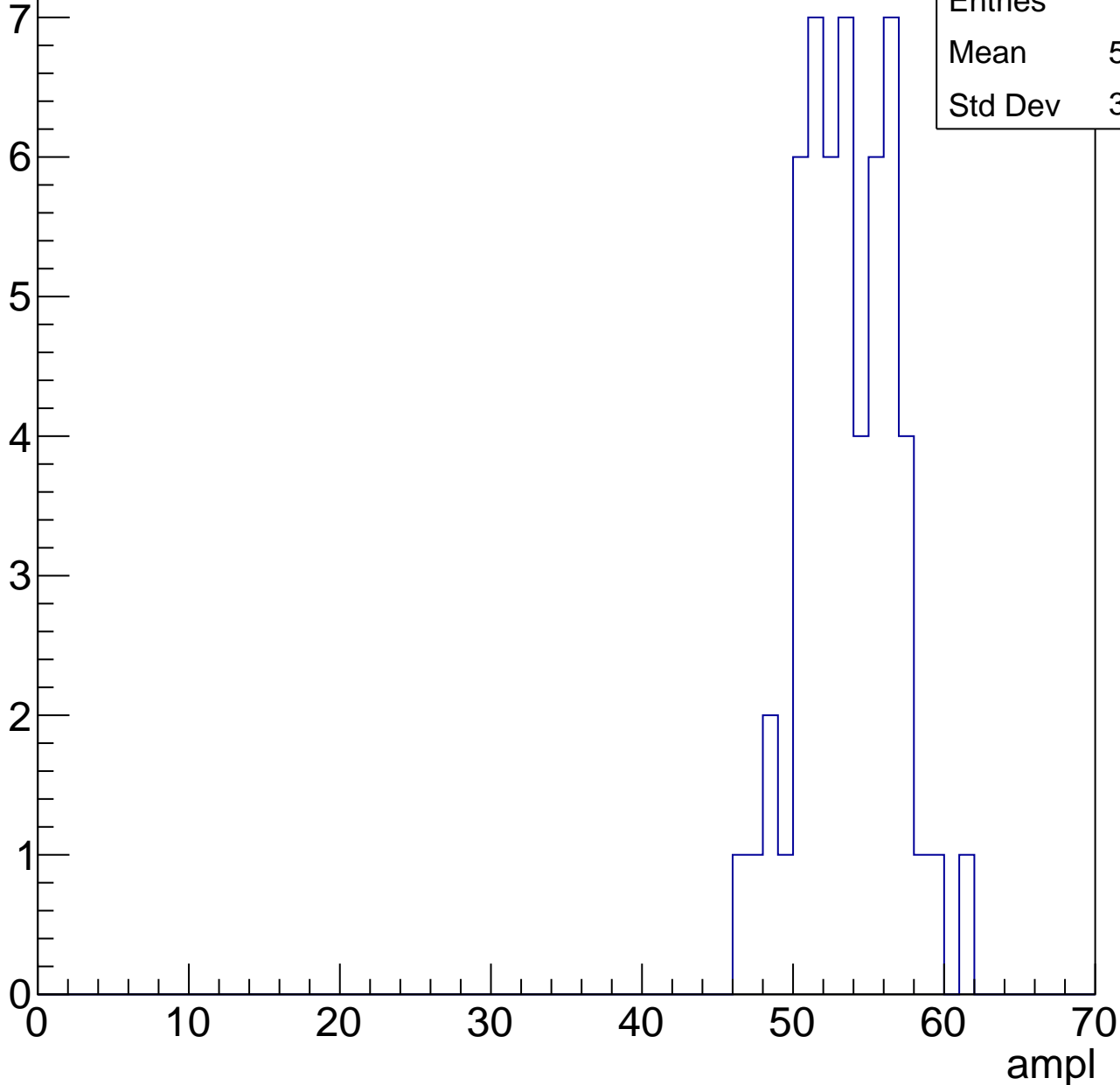


# B1L002S, U17-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.13
Std Dev	3.069

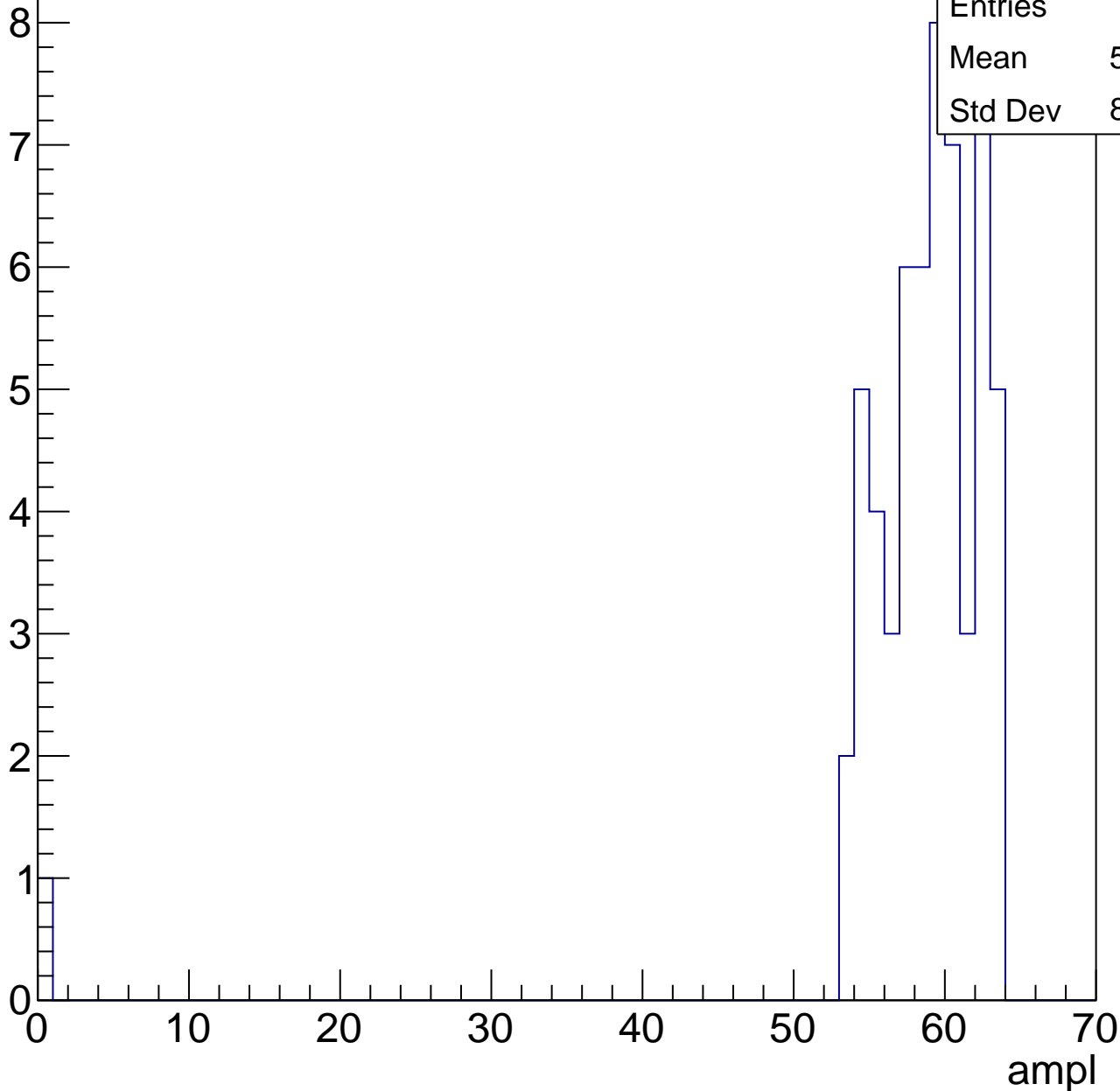


# B1L002S, U17-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.59
Std Dev	8.152

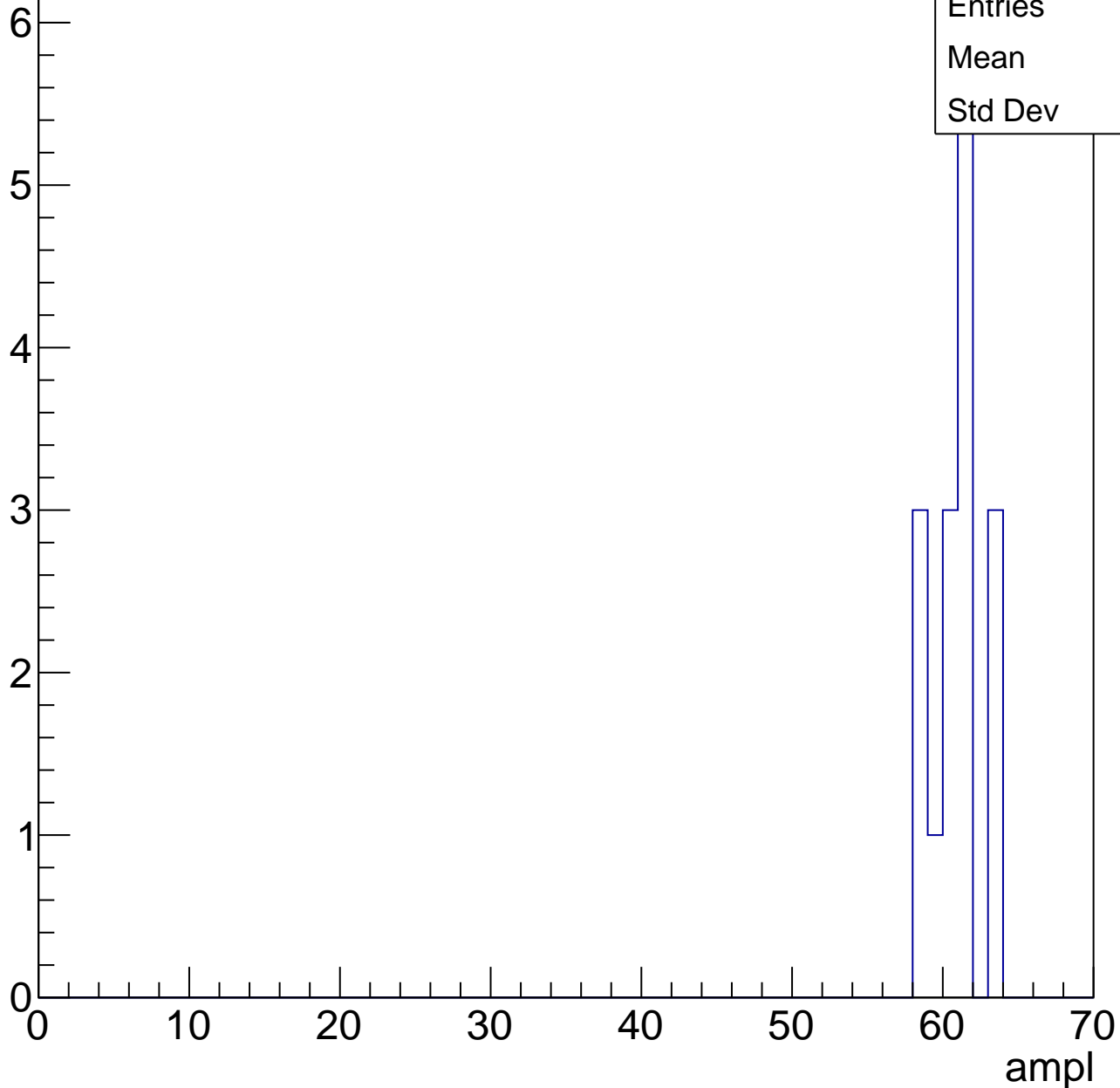


# B1L002S, U17-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	60.5
Std Dev	1.62

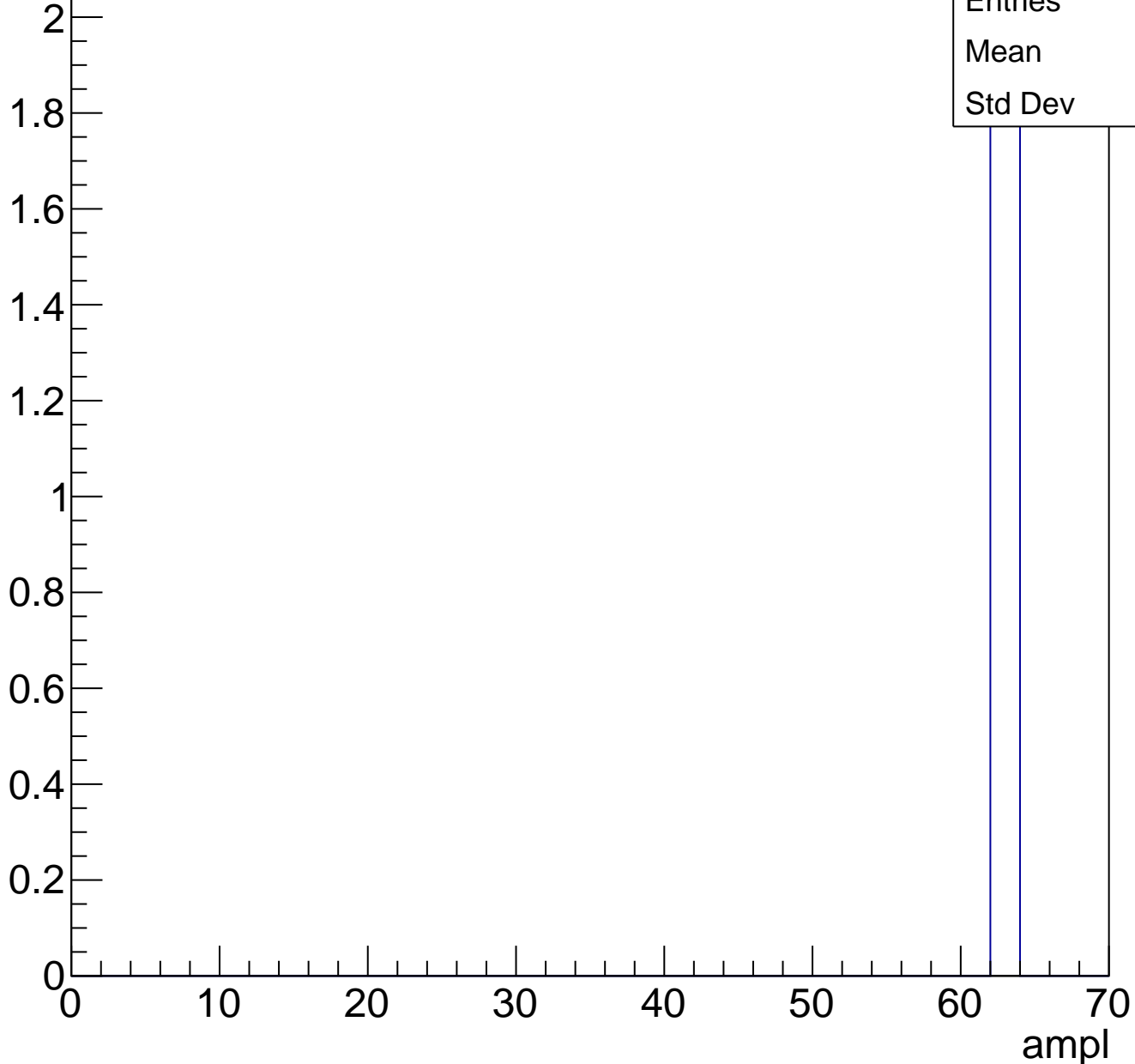




# B1L002S, U17-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch63, adc0

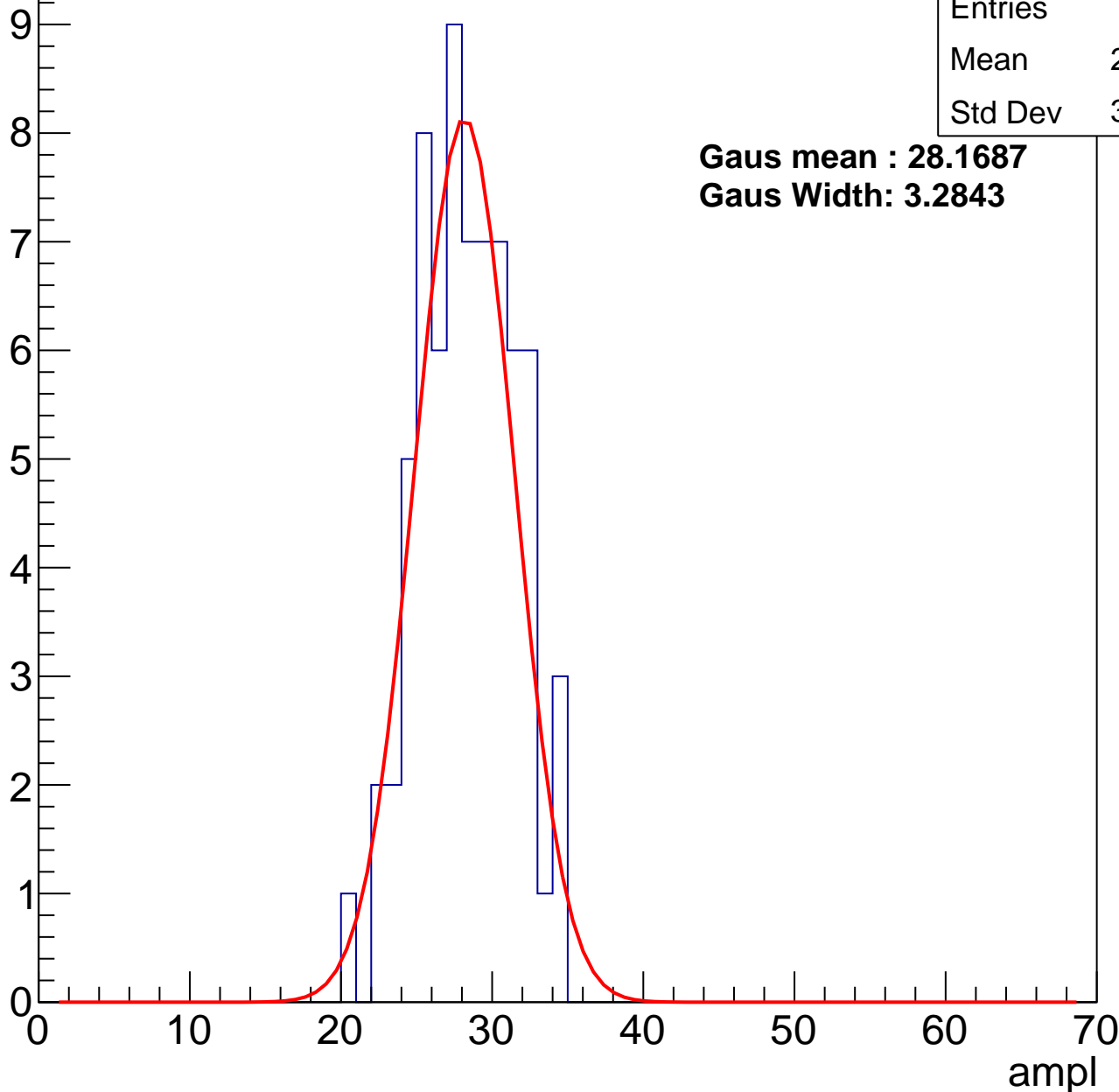
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	27.87
Std Dev	3.135

**Gaus mean : 28.1687**

**Gaus Width: 3.2843**



# B1L002S, U17-ch63, adc1

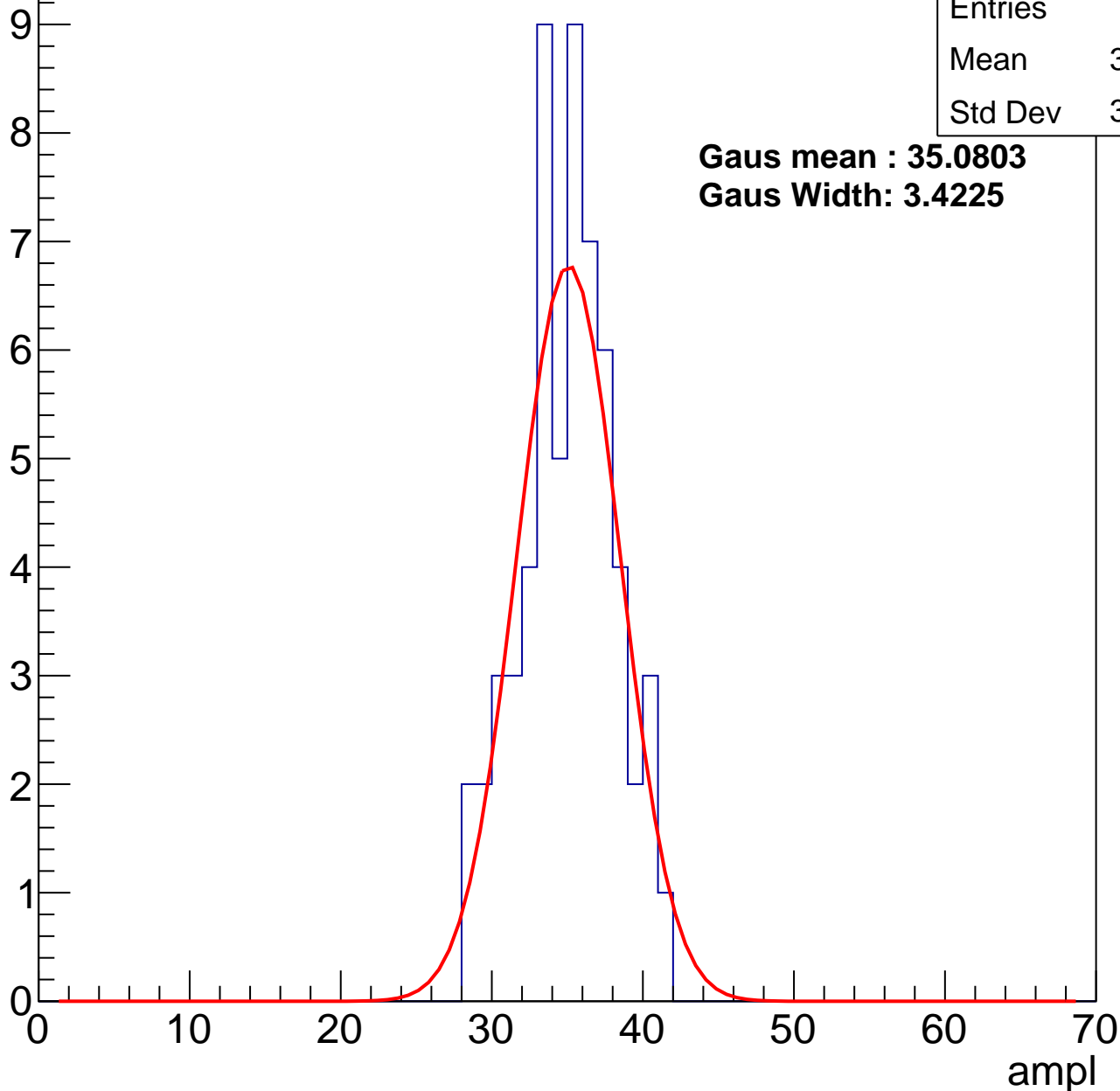
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.53
Std Dev	3.074

**Gaus mean : 35.0803**

**Gaus Width: 3.4225**



# B1L002S, U17-ch63, adc2

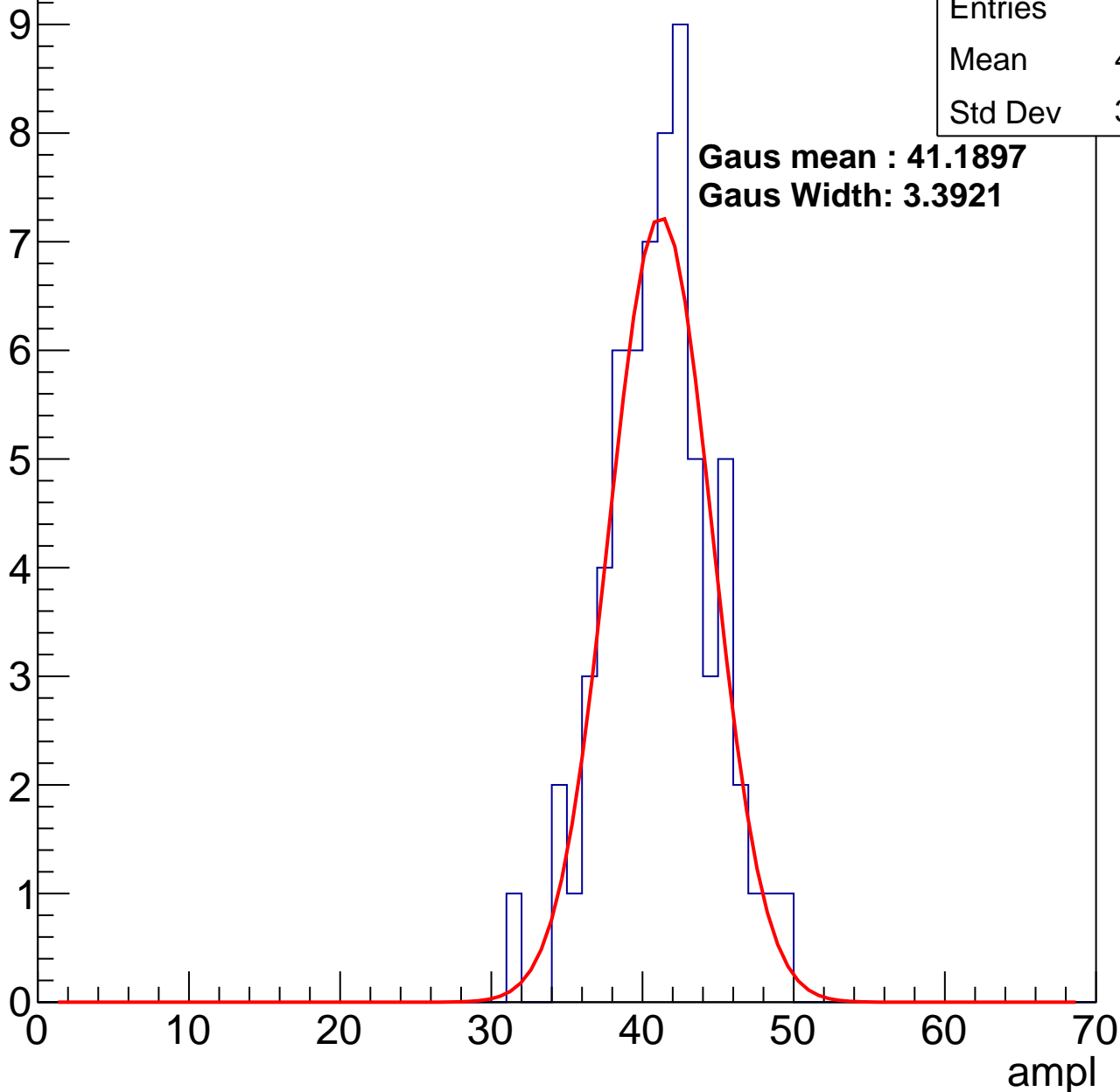
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.71
Std Dev	3.481

**Gaus mean : 41.1897**

**Gaus Width: 3.3921**

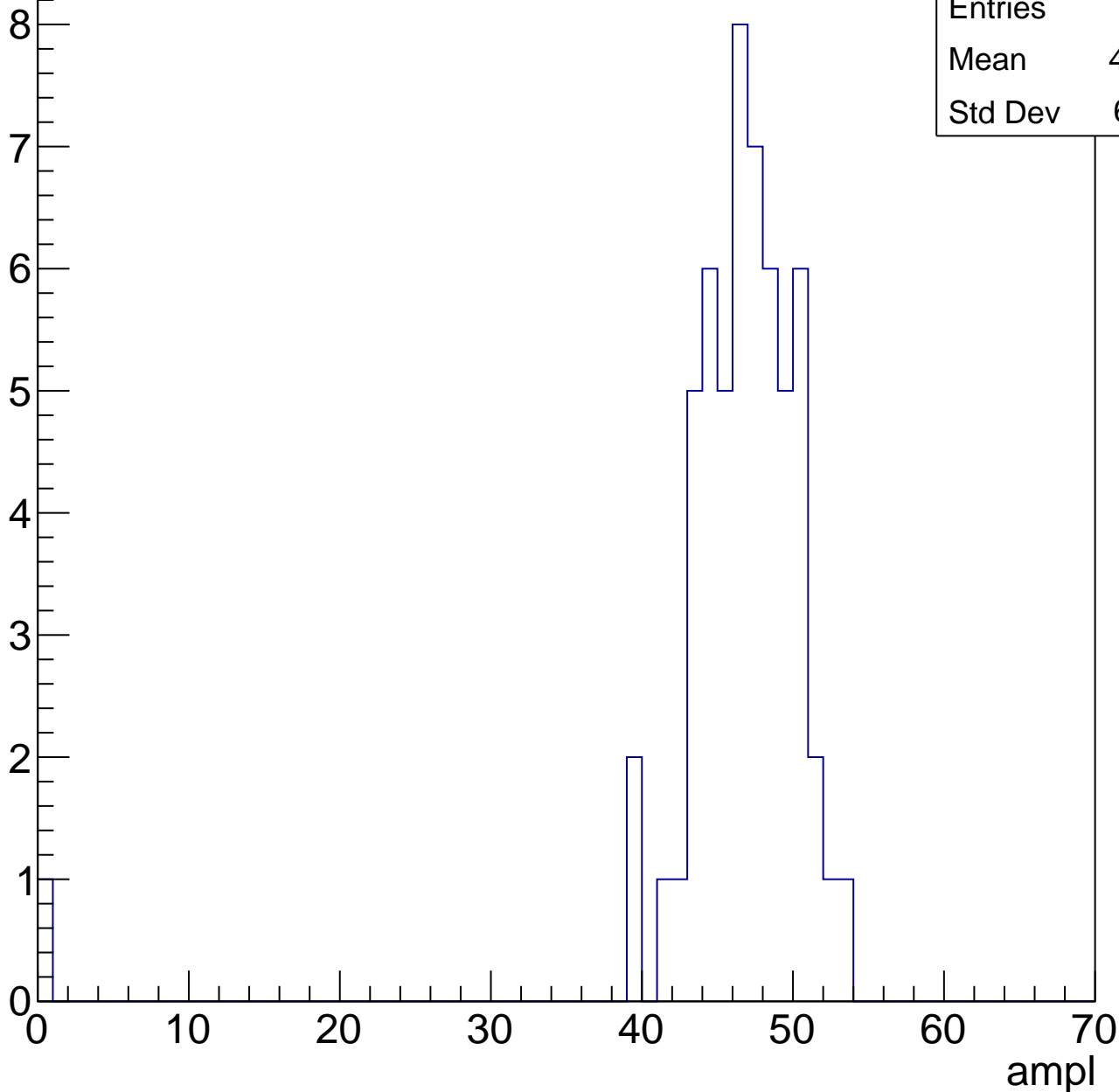


# B1L002S, U17-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	45.65
Std Dev	6.791

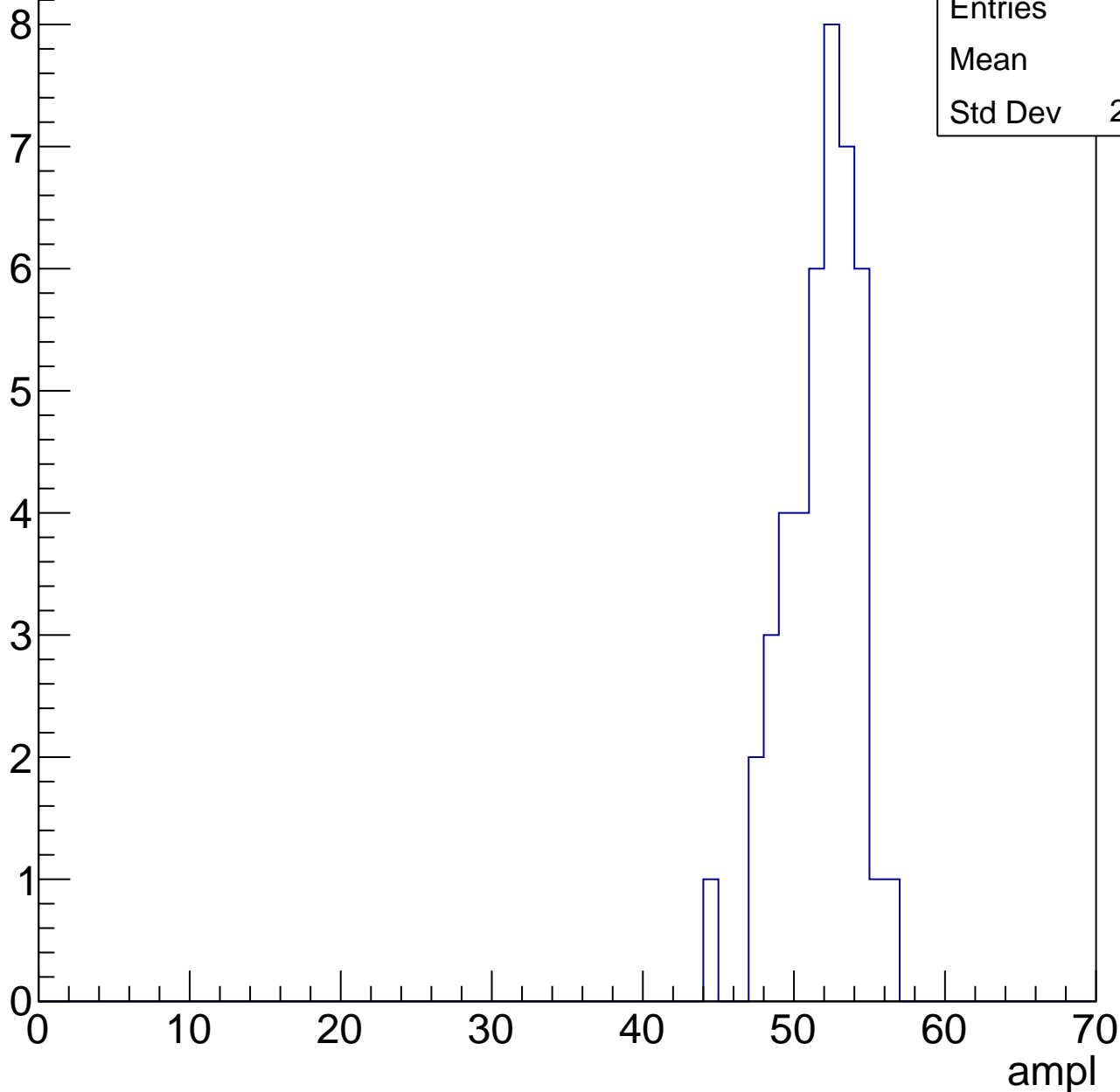


# B1L002S, U17-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	51.3
Std Dev	2.436

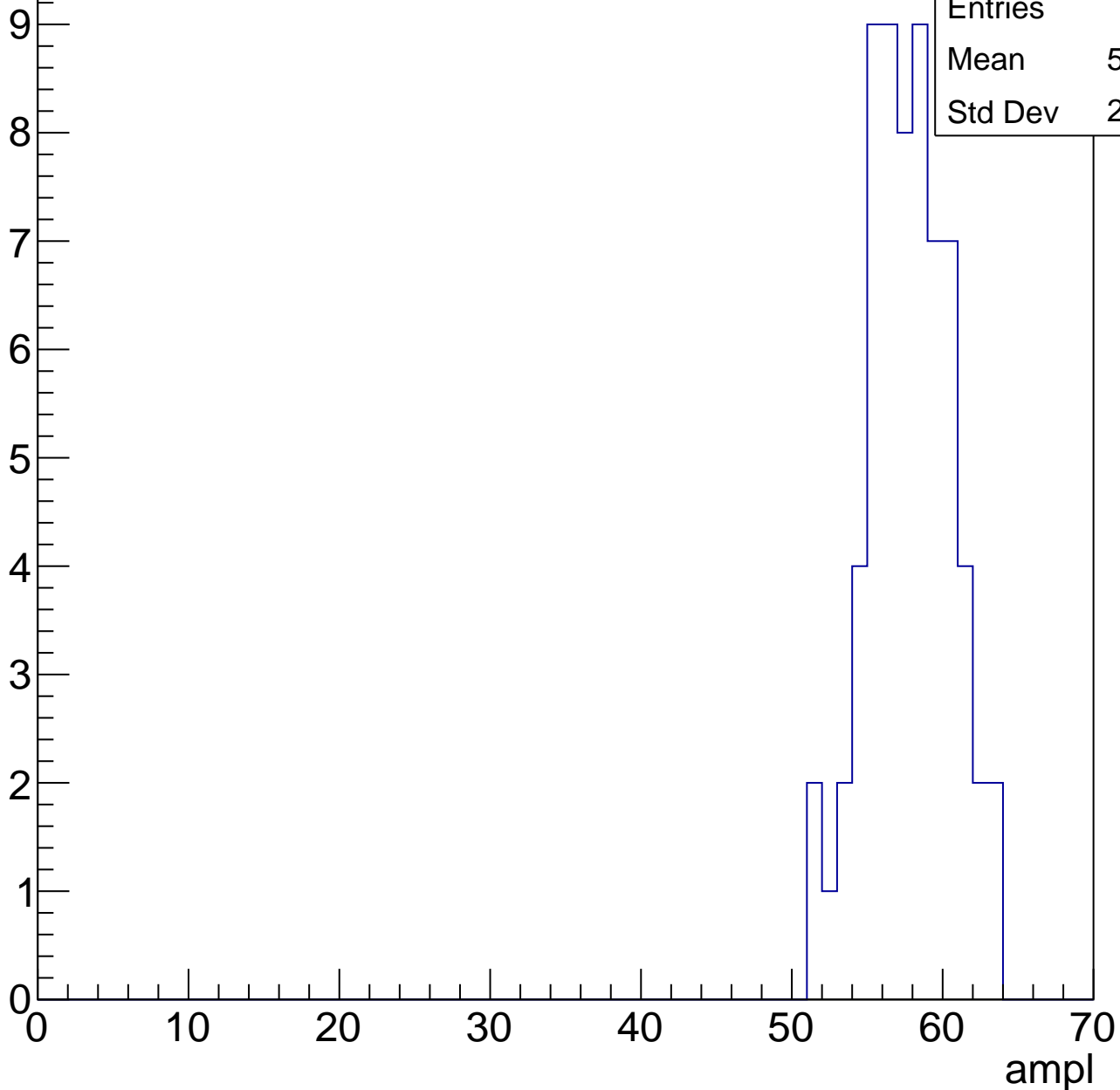


# B1L002S, U17-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.27
Std Dev	2.728

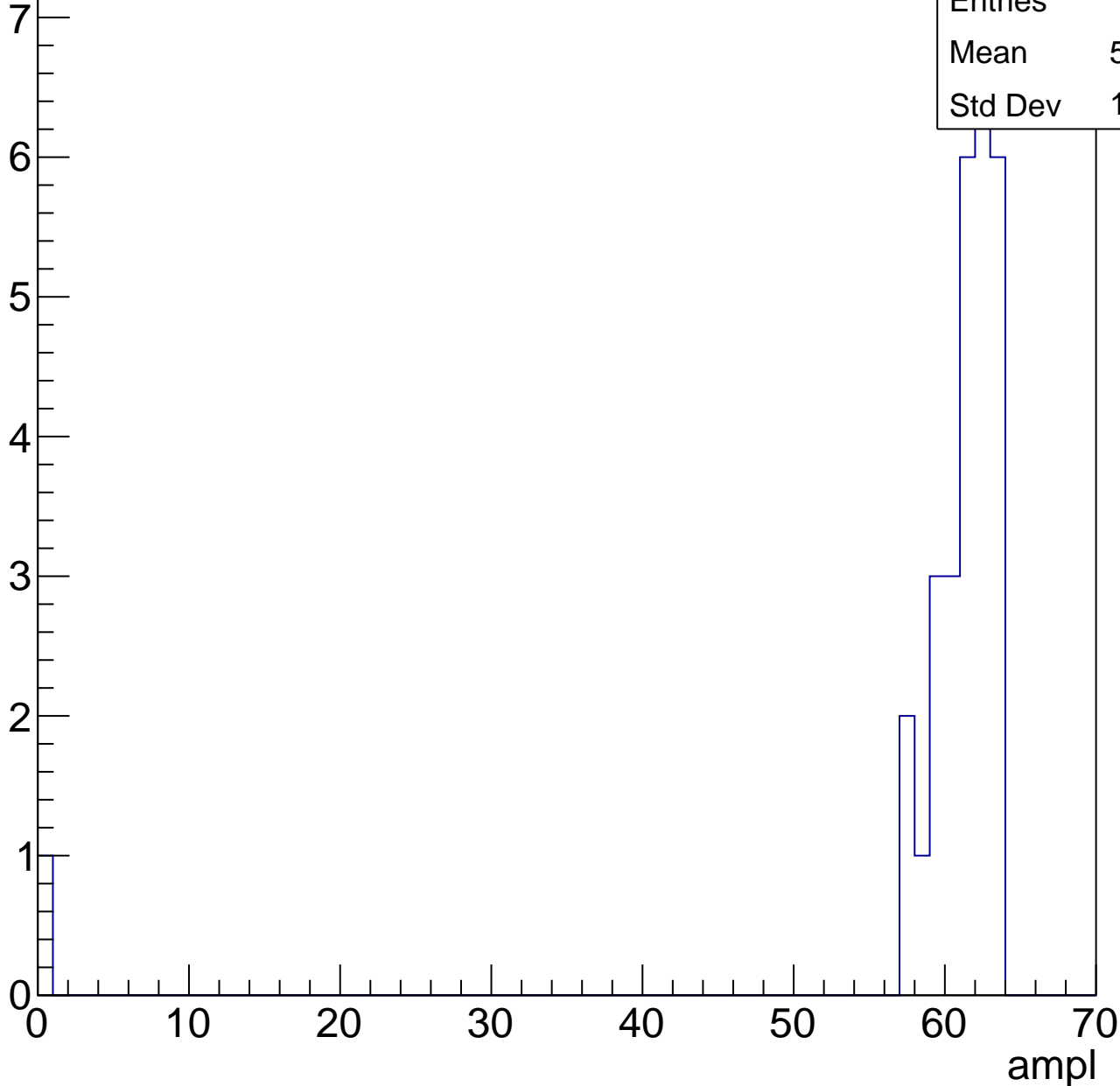


# B1L002S, U17-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.86
Std Dev	11.26

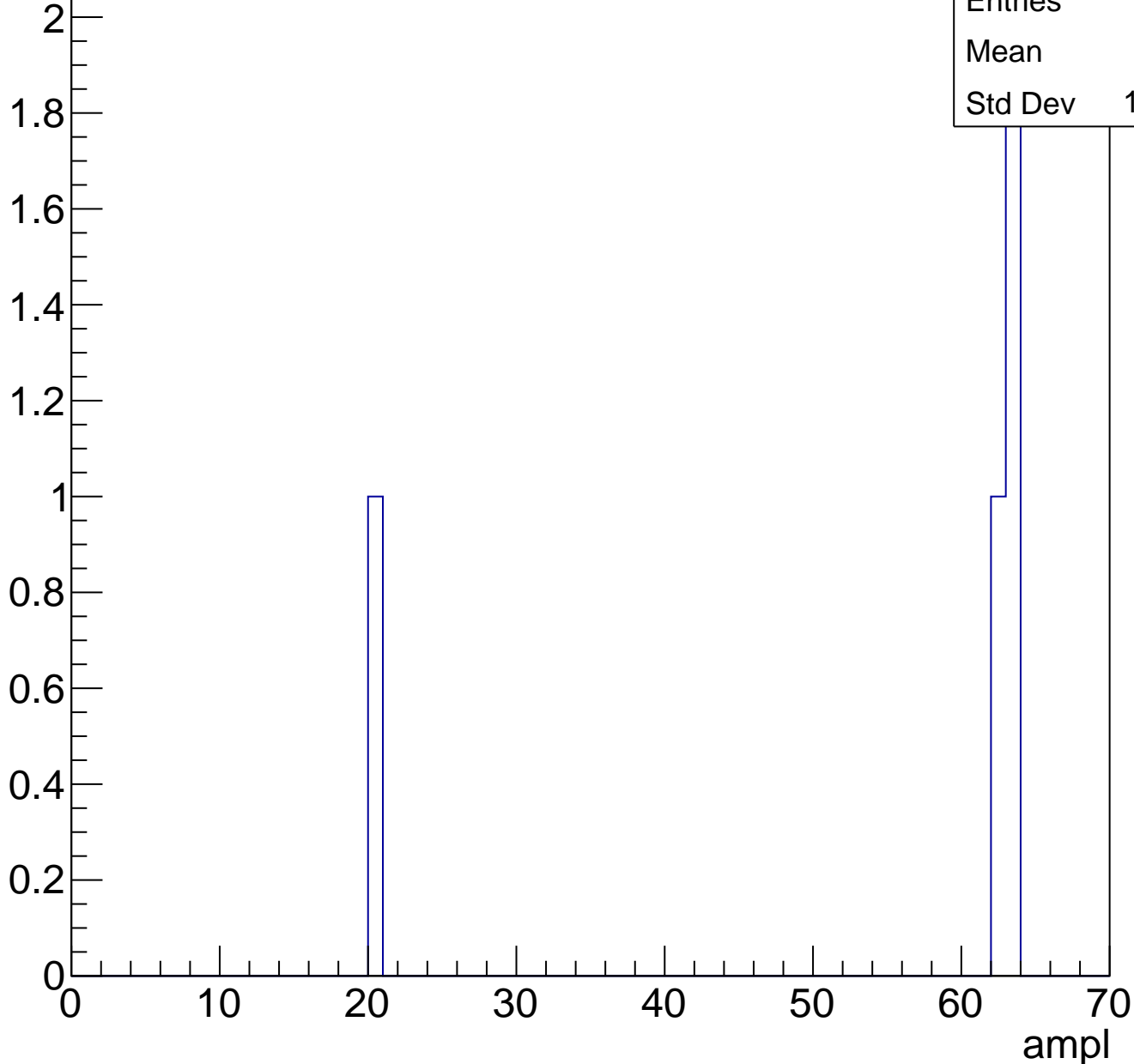




# B1L002S, U17-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch64, adc0

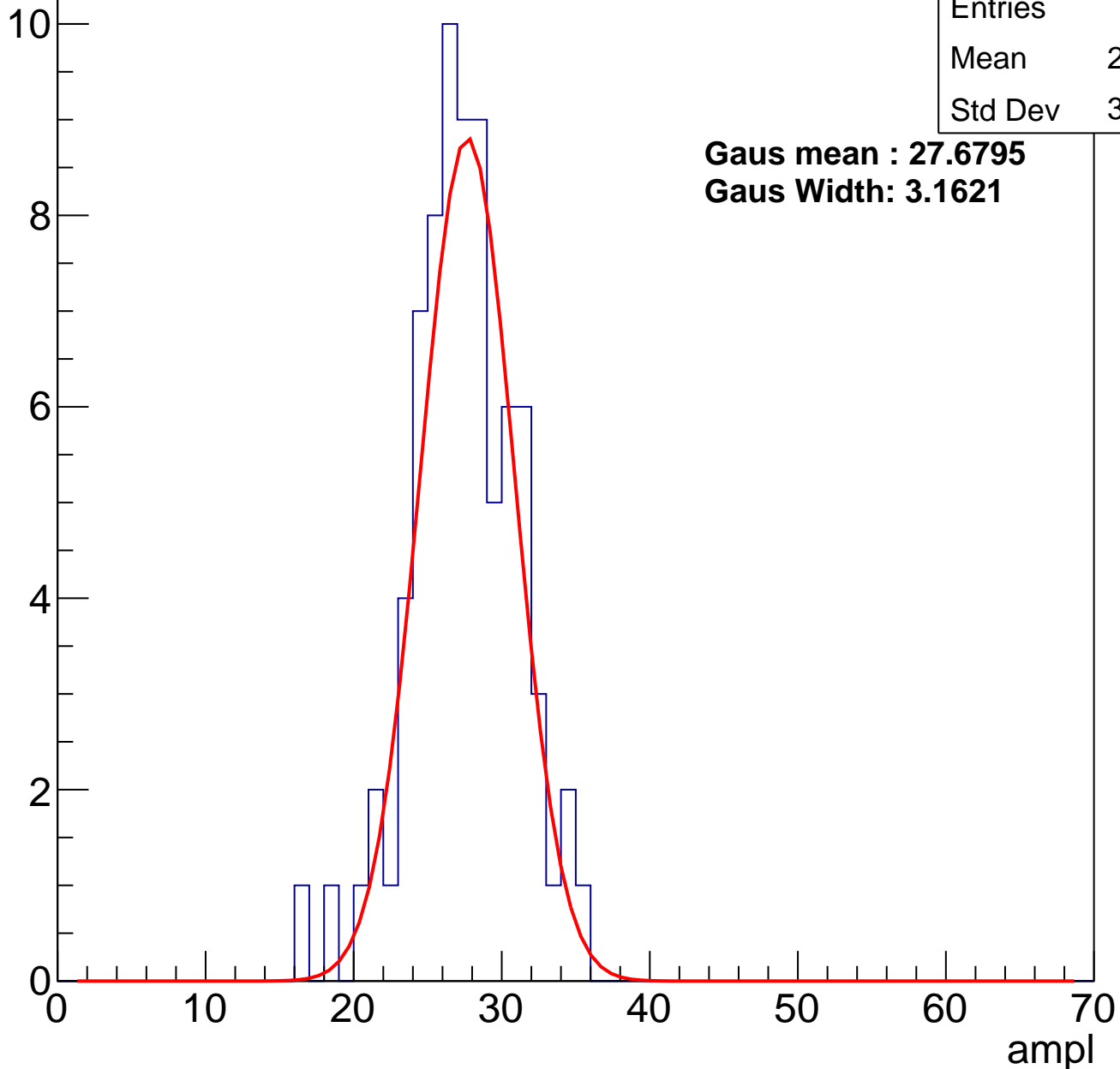
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	26.96
Std Dev	3.547

**Gaus mean : 27.6795**

**Gaus Width: 3.1621**

Entry



# B1L002S, U17-ch64, adc1

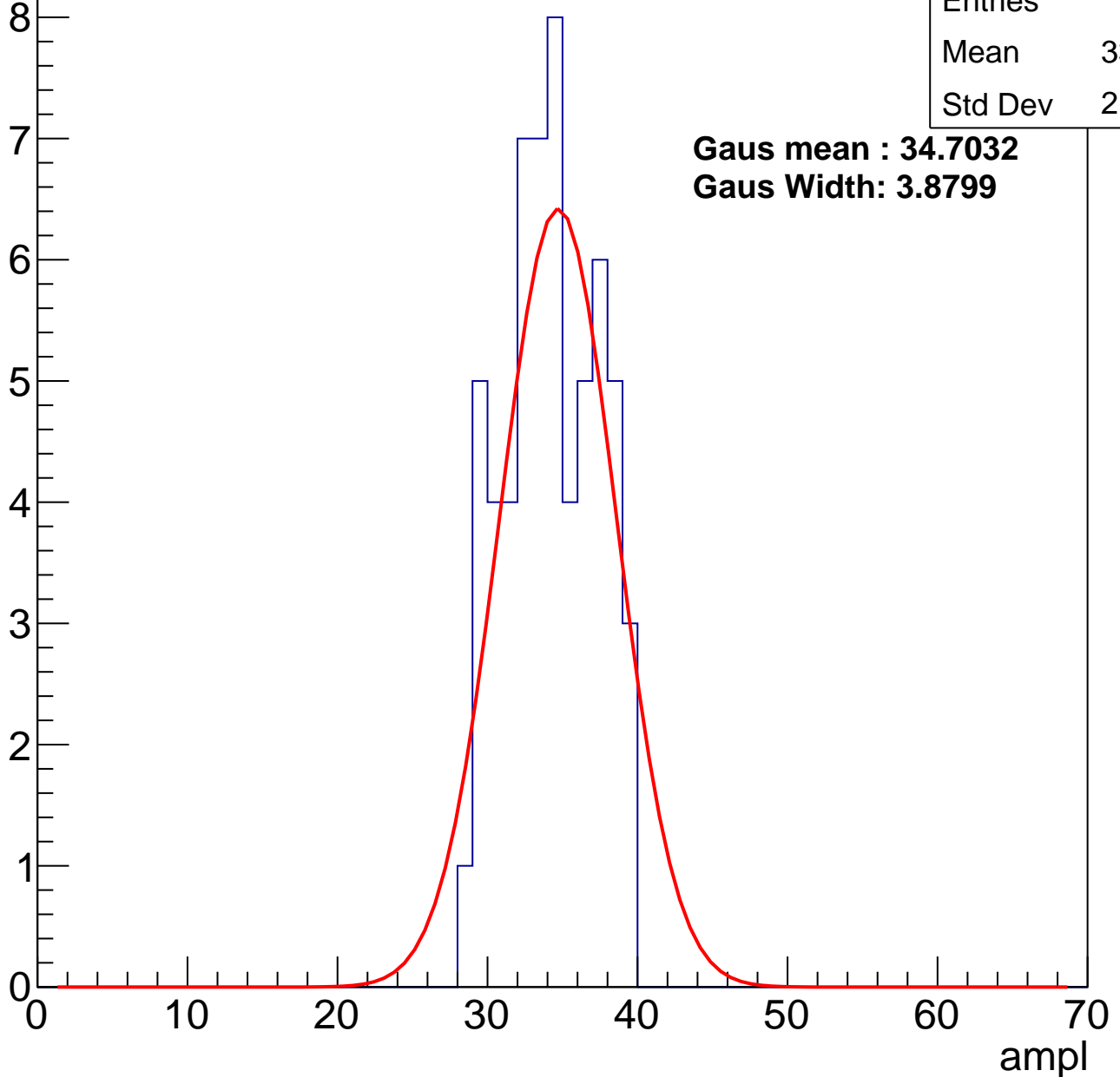
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	33.78
Std Dev	2.986

**Gaus mean : 34.7032**

**Gaus Width: 3.8799**



# B1L002S, U17-ch64, adc2

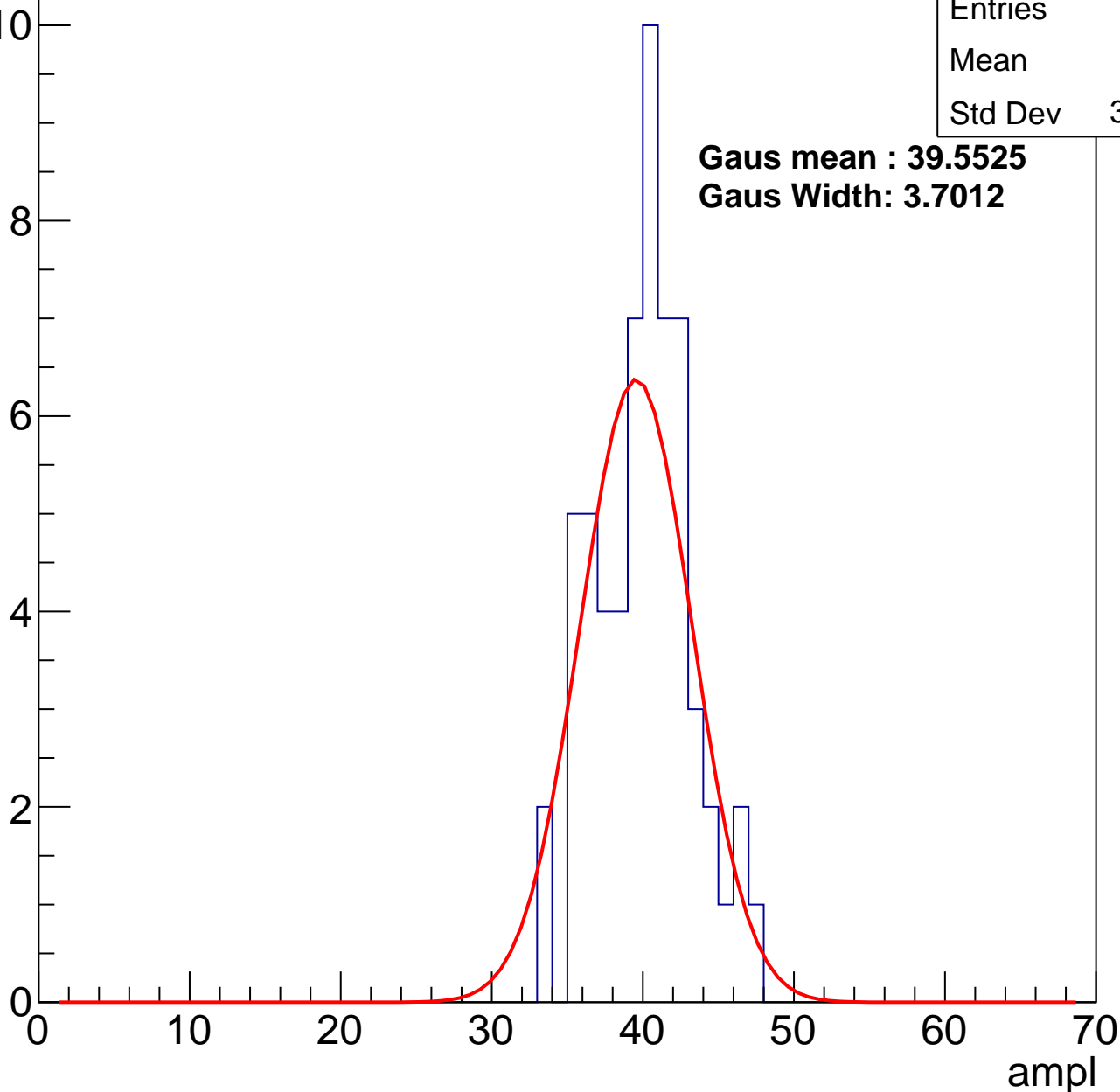
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	39.6
Std Dev	3.142

**Gaus mean : 39.5525**

**Gaus Width: 3.7012**

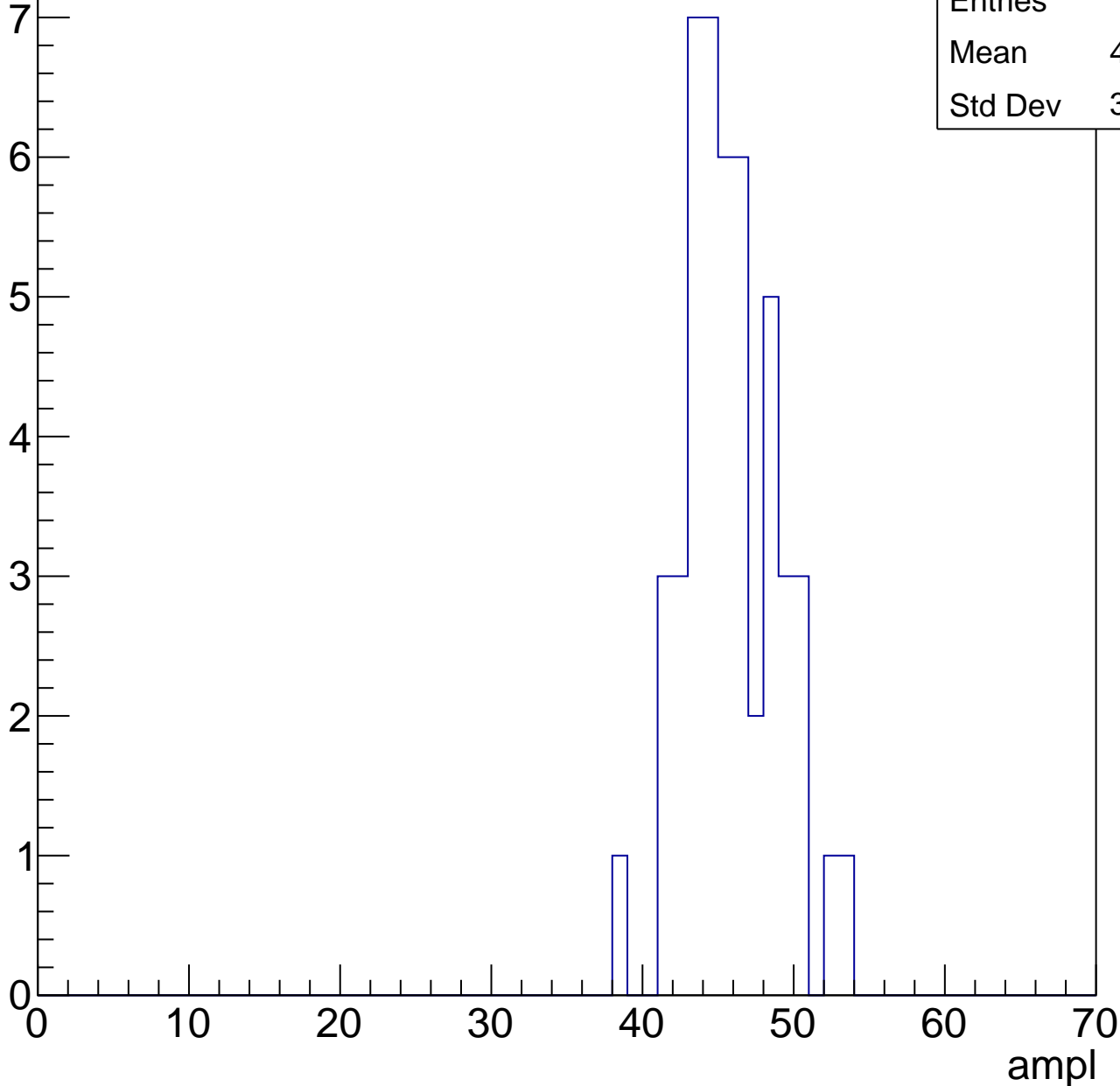


# B1L002S, U17-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	45.38
Std Dev	3.052

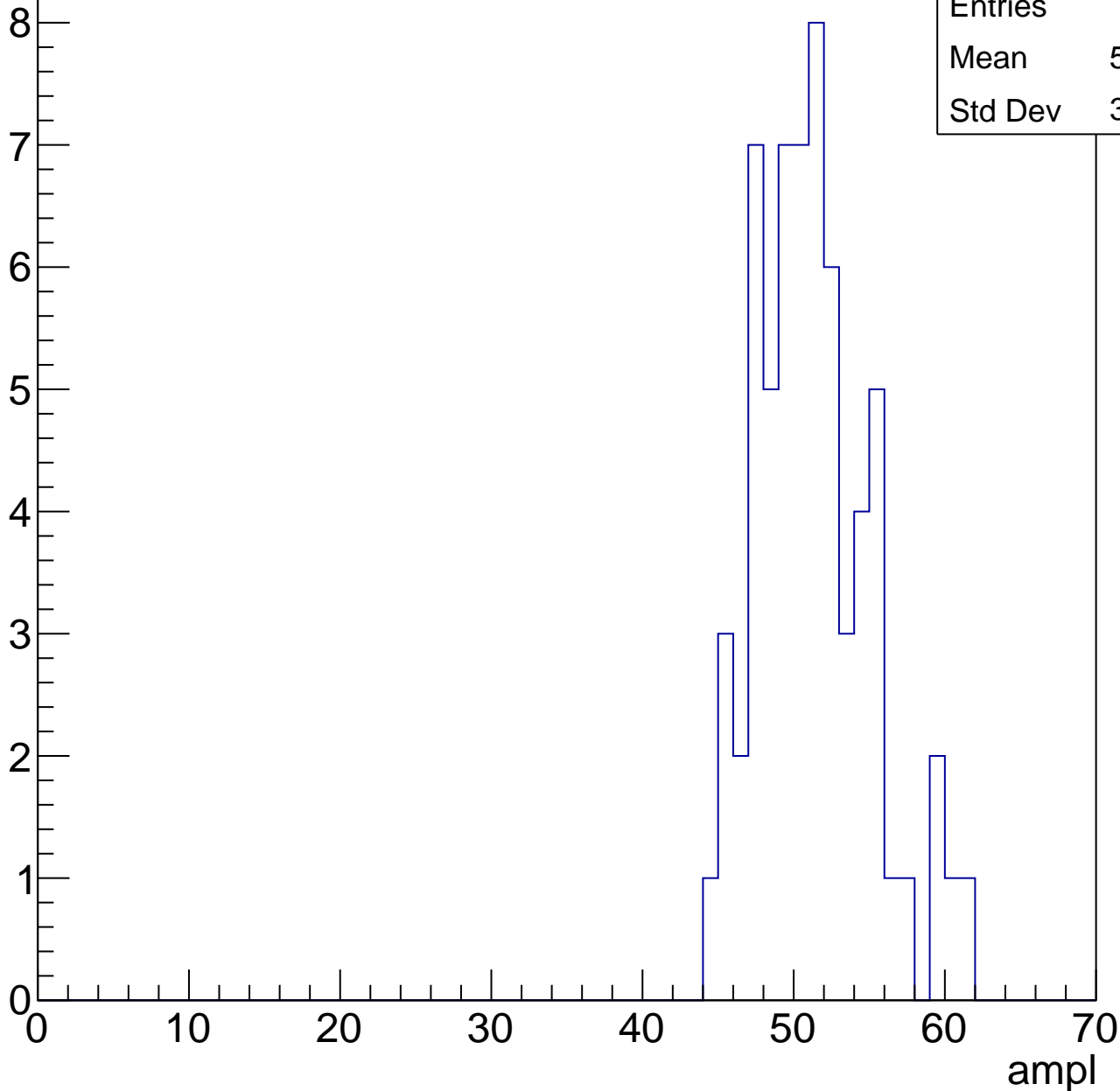


# B1L002S, U17-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	50.86
Std Dev	3.754

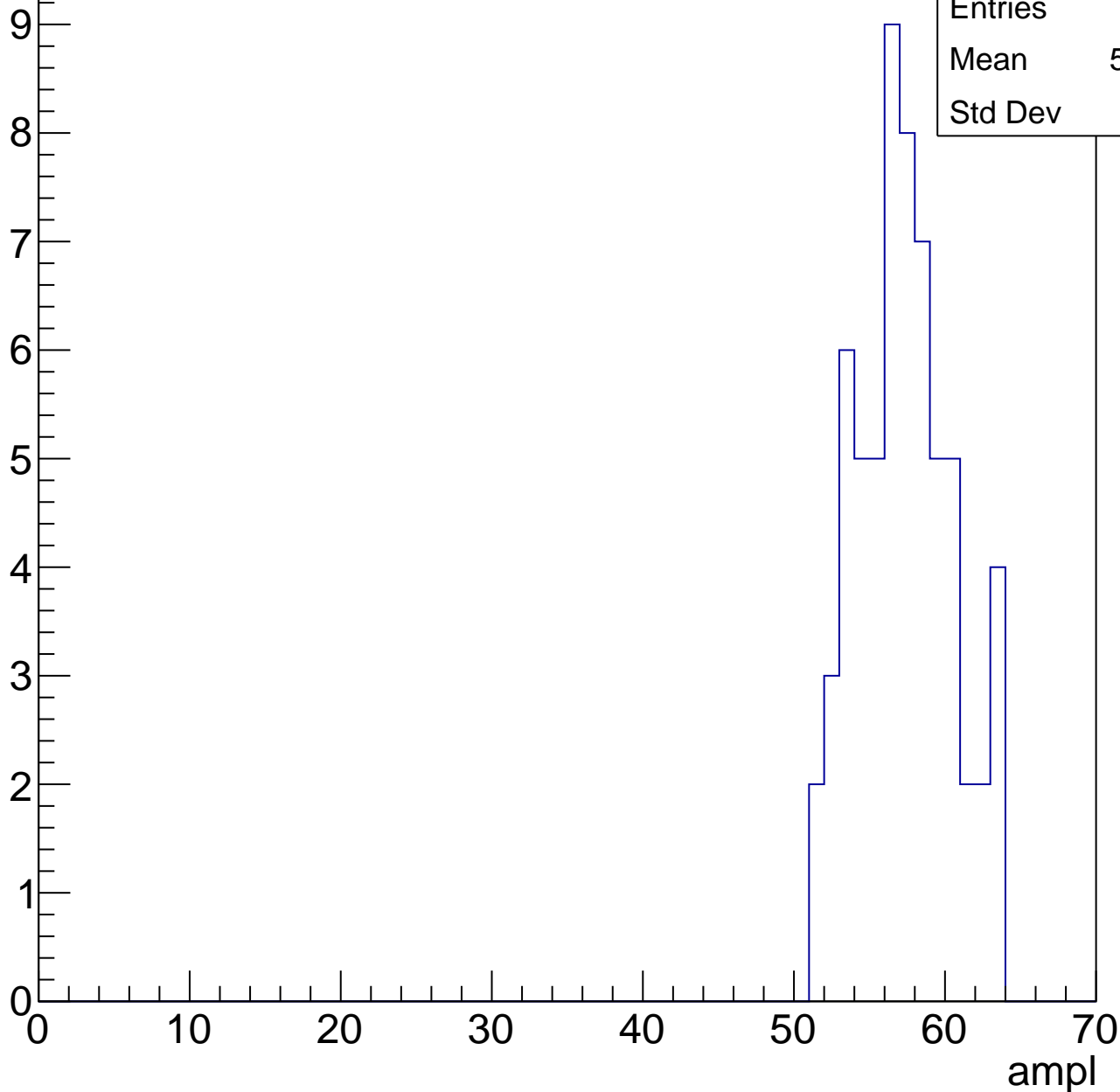


# B1L002S, U17-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	56.83
Std Dev	3.12

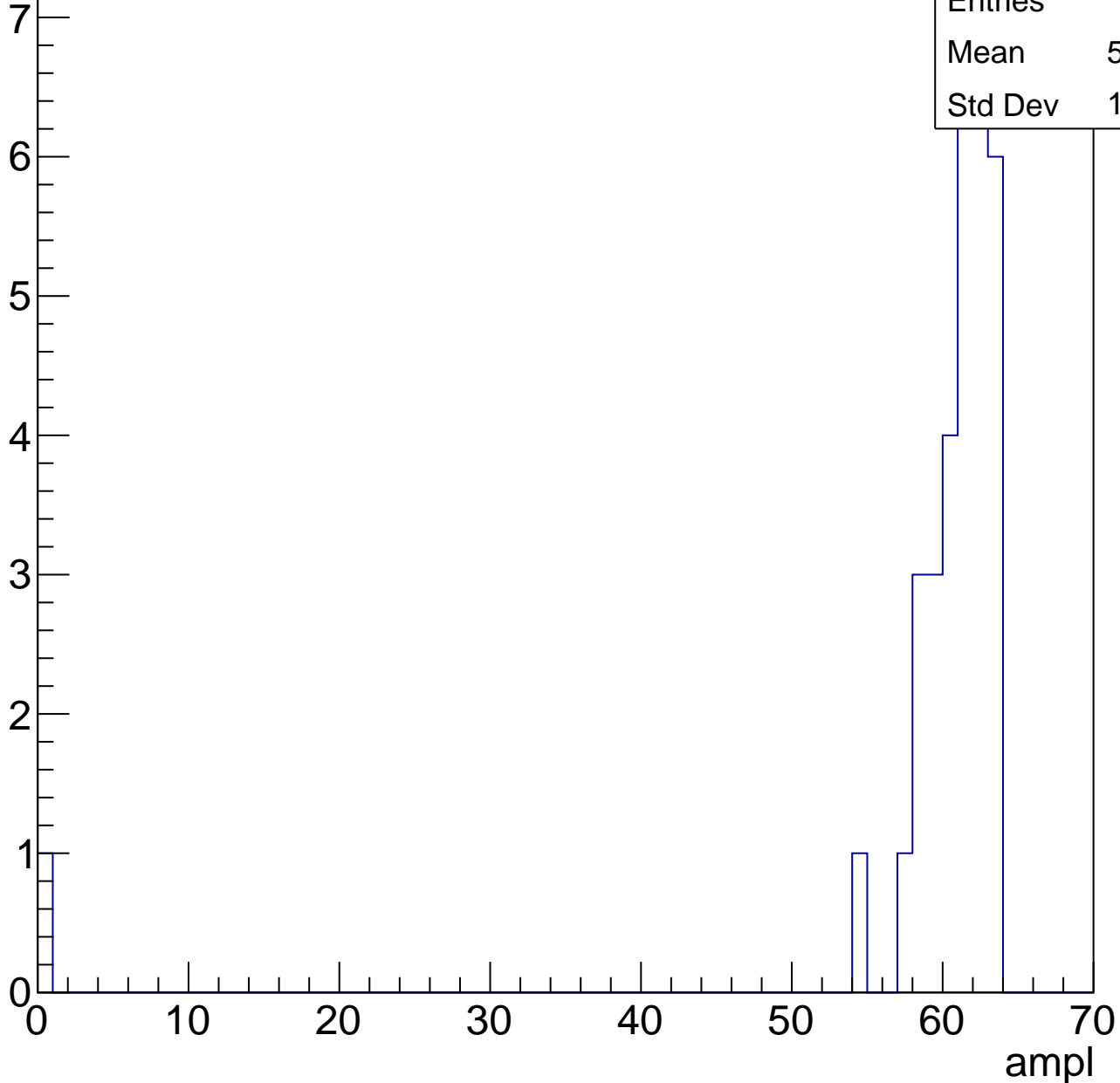


# B1L002S, U17-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	58.82
Std Dev	10.59





# B1L002S, U17-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch65, adc0

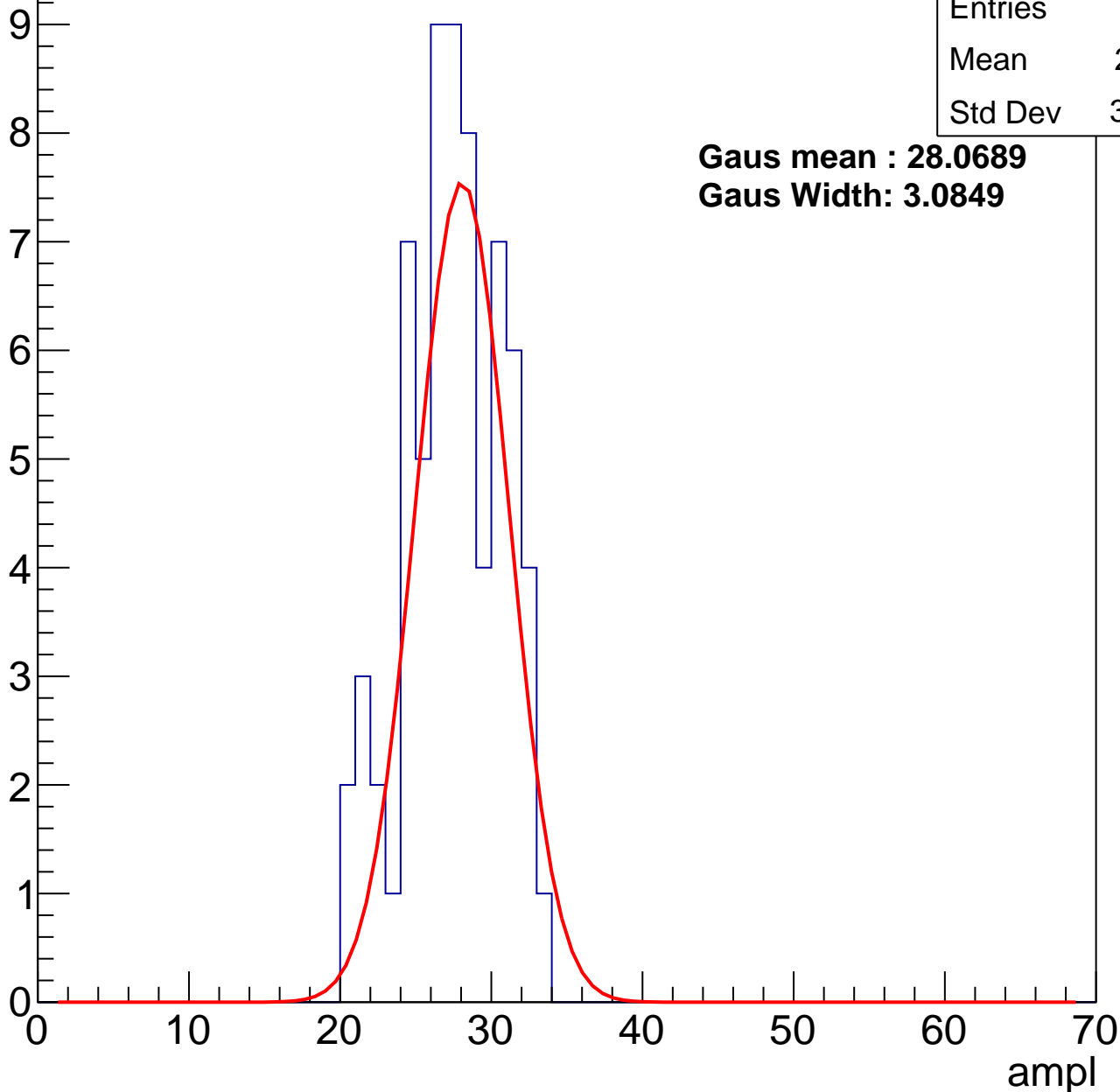
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.01
Std Dev	3.169

**Gaus mean : 28.0689**

**Gaus Width: 3.0849**



# B1L002S, U17-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	33.65
Std Dev	3.468

**Gaus mean : 34.4410**

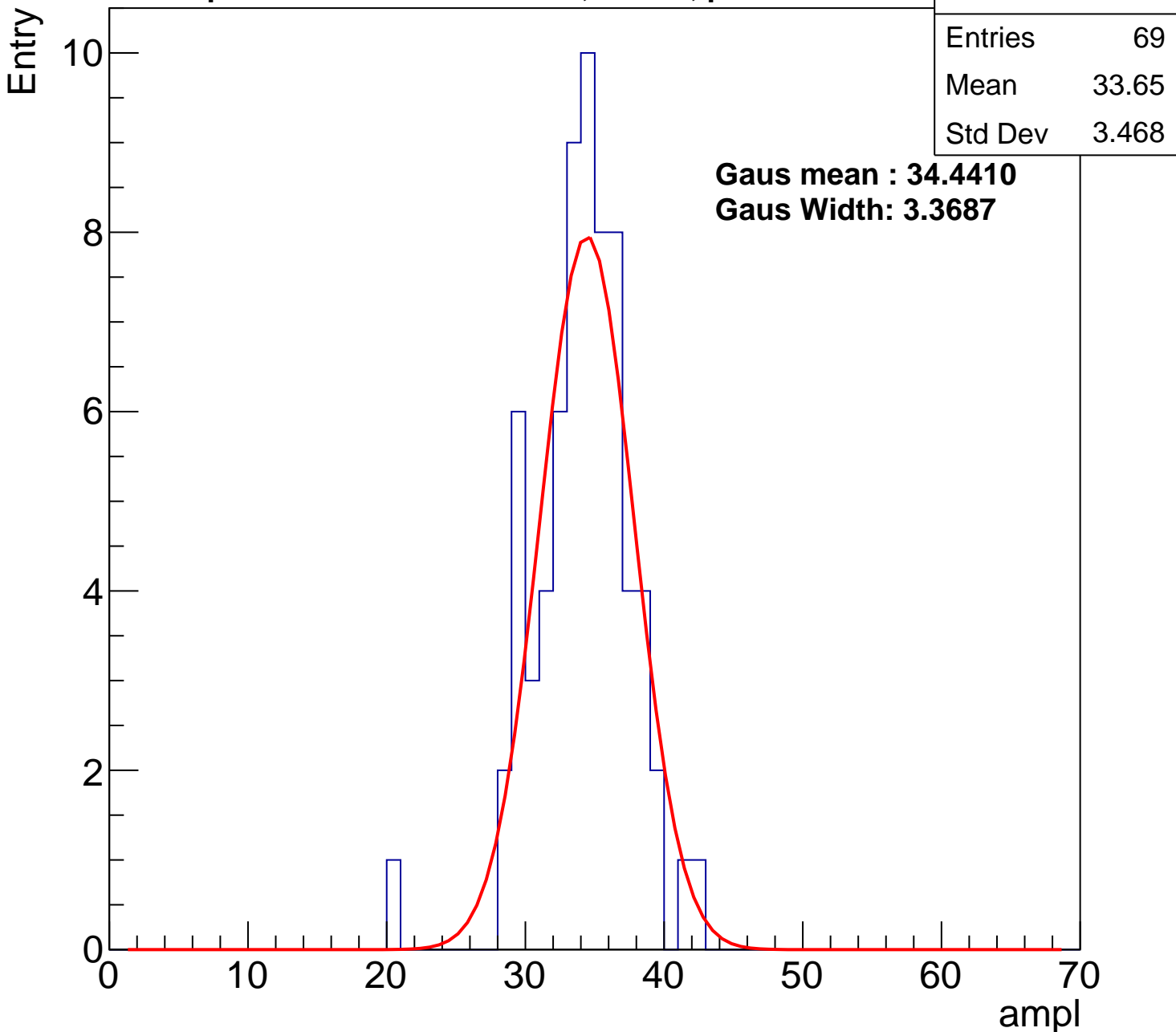
**Gaus Width: 3.3687**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch65, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	55
Mean	40.36
Std Dev	3.035

**Gaus mean : 40.5581**

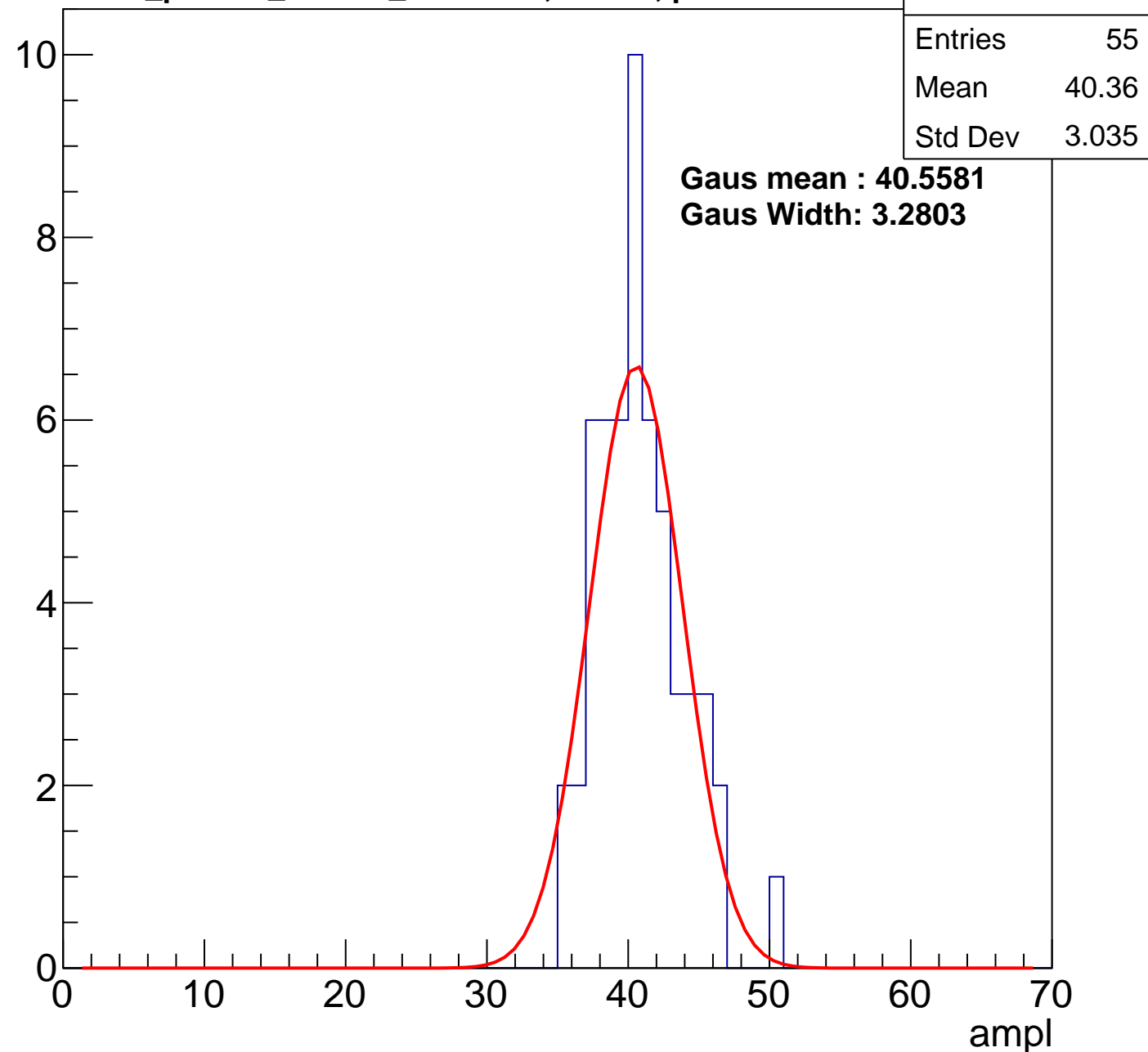
**Gaus Width: 3.2803**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

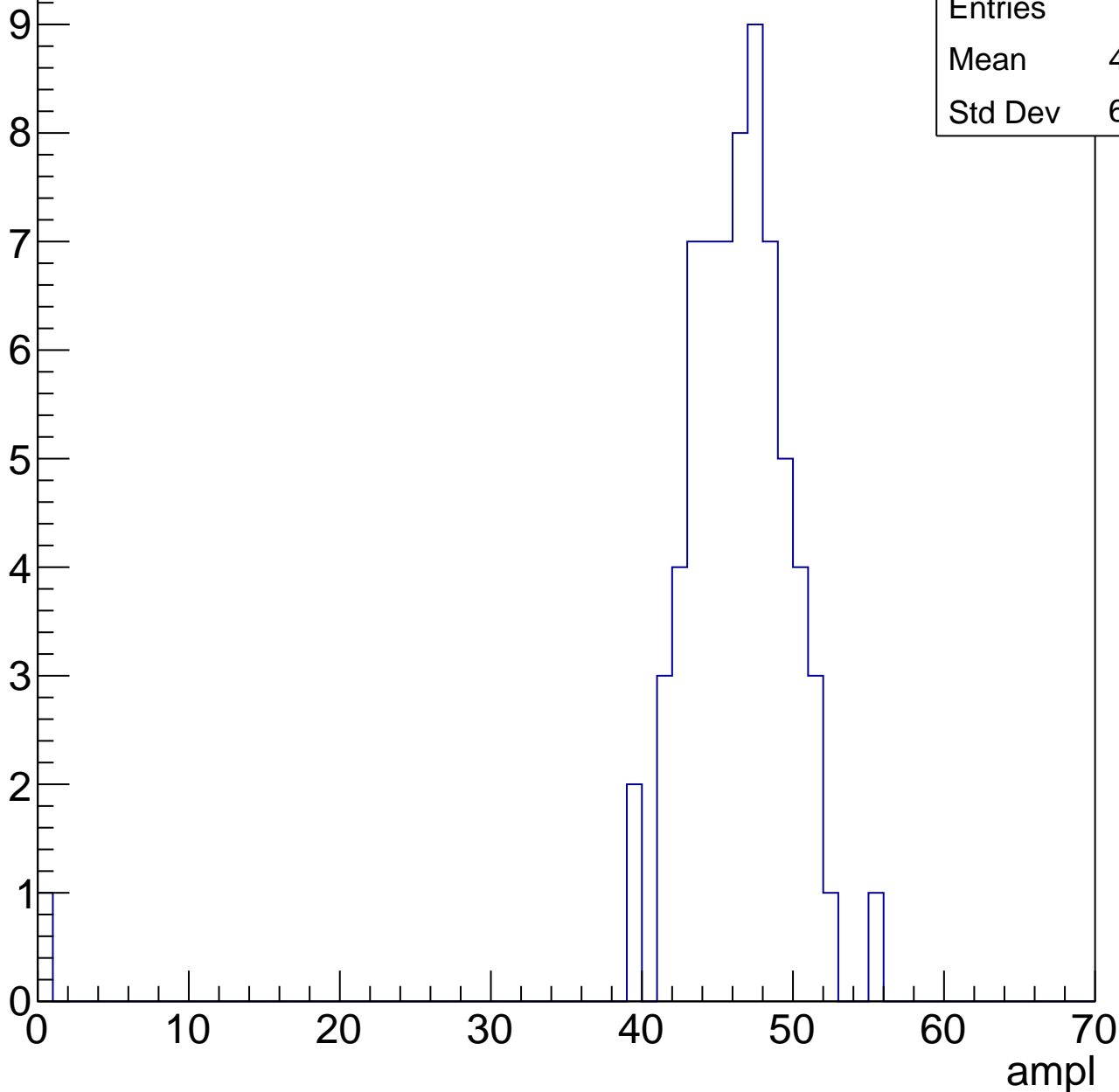


# B1L002S, U17-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.29
Std Dev	6.318

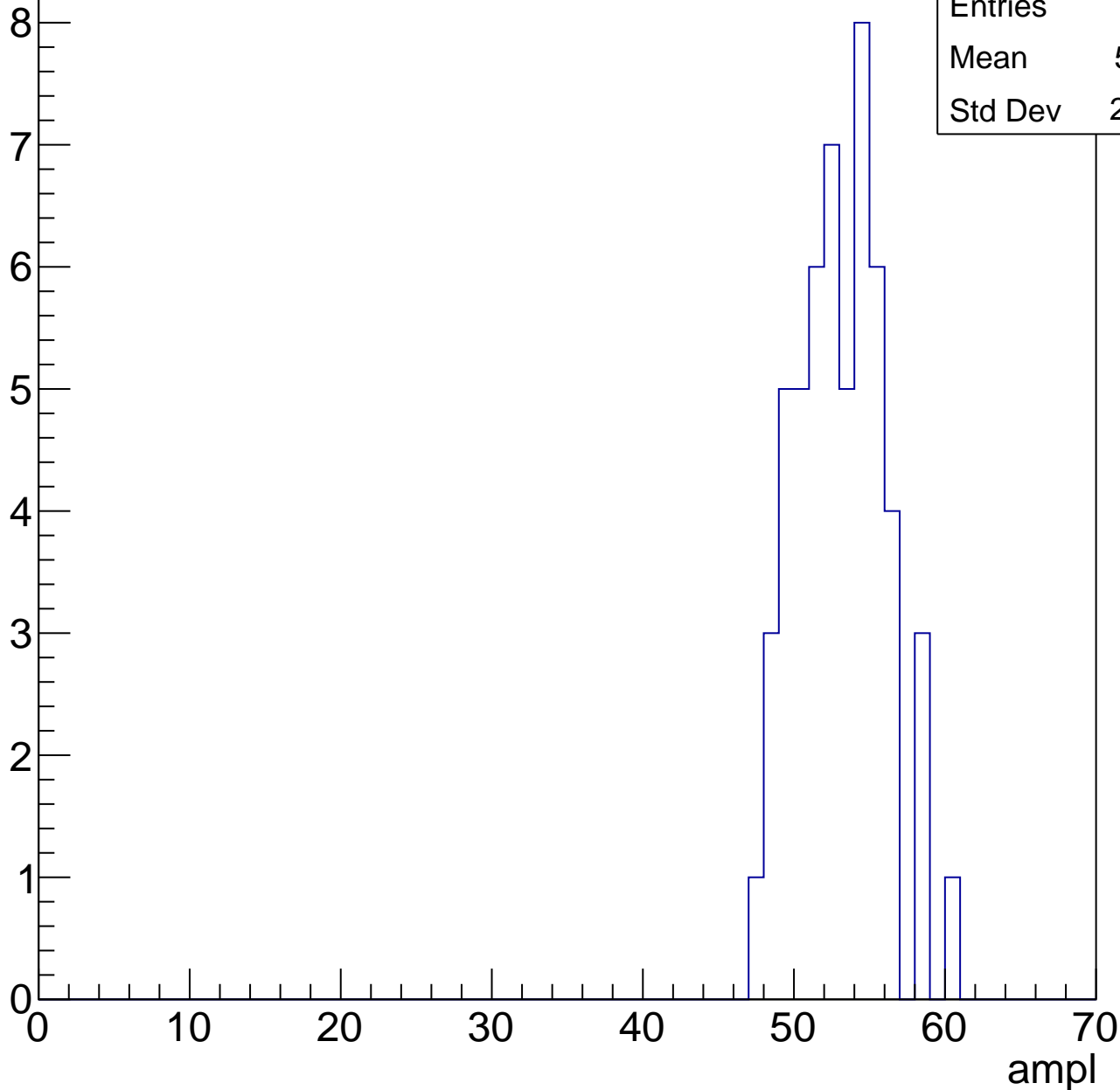


# B1L002S, U17-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

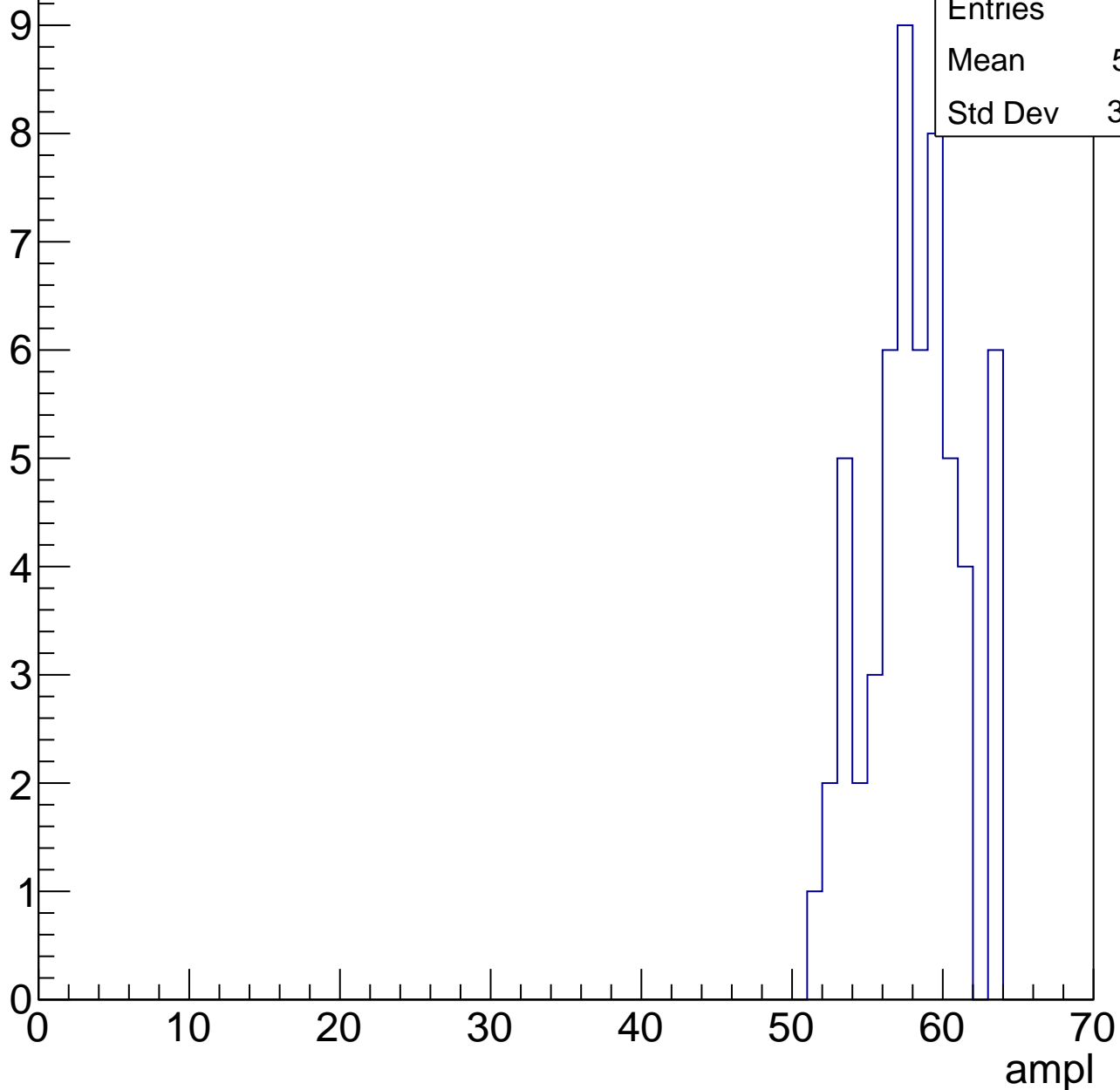
Entries	54
Mean	52.61
Std Dev	2.889



# B1L002S, U17-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



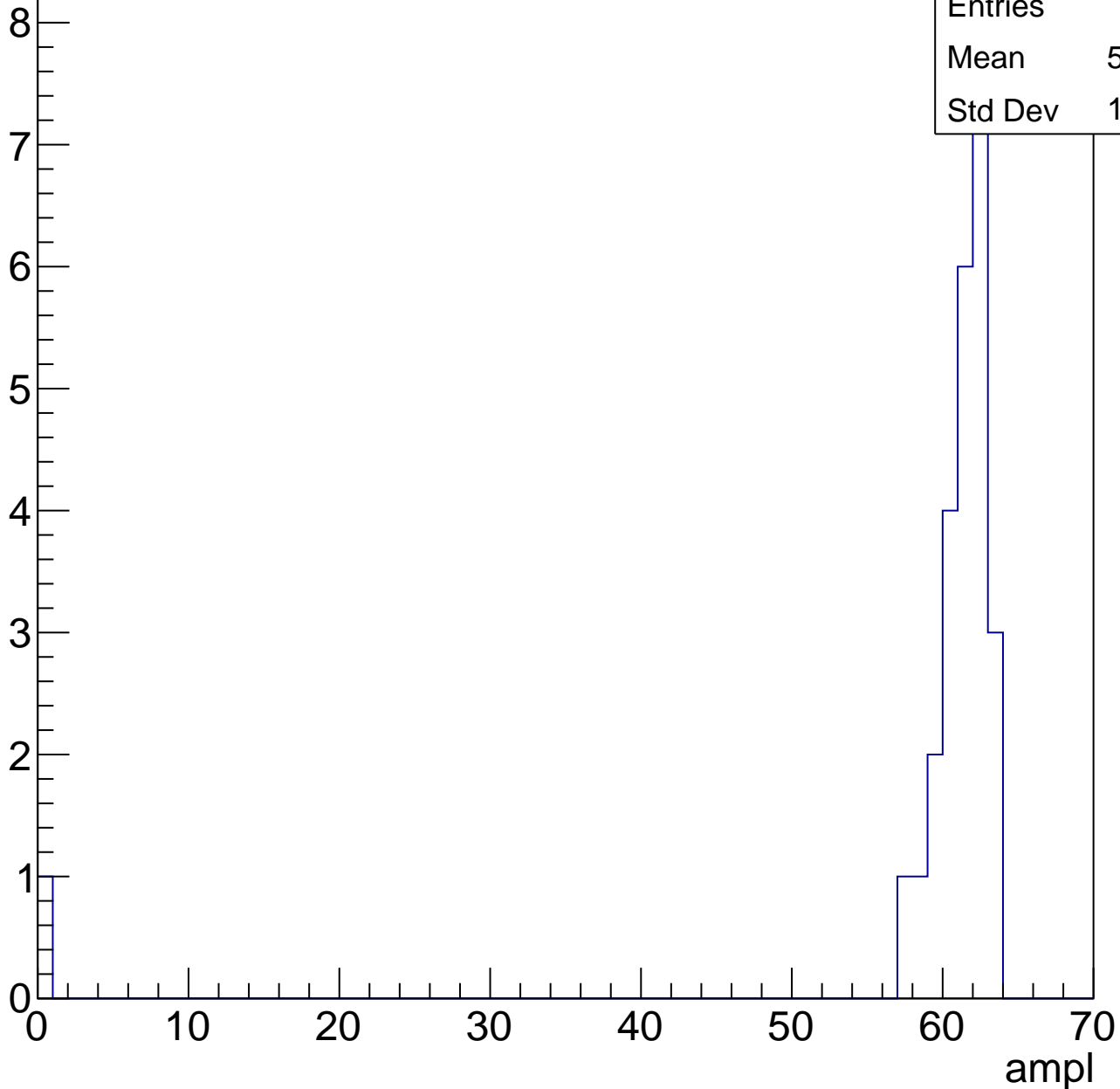
Entries	57
Mean	57.61
Std Dev	3.088

# B1L002S, U17-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.62
Std Dev	11.82





# B1L002S, U17-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

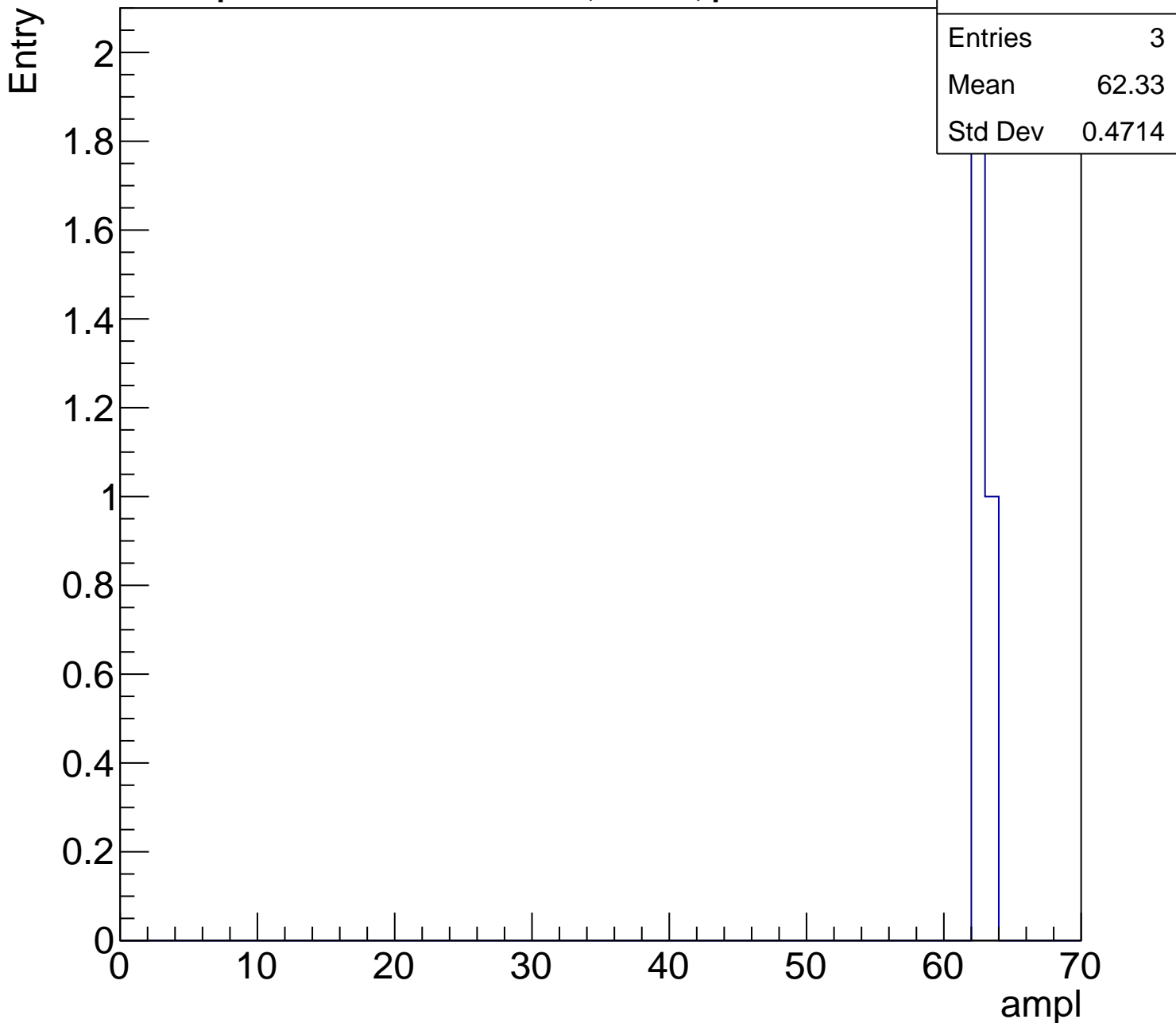
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



# B1L002S, U17-ch66, adc0

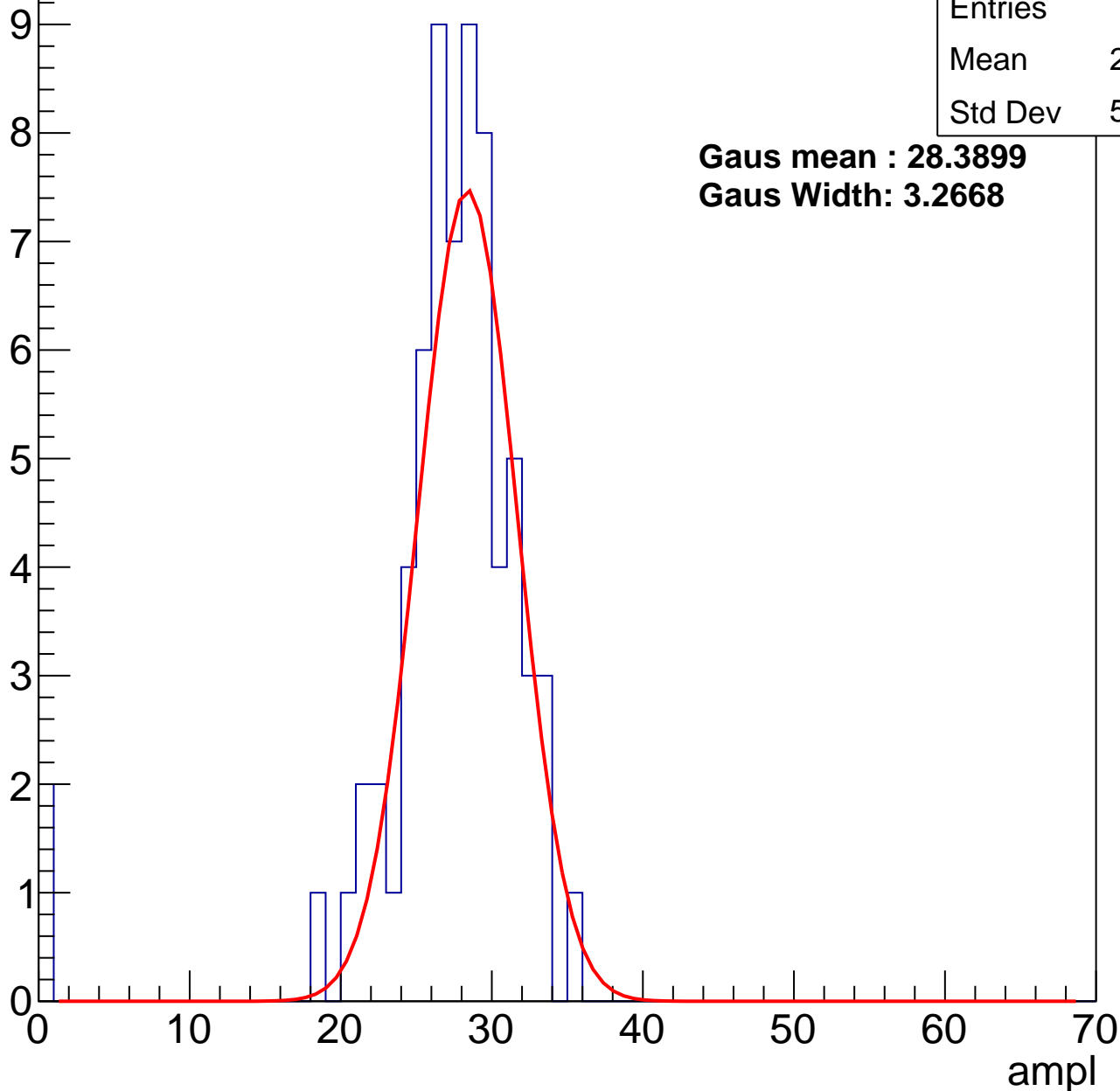
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	26.54
Std Dev	5.668

**Gaus mean : 28.3899**

**Gaus Width: 3.2668**



# B1L002S, U17-ch66, adc1

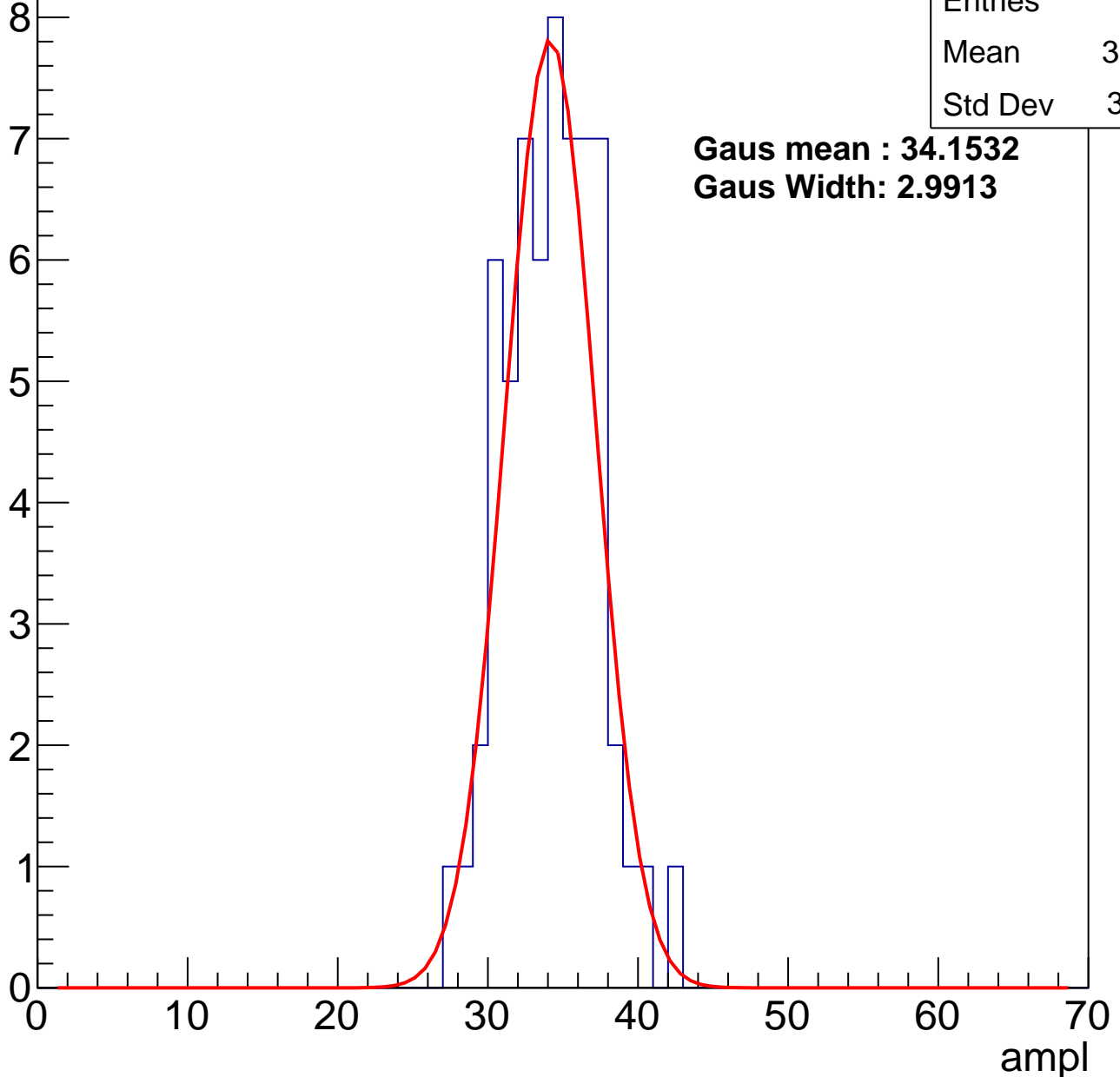
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.79
Std Dev	3.011

**Gaus mean : 34.1532**

**Gaus Width: 2.9913**



# B1L002S, U17-ch66, adc2

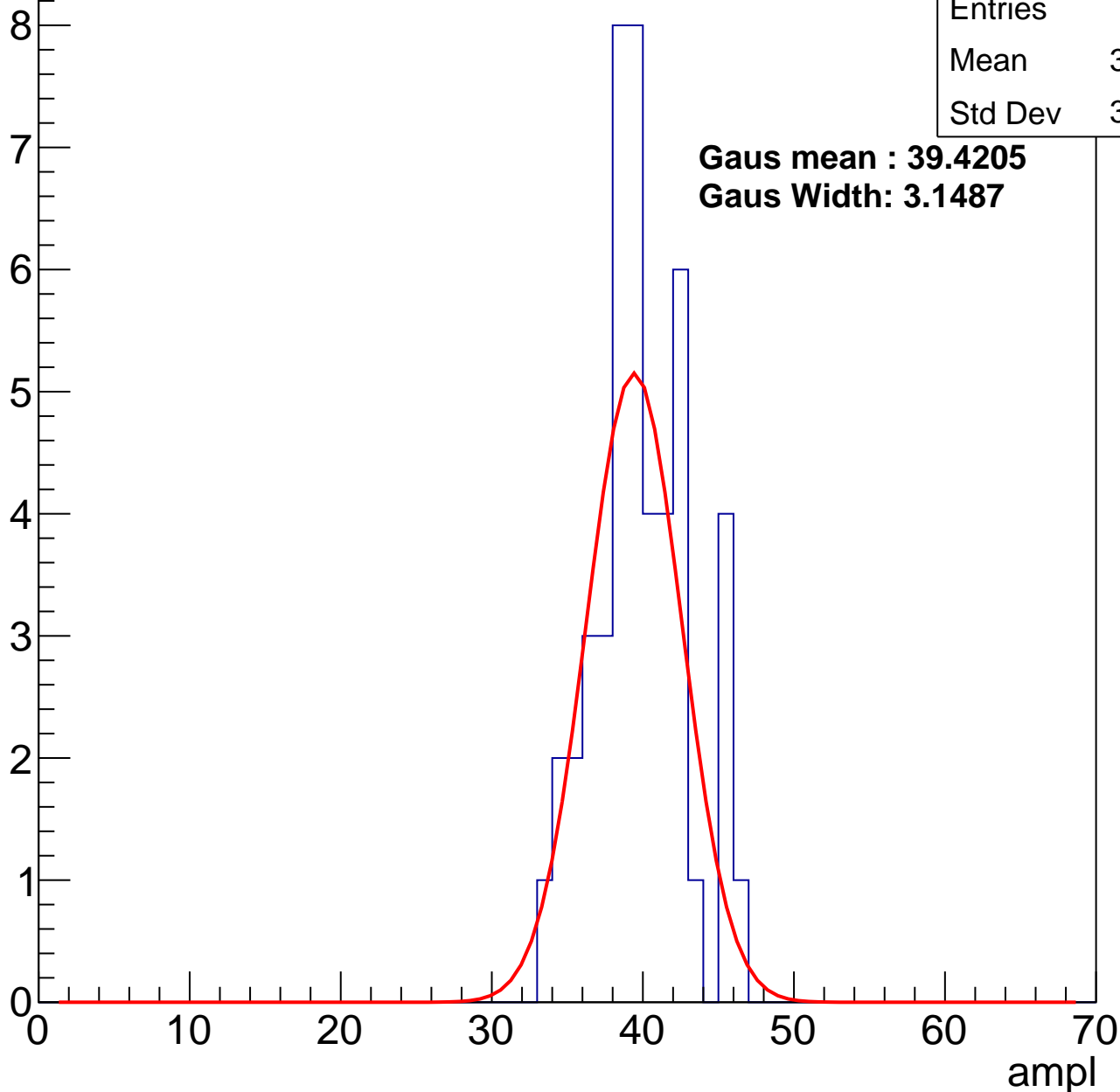
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	39.38
Std Dev	3.064

**Gaus mean : 39.4205**

**Gaus Width: 3.1487**

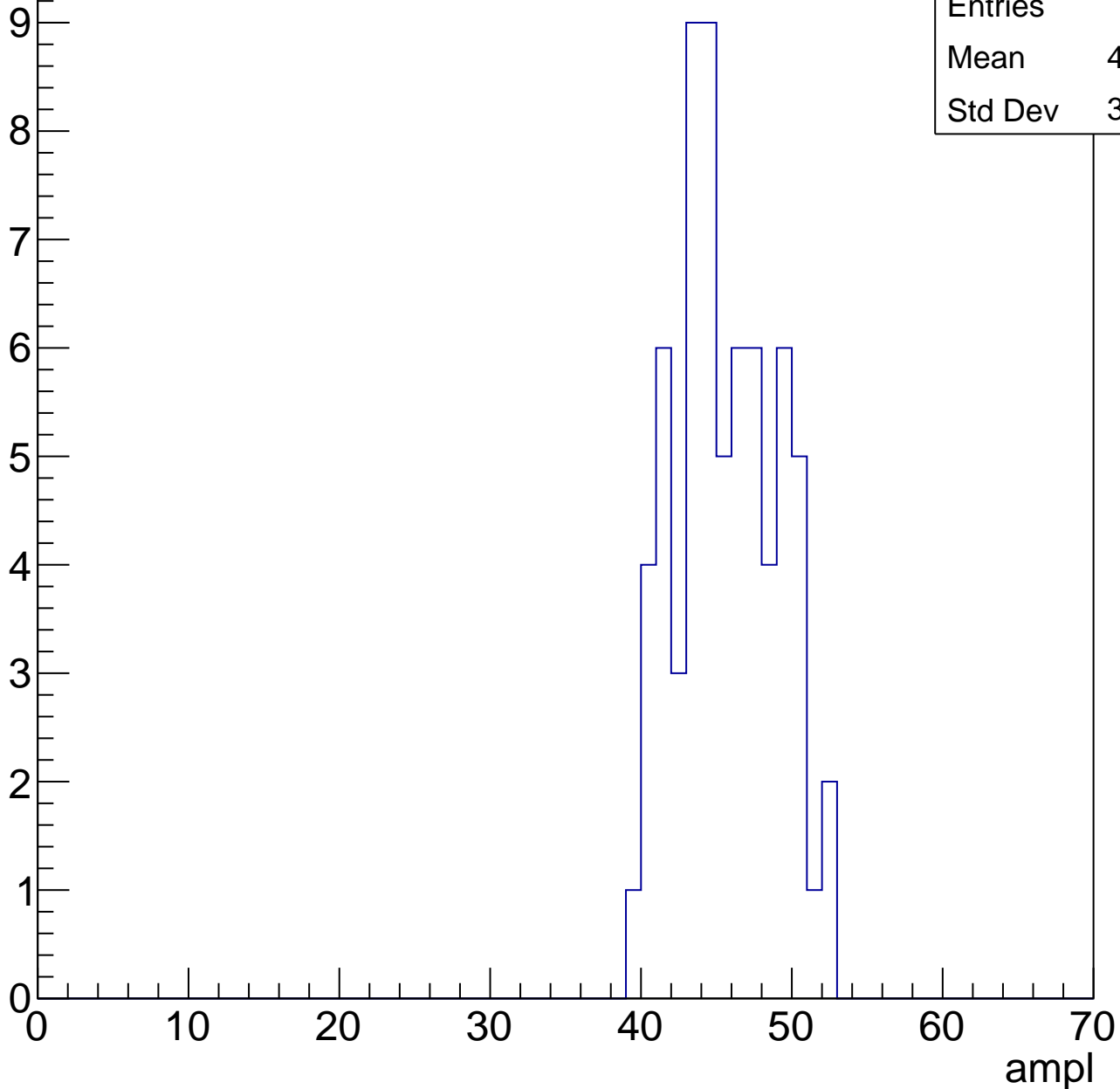


# B1L002S, U17-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	45.19
Std Dev	3.284

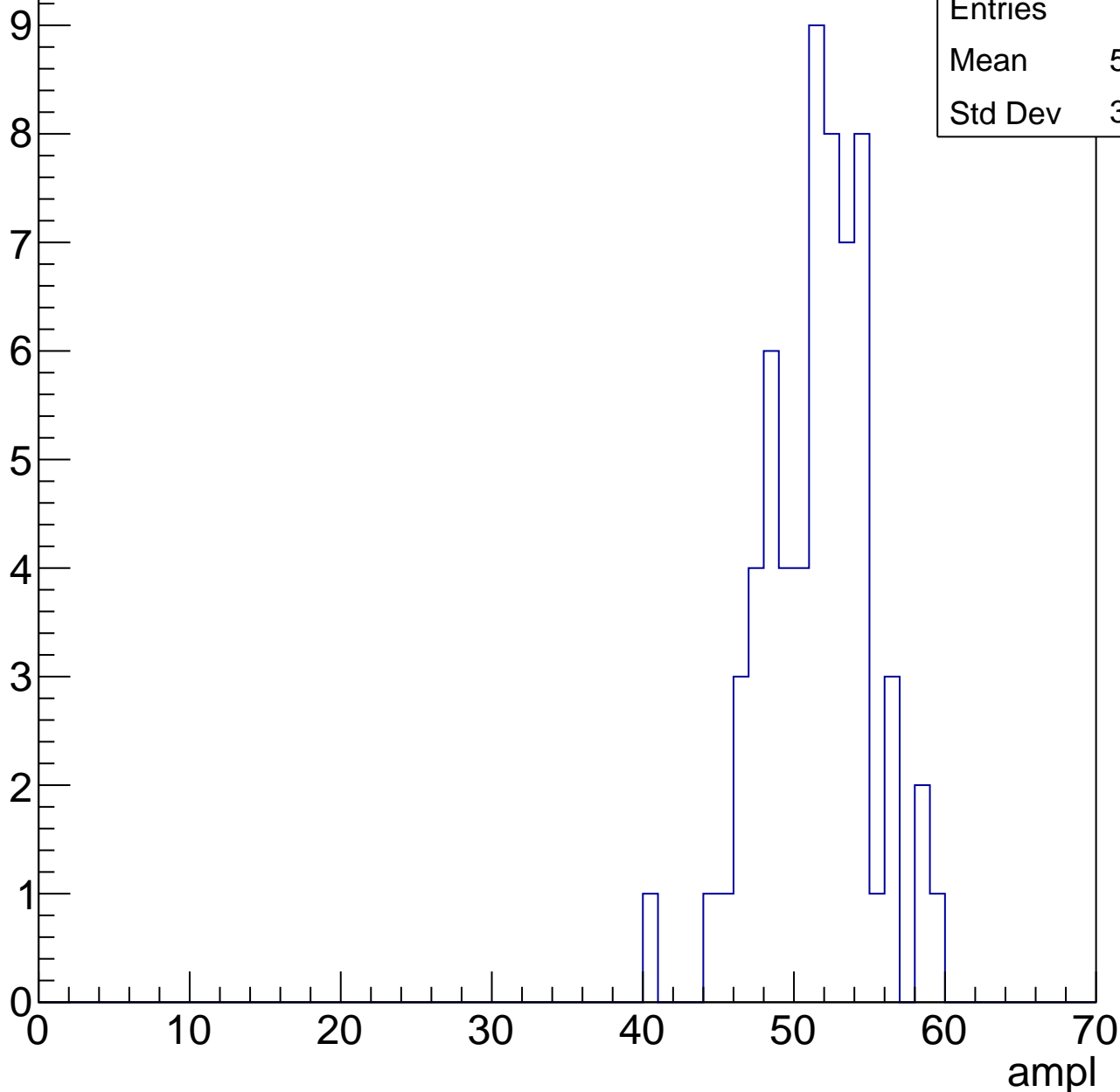


# B1L002S, U17-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	51.03
Std Dev	3.519



# B1L002S, U17-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	56
Mean	56.43
Std Dev	8.08

Entry

10

8

6

4

2

0

ampl

0

10

20

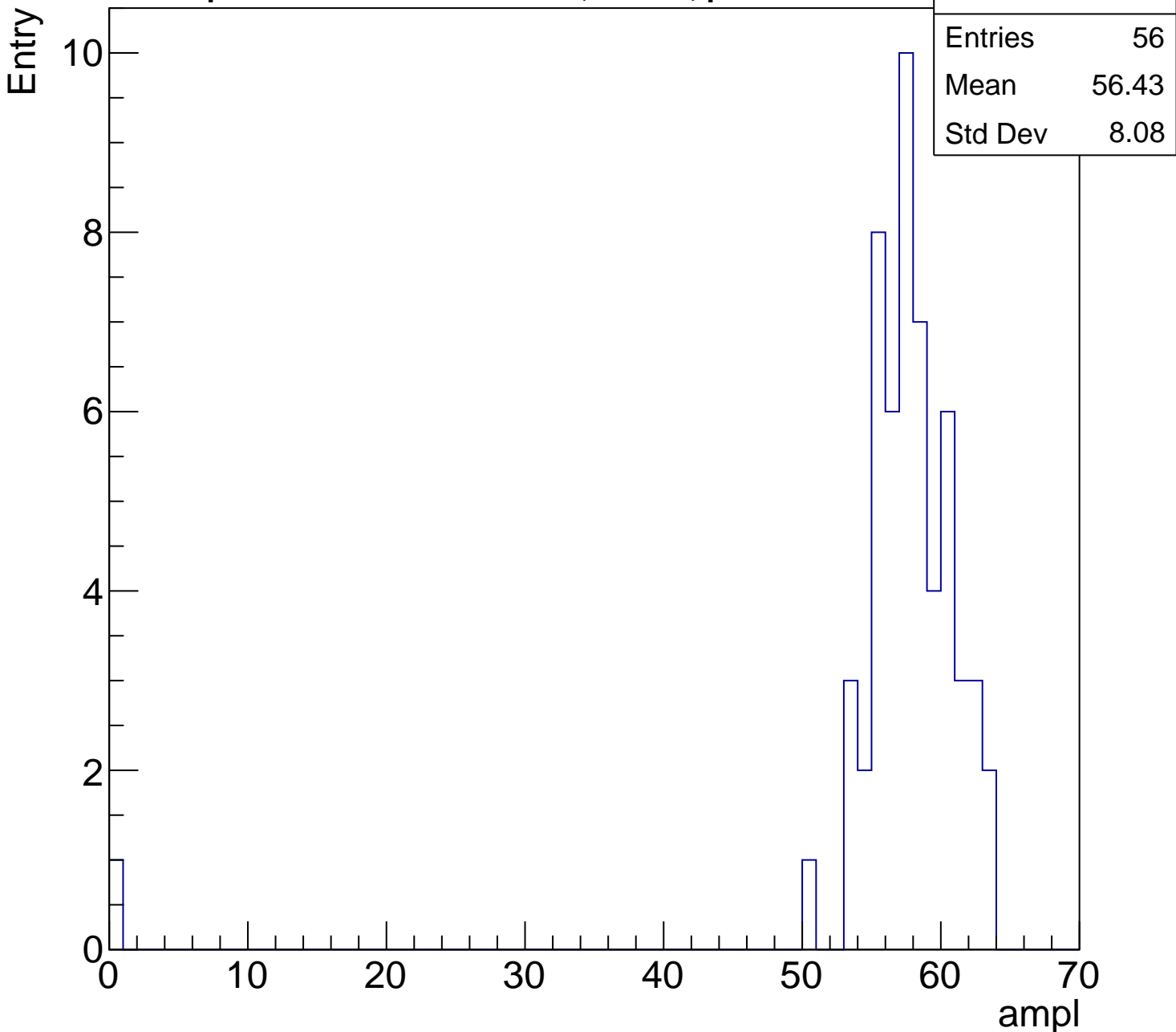
30

40

50

60

70

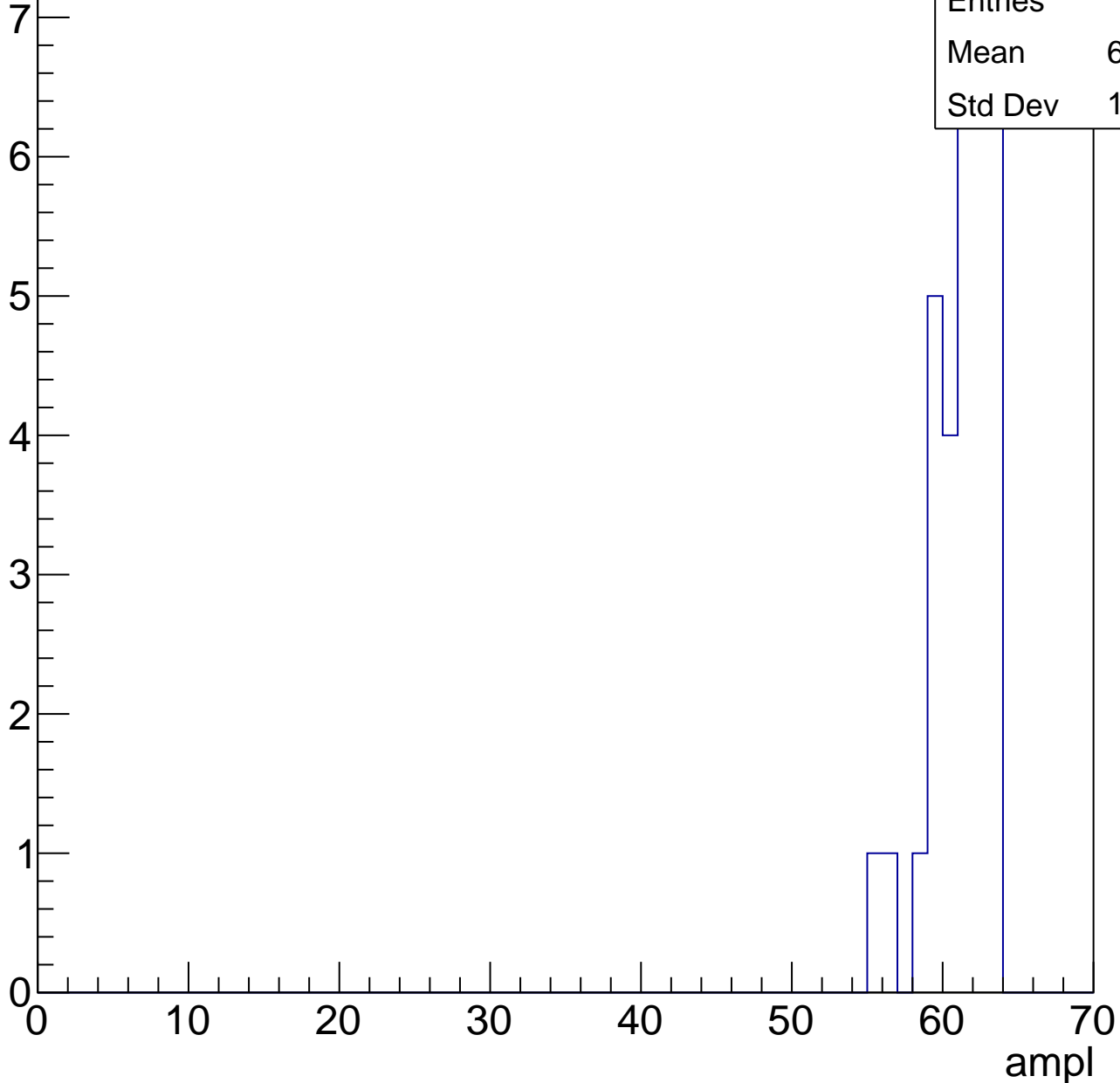


# B1L002S, U17-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	60.79
Std Dev	1.966

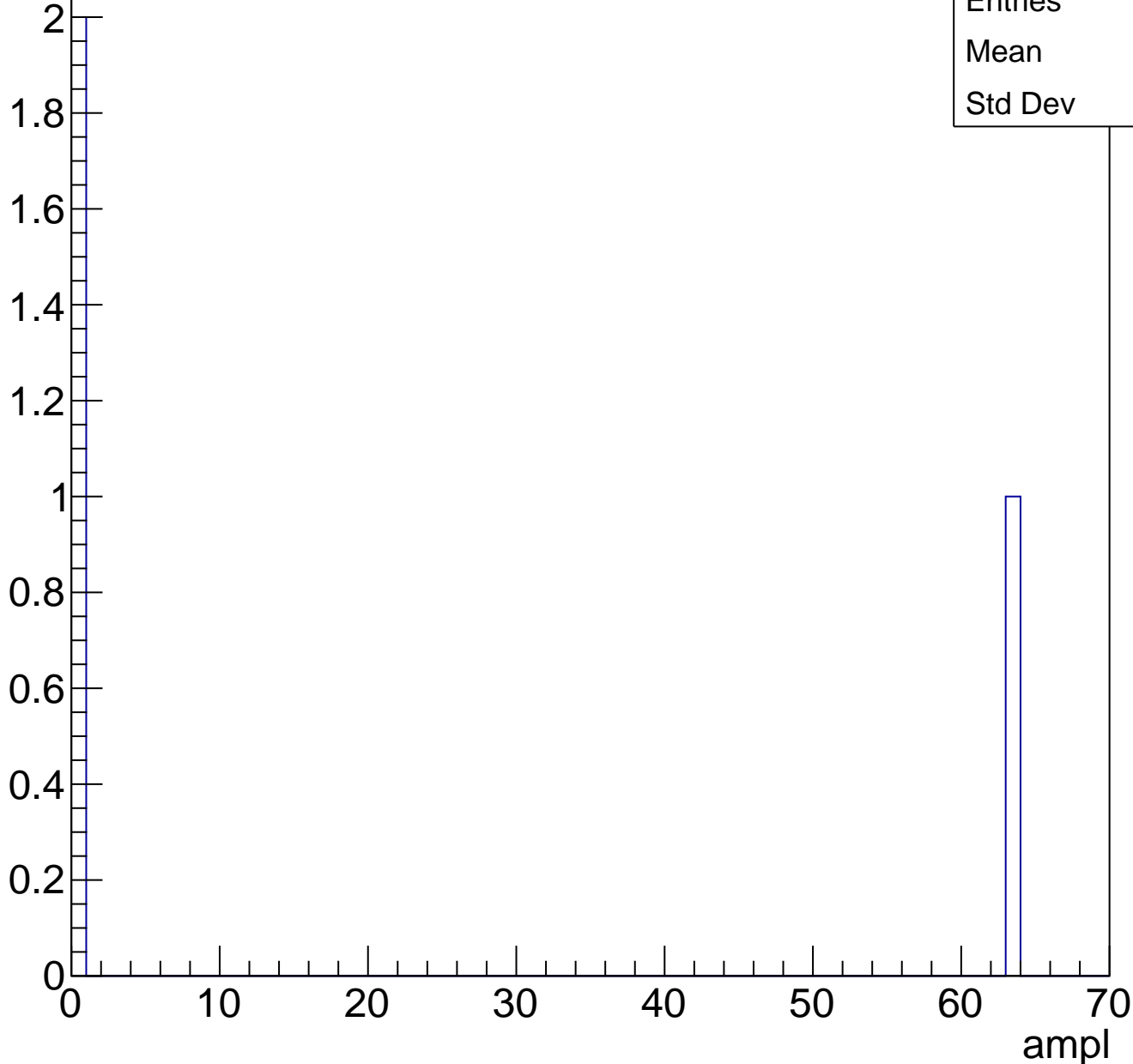




# B1L002S, U17-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U17-ch67, adc0

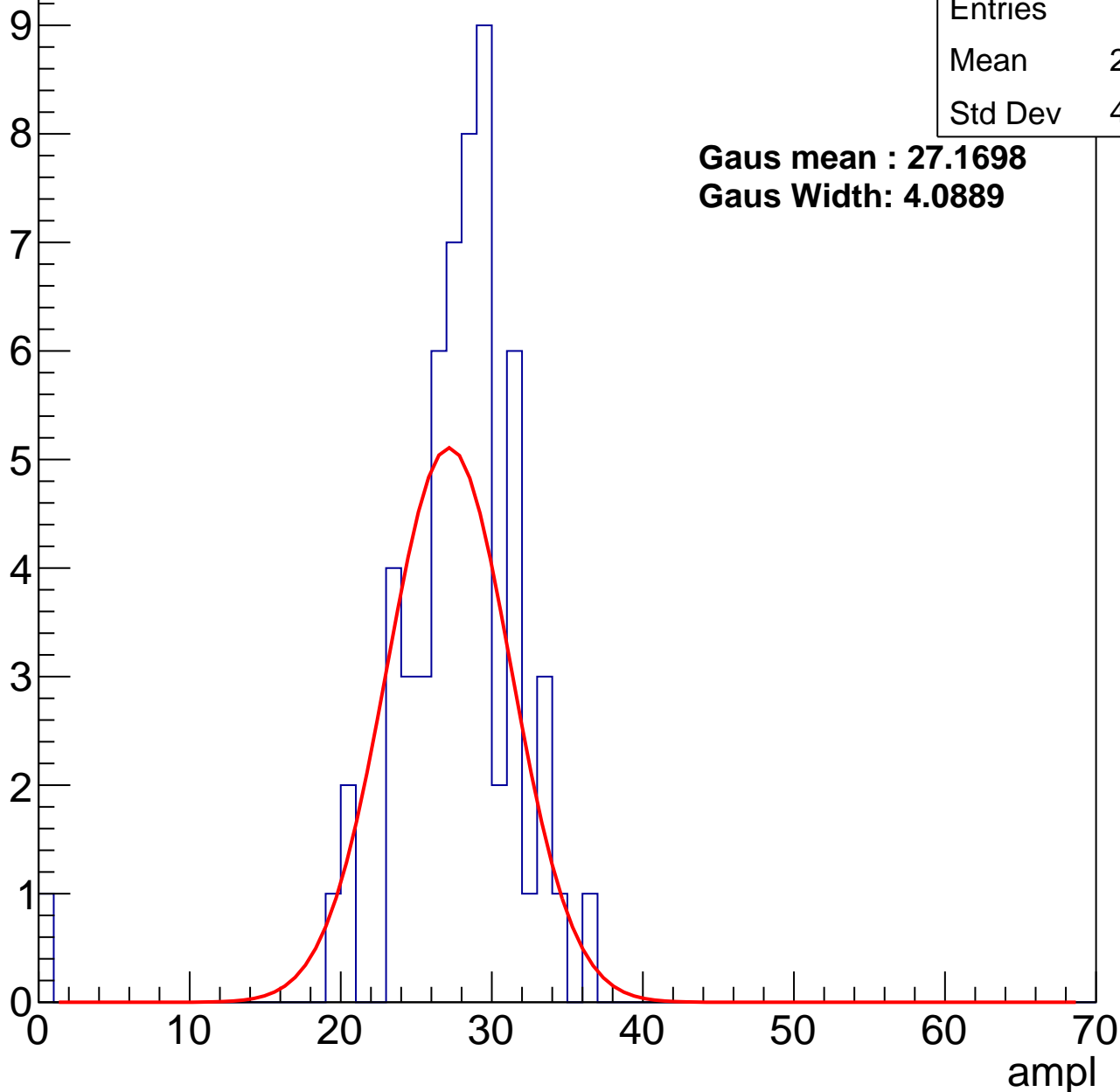
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.16
Std Dev	4.947

**Gaus mean : 27.1698**

**Gaus Width: 4.0889**



# B1L002S, U17-ch67, adc1

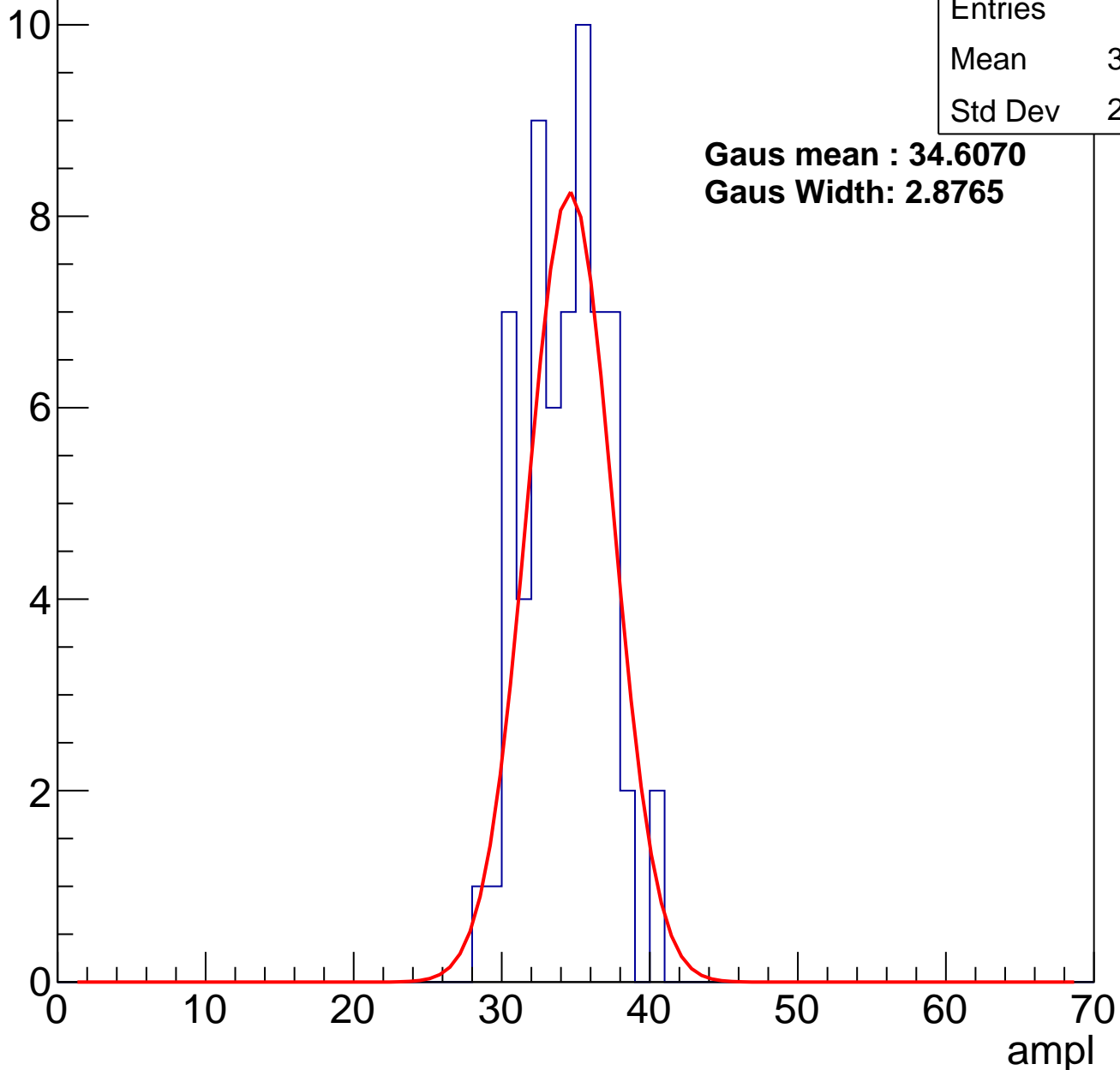
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	33.84
Std Dev	2.686

**Gaus mean : 34.6070**

**Gaus Width: 2.8765**

Entry



# B1L002S, U17-ch67, adc2

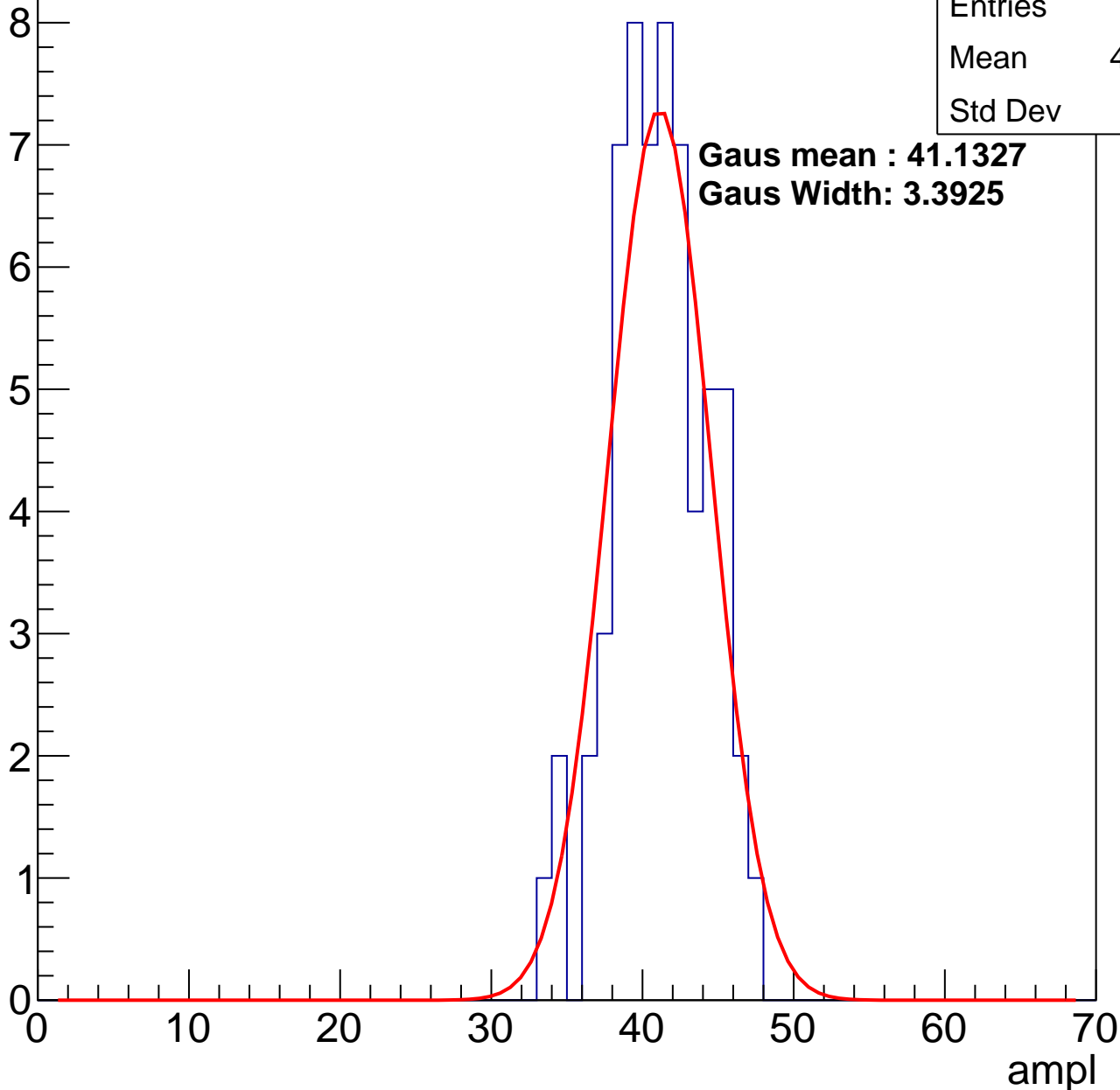
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	40.65
Std Dev	3.08

**Gaus mean : 41.1327**

**Gaus Width: 3.3925**

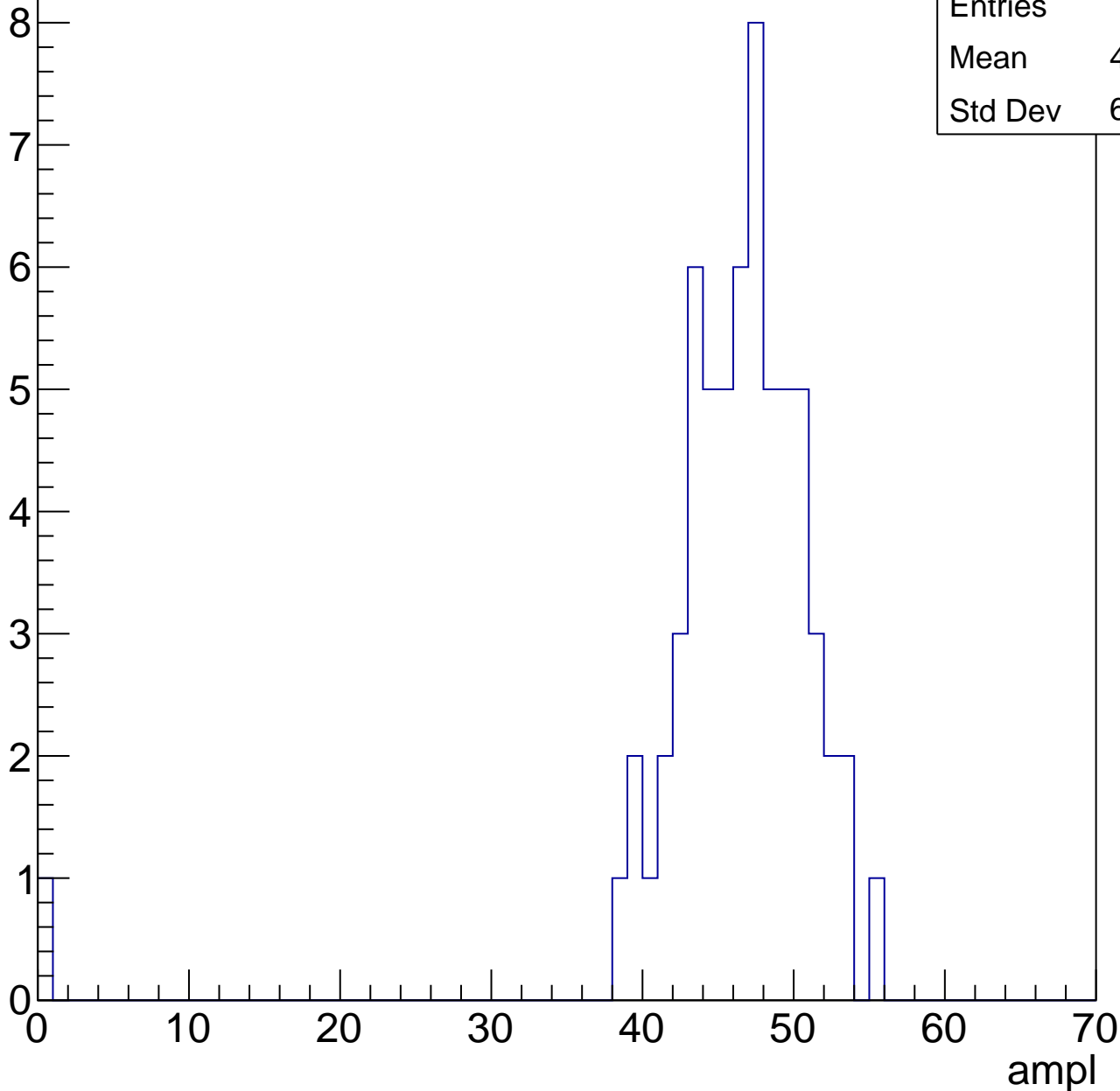


# B1L002S, U17-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	45.59
Std Dev	6.854

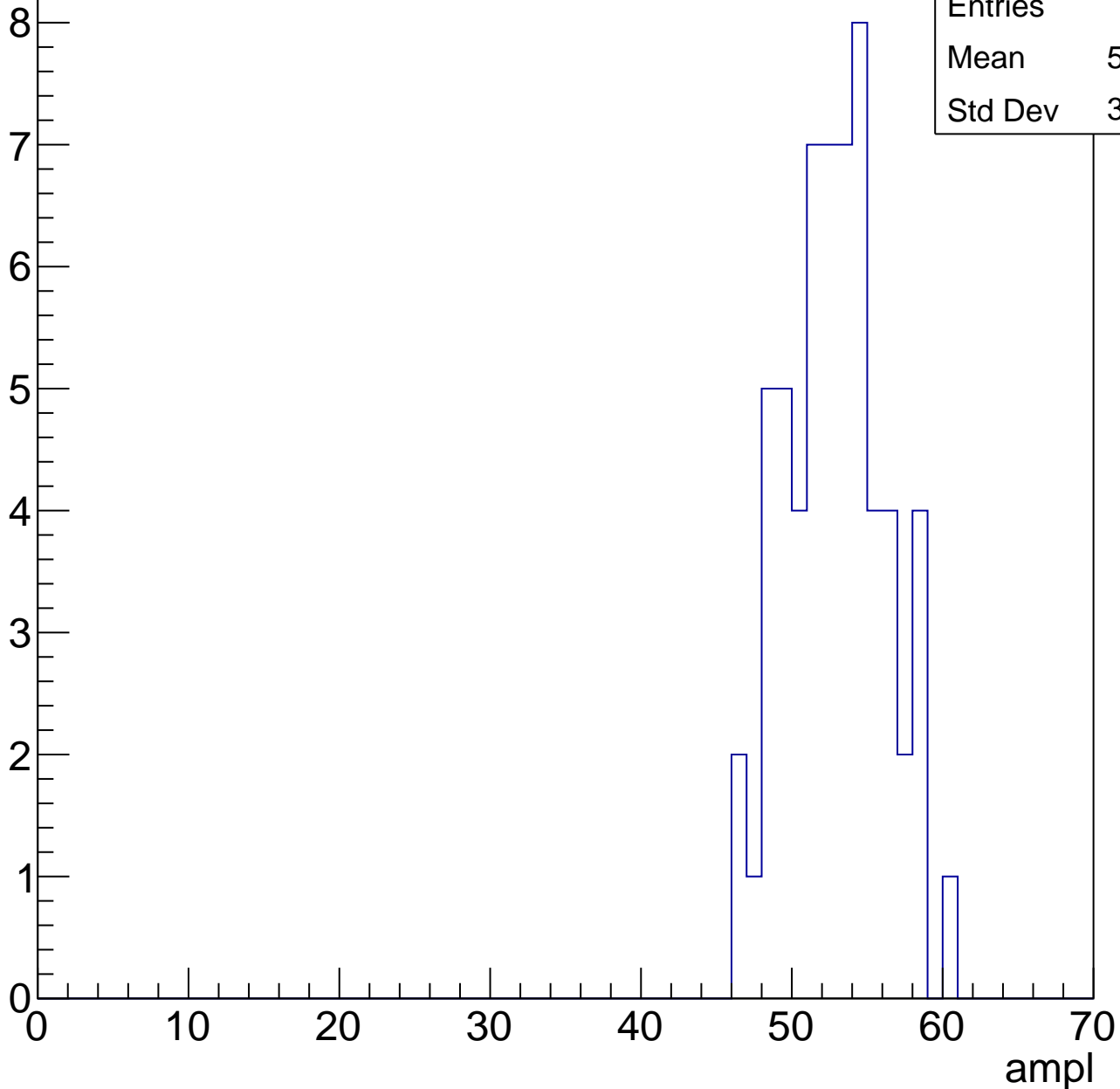


# B1L002S, U17-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

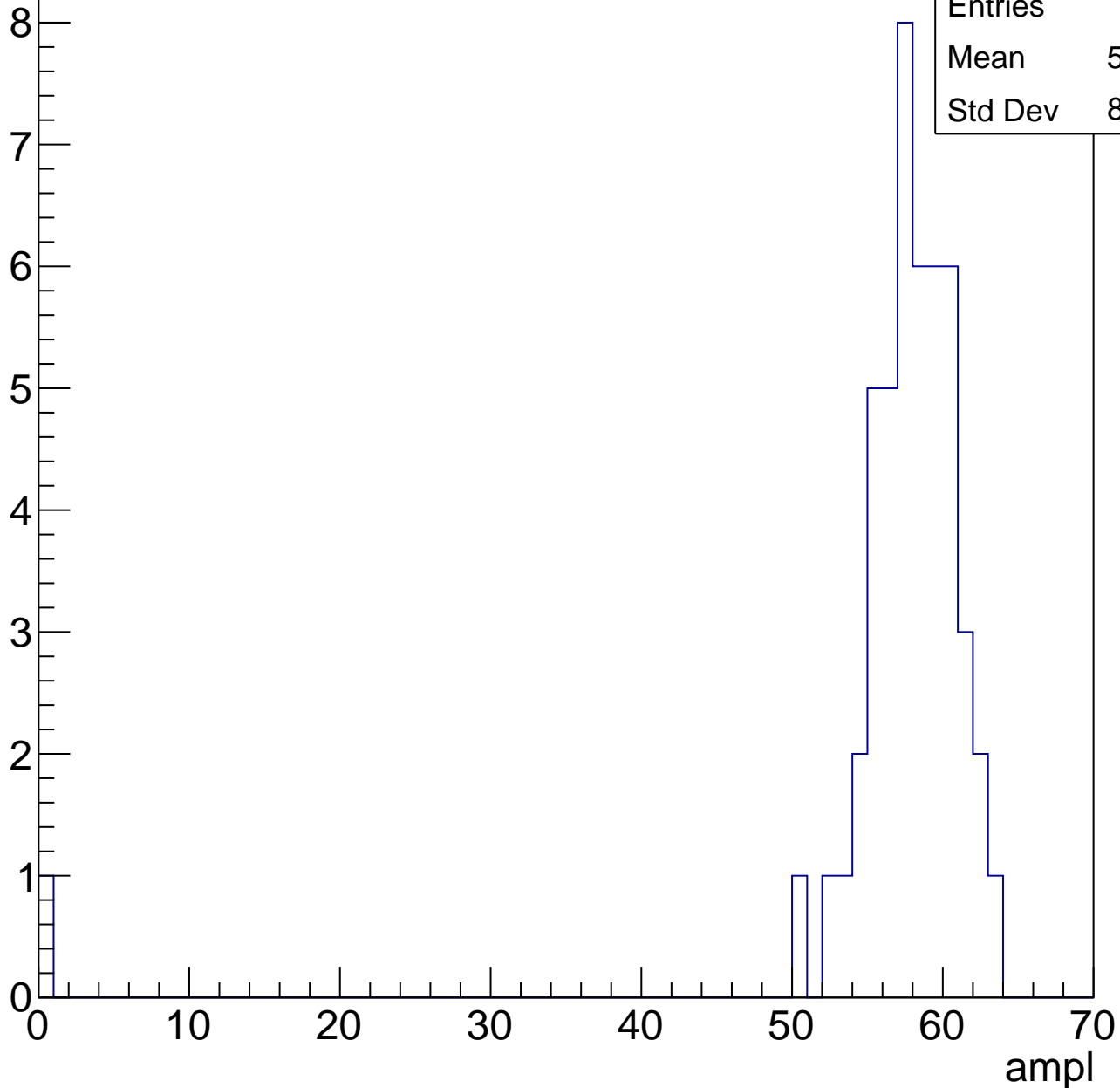
Entries	61
Mean	52.43
Std Dev	3.216



# B1L002S, U17-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

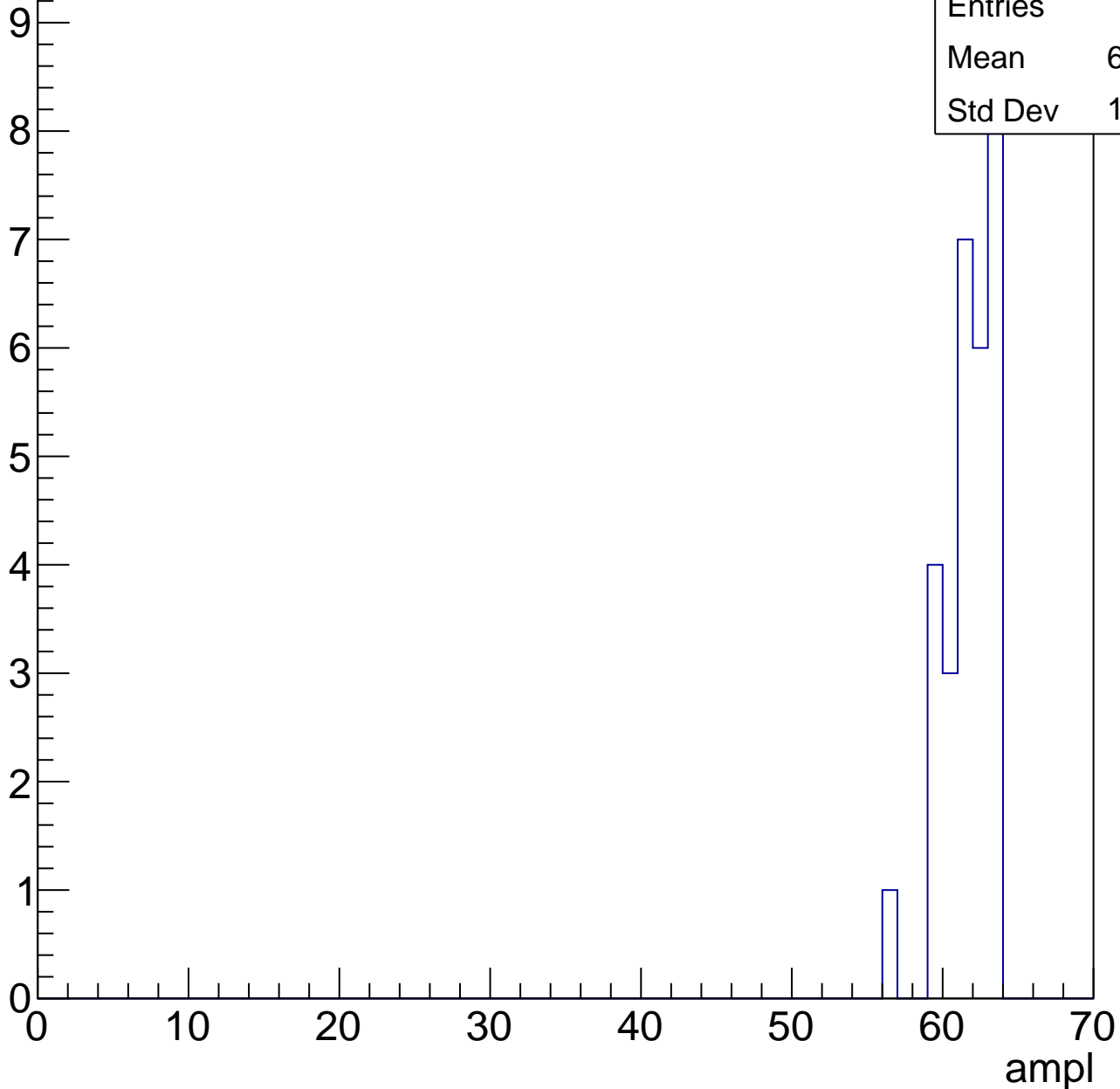


# B1L002S, U17-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	61.27
Std Dev	1.672

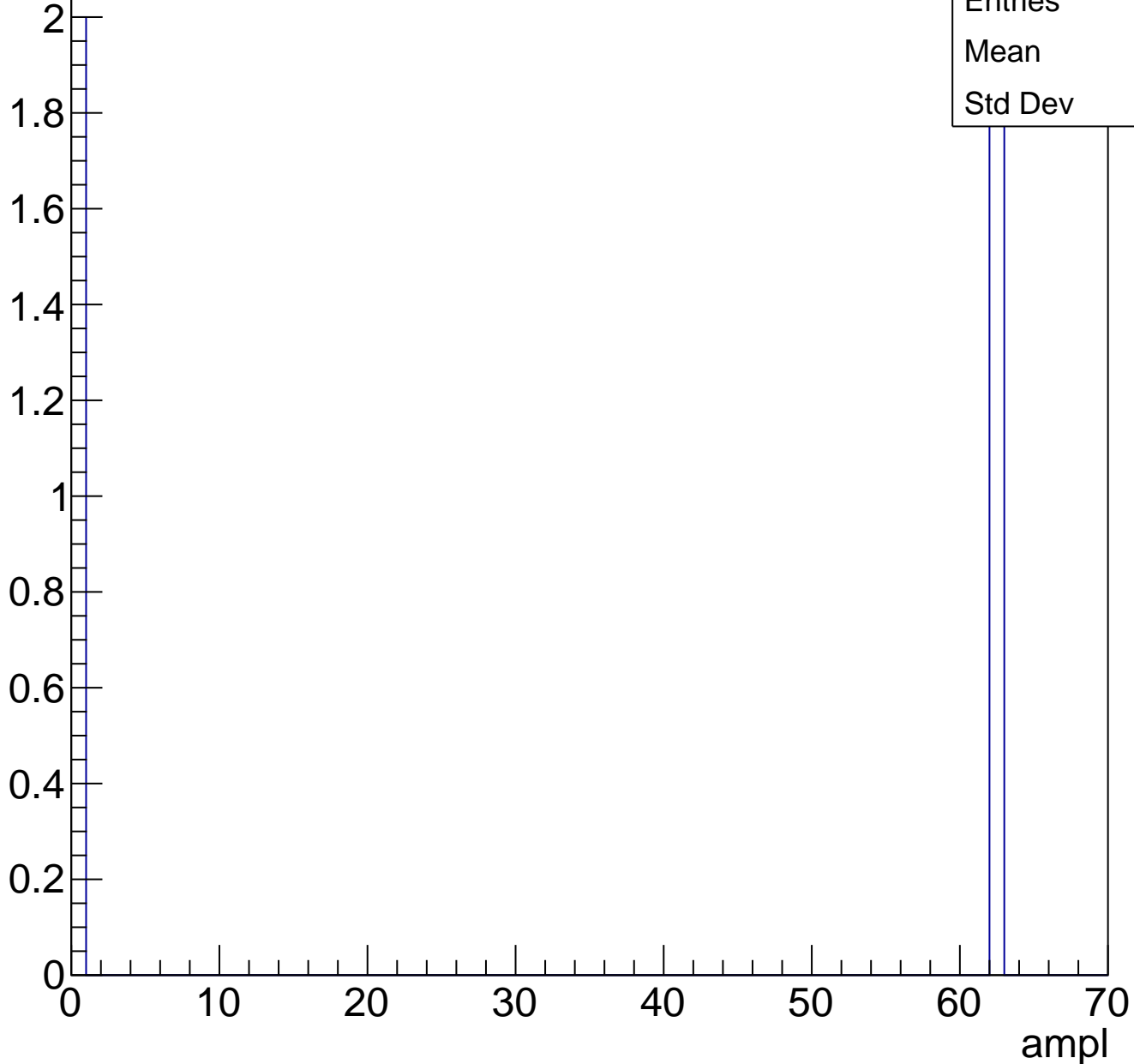




# B1L002S, U17-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch68, adc0

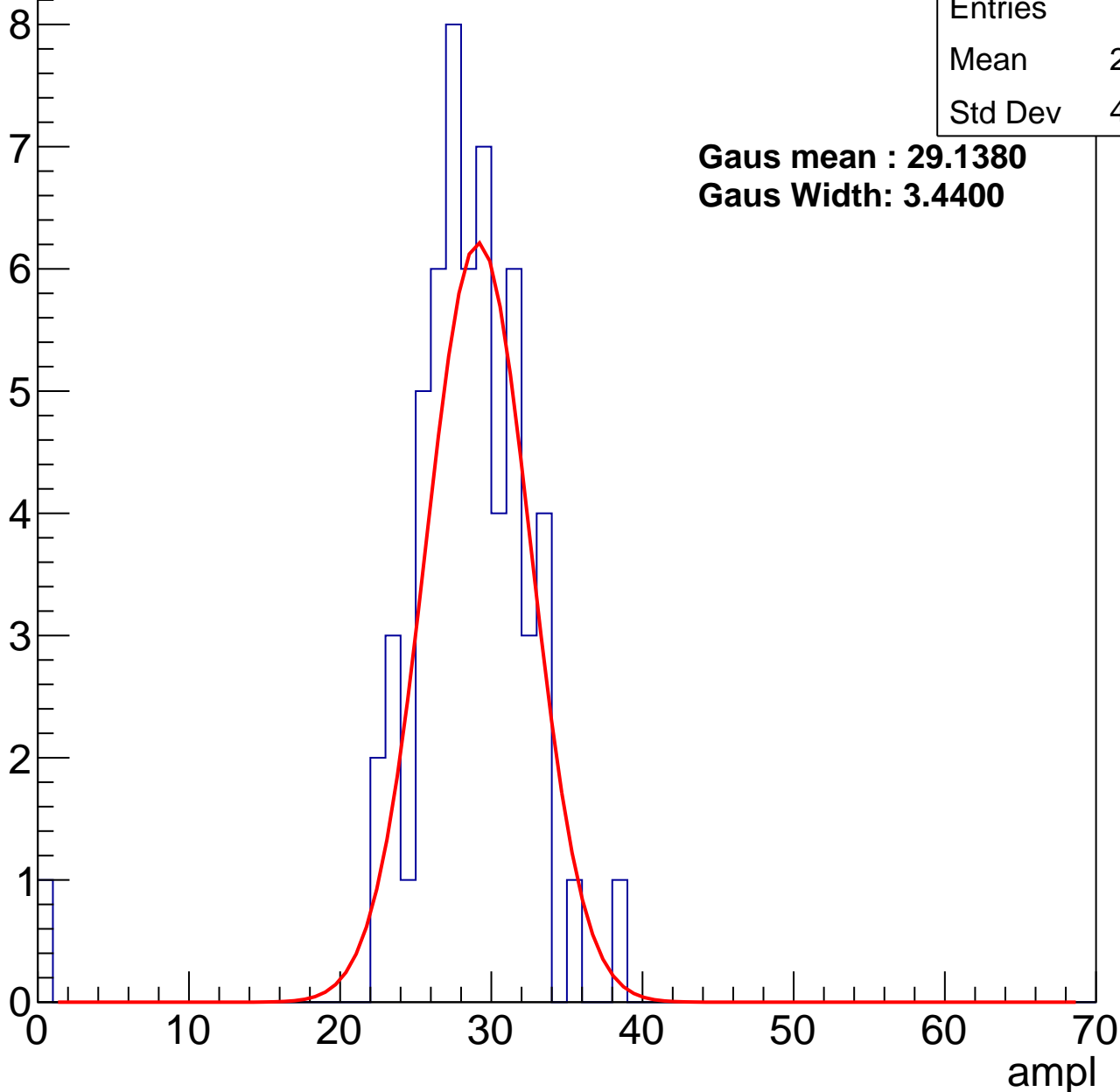
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.79
Std Dev	4.905

**Gaus mean : 29.1380**

**Gaus Width: 3.4400**



# B1L002S, U17-ch68, adc1

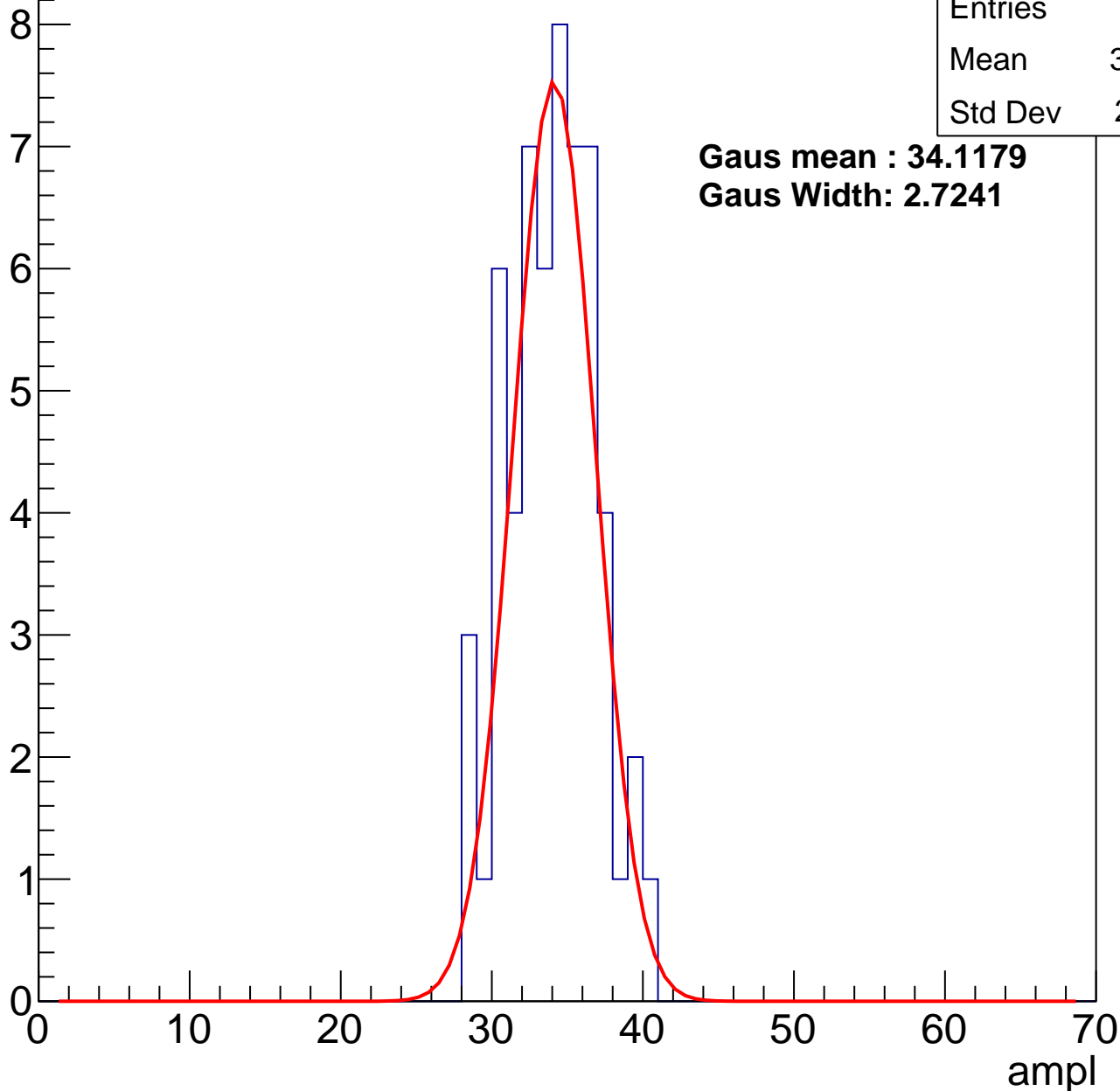
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.54
Std Dev	2.841

**Gaus mean : 34.1179**

**Gaus Width: 2.7241**



# B1L002S, U17-ch68, adc2

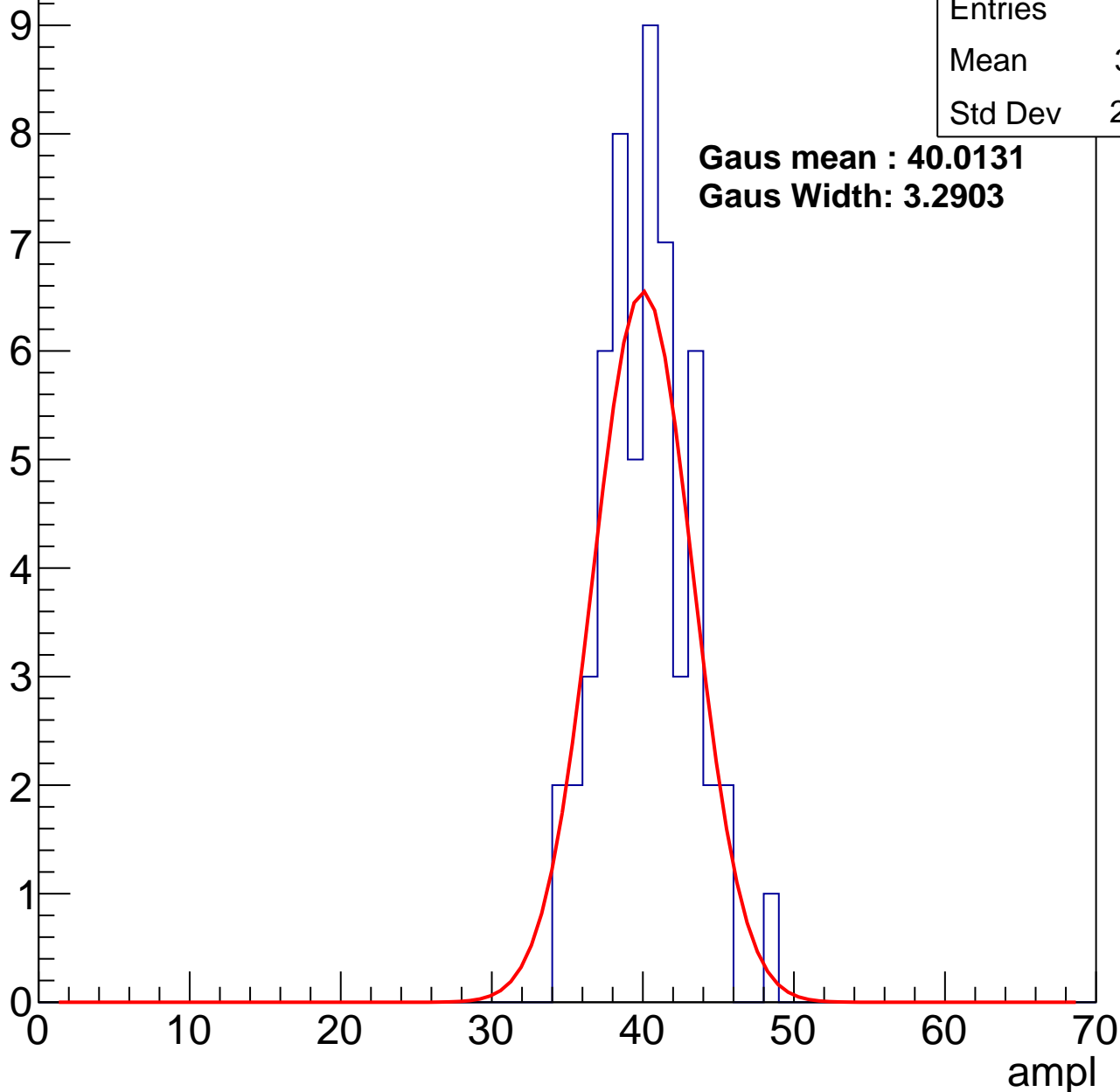
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	39.71
Std Dev	2.914

**Gaus mean : 40.0131**

**Gaus Width: 3.2903**

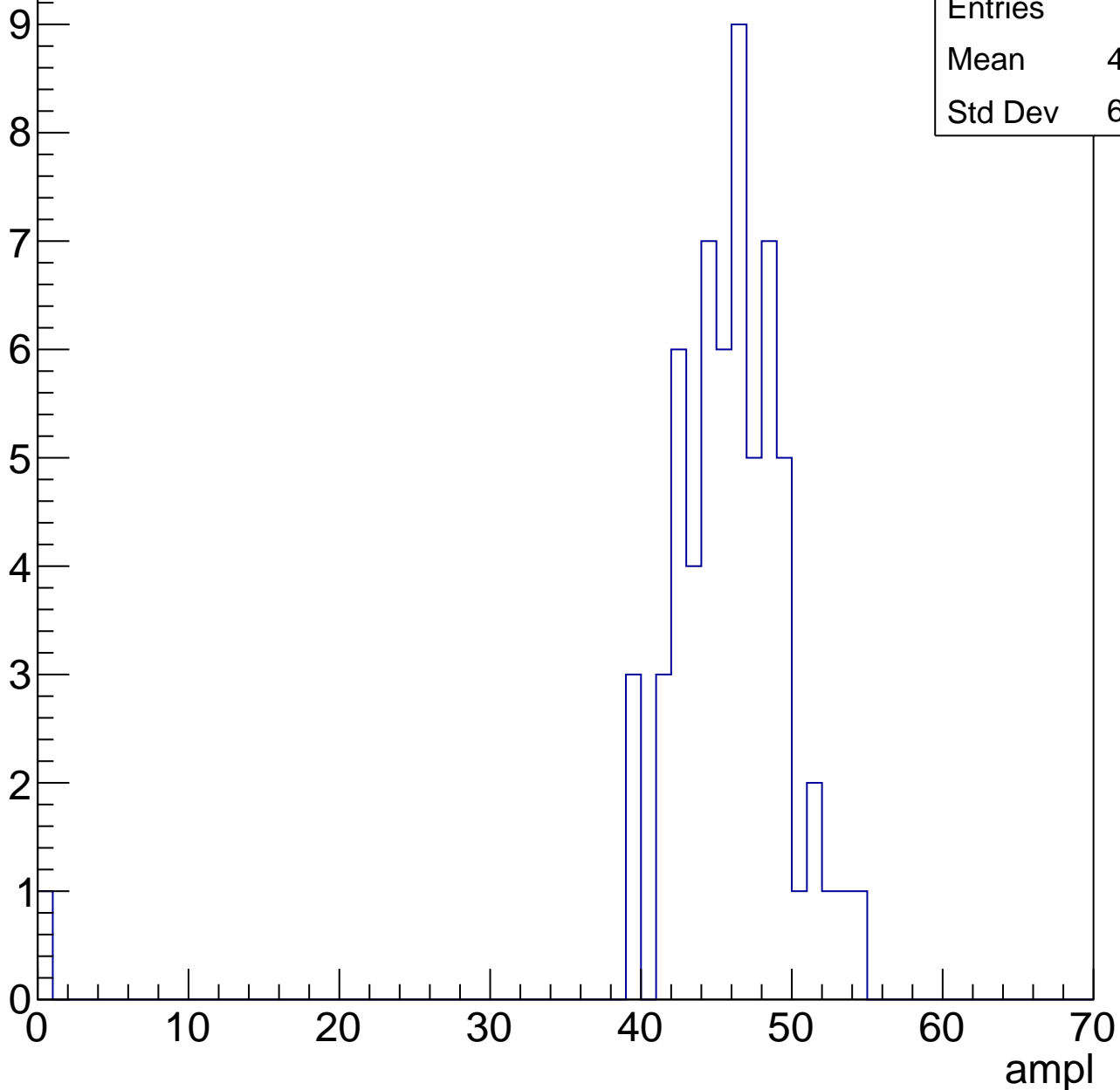


# B1L002S, U17-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	44.89
Std Dev	6.624

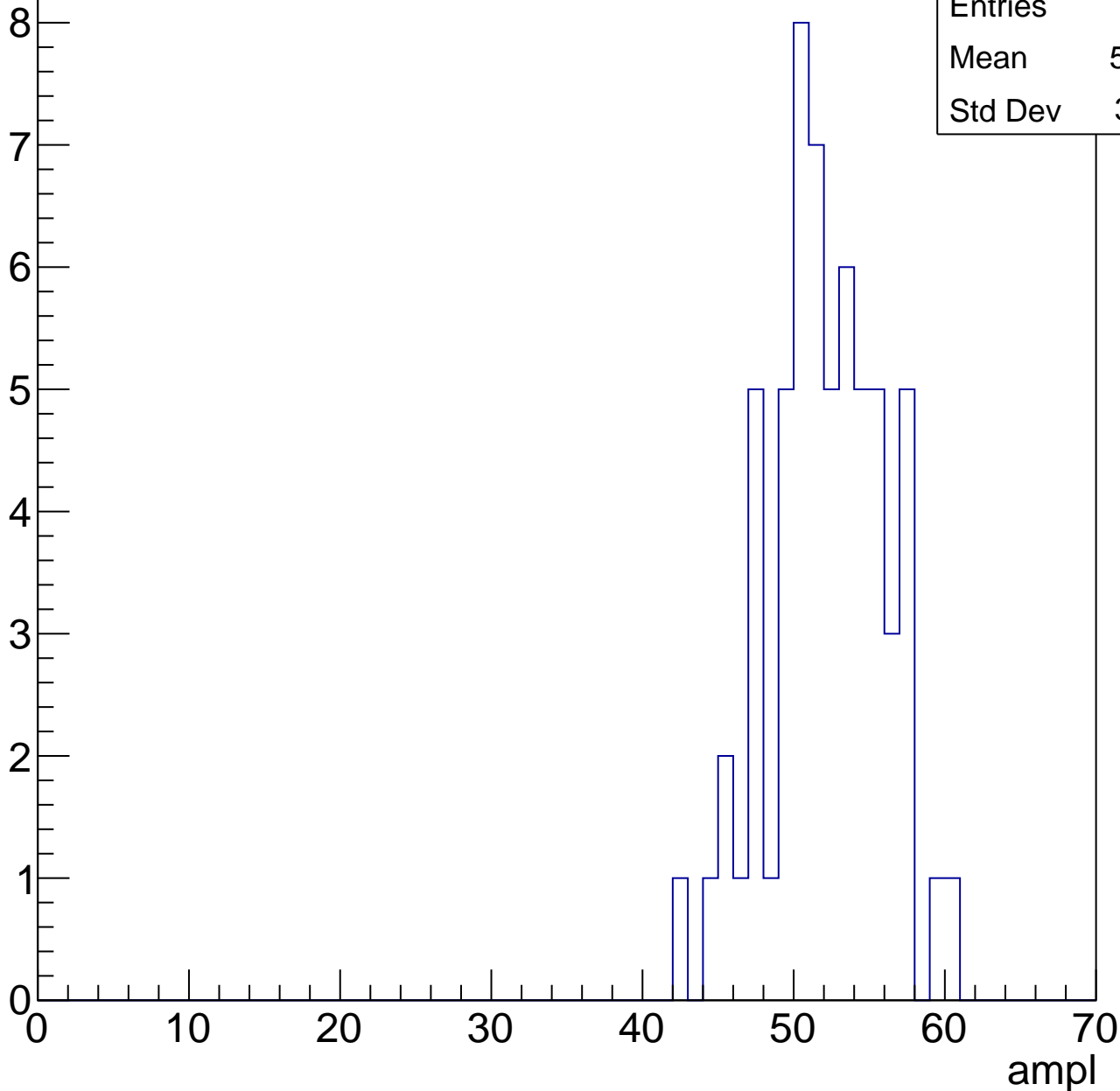


# B1L002S, U17-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	51.65
Std Dev	3.751

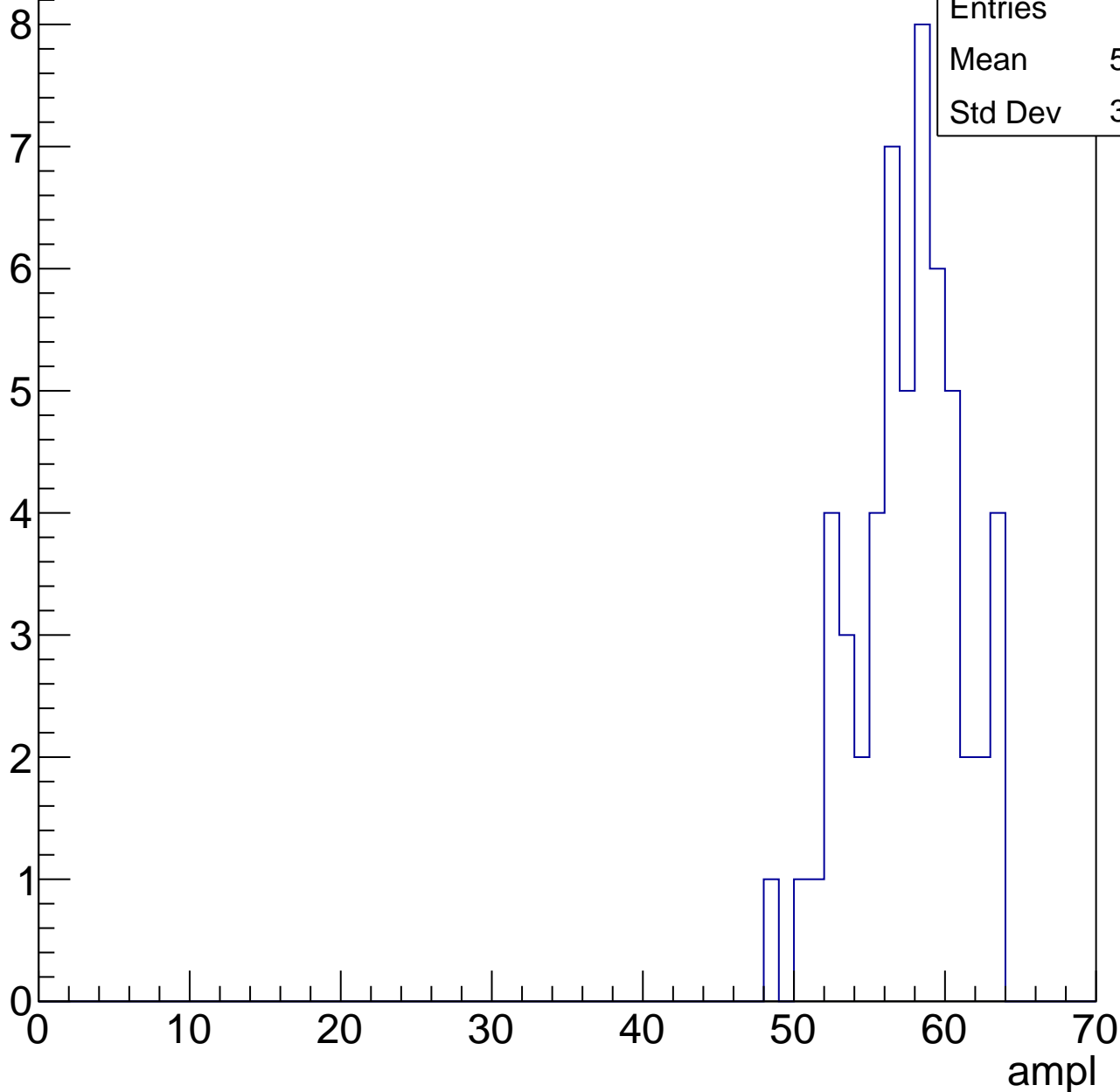


# B1L002S, U17-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.04
Std Dev	3.459

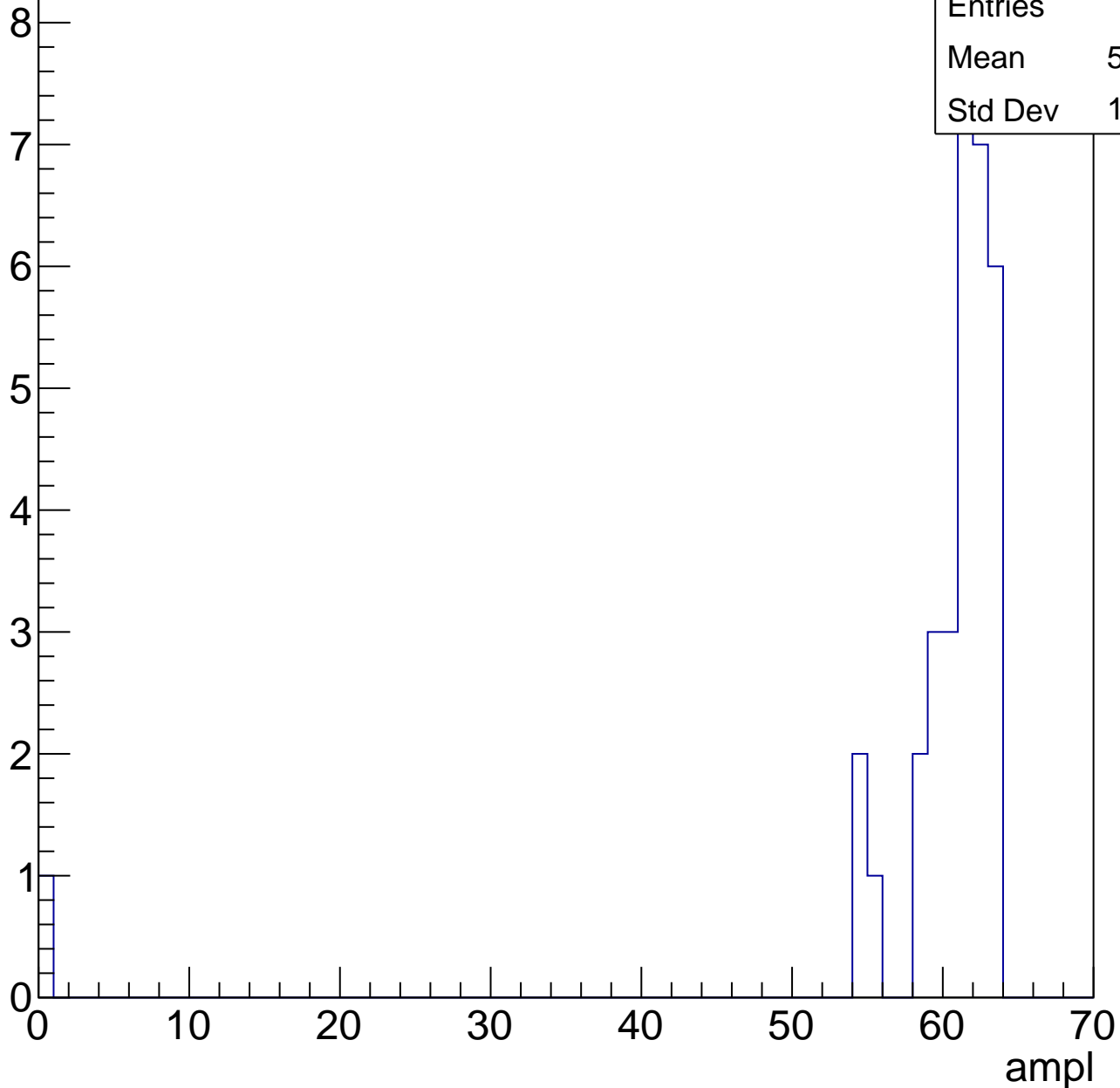


# B1L002S, U17-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	58.67
Std Dev	10.64

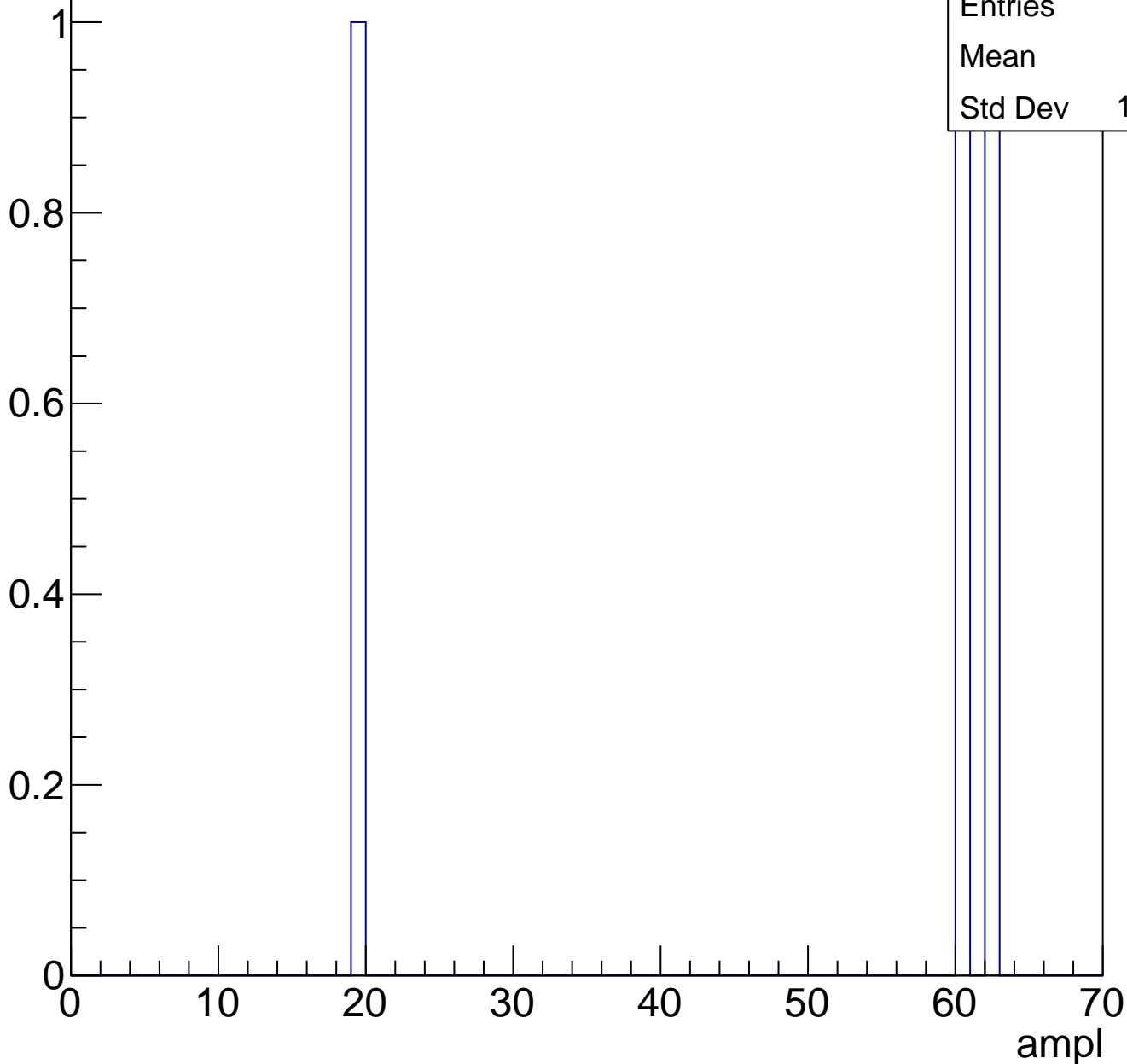




# B1L002S, U17-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch69, adc0

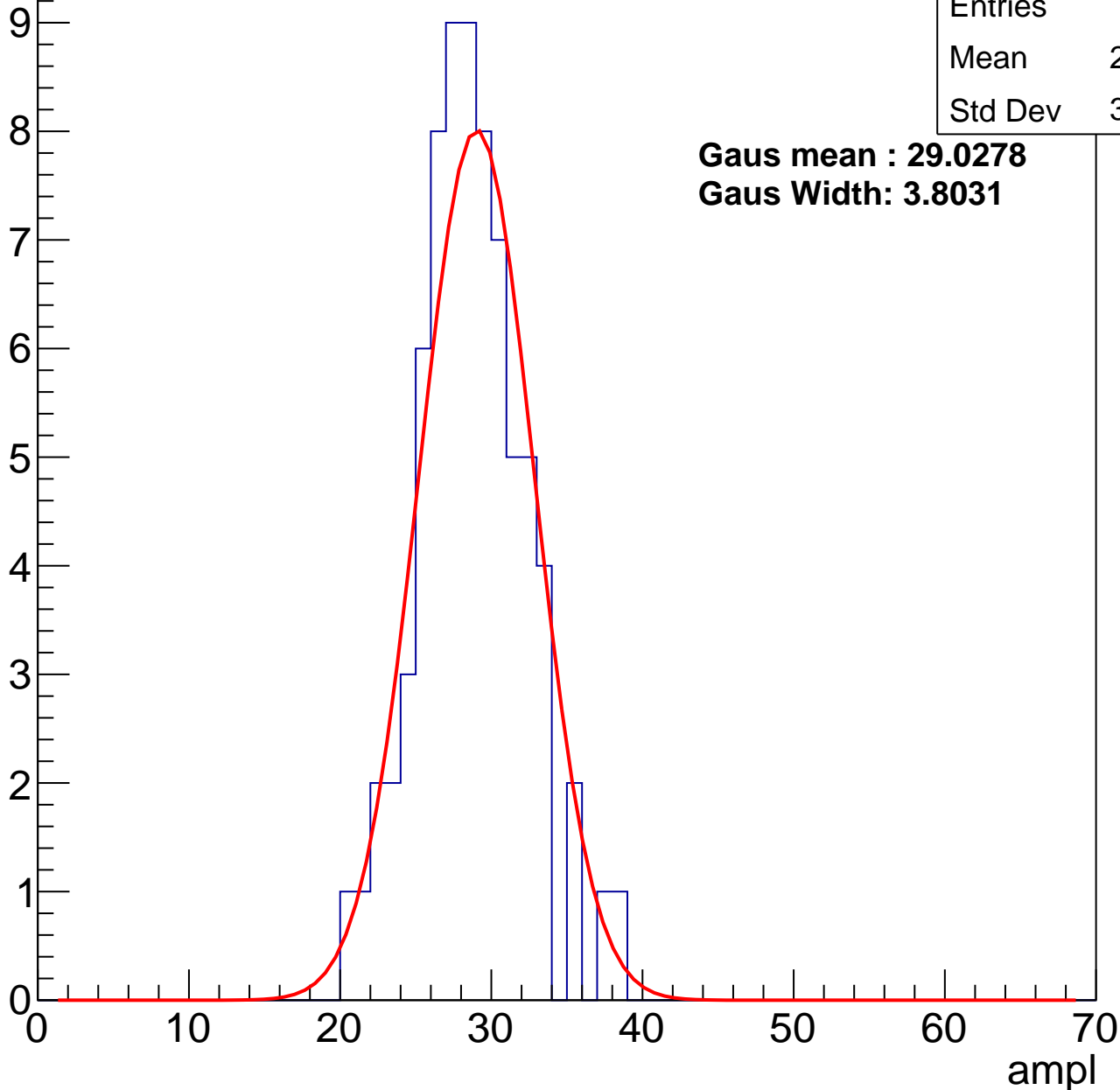
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.24
Std Dev	3.514

**Gaus mean : 29.0278**

**Gaus Width: 3.8031**



# B1L002S, U17-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	35.07
Std Dev	3.098

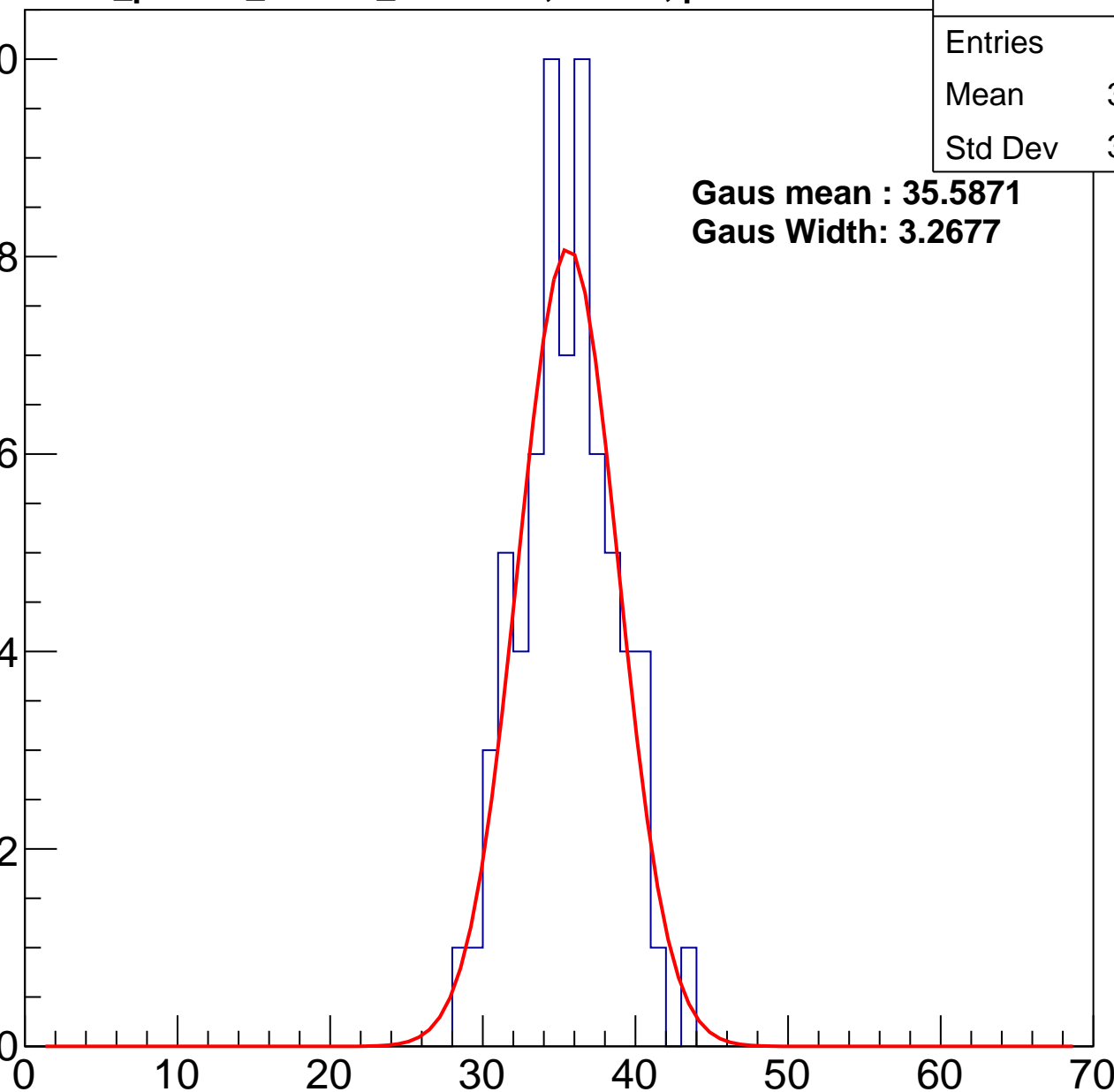
**Gaus mean : 35.5871**

**Gaus Width: 3.2677**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U17-ch69, adc2

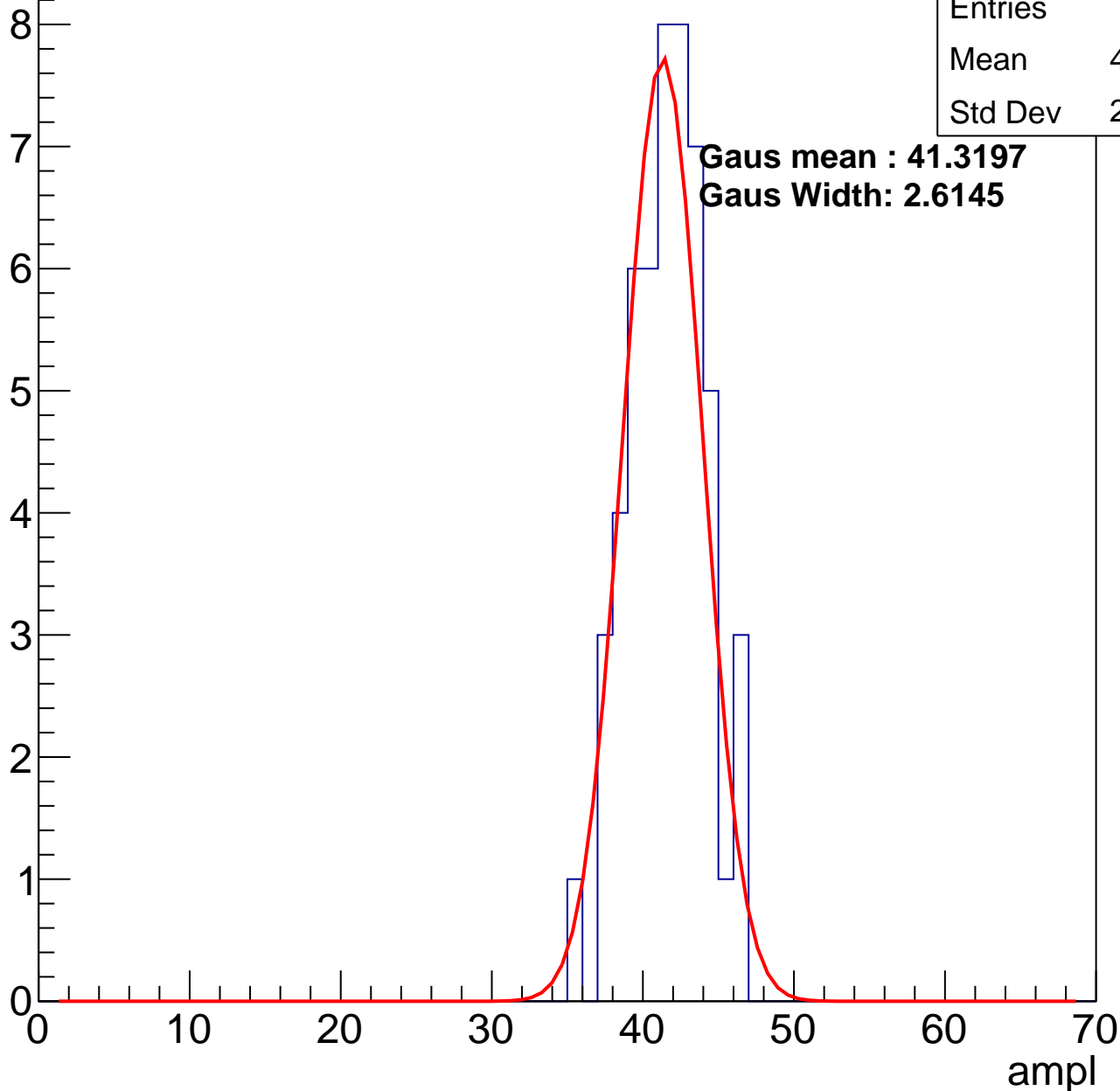
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	41.15
Std Dev	2.484

**Gaus mean : 41.3197**

**Gaus Width: 2.6145**

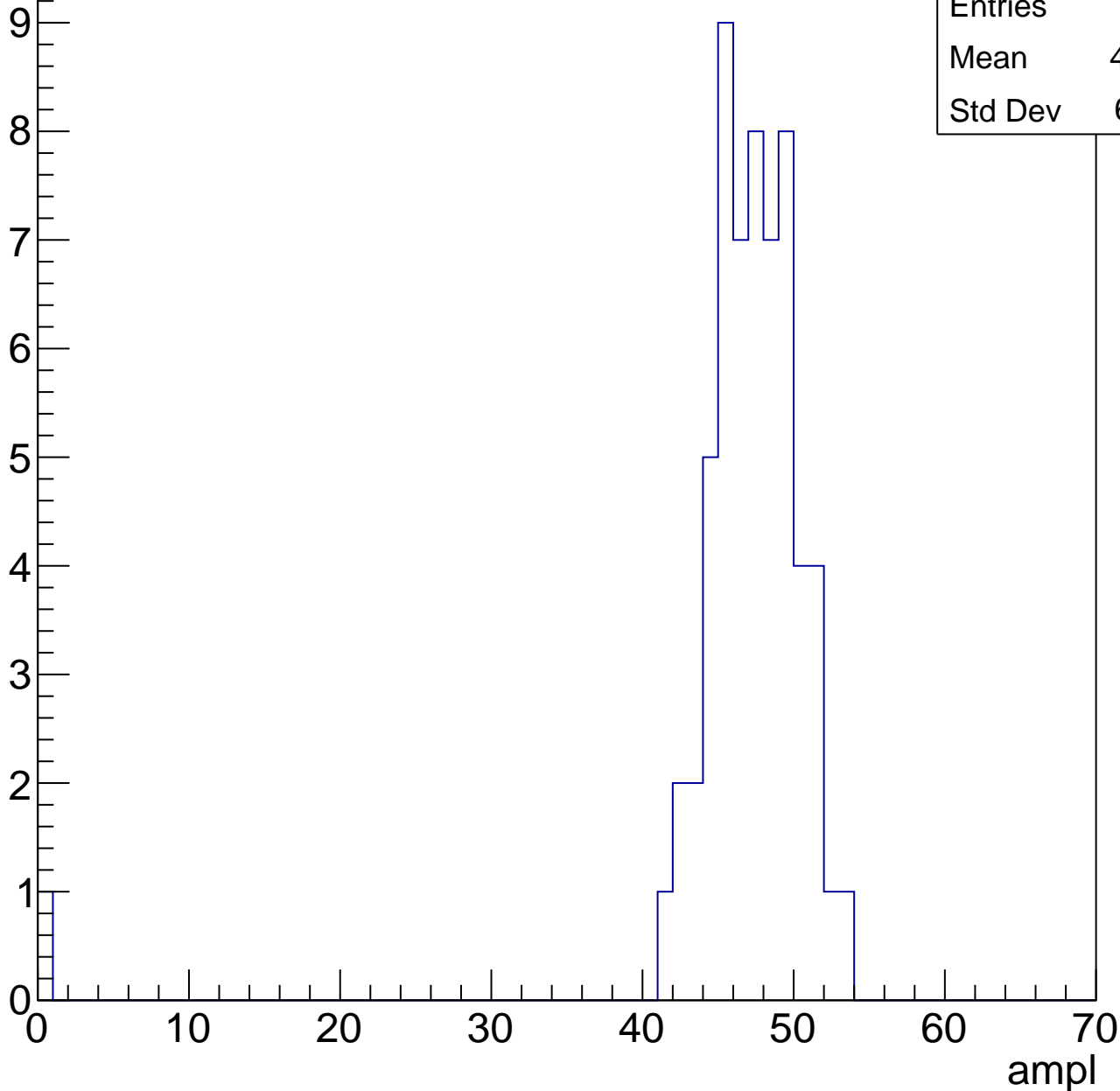


# B1L002S, U17-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.18
Std Dev	6.551

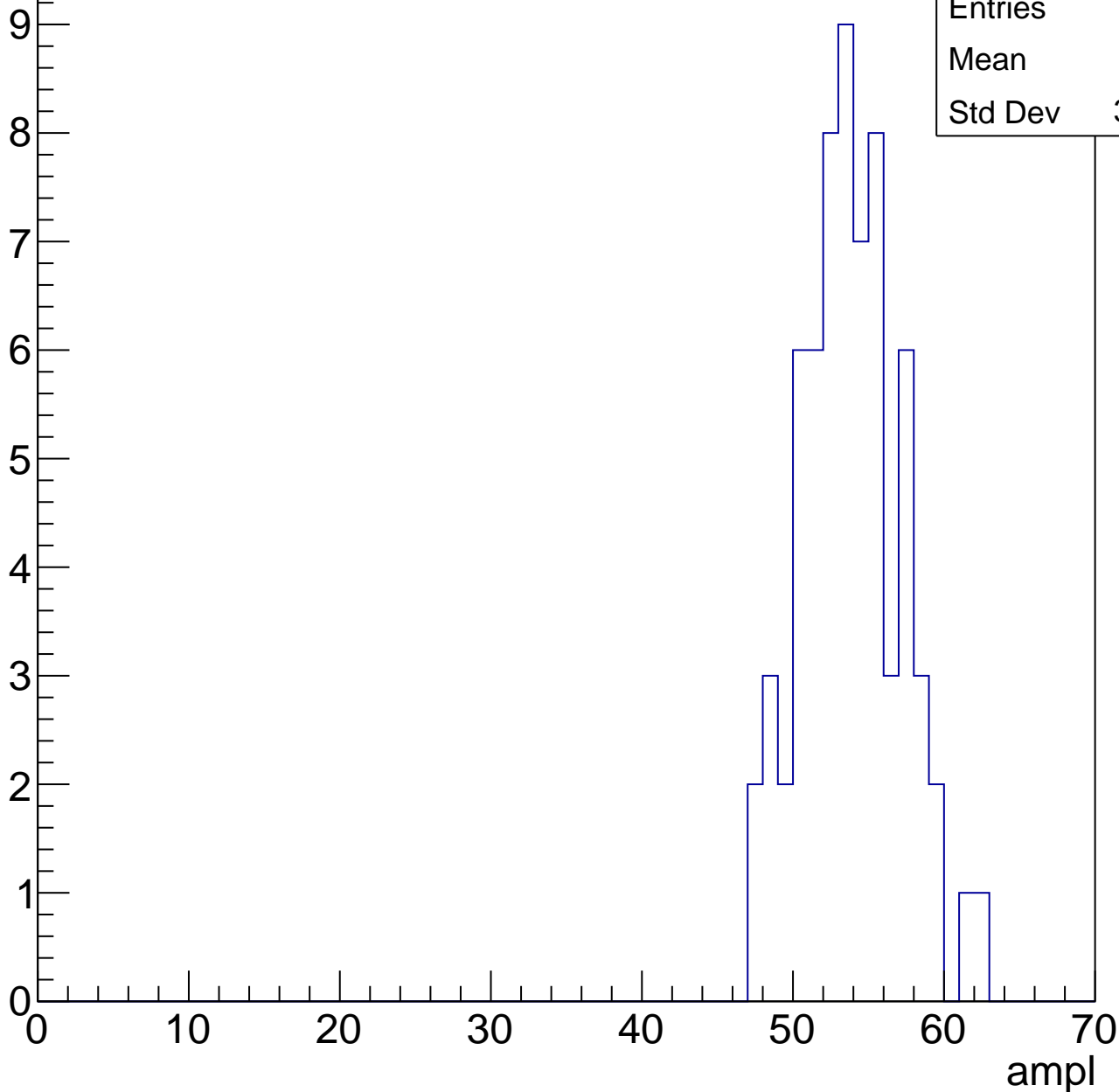


# B1L002S, U17-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	53.4
Std Dev	3.251

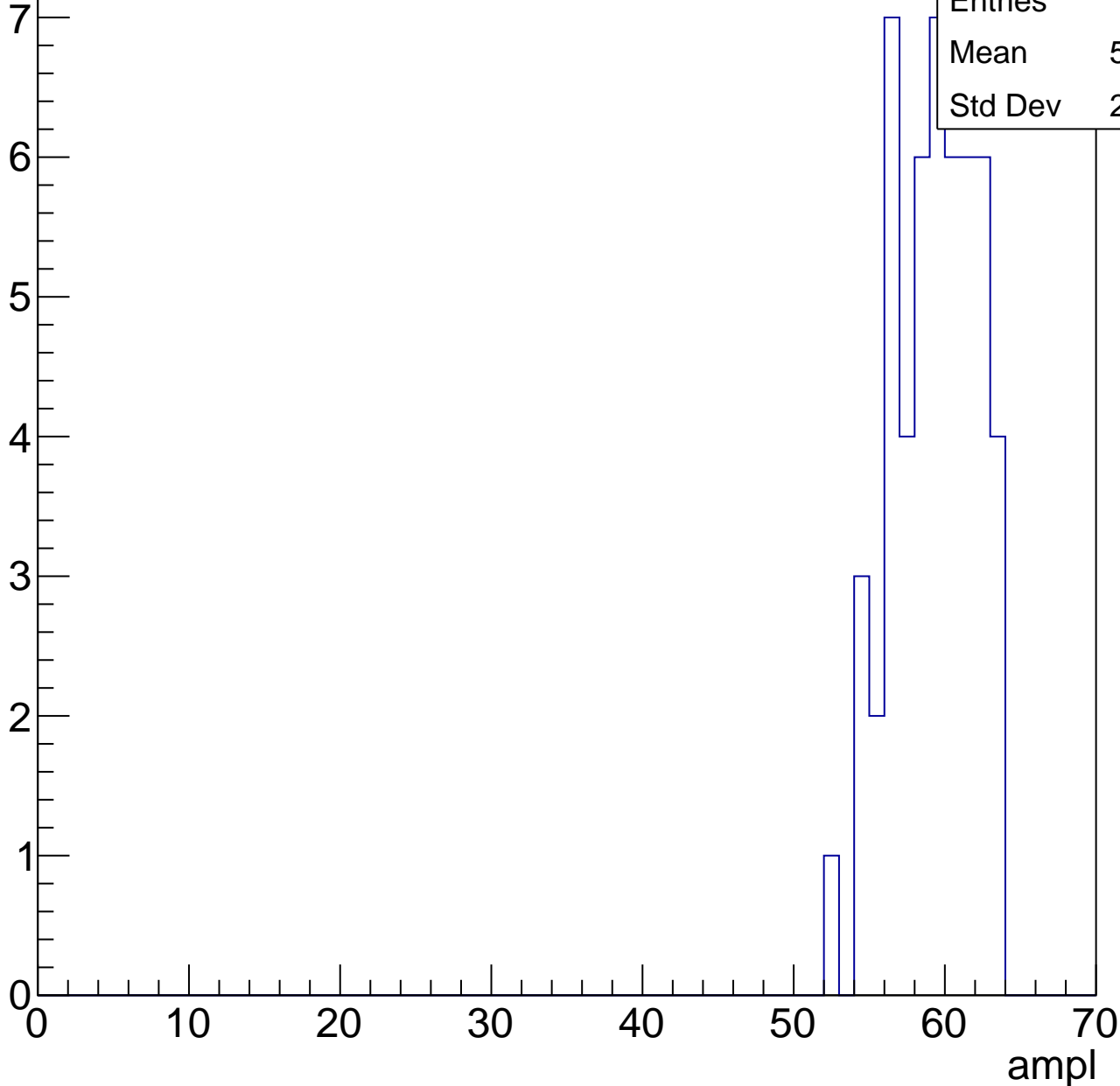


# B1L002S, U17-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.75
Std Dev	2.724

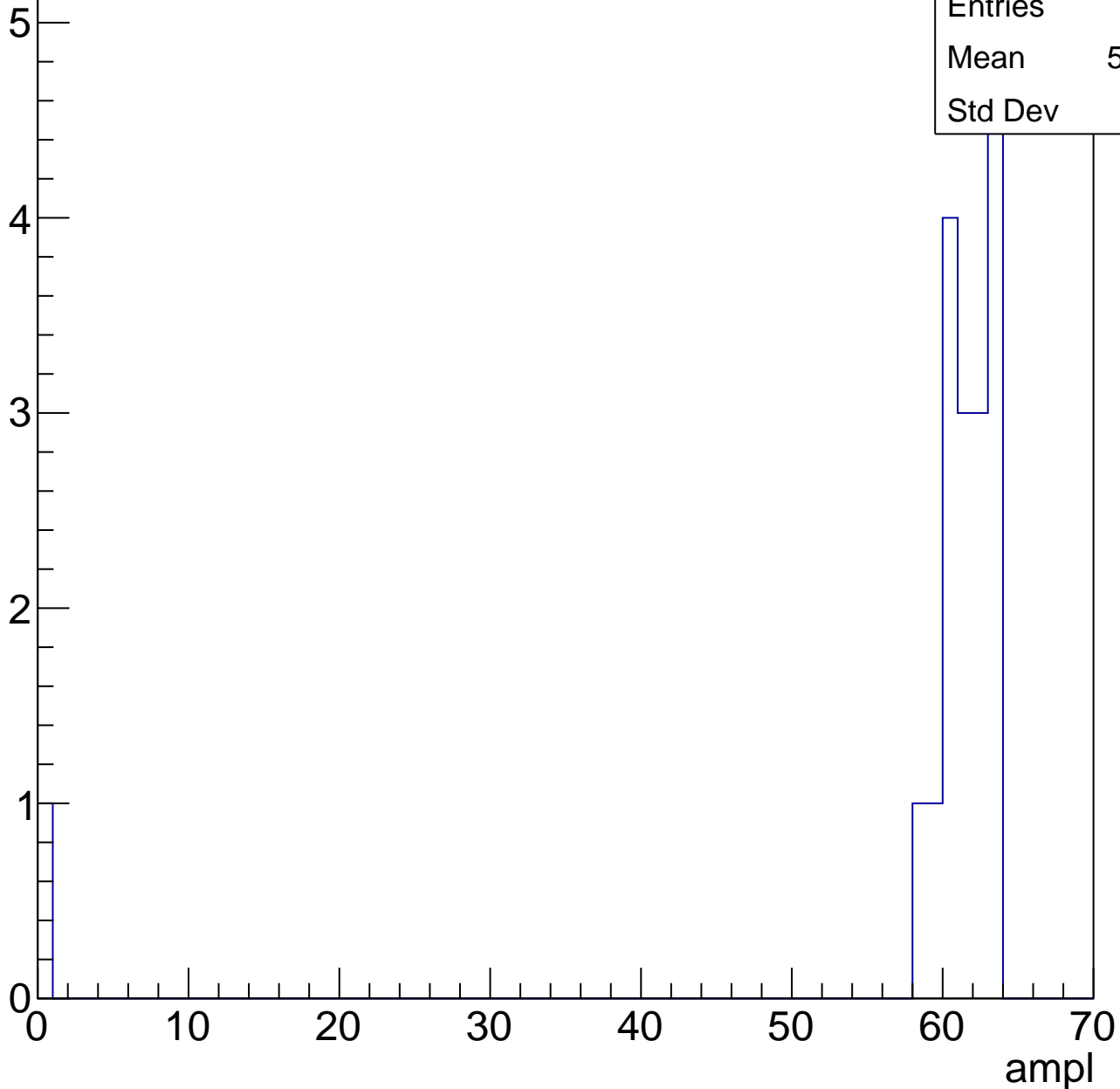


# B1L002S, U17-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	57.83
Std Dev	14.1





# B1L002S, U17-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch70, adc0

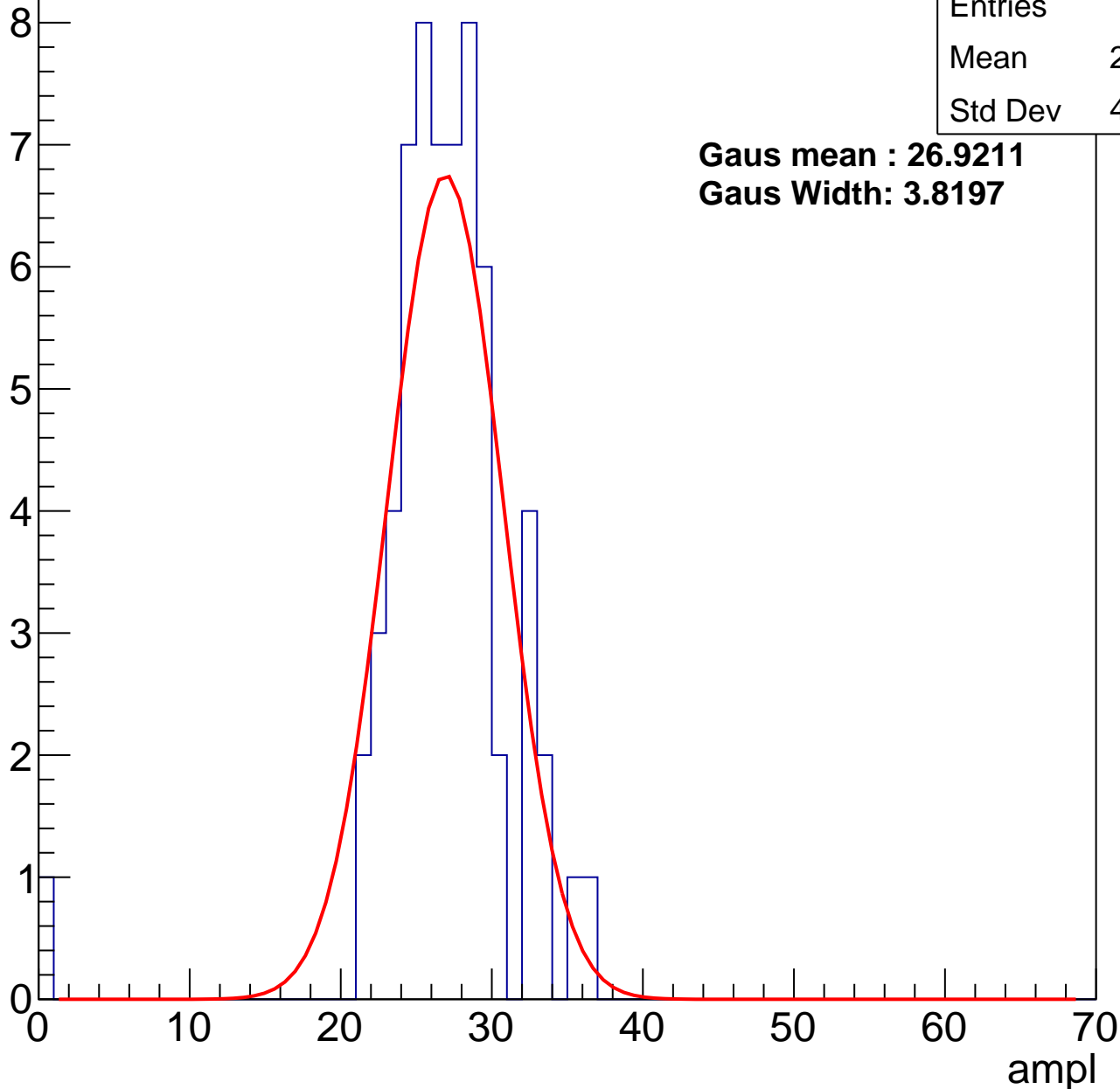
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	26.38
Std Dev	4.688

**Gaus mean : 26.9211**

**Gaus Width: 3.8197**



# B1L002S, U17-ch70, adc1

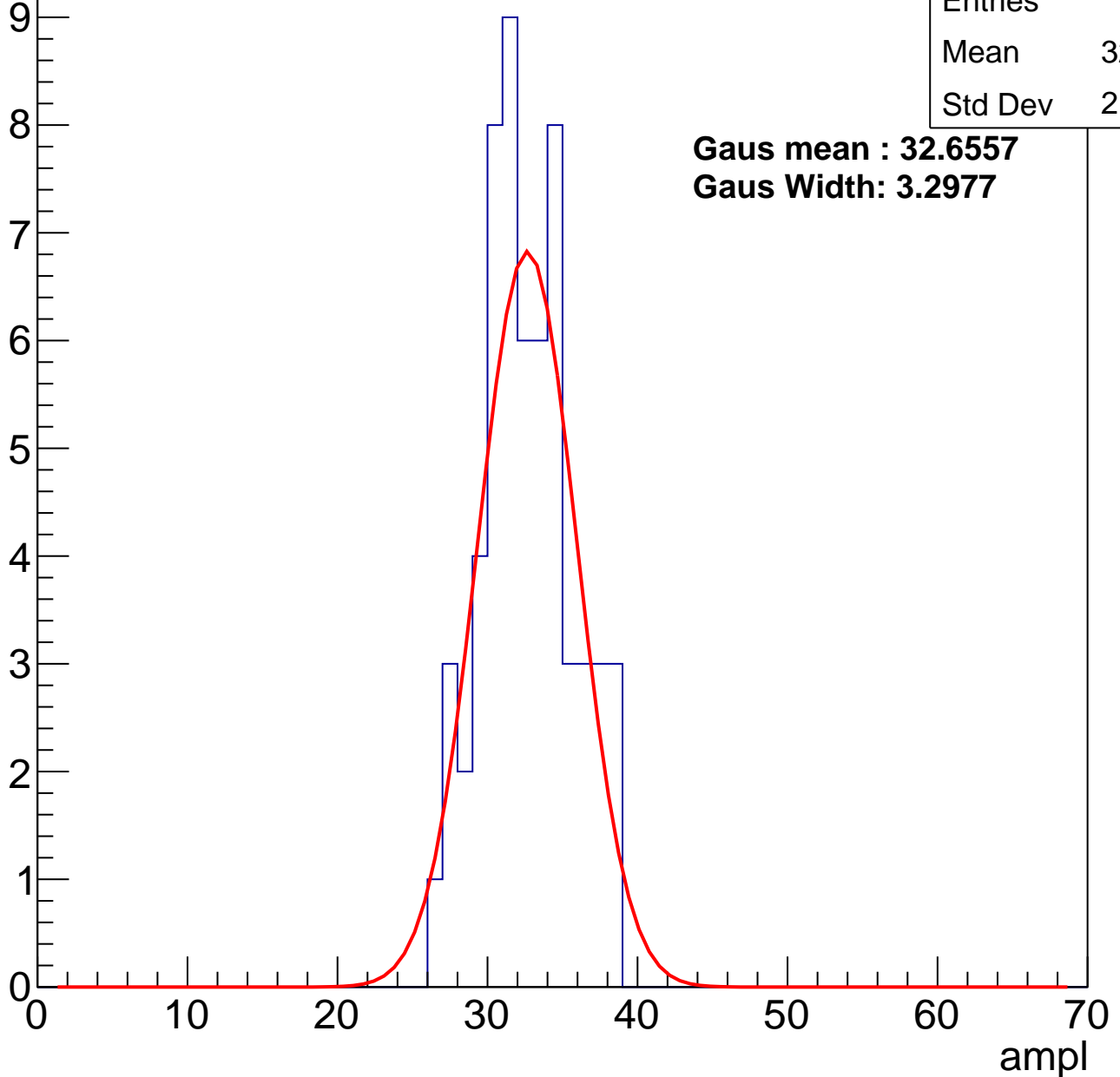
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	32.17
Std Dev	2.952

**Gaus mean : 32.6557**

**Gaus Width: 3.2977**



# B1L002S, U17-ch70, adc2

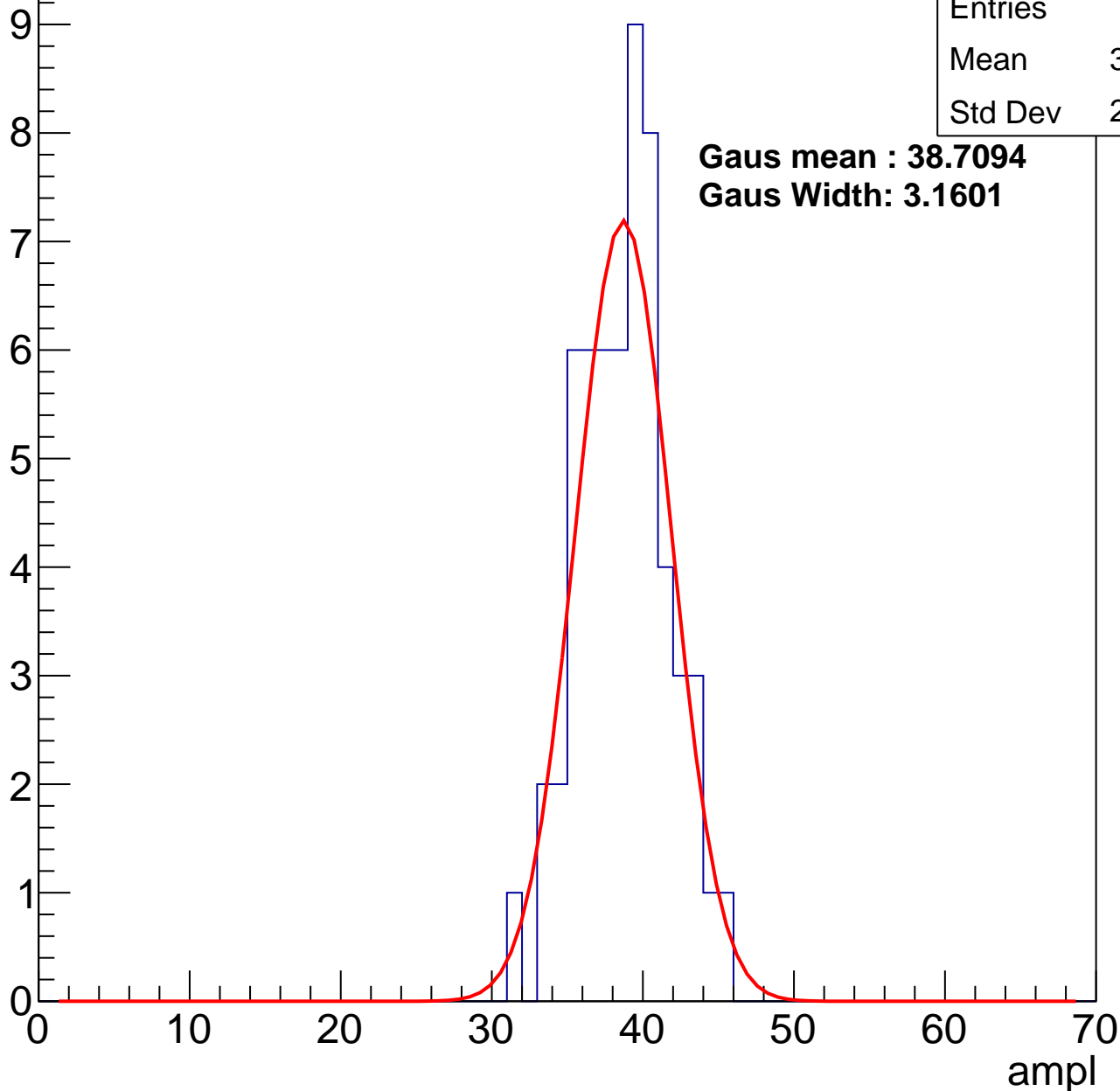
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	38.28
Std Dev	2.923

**Gaus mean : 38.7094**

**Gaus Width: 3.1601**

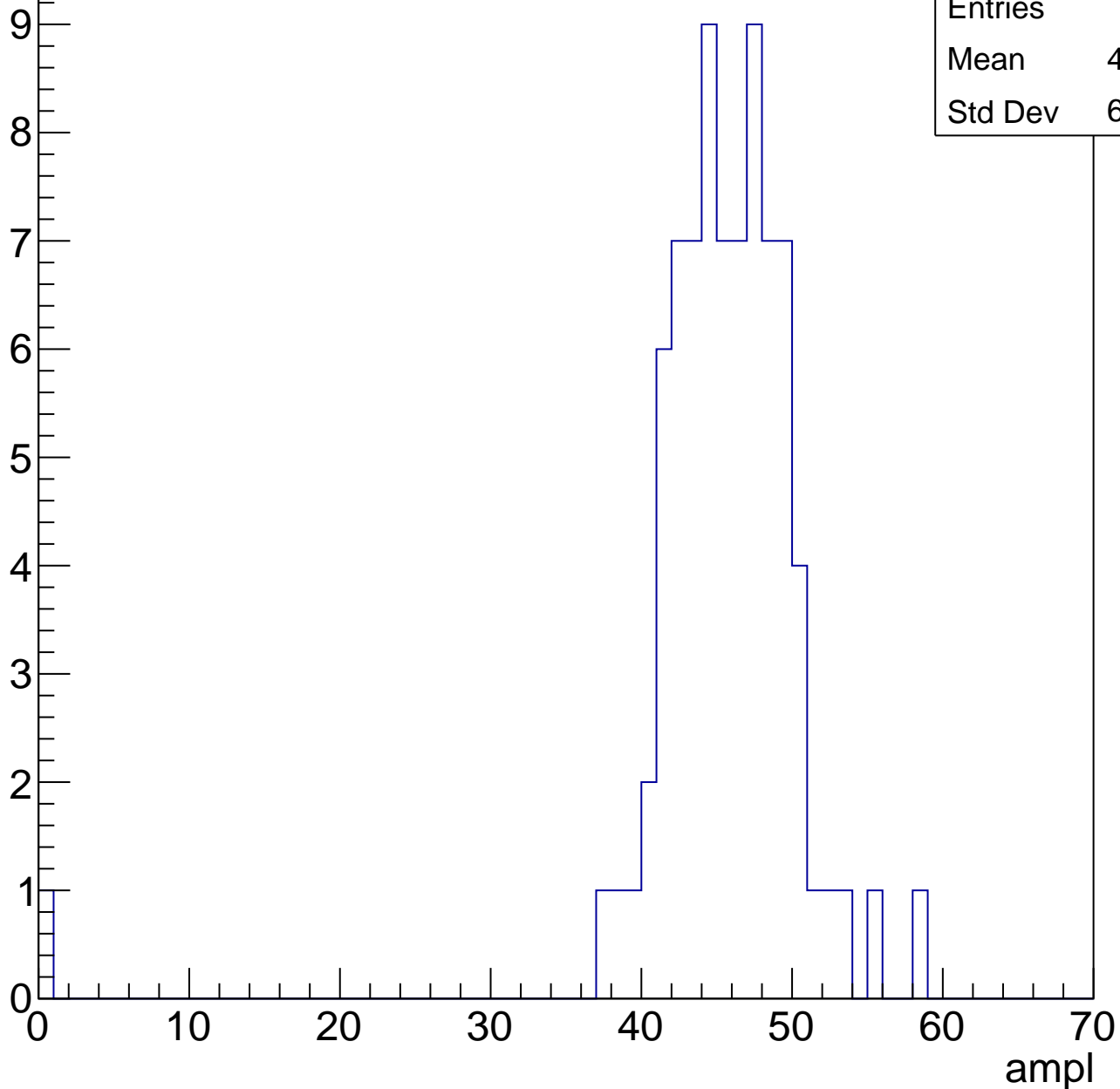


# B1L002S, U17-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	44.93
Std Dev	6.242

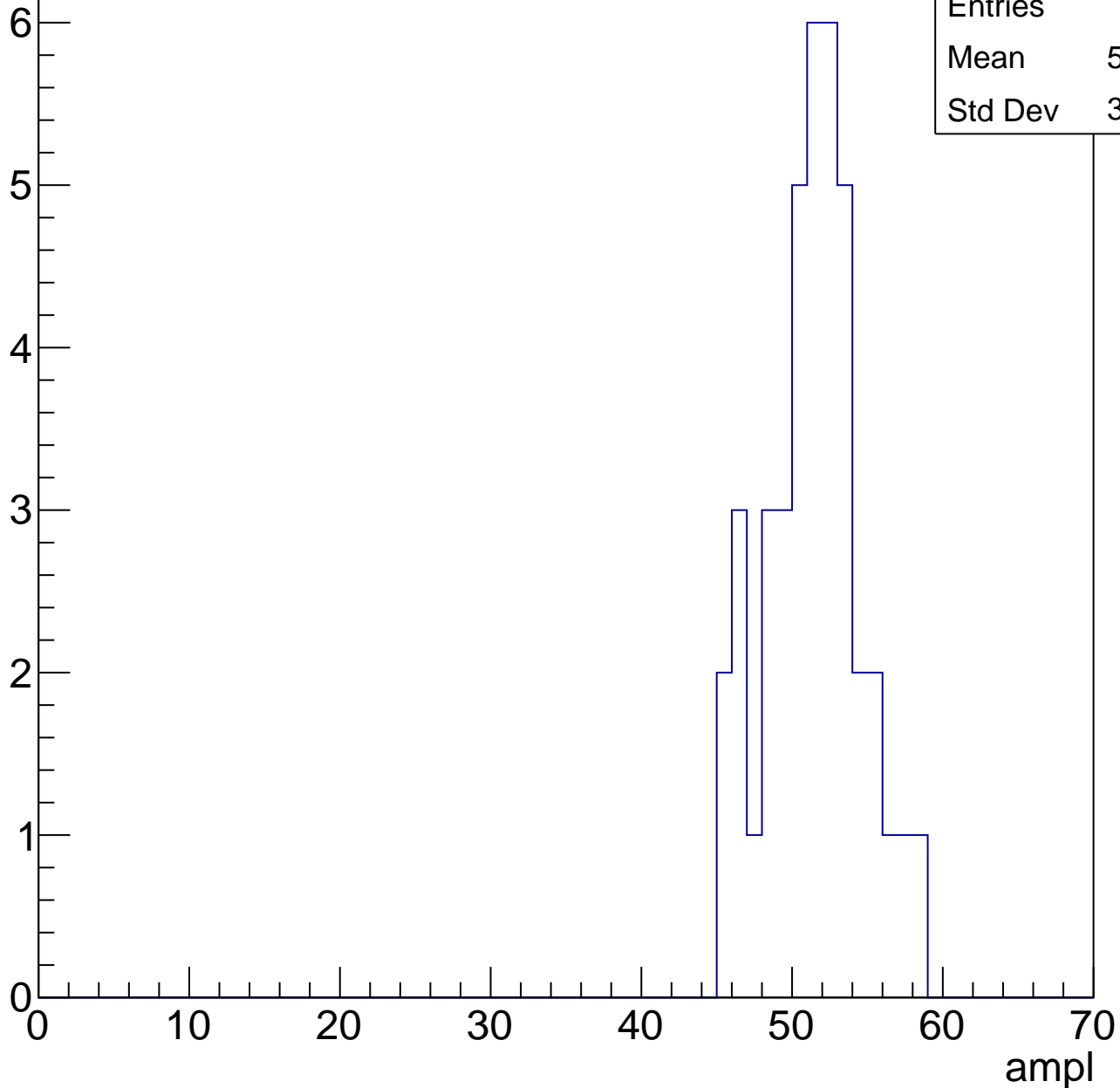


# B1L002S, U17-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

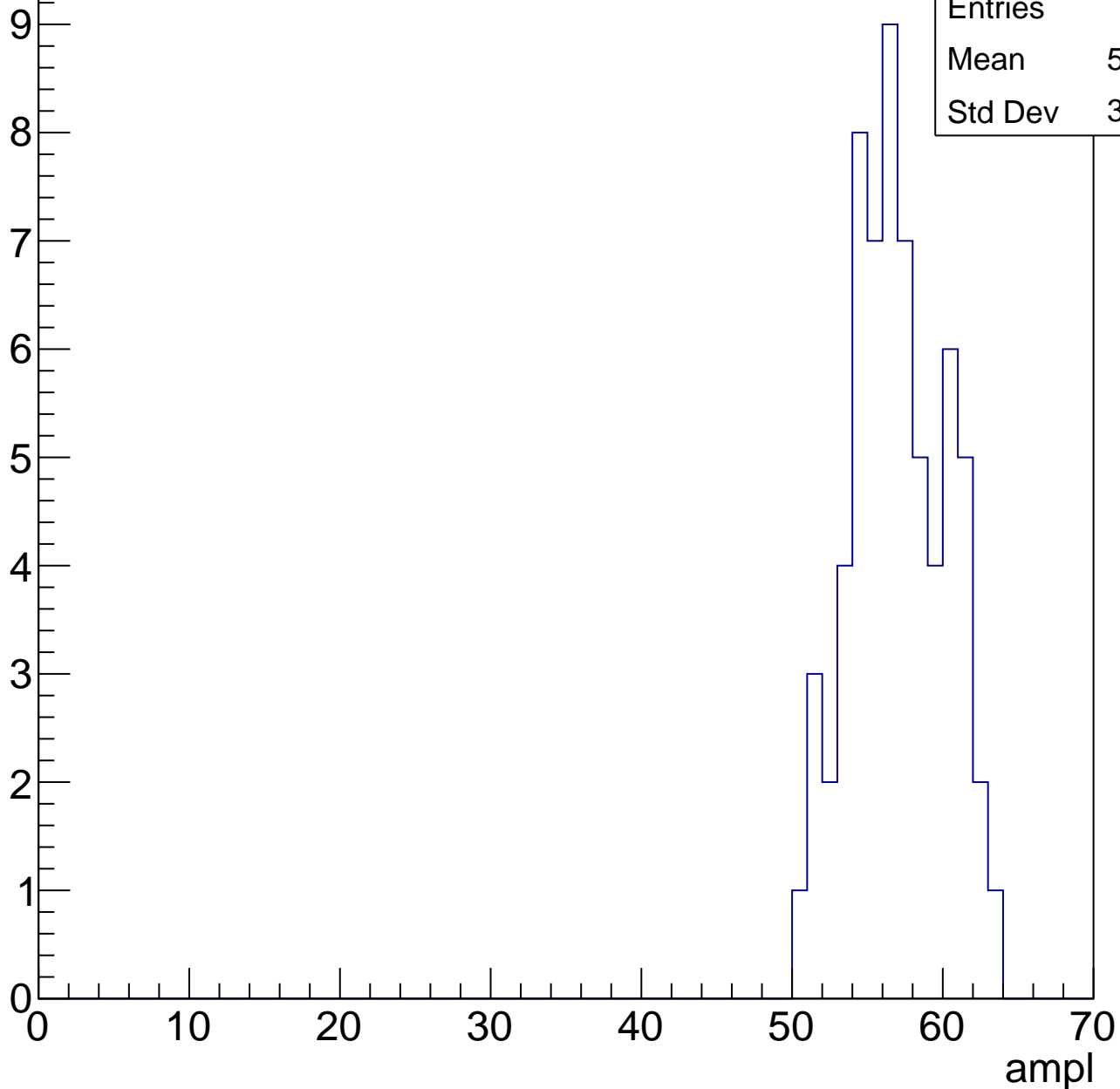
Entries	41
Mean	50.93
Std Dev	3.095



# B1L002S, U17-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



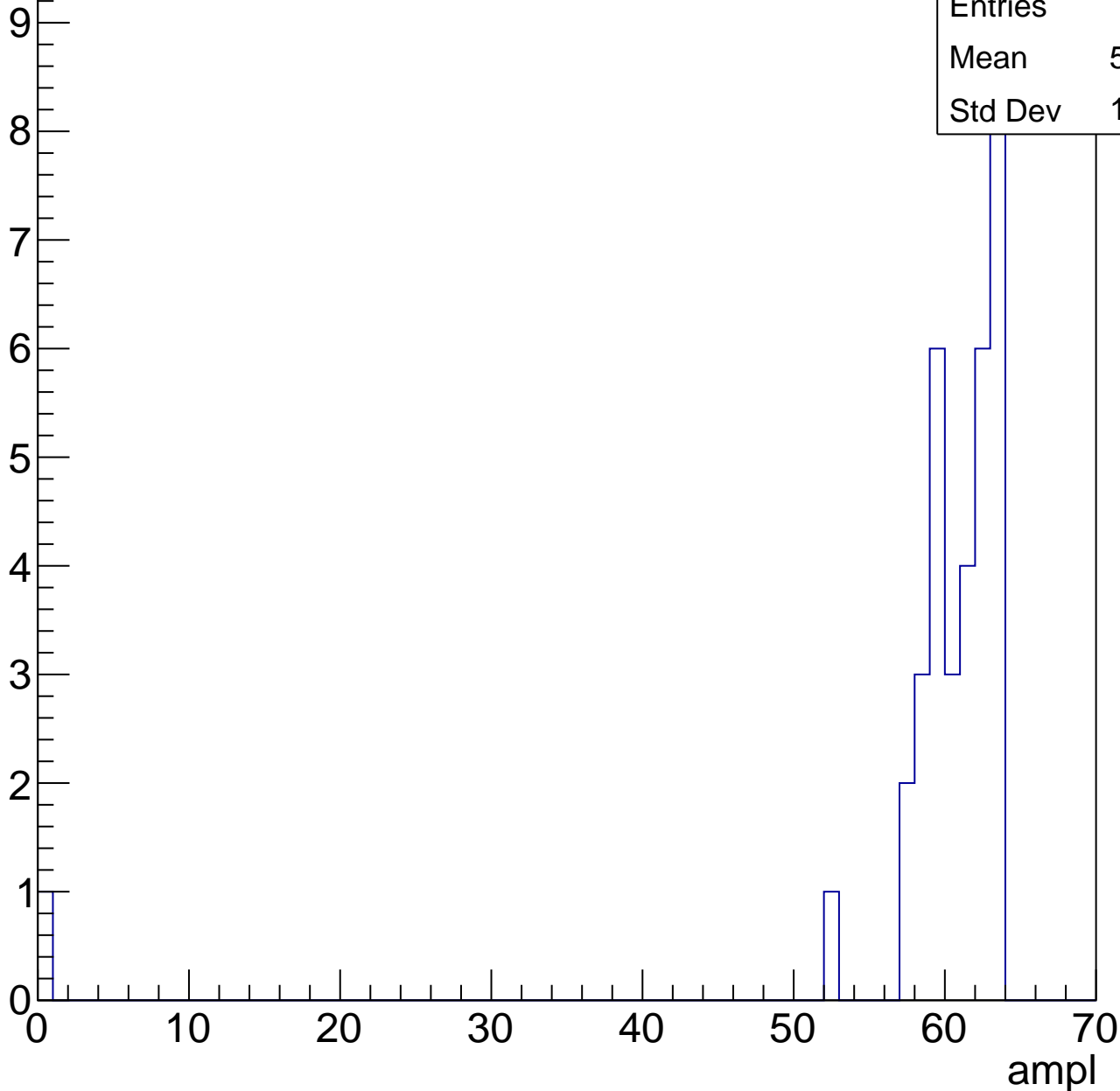
Entries	64
Mean	56.52
Std Dev	3.077

# B1L002S, U17-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.77
Std Dev	10.36

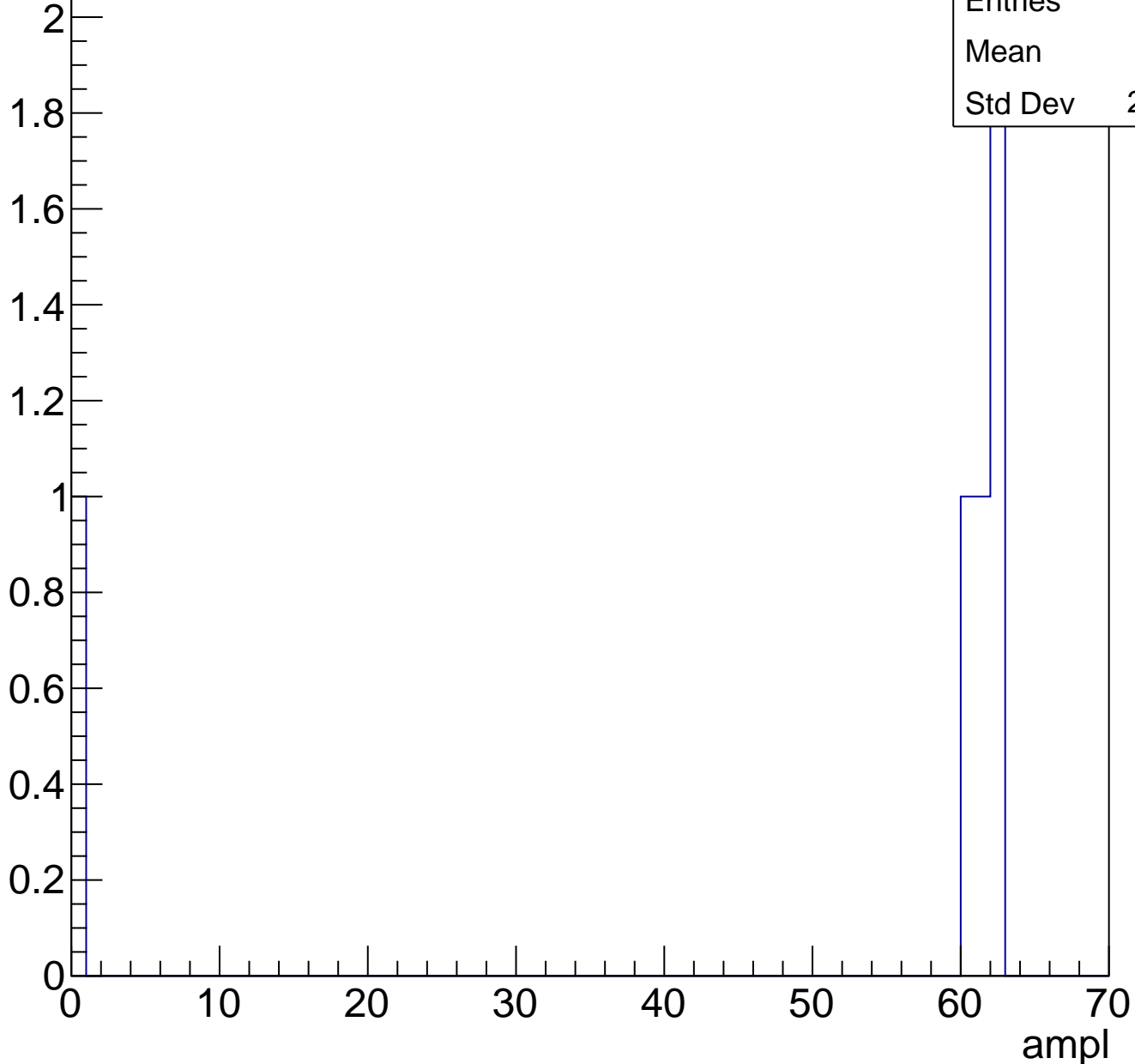




# B1L002S, U17-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	49
Std Dev	24.51

# B1L002S, U17-ch71, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	91
Mean	28.56
Std Dev	3.866

**Gaus mean : 28.7624**

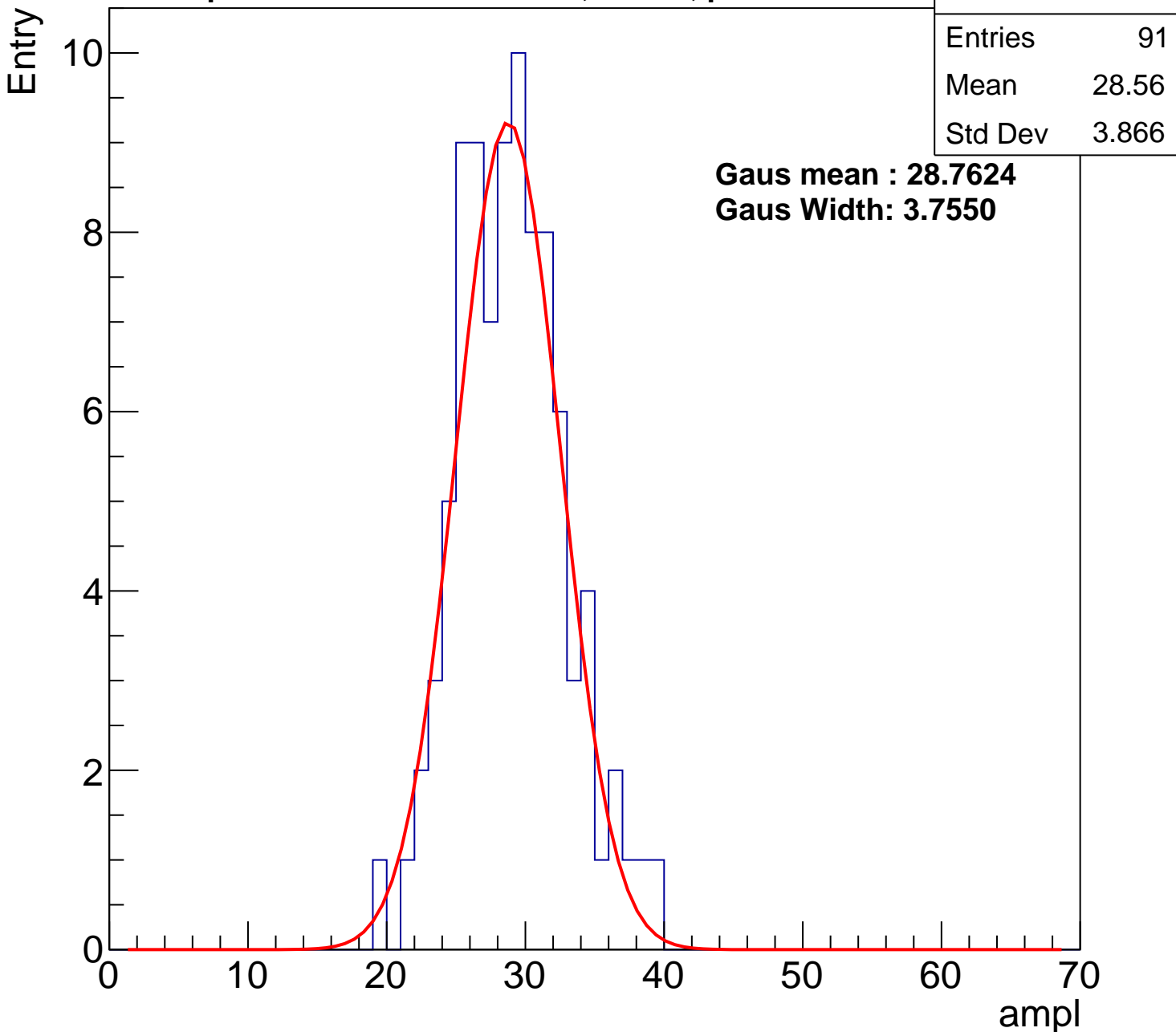
**Gaus Width: 3.7550**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch71, adc1

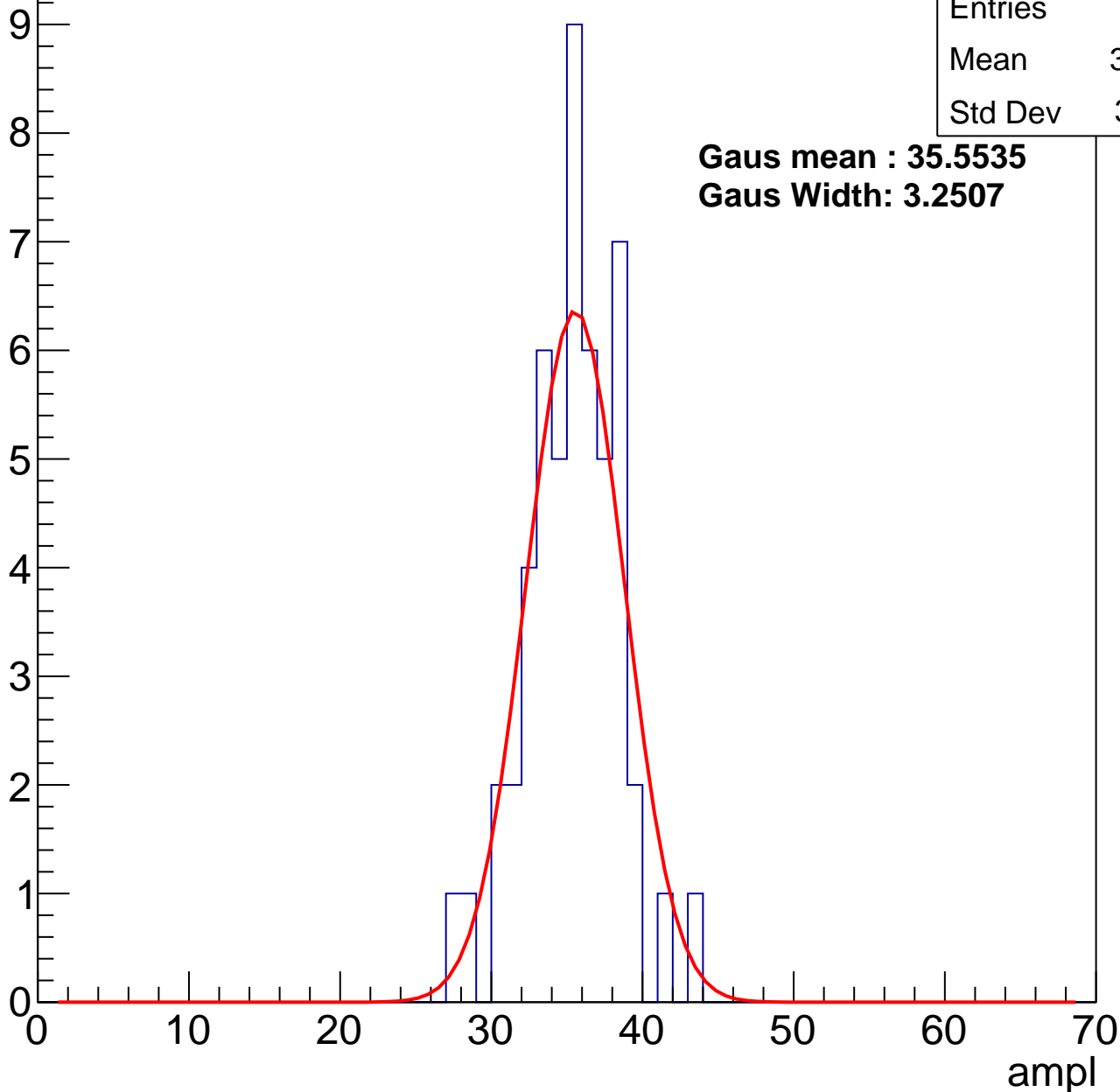
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	34.94
Std Dev	3.041

**Gaus mean : 35.5535**

**Gaus Width: 3.2507**



# B1L002S, U17-ch71, adc2

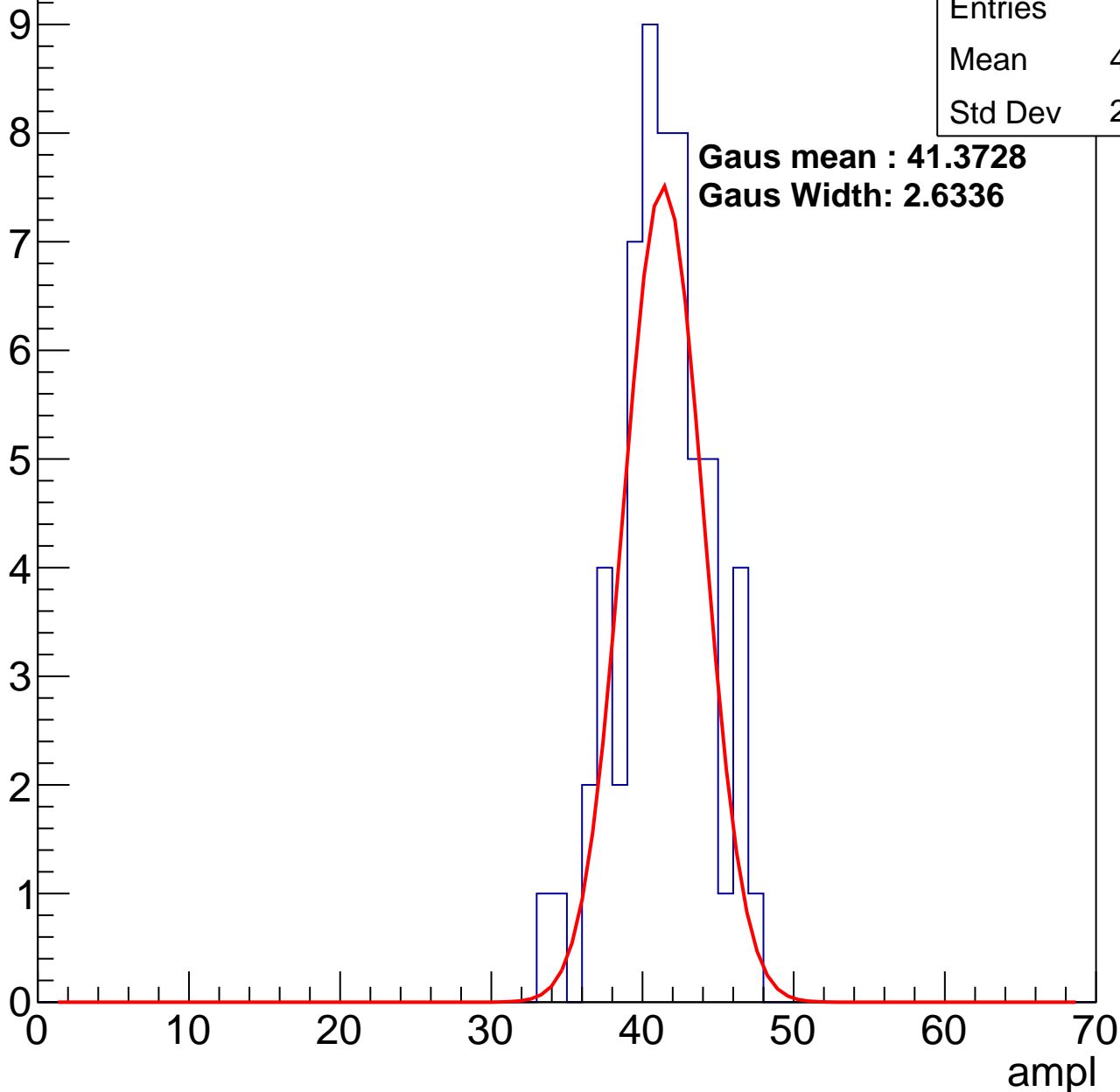
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.88
Std Dev	2.954

**Gaus mean : 41.3728**

**Gaus Width: 2.6336**



# B1L002S, U17-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	47.27
Std Dev	3.391

Entry

10

8

6

4

2

0

0

10

20

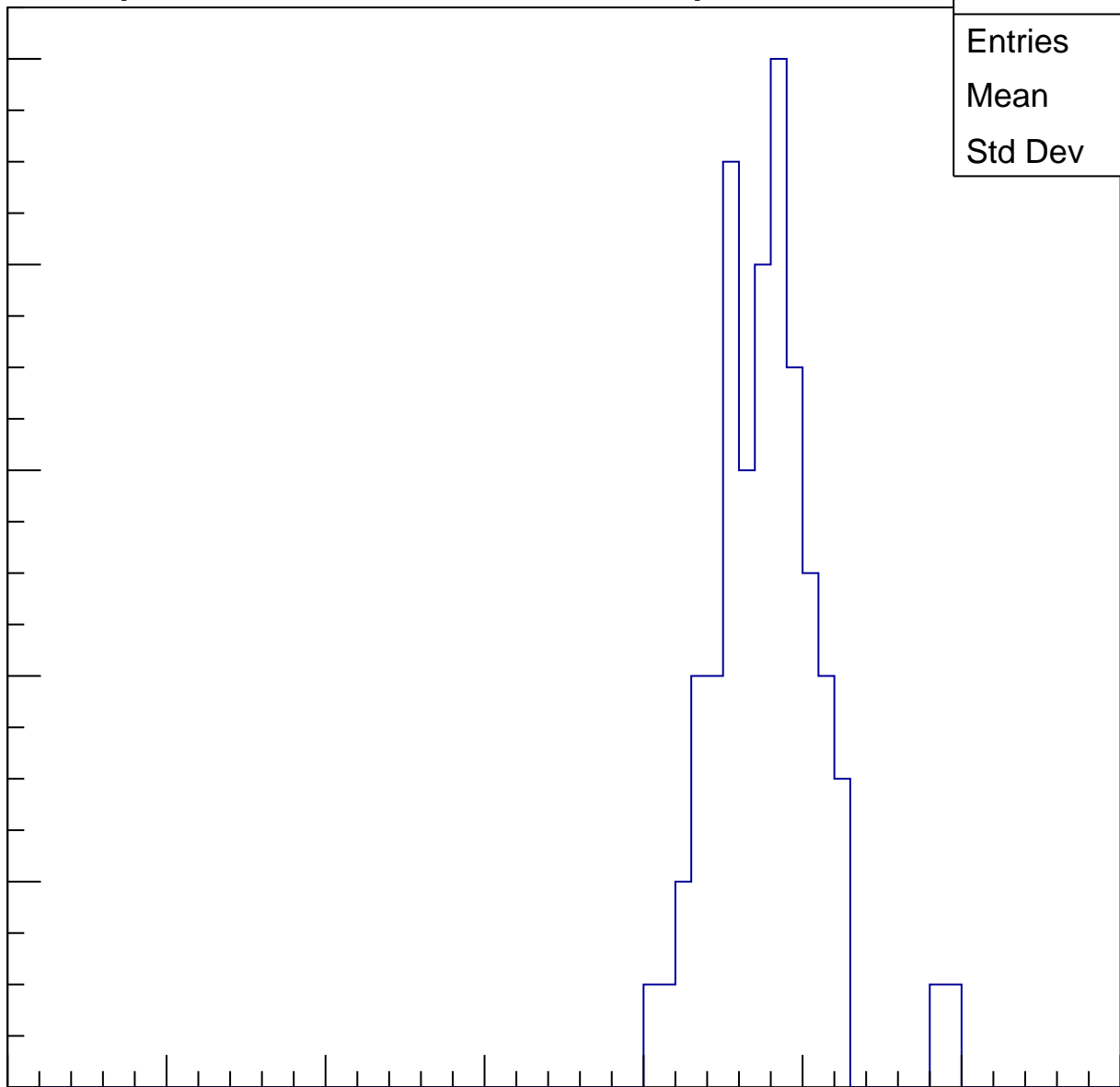
30

40

50

60

ampl



# B1L002S, U17-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	53.4
Std Dev	3.169

Entry

10

8

6

4

2

0

0

10

20

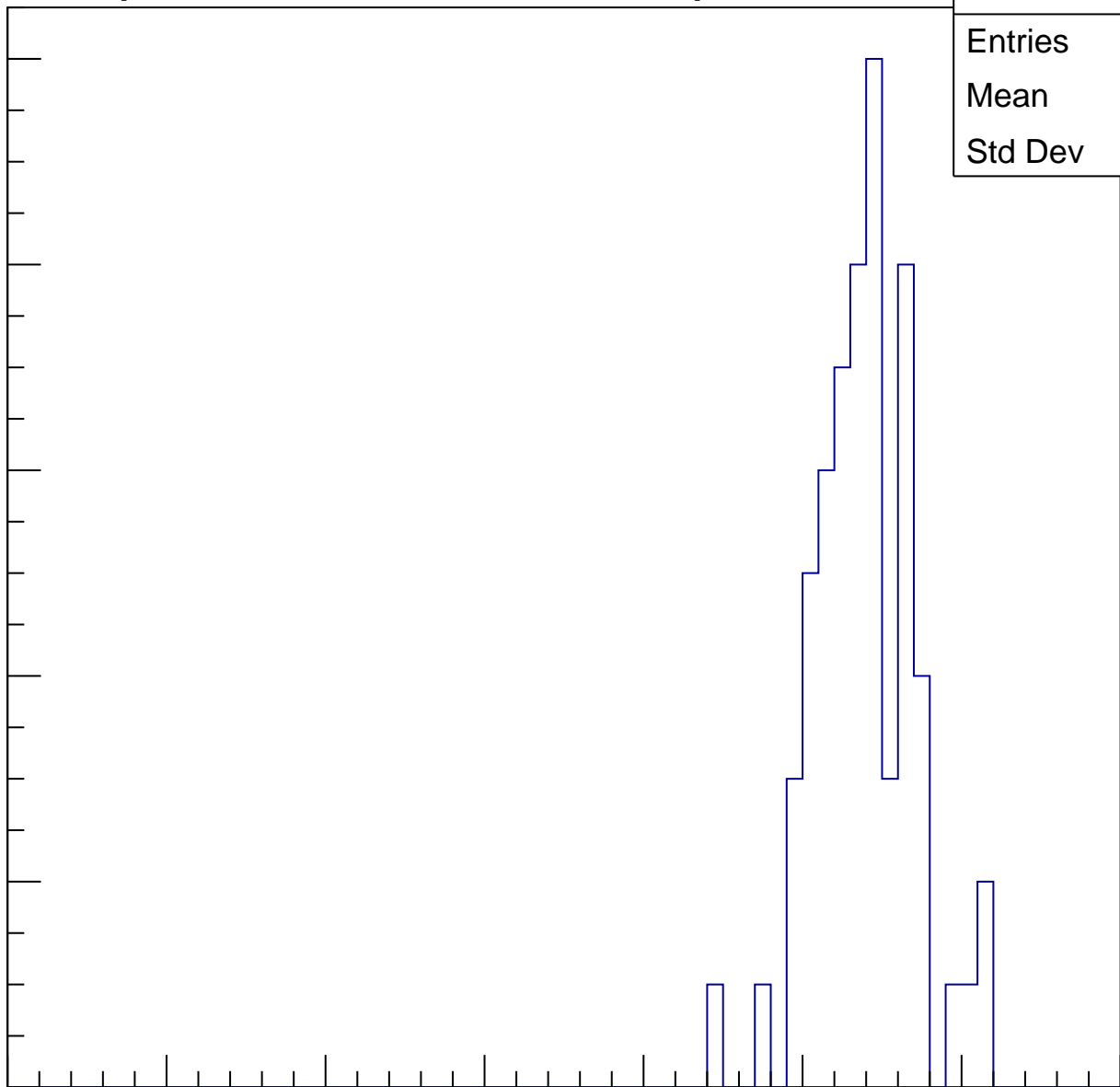
30

40

50

60

ampl

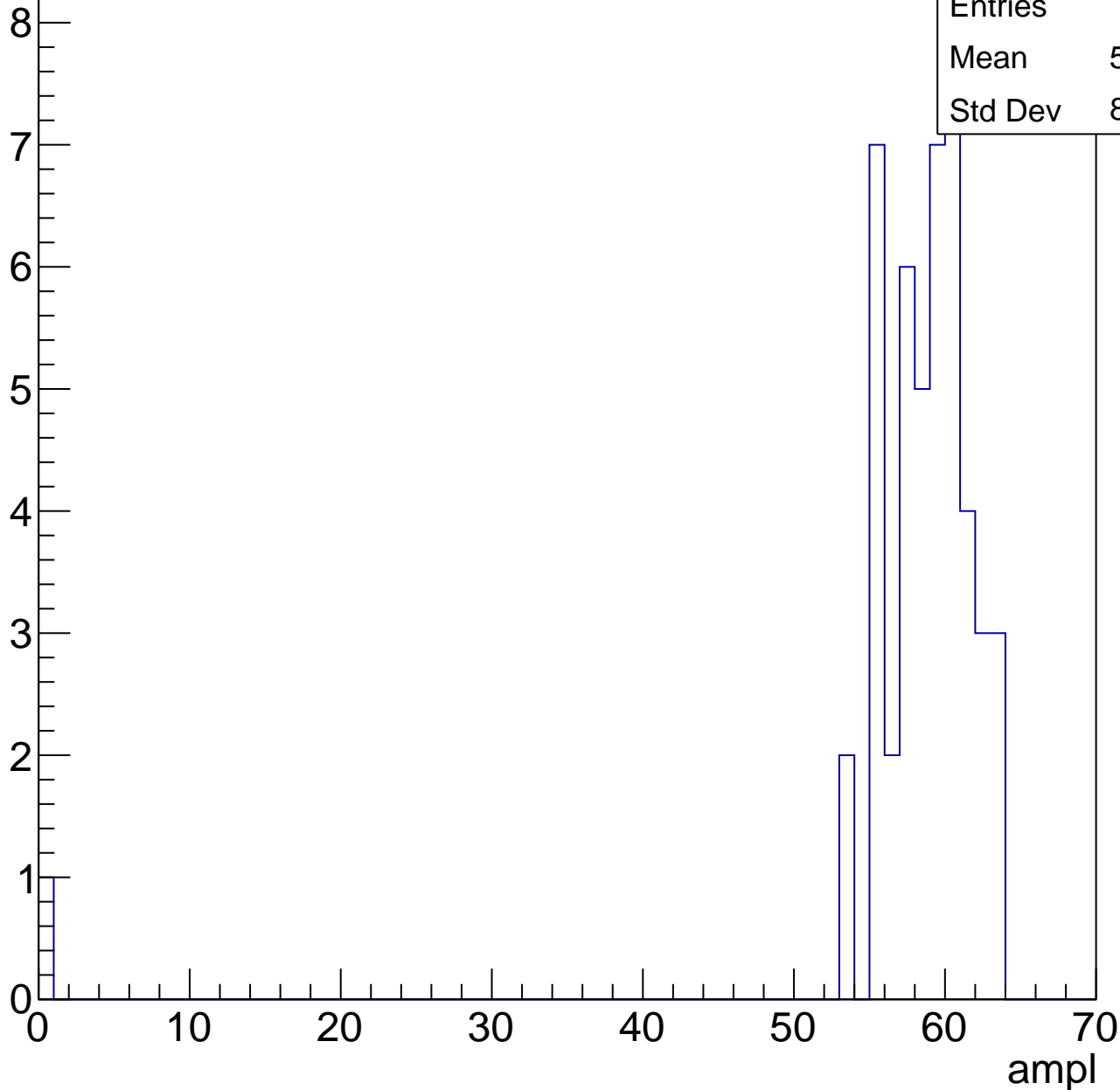


# B1L002S, U17-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	57.23
Std Dev	8.733

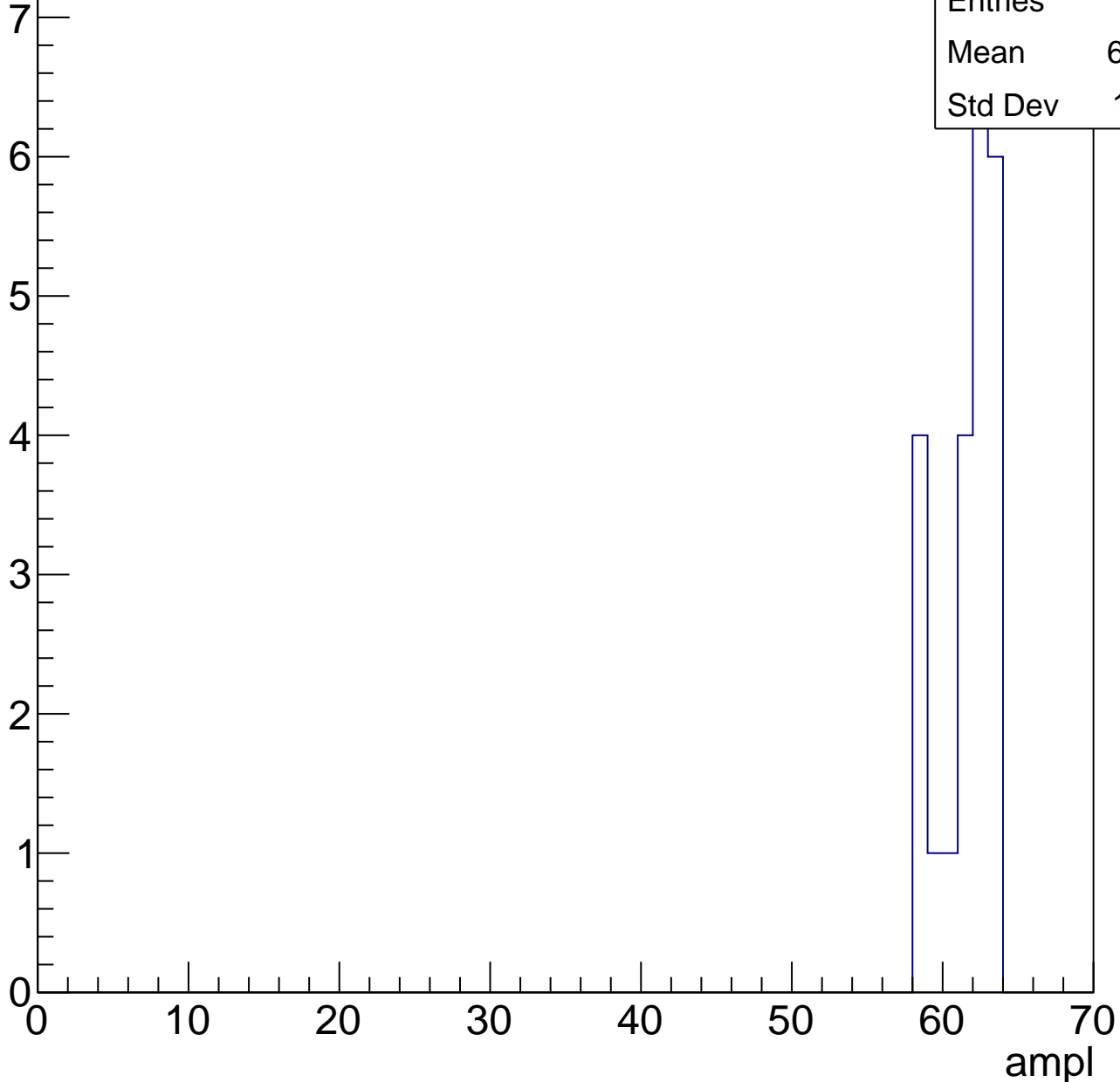


# B1L002S, U17-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.17
Std Dev	1.761





# B1L002S, U17-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L002S, U17-ch72, adc0

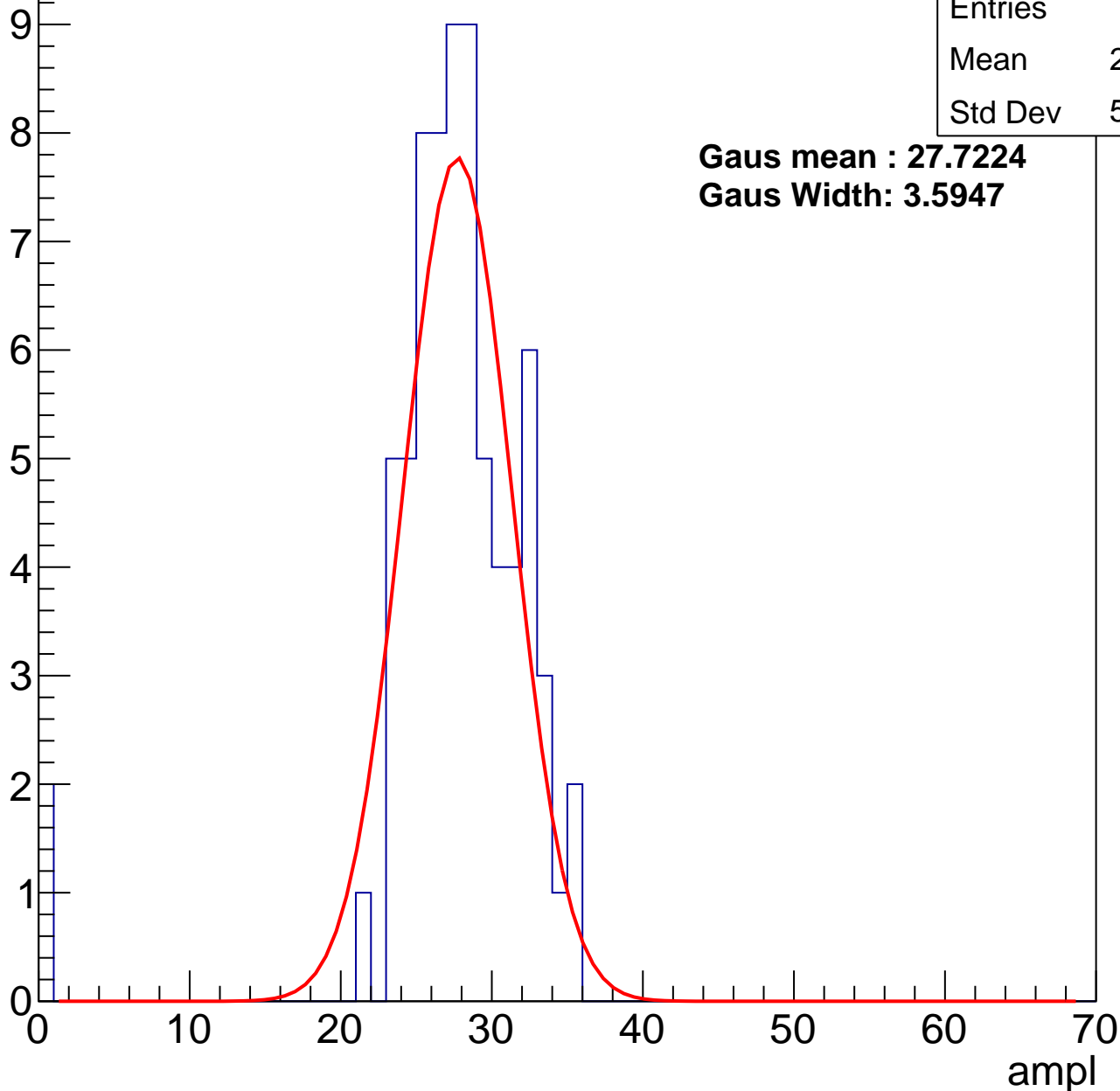
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	26.99
Std Dev	5.557

**Gaus mean : 27.7224**

**Gaus Width: 3.5947**



# B1L002S, U17-ch72, adc1

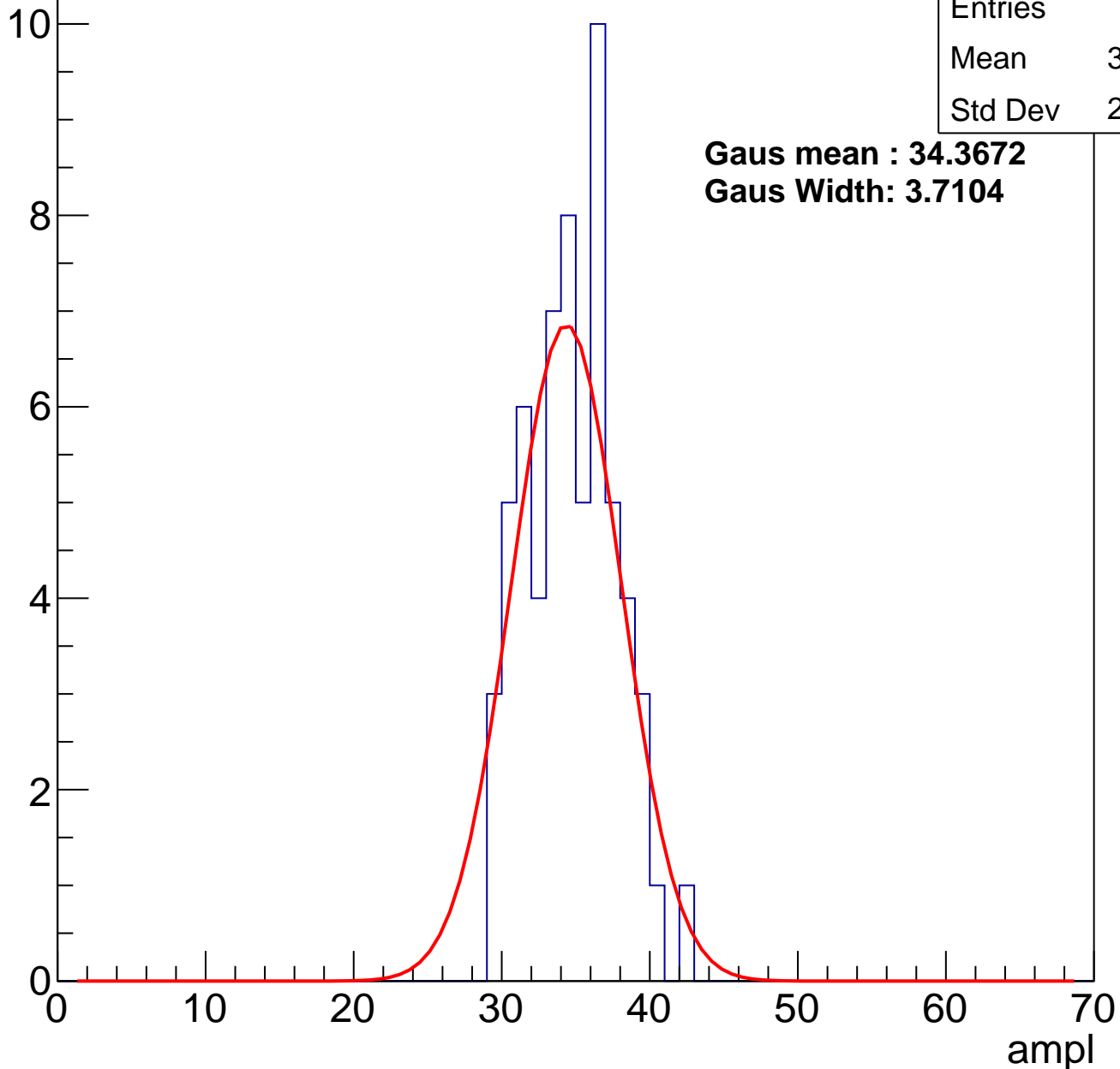
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	34.27
Std Dev	2.996

**Gaus mean : 34.3672**

**Gaus Width: 3.7104**

Entry



# B1L002S, U17-ch72, adc2

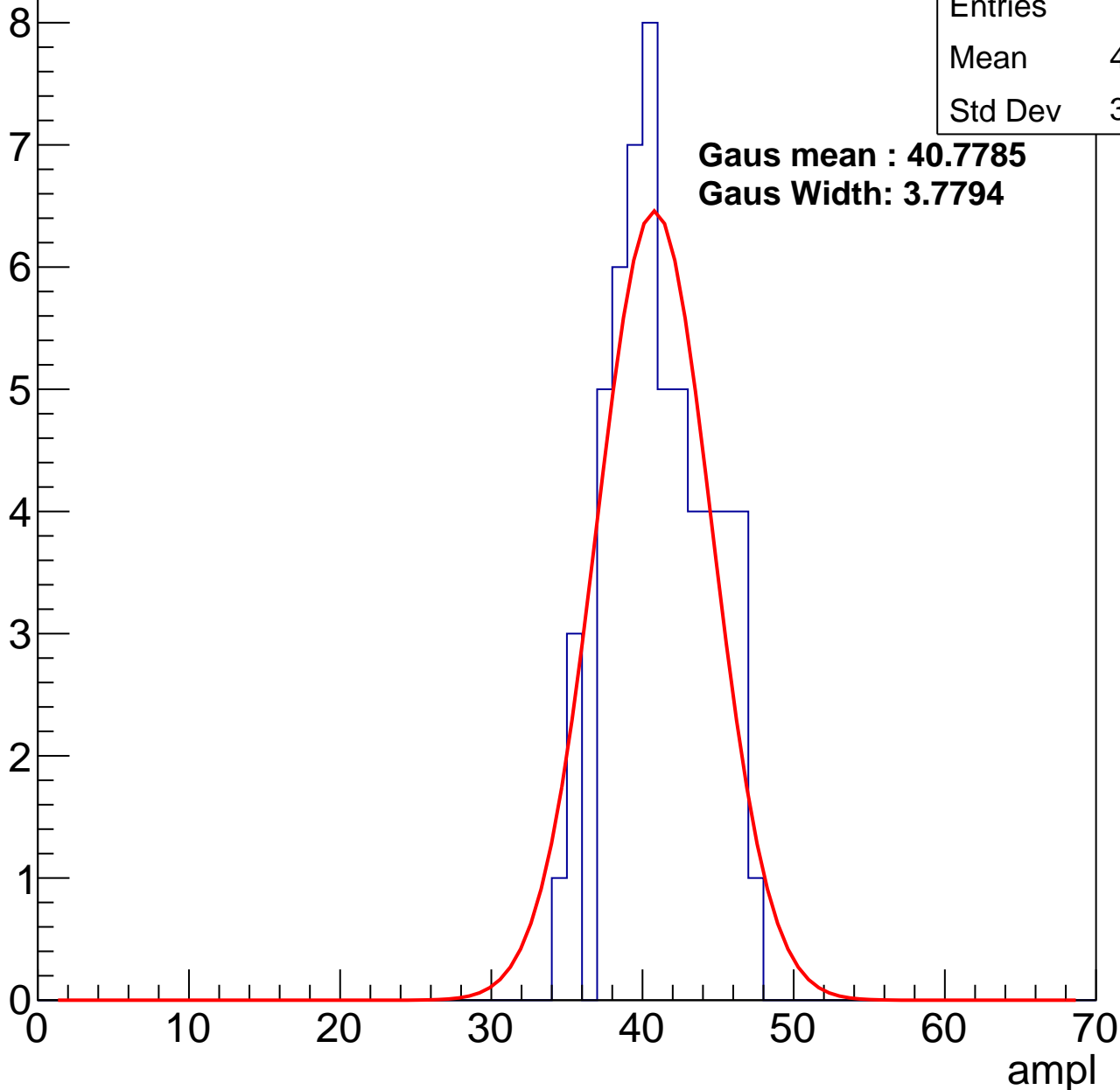
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.68
Std Dev	3.185

**Gaus mean : 40.7785**

**Gaus Width: 3.7794**

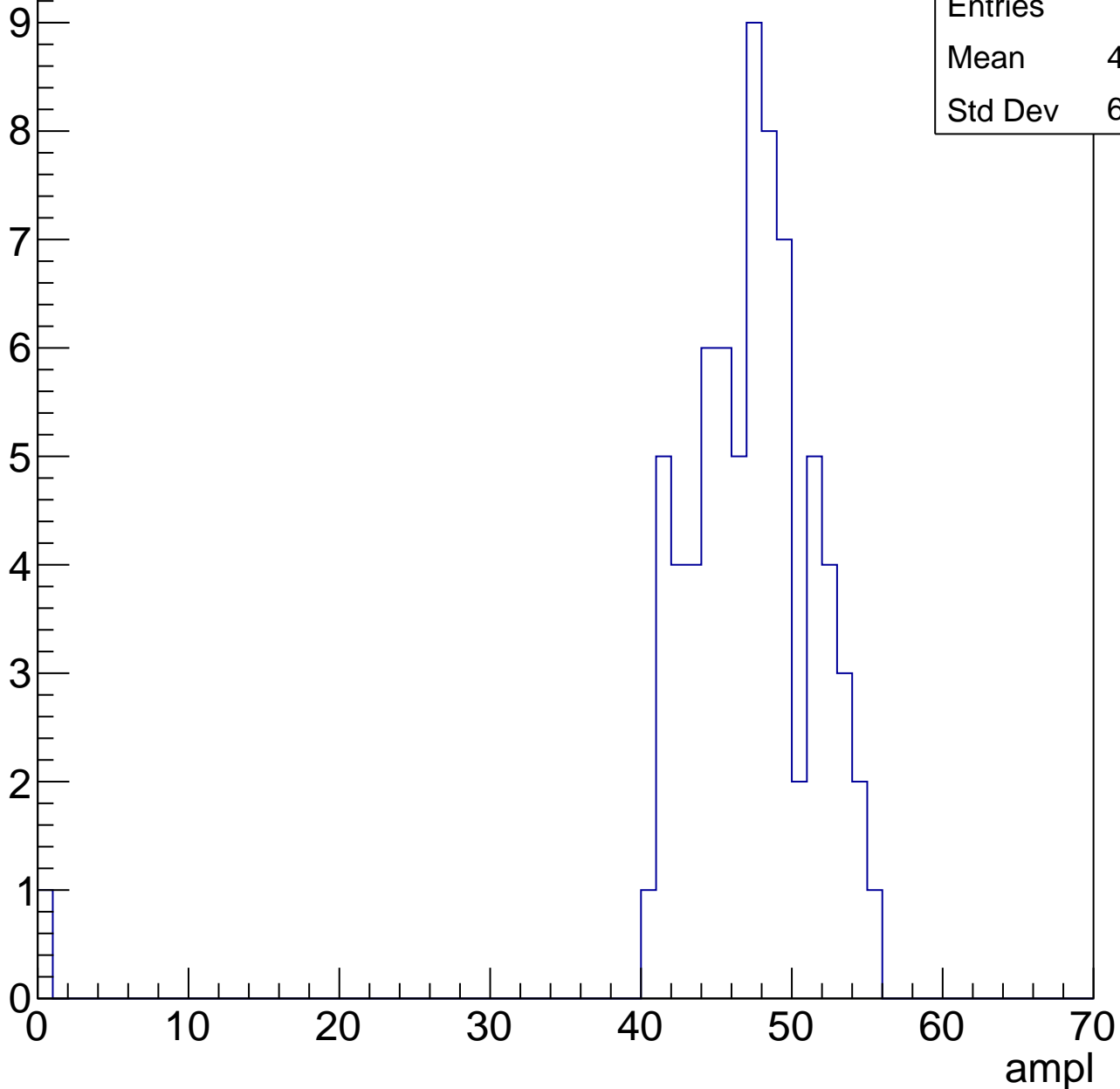


# B1L002S, U17-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	46.36
Std Dev	6.578

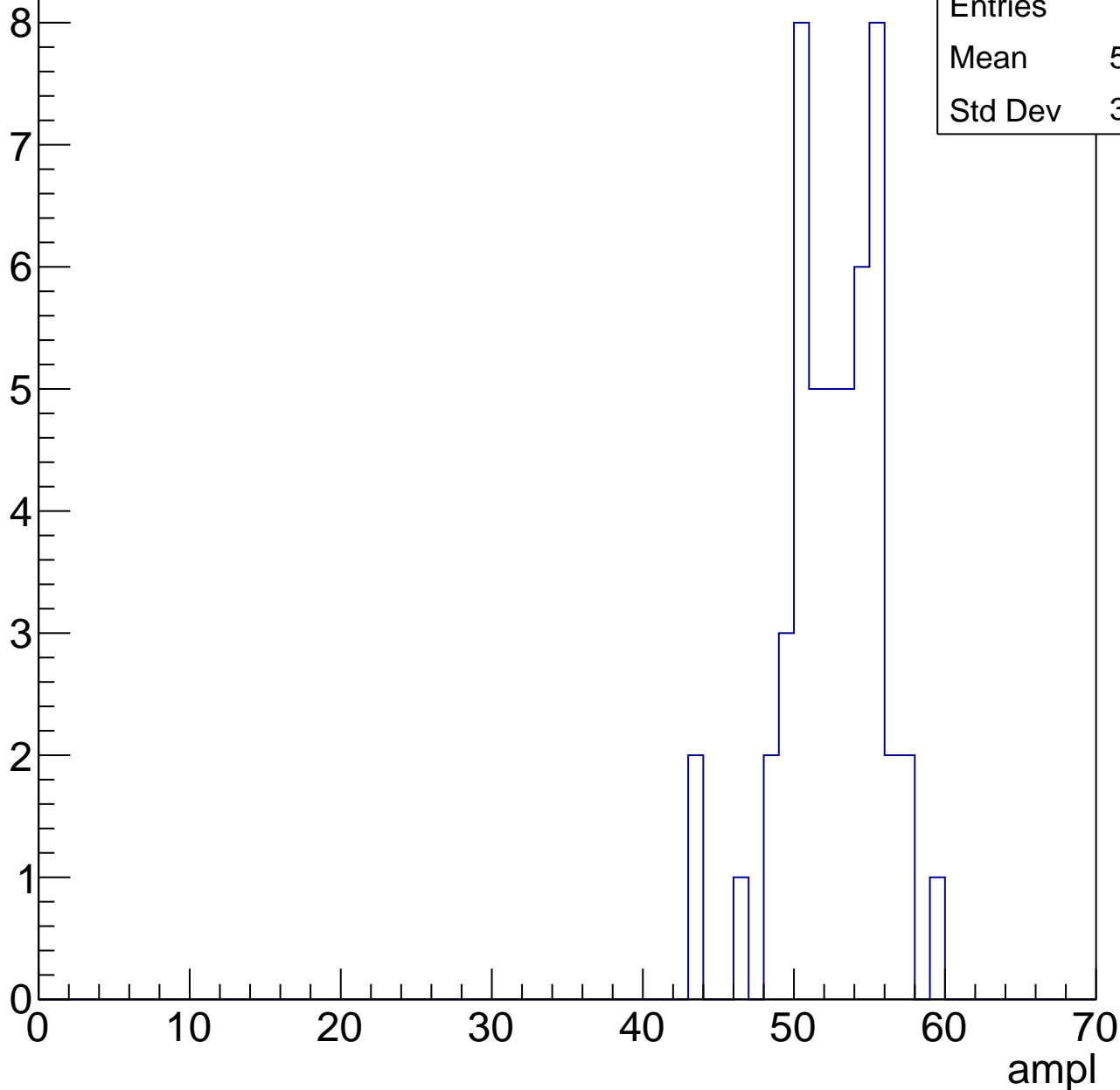


# B1L002S, U17-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	52.08
Std Dev	3.242

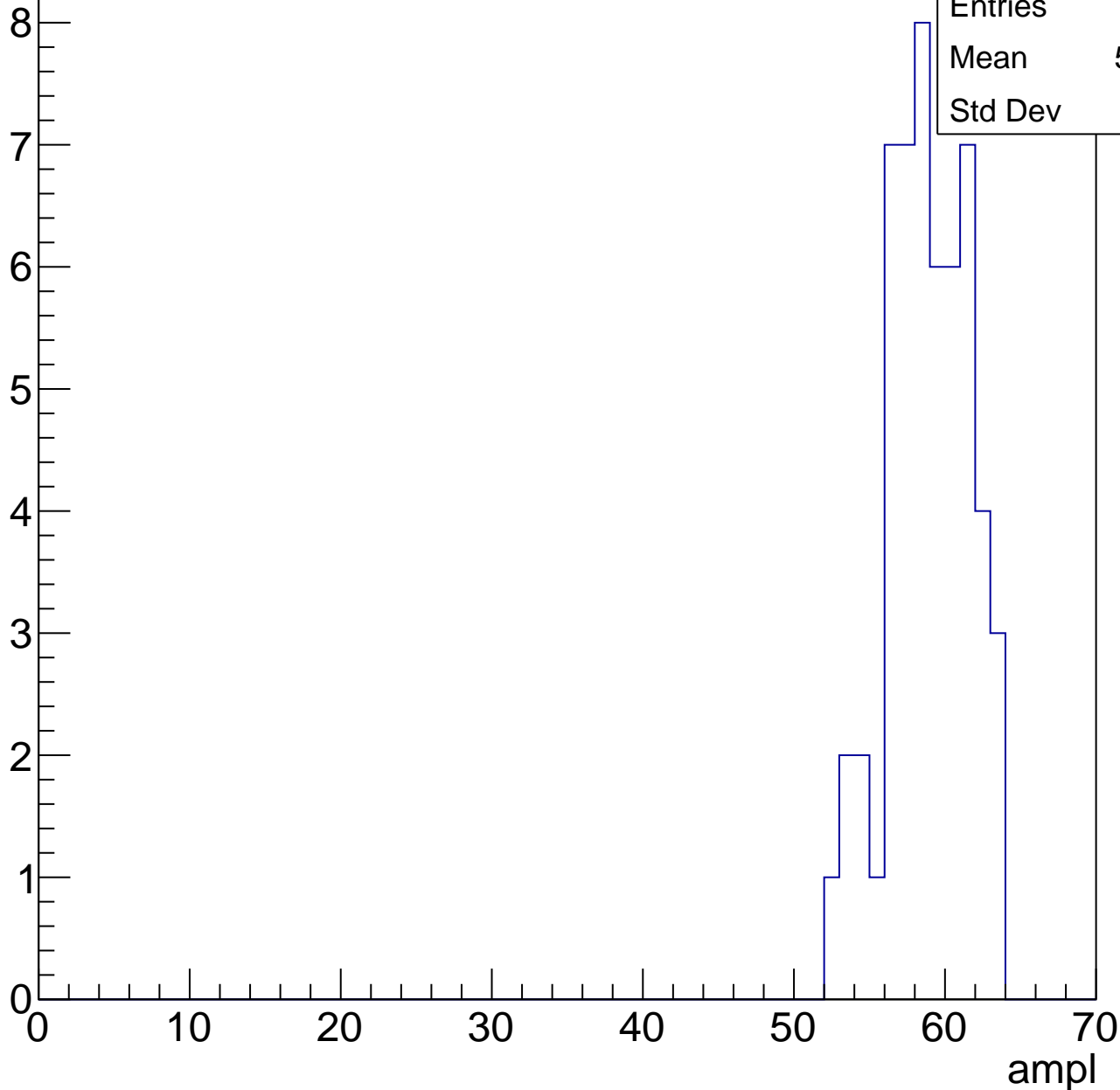


# B1L002S, U17-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.41
Std Dev	2.67

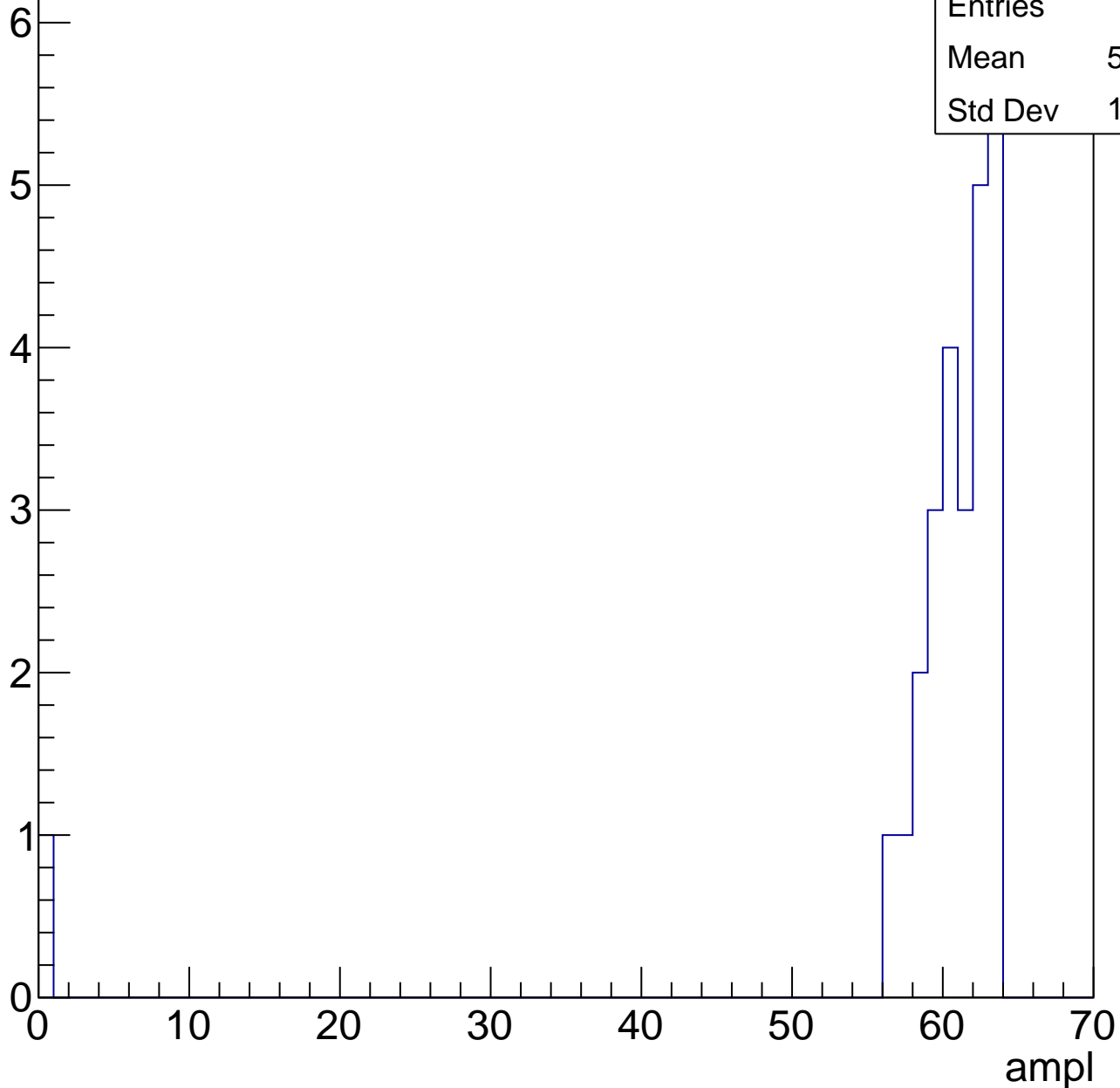


# B1L002S, U17-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.35
Std Dev	11.84

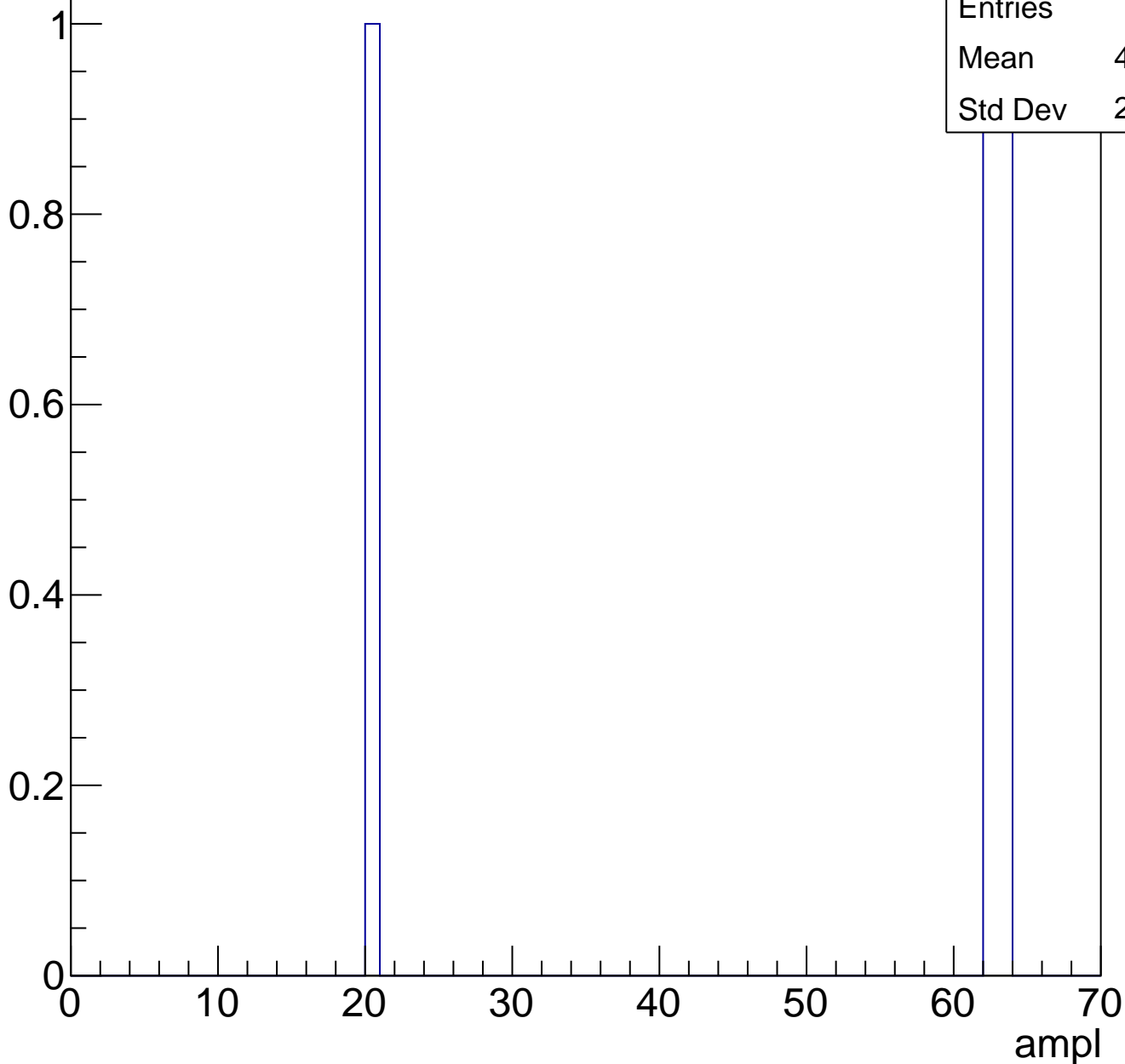




# B1L002S, U17-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch73, adc0

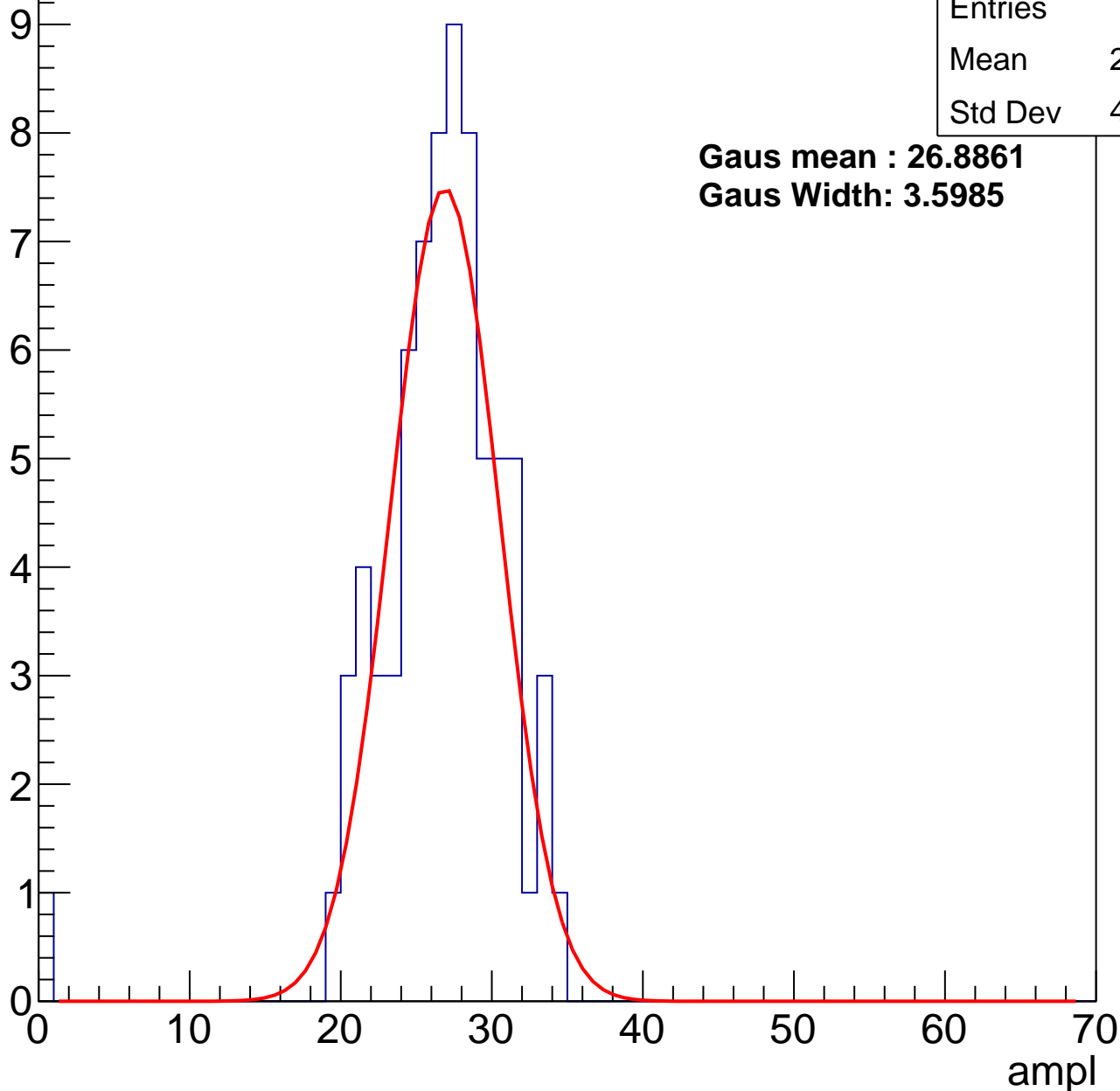
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	26.12
Std Dev	4.646

**Gaus mean : 26.8861**

**Gaus Width: 3.5985**



# B1L002S, U17-ch73, adc1

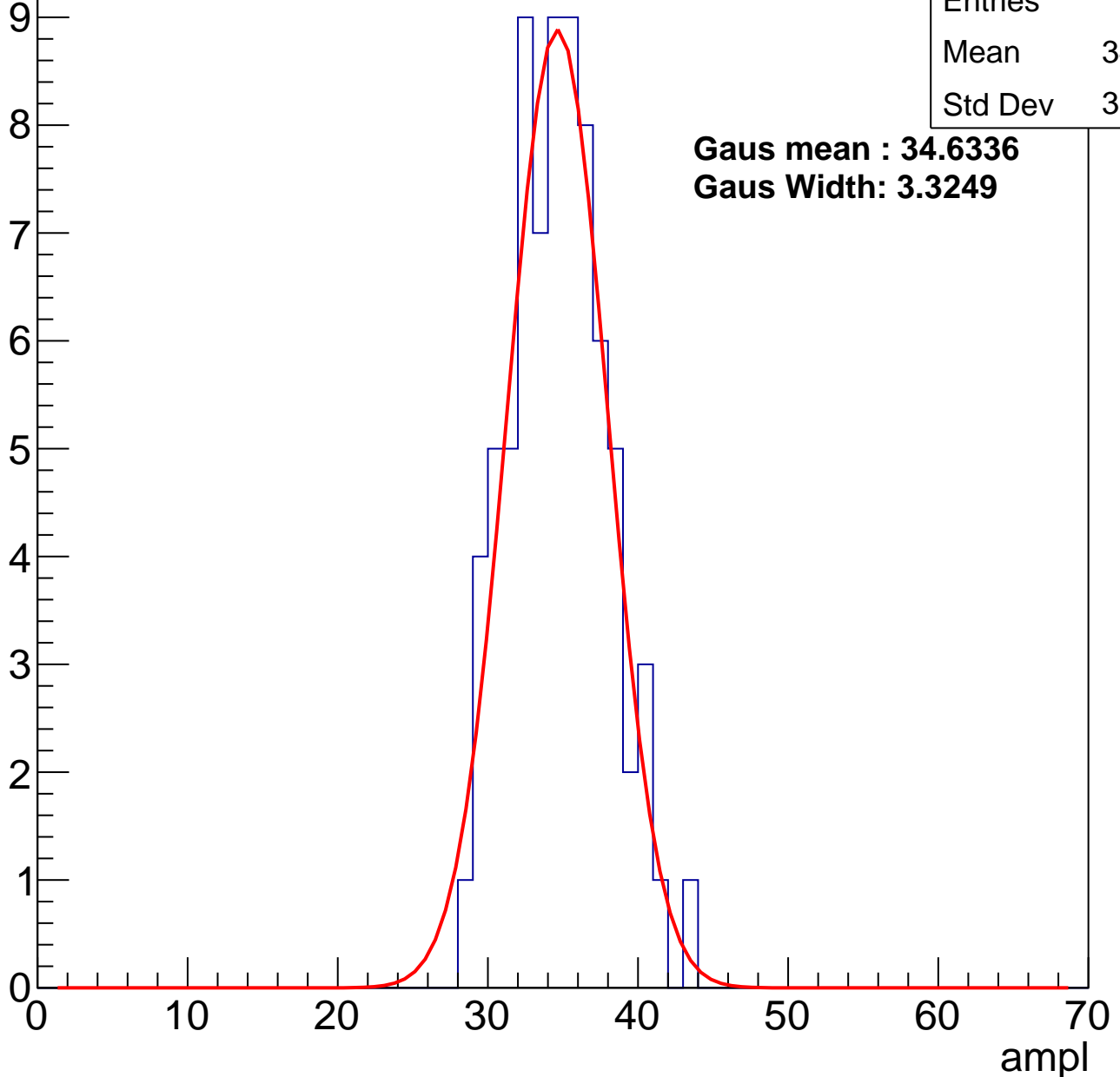
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	34.28
Std Dev	3.186

**Gaus mean : 34.6336**

**Gaus Width: 3.3249**



# B1L002S, U17-ch73, adc2

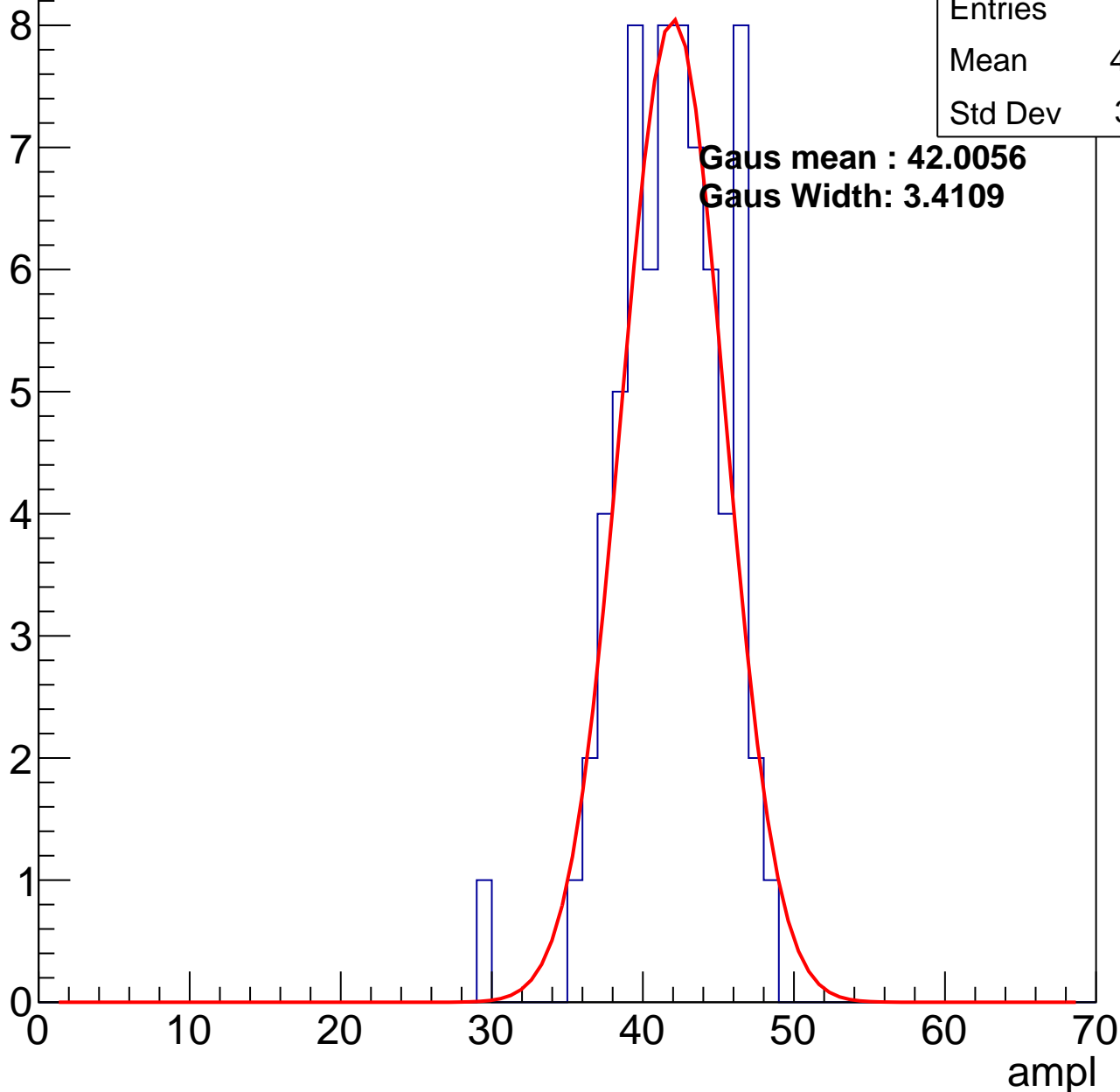
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	41.48
Std Dev	3.431

**Gaus mean : 42.0056**

**Gaus Width: 3.4109**

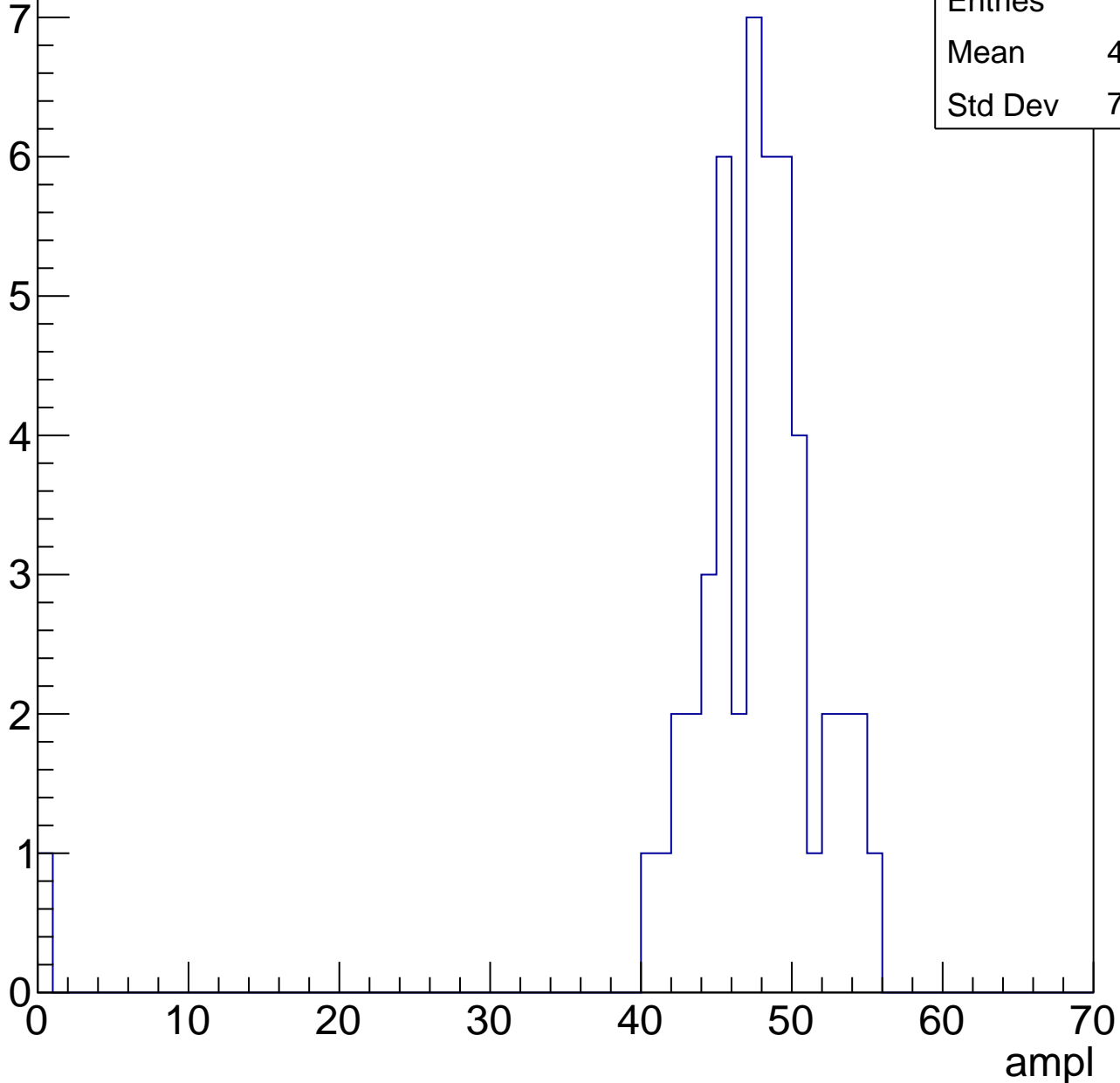


# B1L002S, U17-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	46.53
Std Dev	7.538

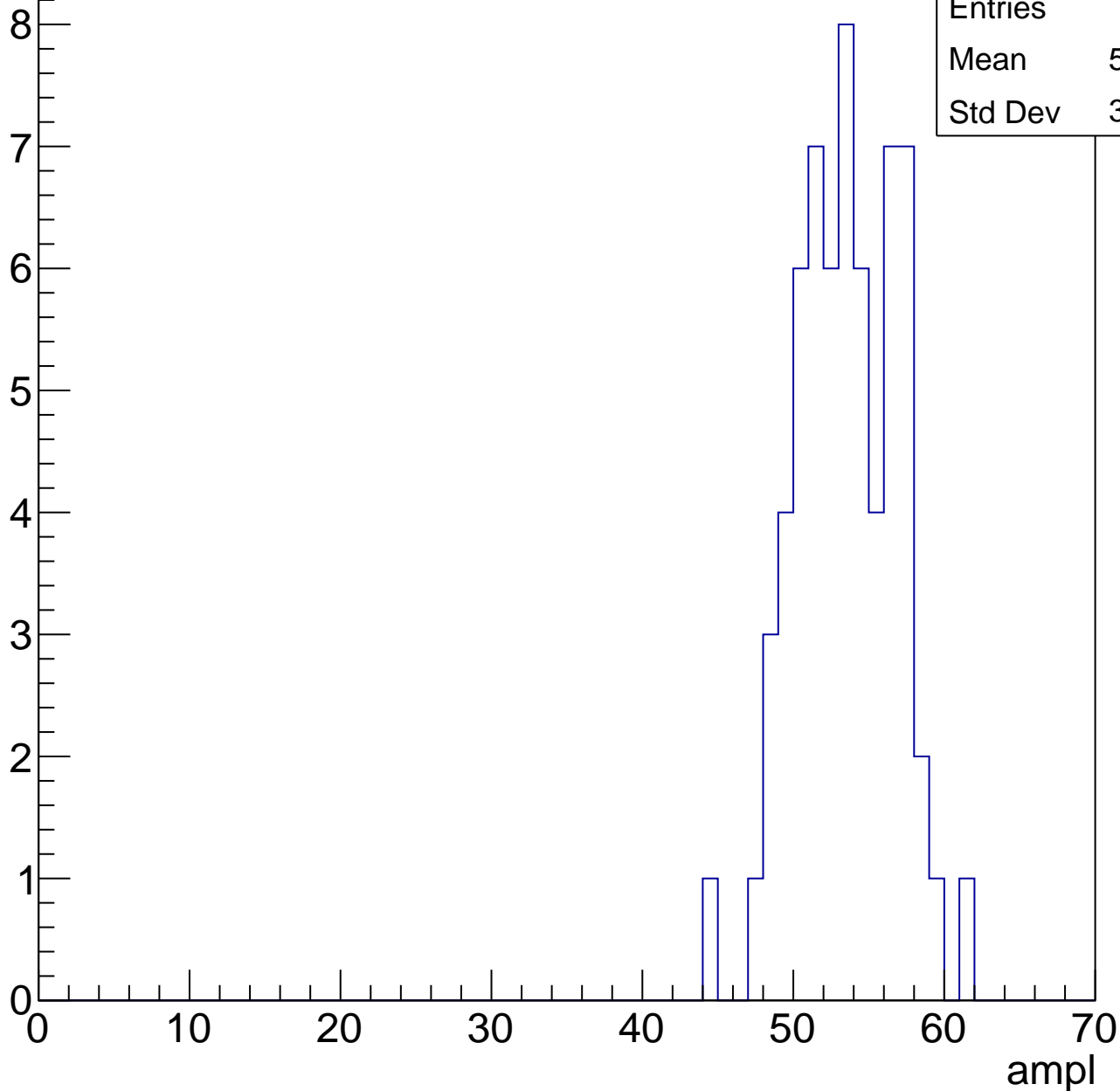


# B1L002S, U17-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

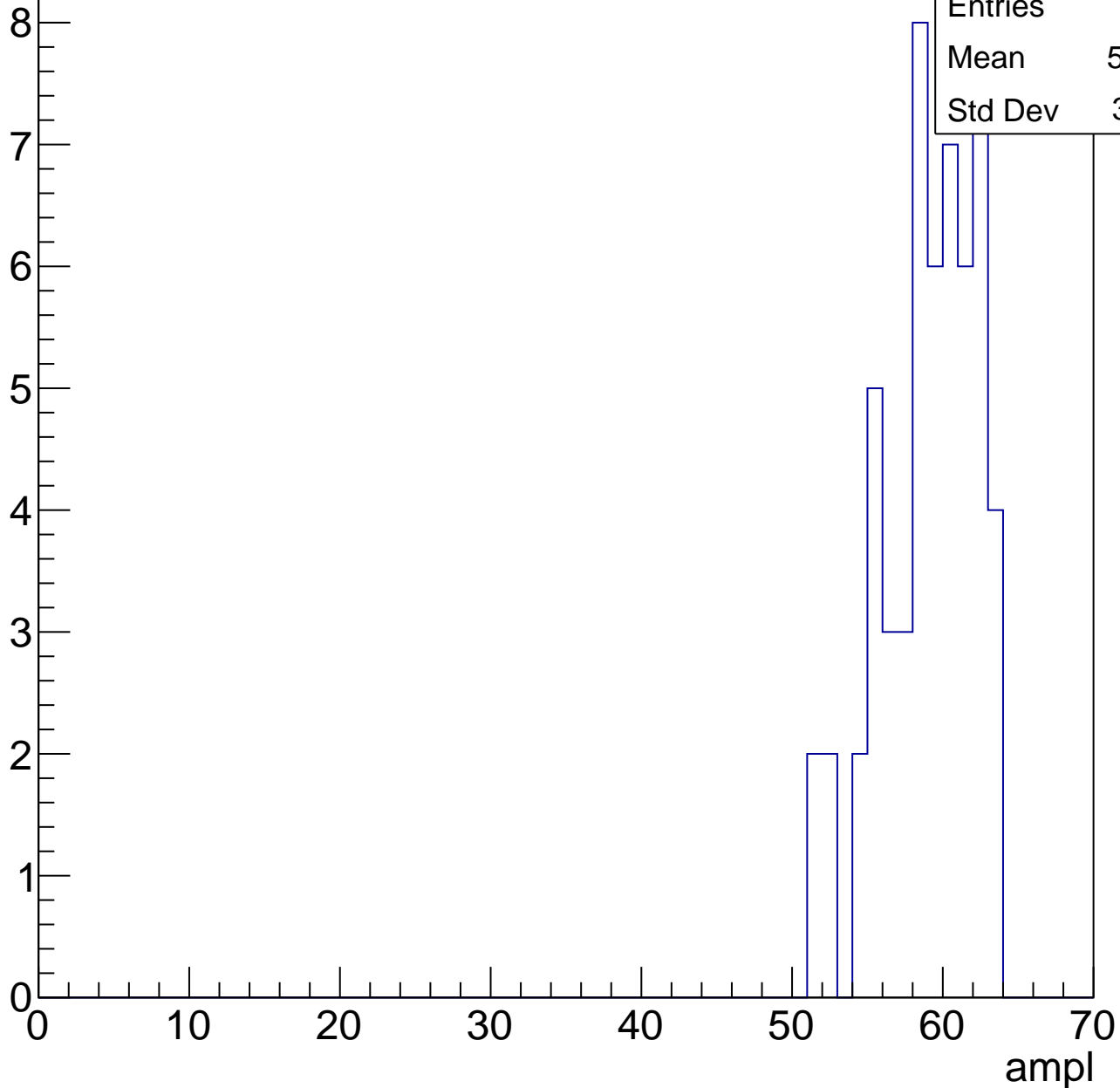
Entries	64
Mean	53.05
Std Dev	3.286



# B1L002S, U17-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

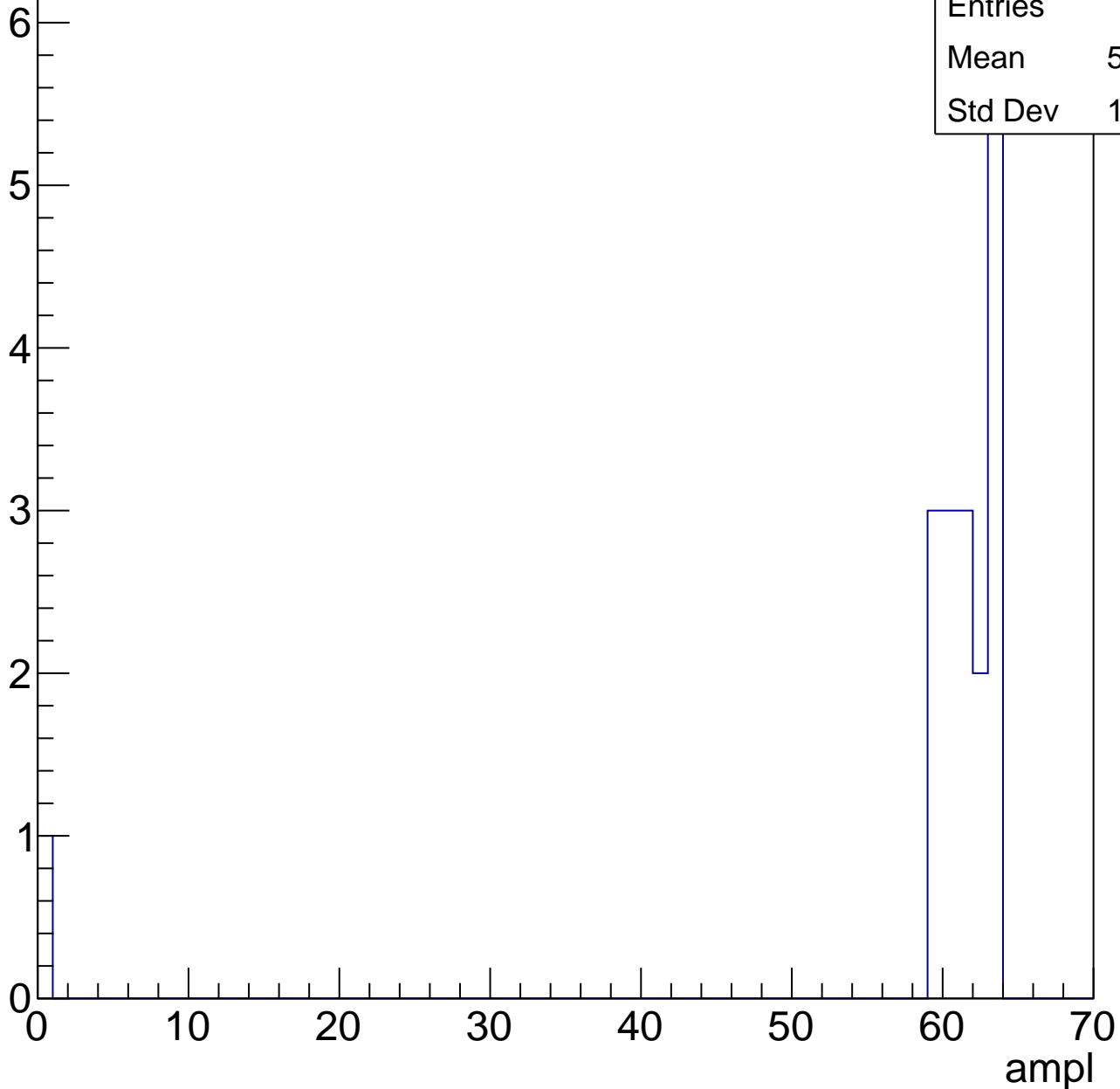


# B1L002S, U17-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	57.89
Std Dev	14.12





# B1L002S, U17-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U17-ch74, adc0

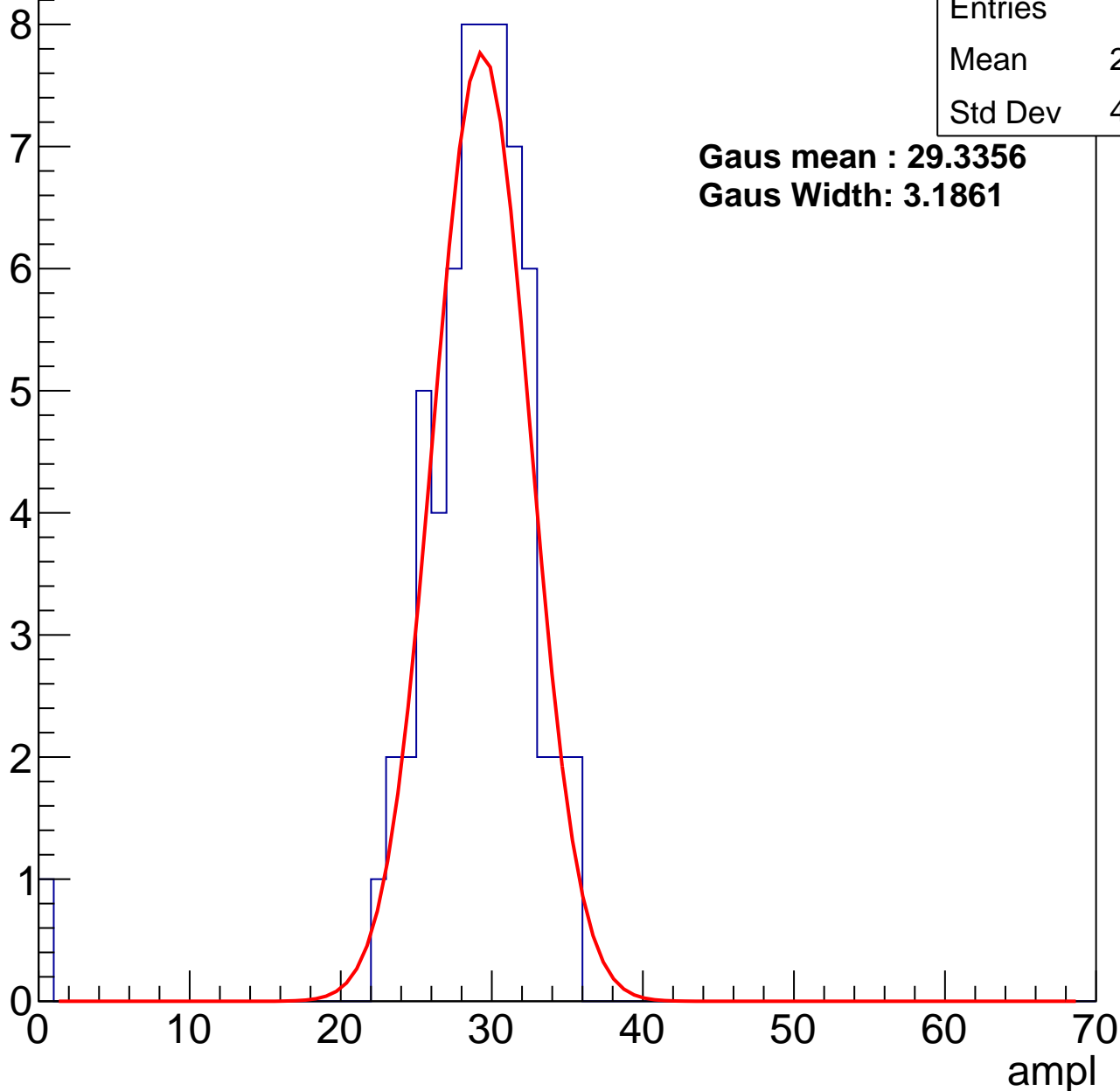
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.38
Std Dev	4.642

**Gaus mean : 29.3356**

**Gaus Width: 3.1861**



# B1L002S, U17-ch74, adc1

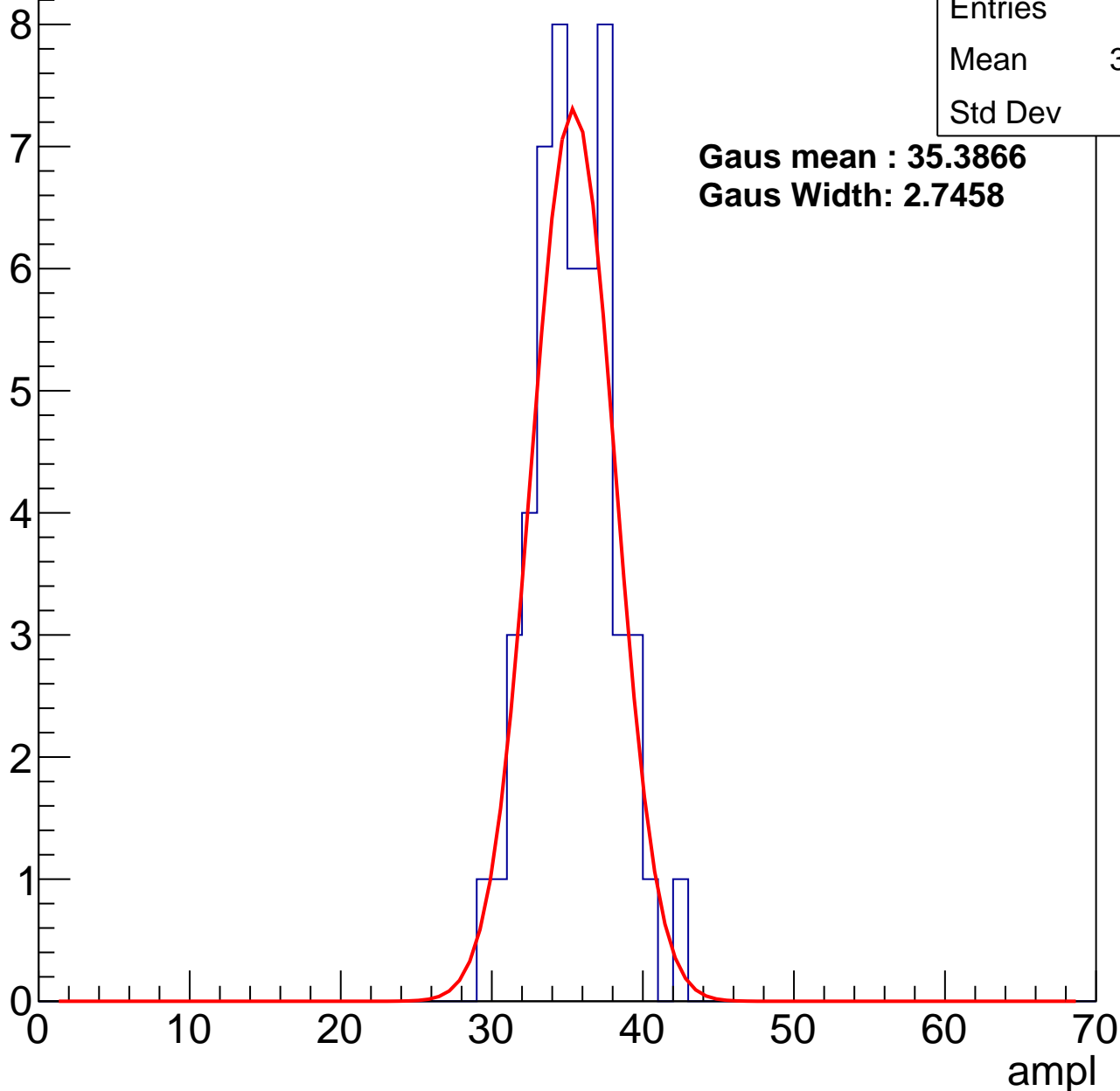
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	34.96
Std Dev	2.66

**Gaus mean : 35.3866**

**Gaus Width: 2.7458**



# B1L002S, U17-ch74, adc2

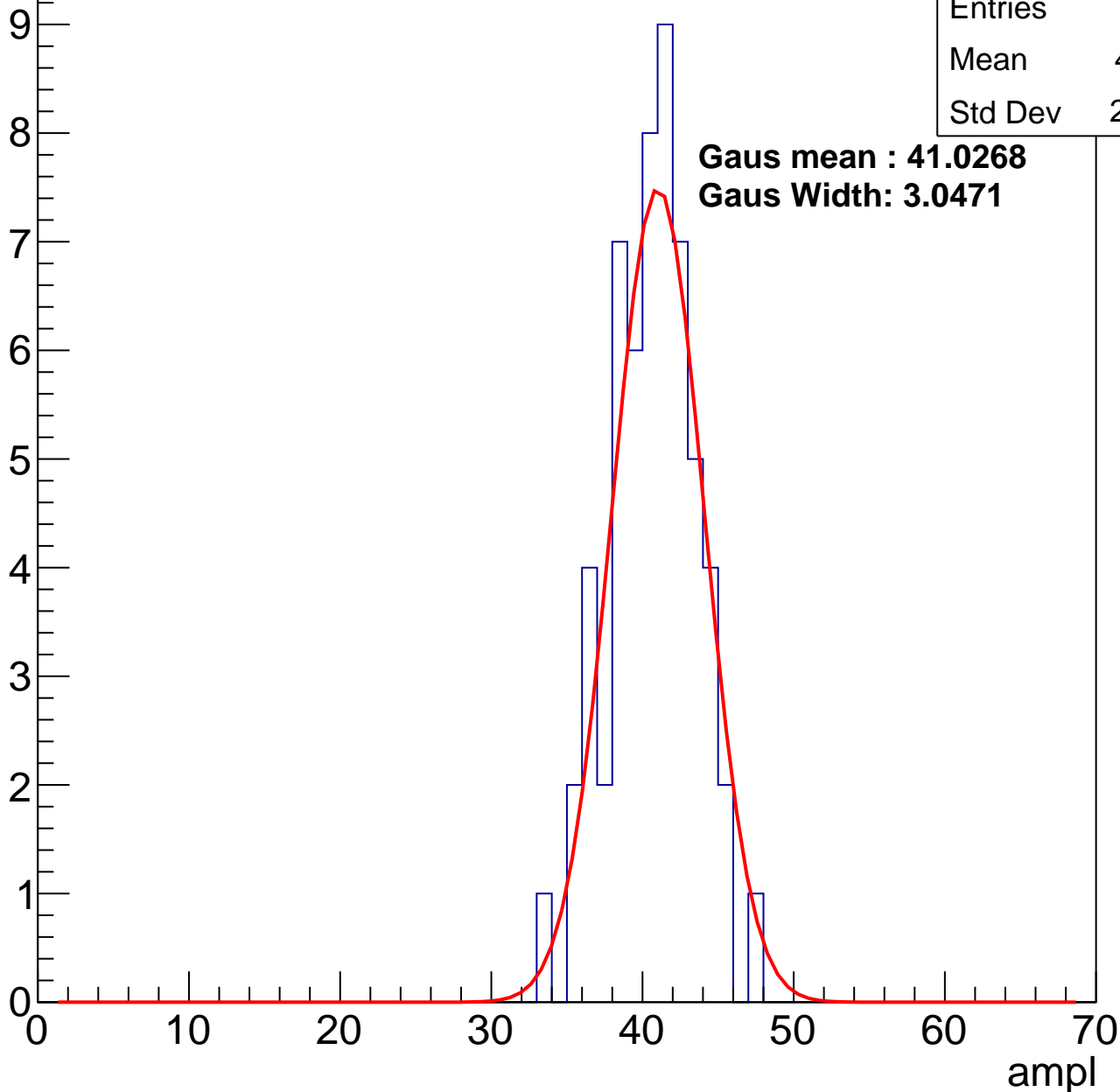
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.21
Std Dev	2.809

**Gaus mean : 41.0268**

**Gaus Width: 3.0471**



# B1L002S, U17-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

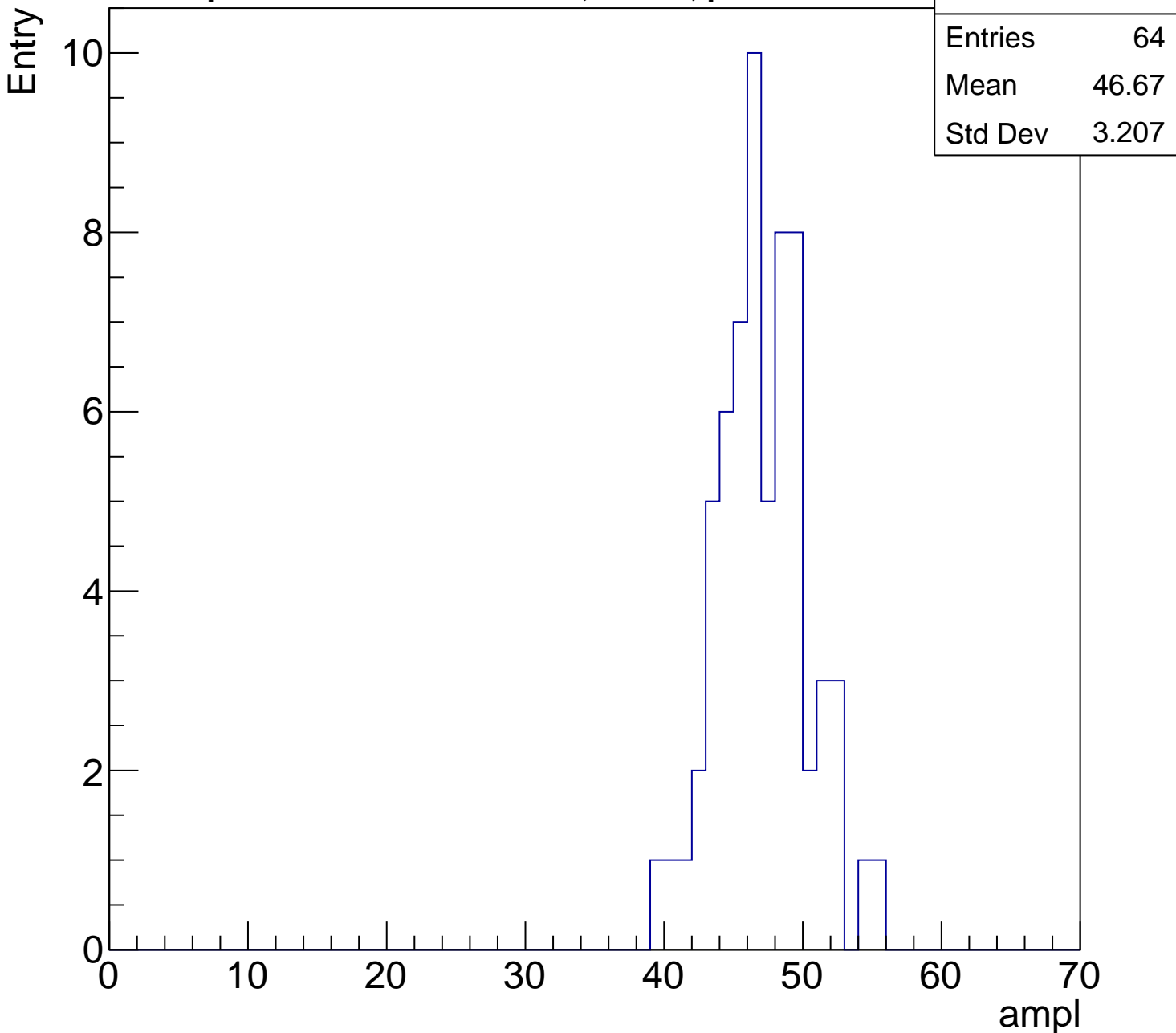
Entries	64
Mean	46.67
Std Dev	3.207

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

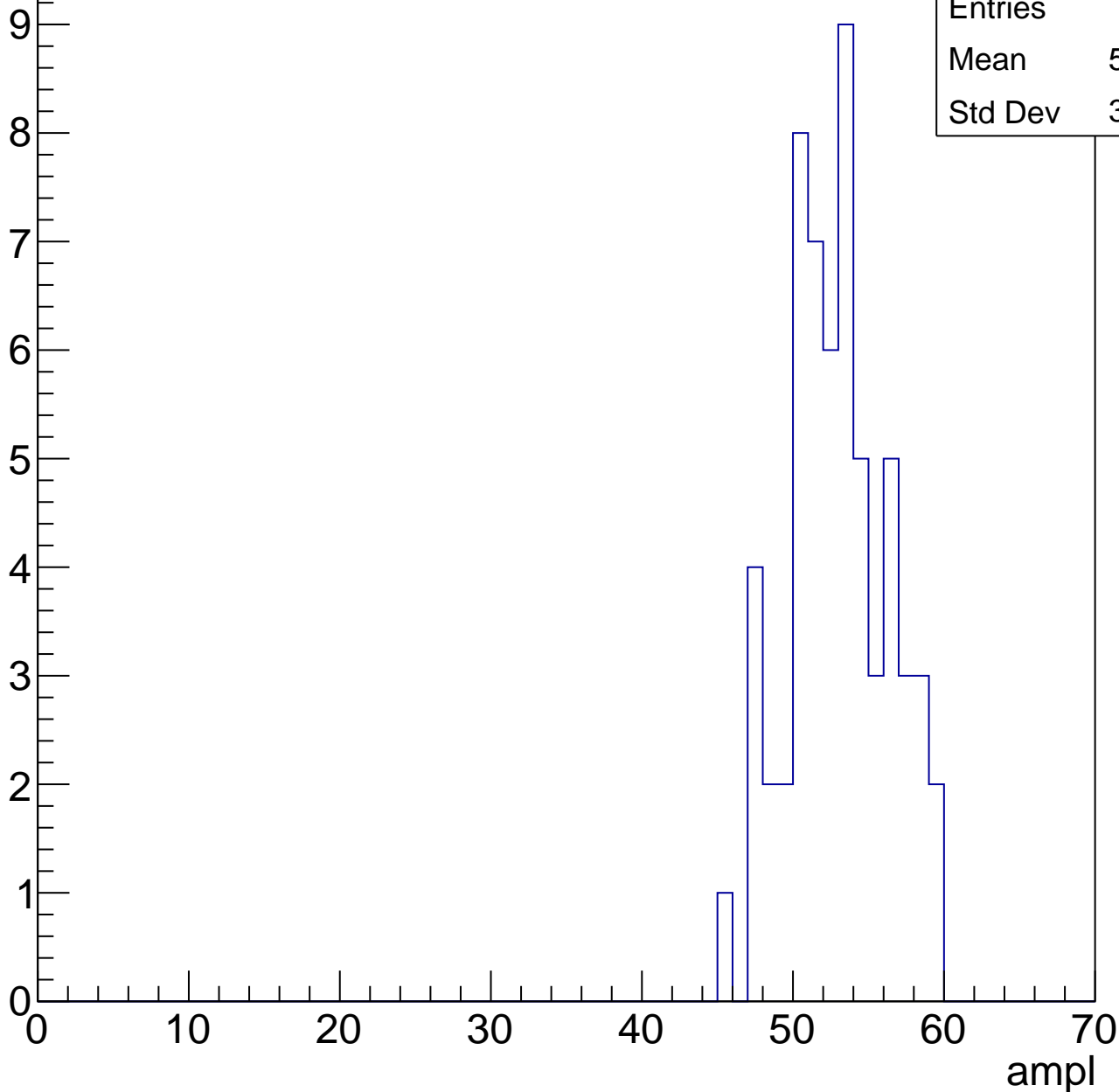


# B1L002S, U17-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.52
Std Dev	3.263

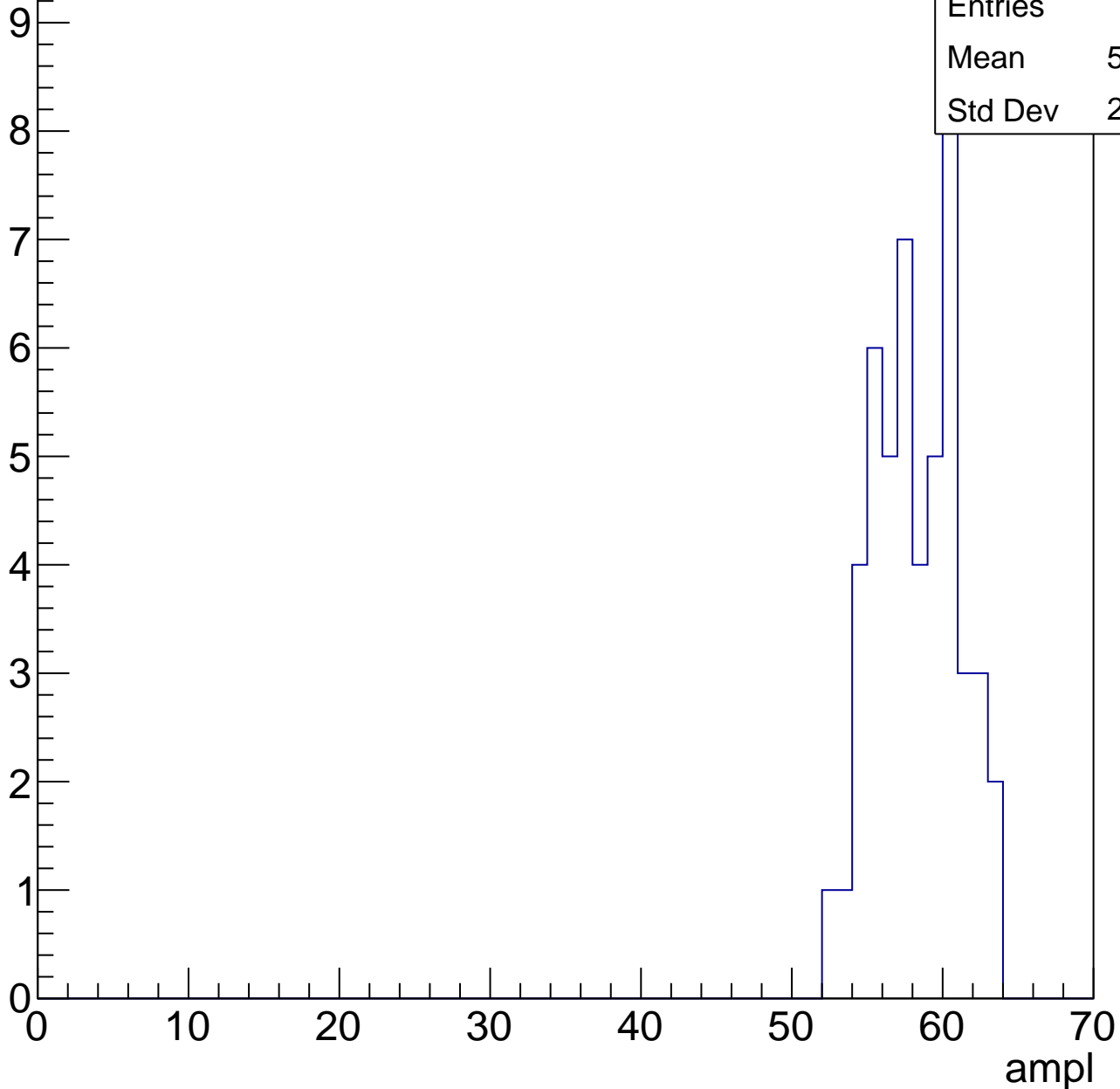


# B1L002S, U17-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	57.84
Std Dev	2.723

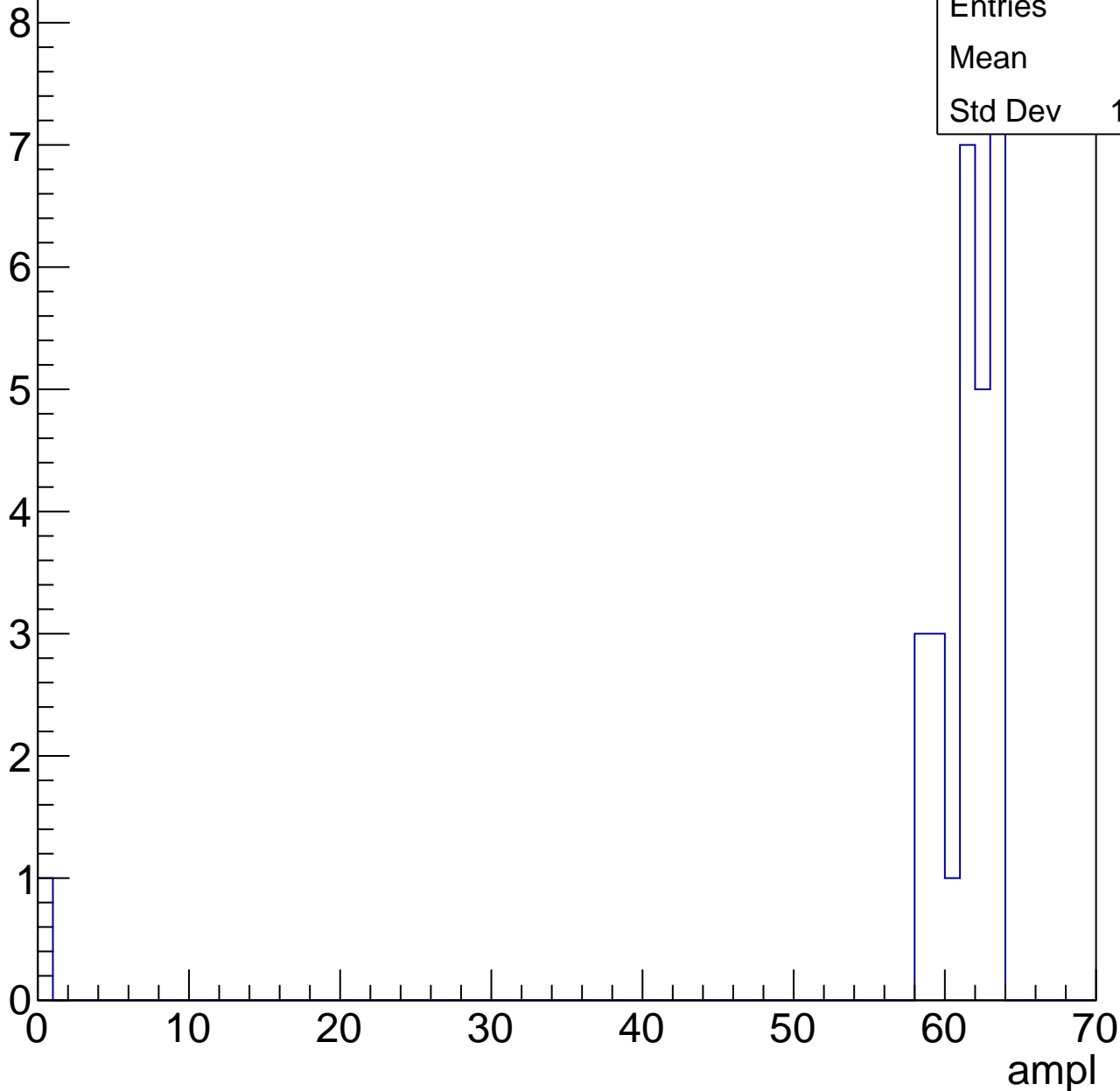


# B1L002S, U17-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59
Std Dev	11.47

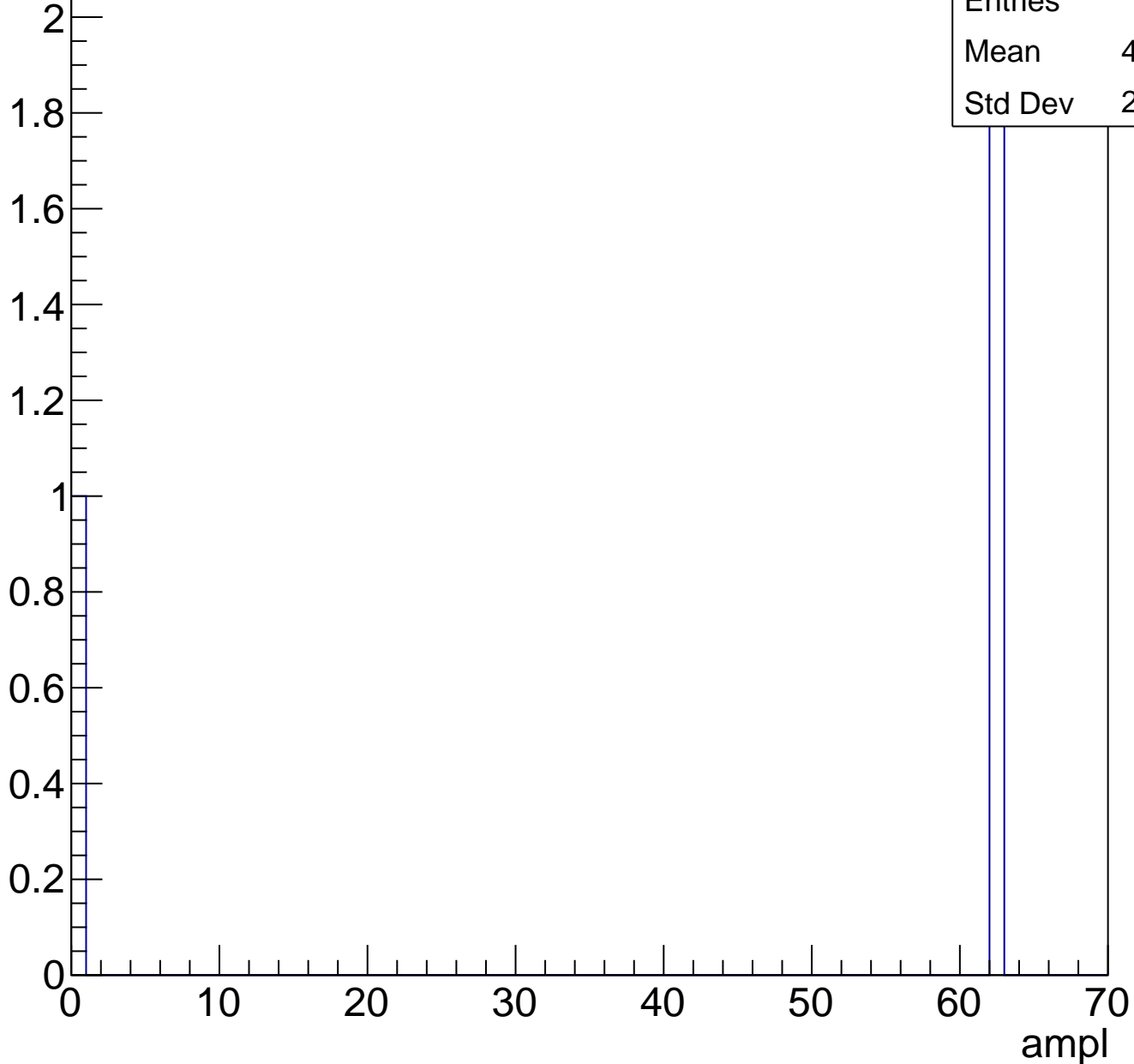




# B1L002S, U17-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch75, adc0

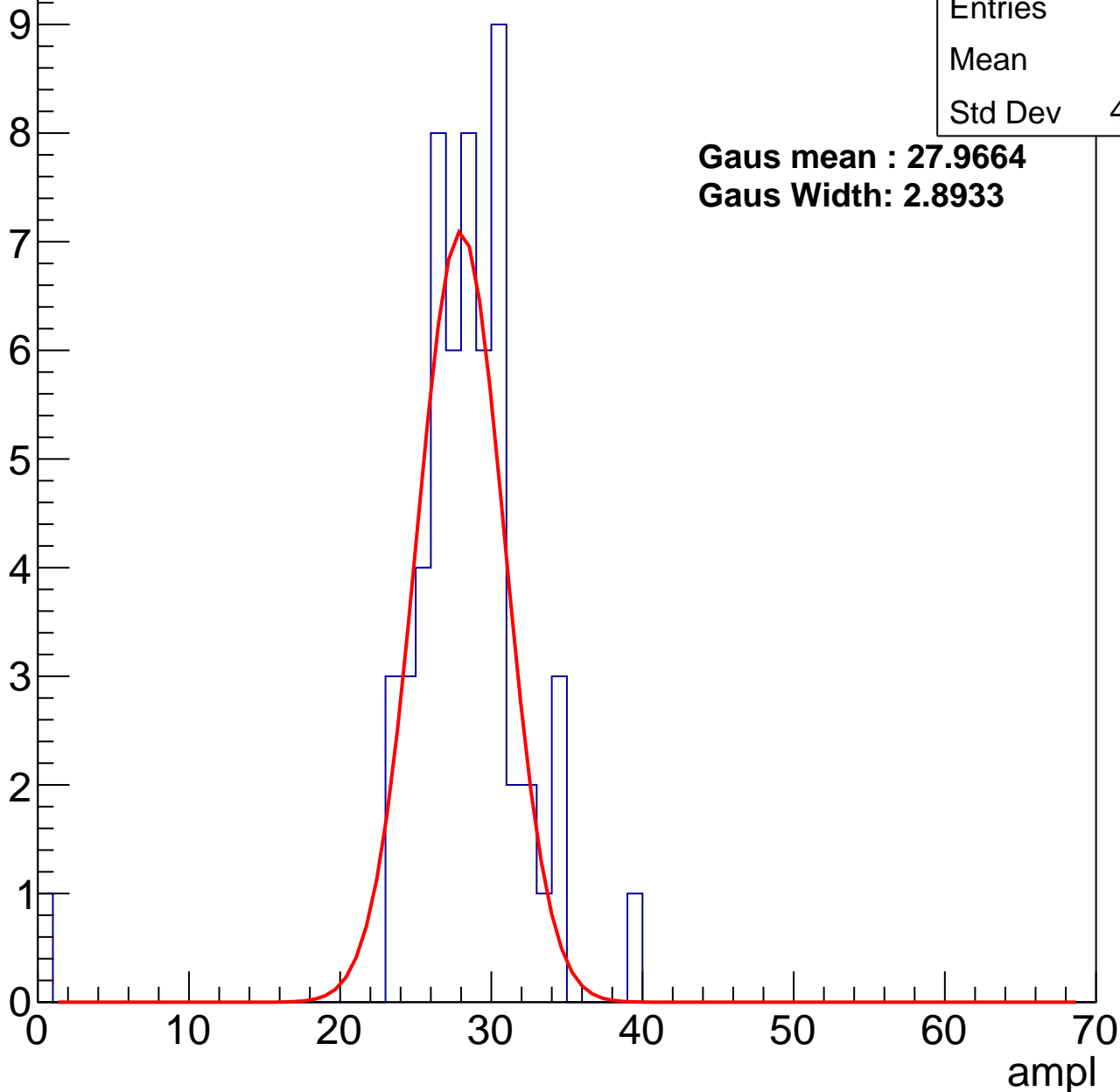
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	27.7
Std Dev	4.816

**Gaus mean : 27.9664**

**Gaus Width: 2.8933**



# B1L002S, U17-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	35.17
Std Dev	3.469

**Gaus mean : 36.0563**

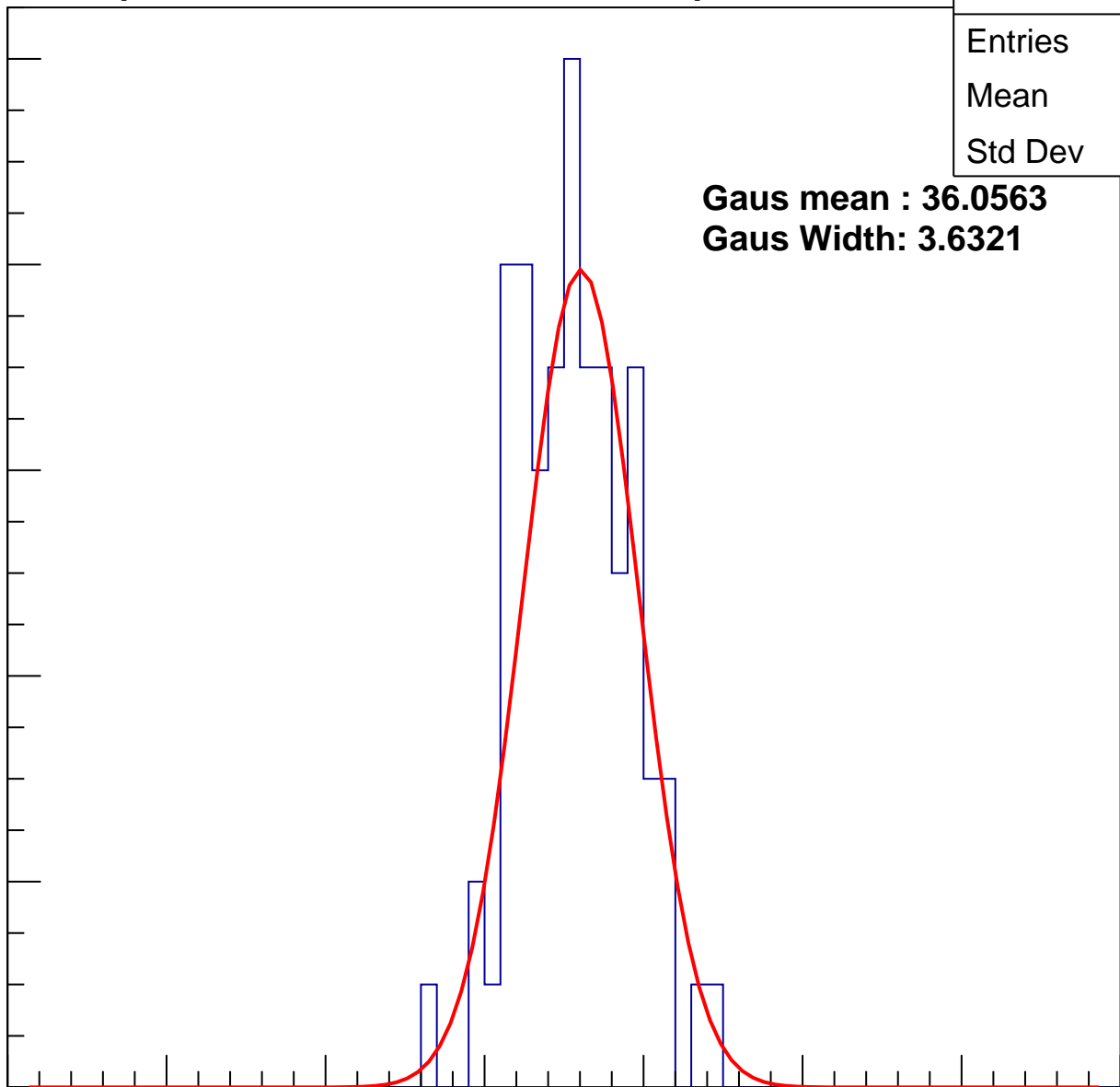
**Gaus Width: 3.6321**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch75, adc2

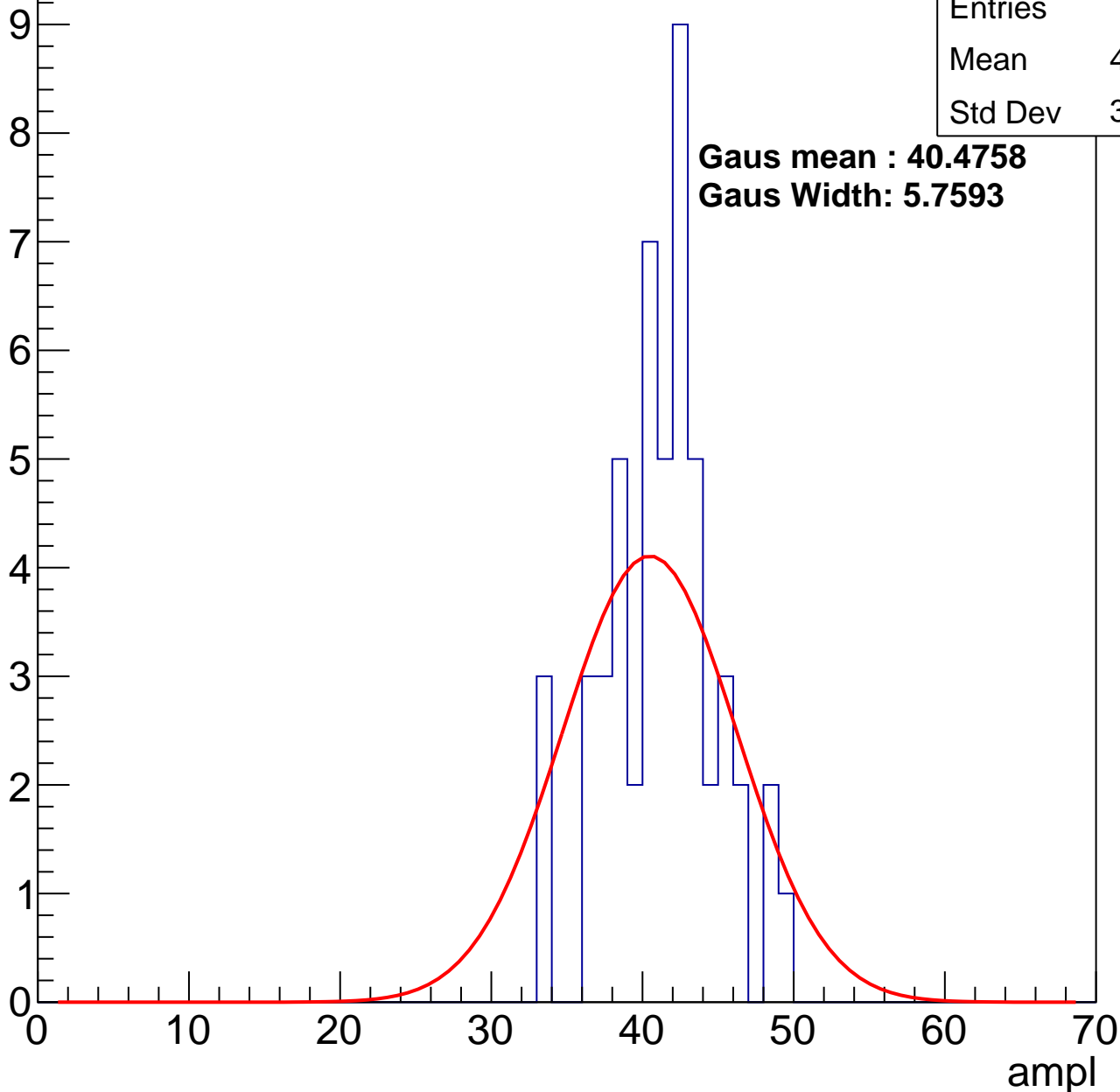
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.85
Std Dev	3.618

**Gaus mean : 40.4758**

**Gaus Width: 5.7593**

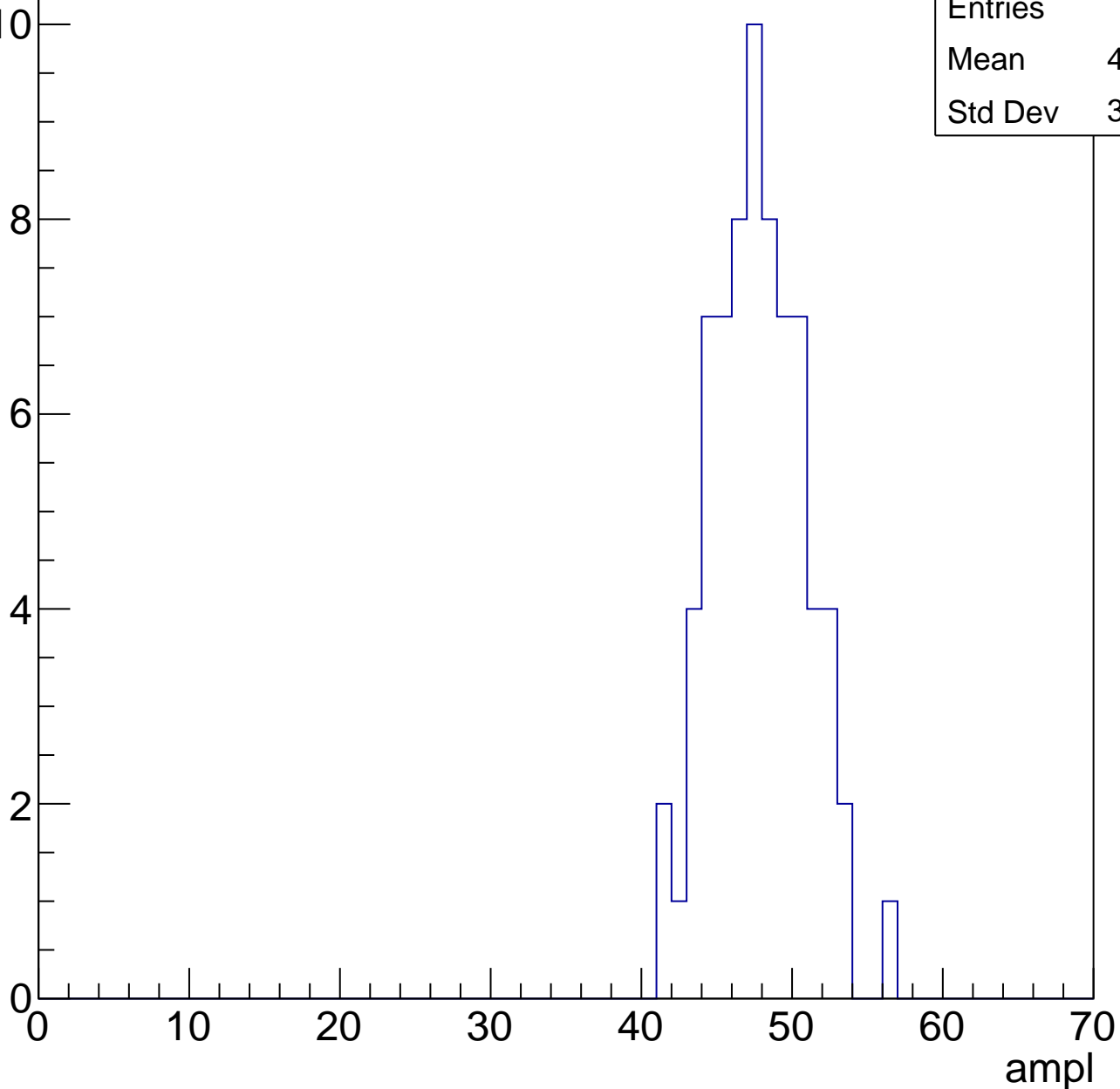


# B1L002S, U17-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

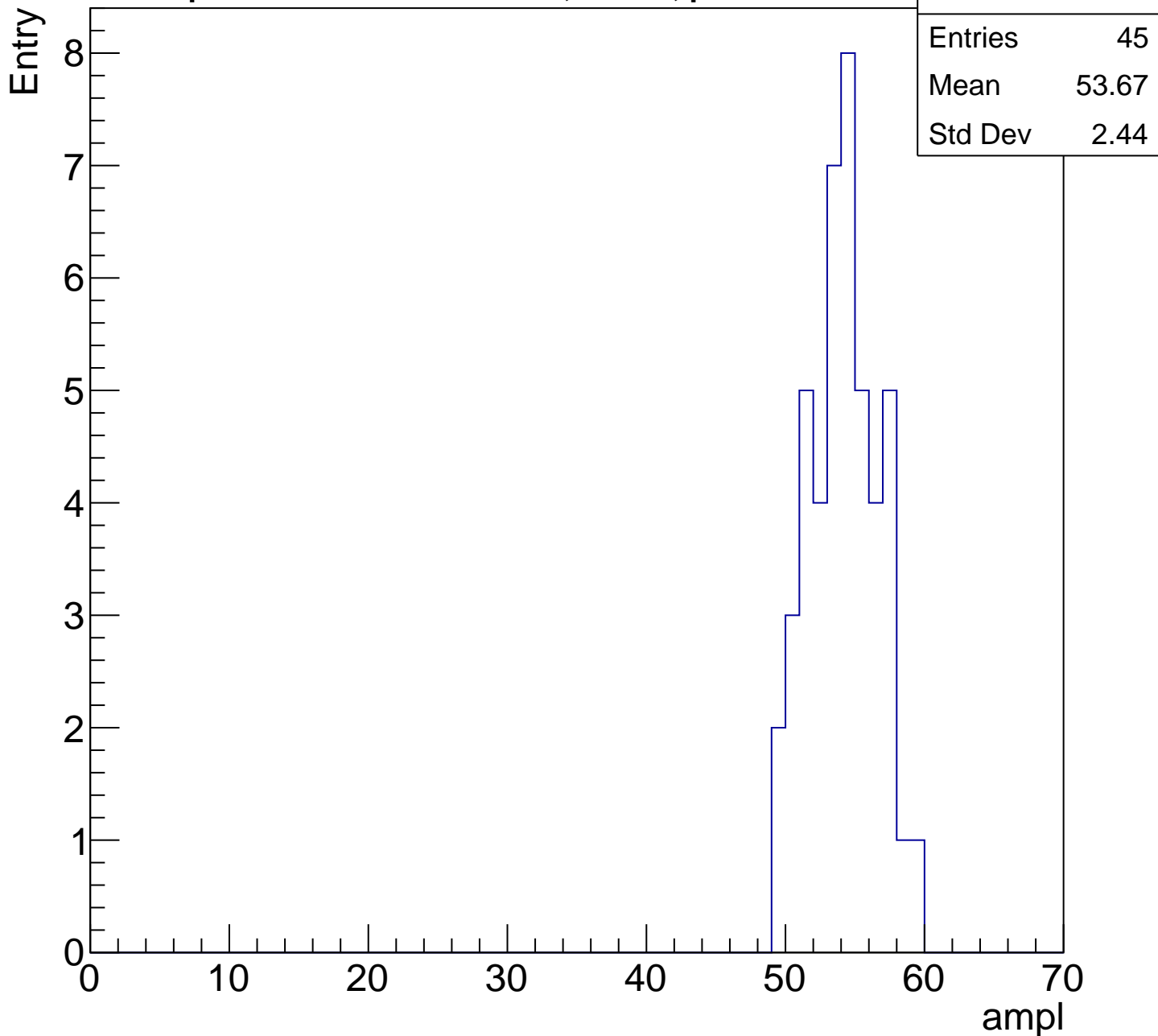
Entry

Entries	72
Mean	47.33
Std Dev	3.046



# B1L002S, U17-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

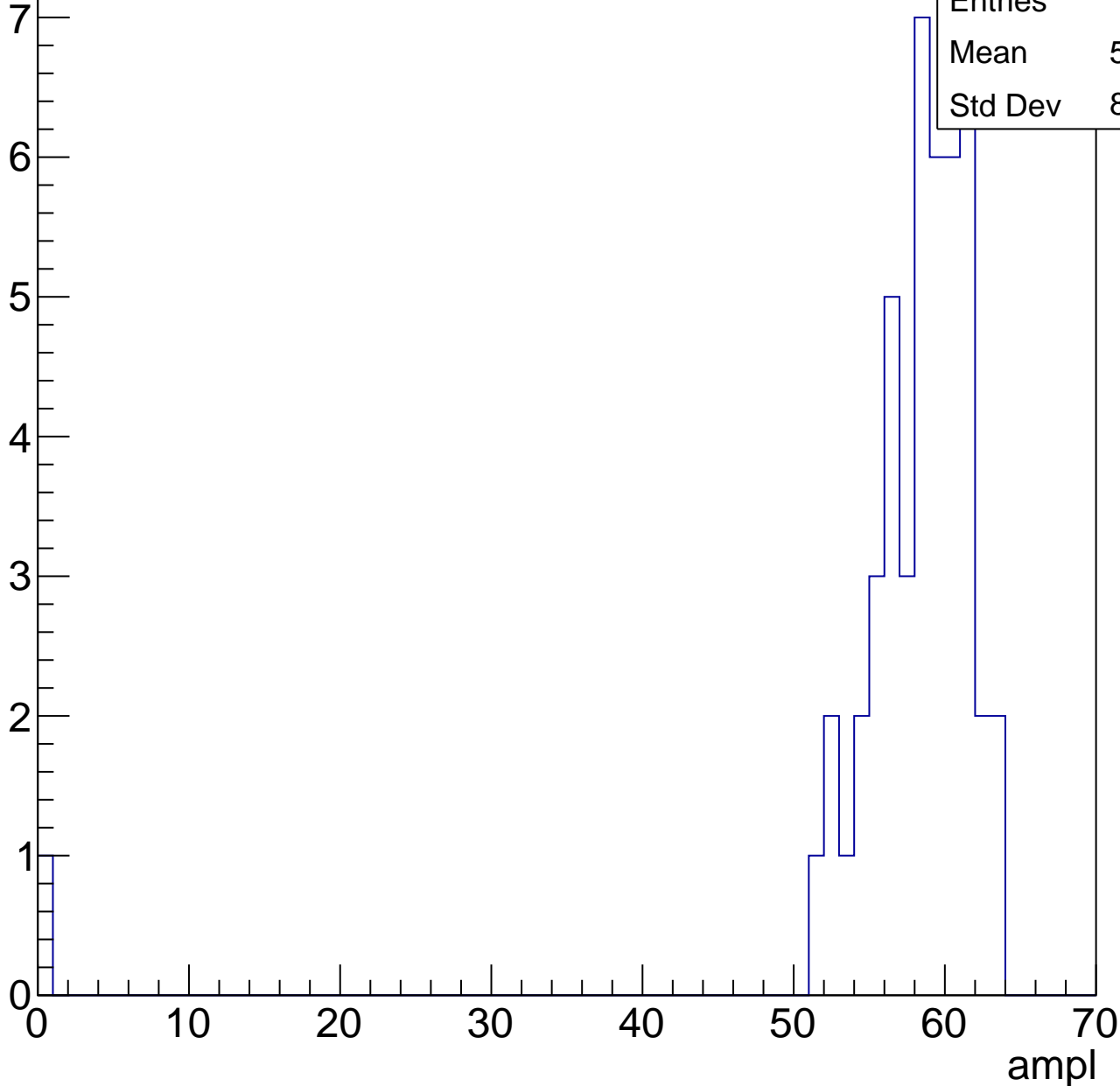


# B1L002S, U17-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	56.85
Std Dev	8.784

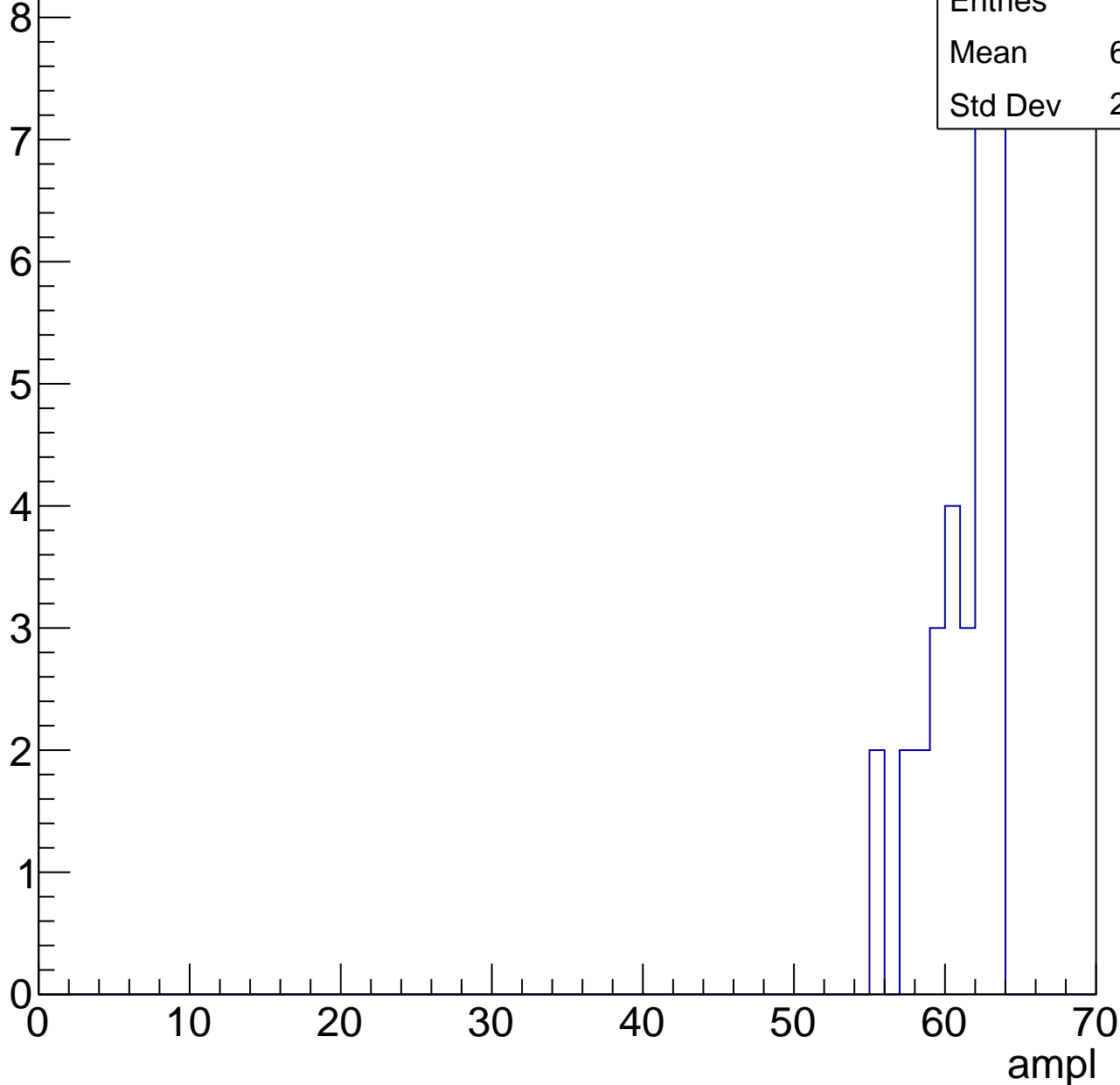


# B1L002S, U17-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	60.62
Std Dev	2.328





# B1L002S, U17-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U17-ch76, adc0

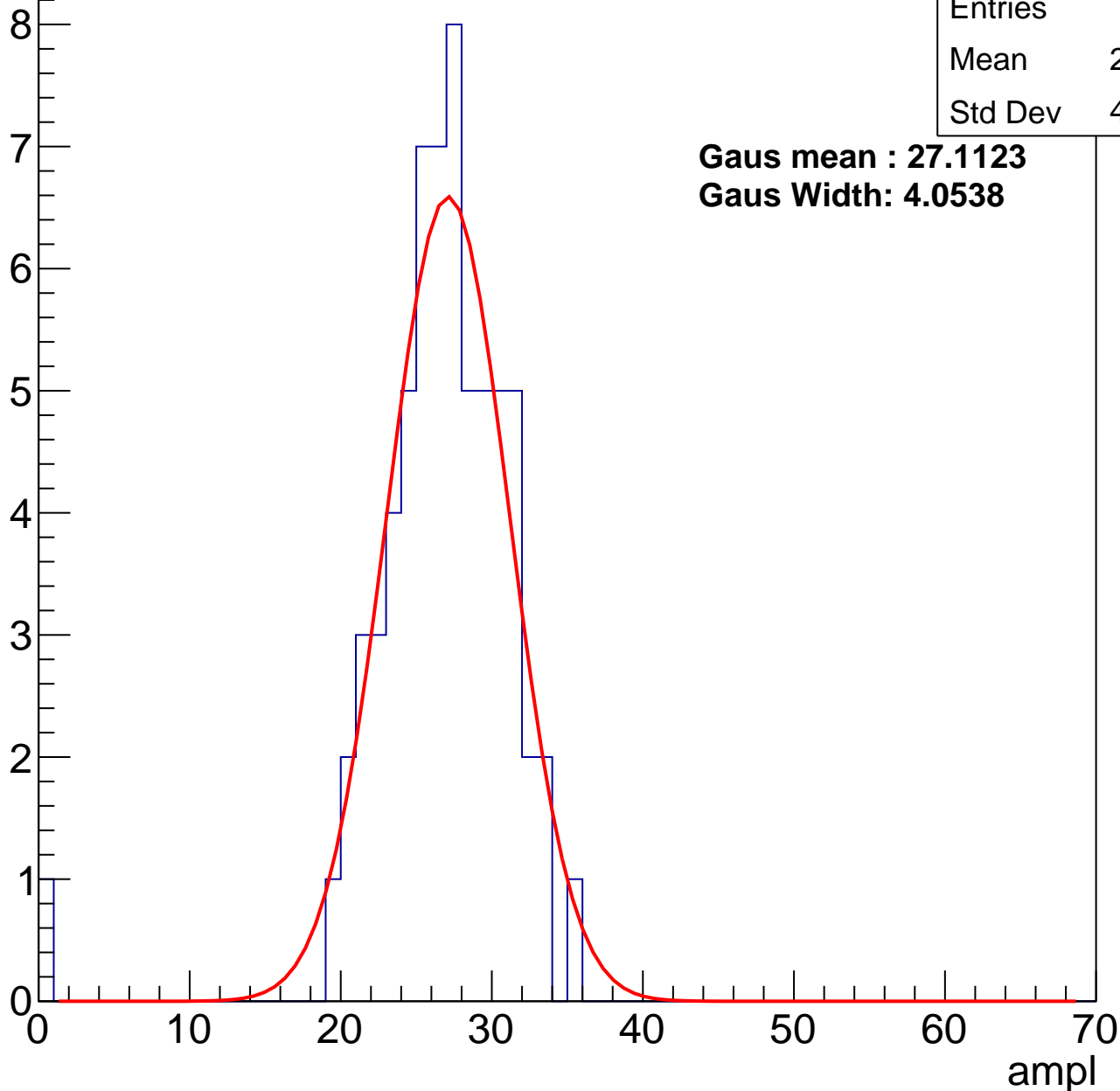
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	26.18
Std Dev	4.783

**Gaus mean : 27.123**

**Gaus Width: 4.0538**



# B1L002S, U17-ch76, adc1

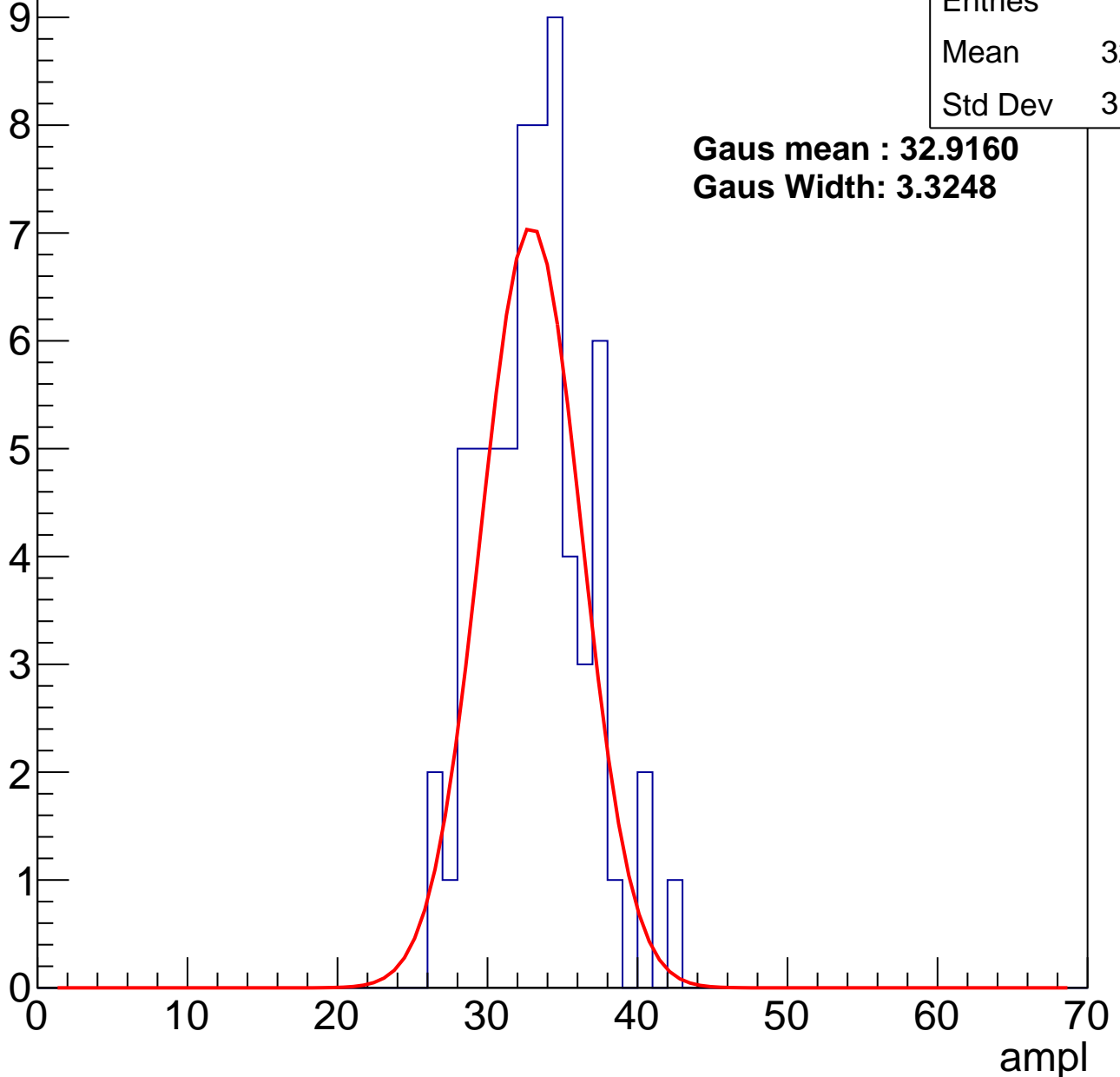
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	32.69
Std Dev	3.415

**Gaus mean : 32.9160**

**Gaus Width: 3.3248**



# B1L002S, U17-ch76, adc2

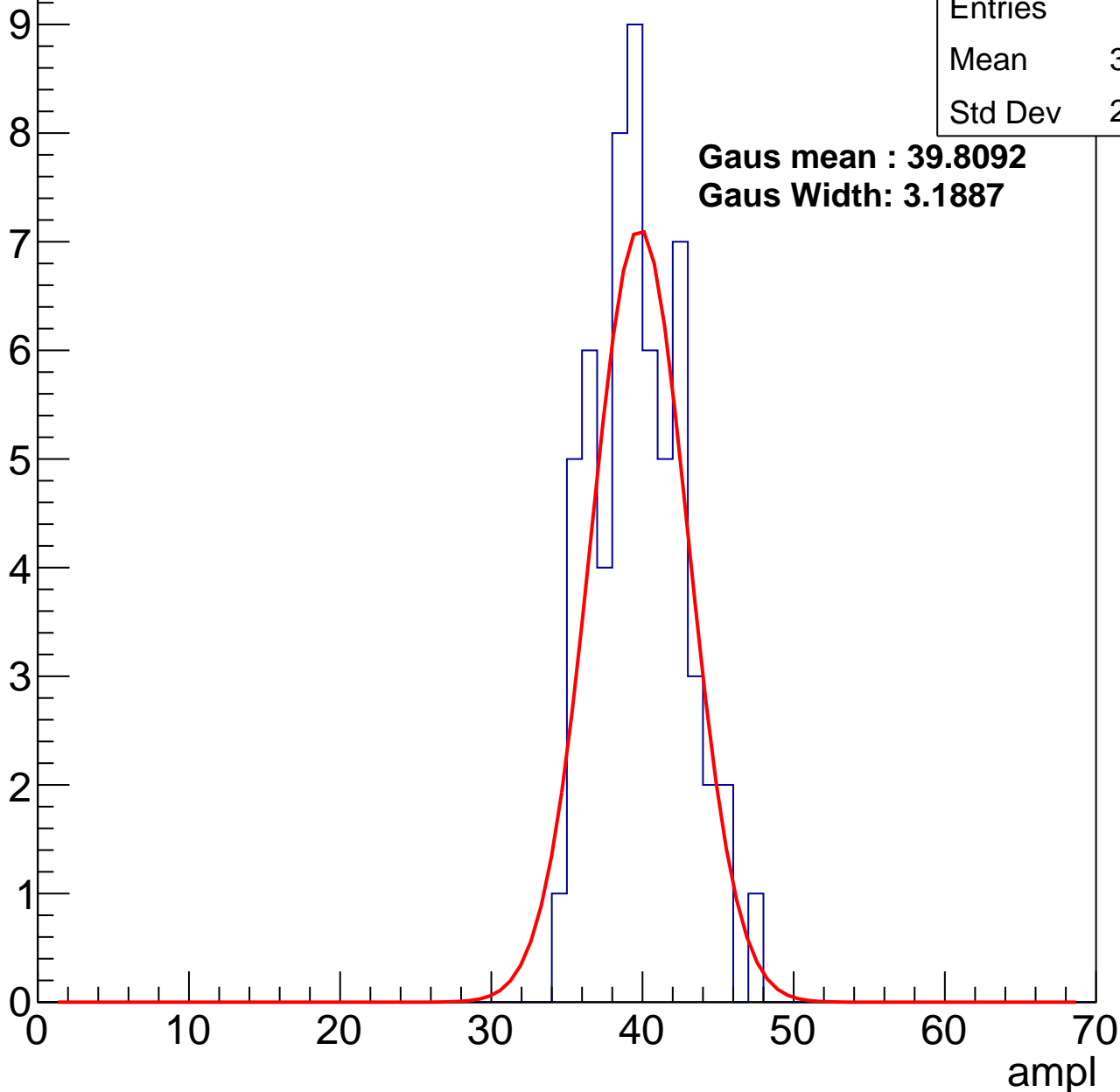
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	39.34
Std Dev	2.909

**Gaus mean : 39.8092**

**Gaus Width: 3.1887**

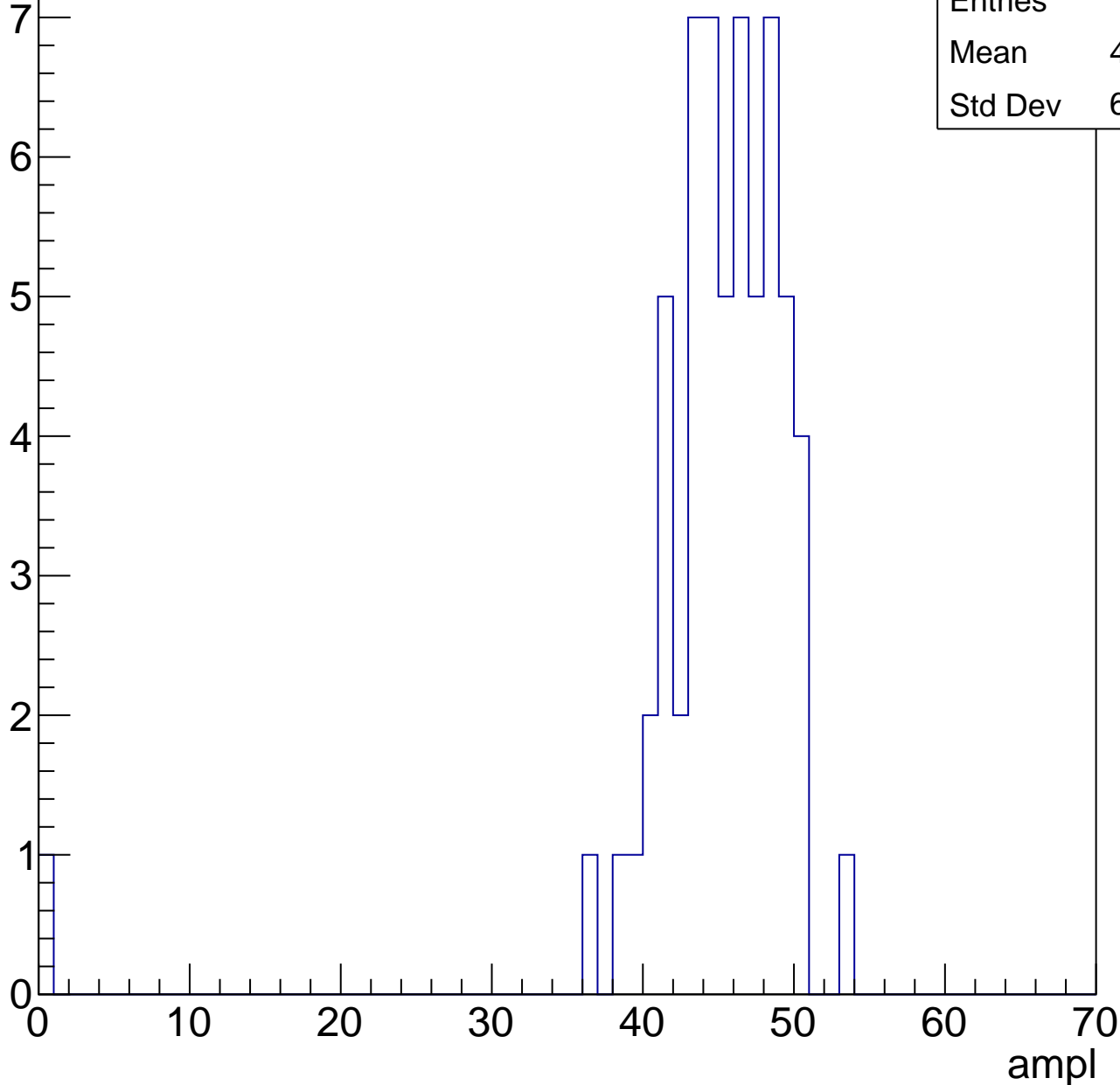


# B1L002S, U17-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

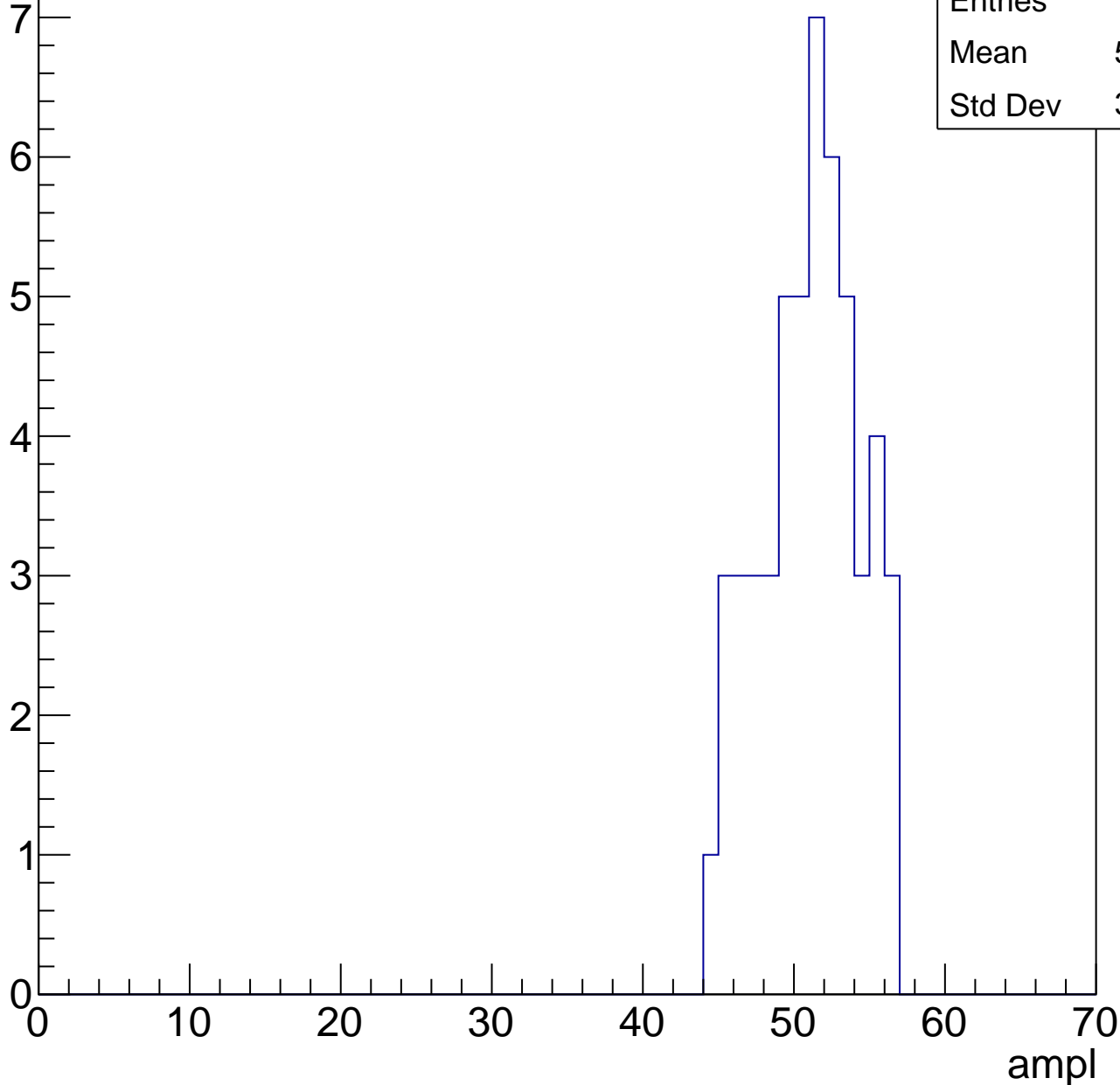
Entries	61
Mean	44.38
Std Dev	6.634



# B1L002S, U17-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

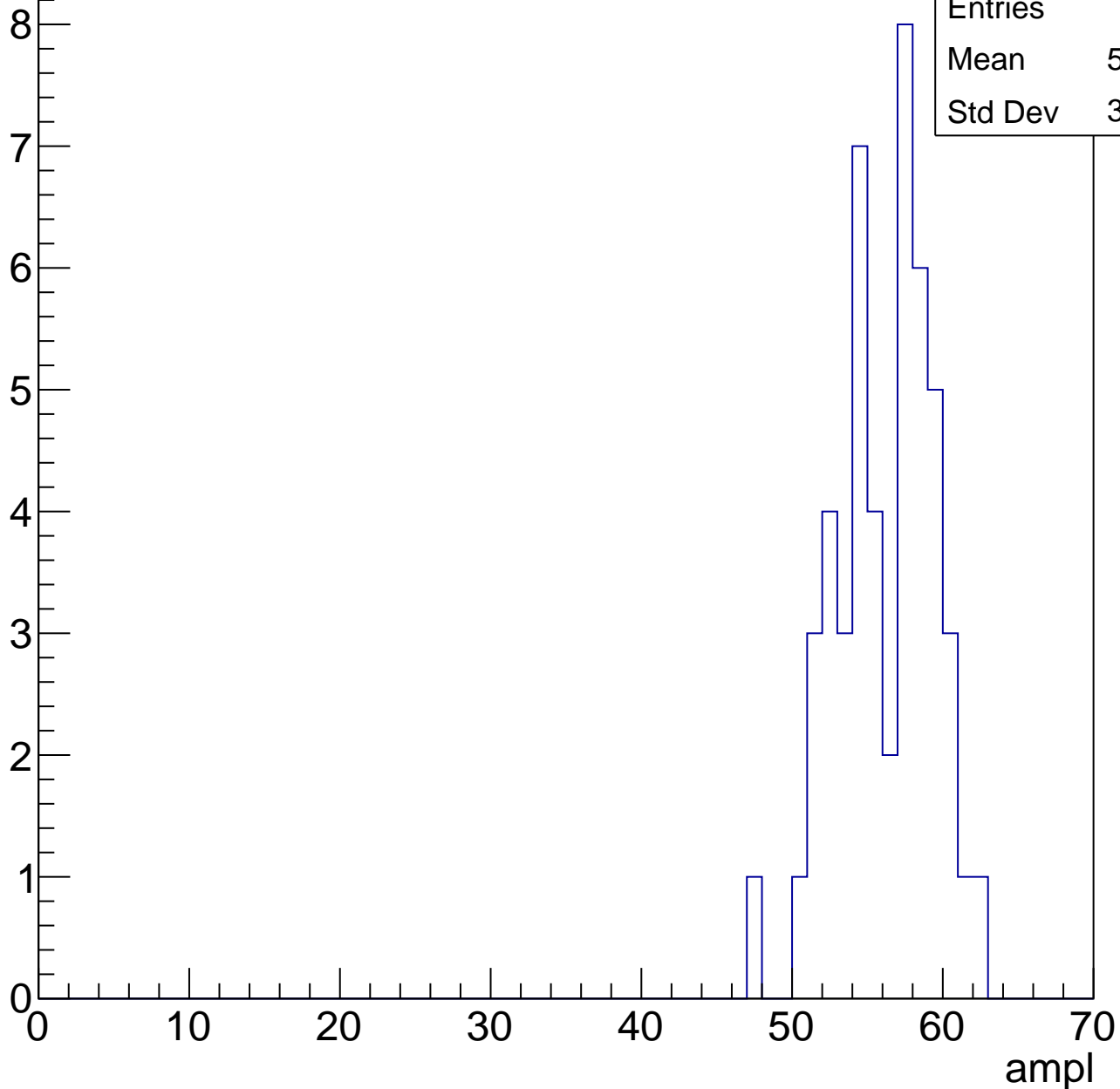


# B1L002S, U17-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	55.69
Std Dev	3.183

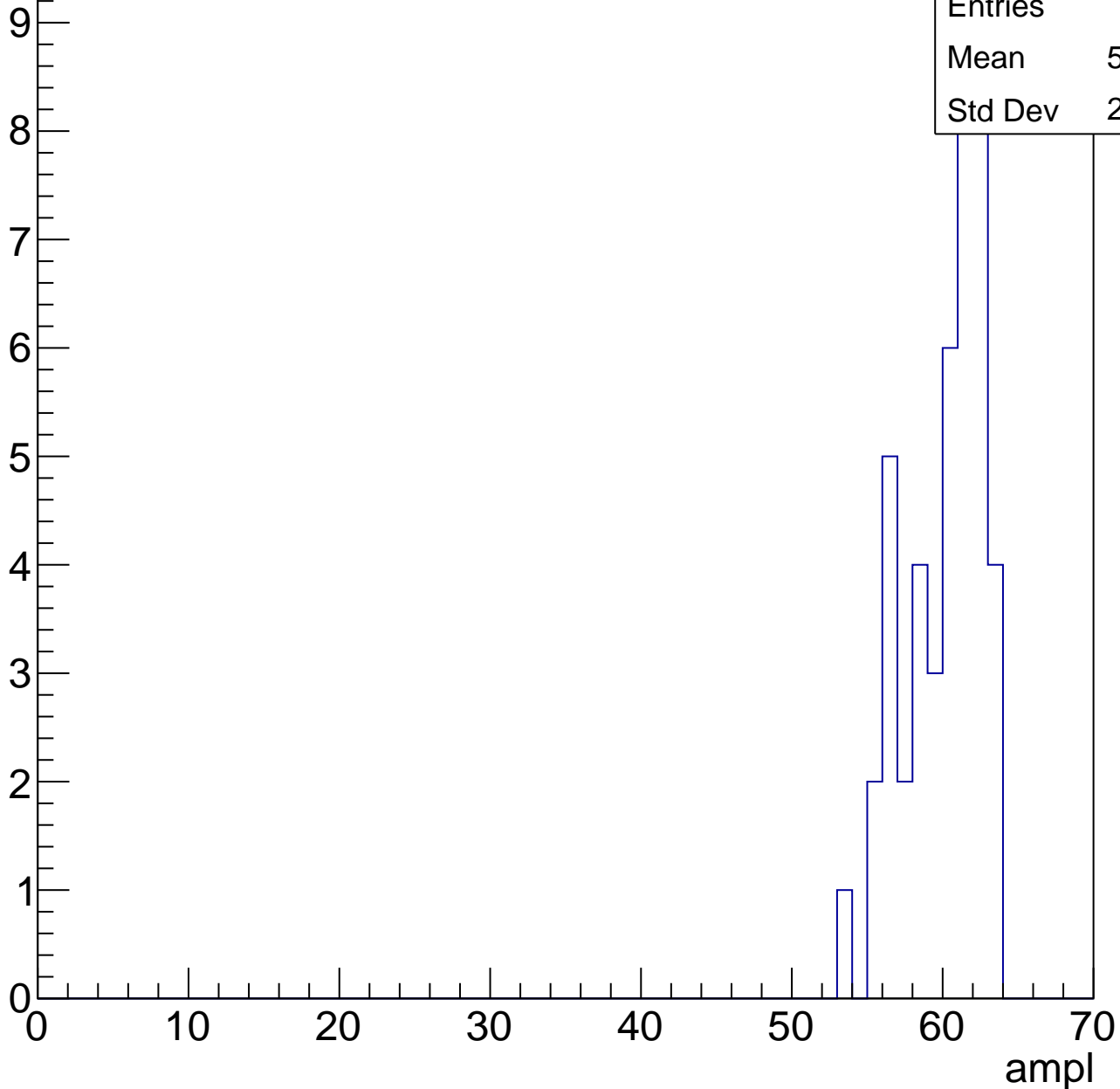


# B1L002S, U17-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	59.64
Std Dev	2.568

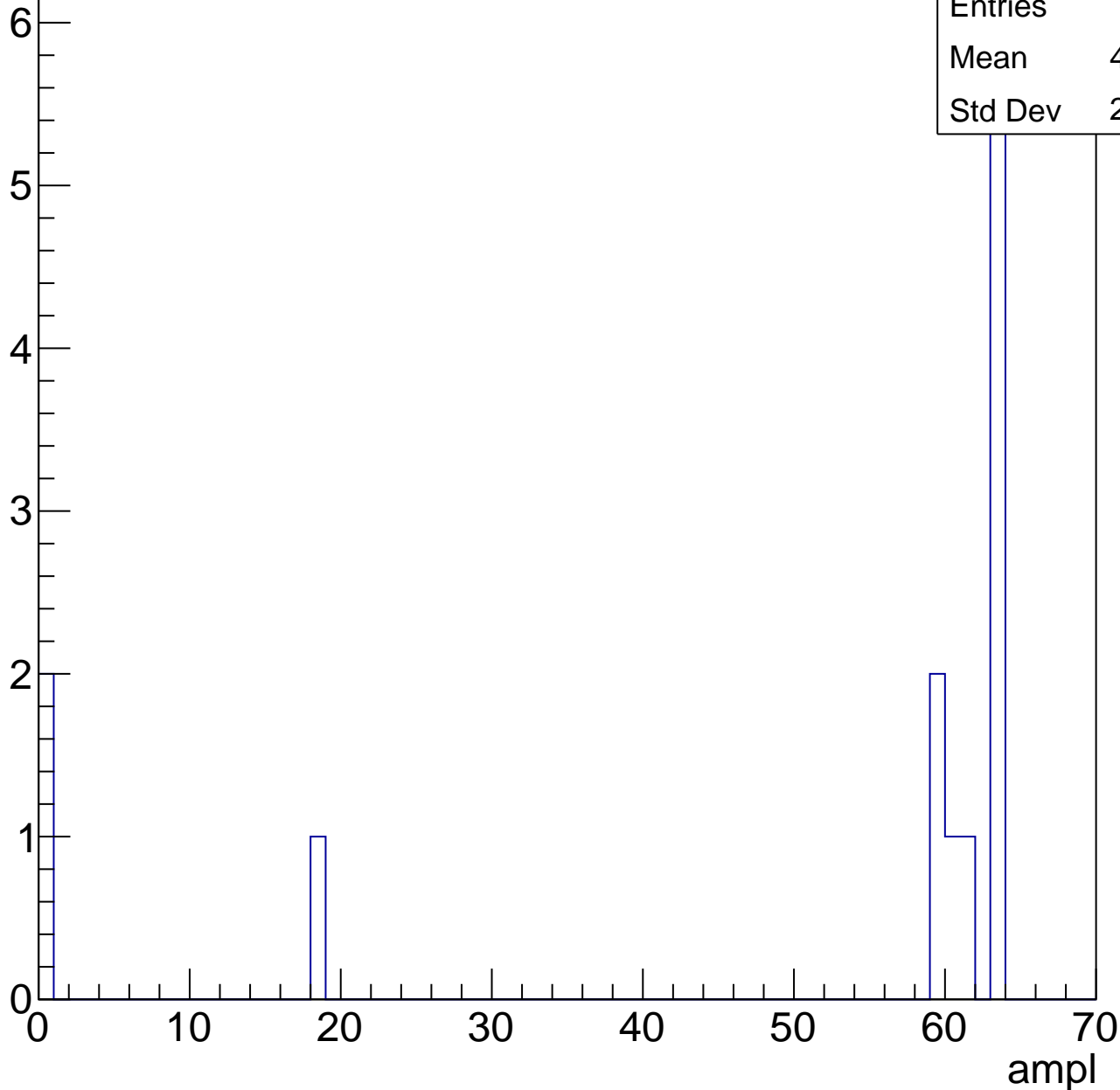




# B1L002S, U17-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	13
Mean	48.85
Std Dev	23.86

# B1L002S, U17-ch77, adc0

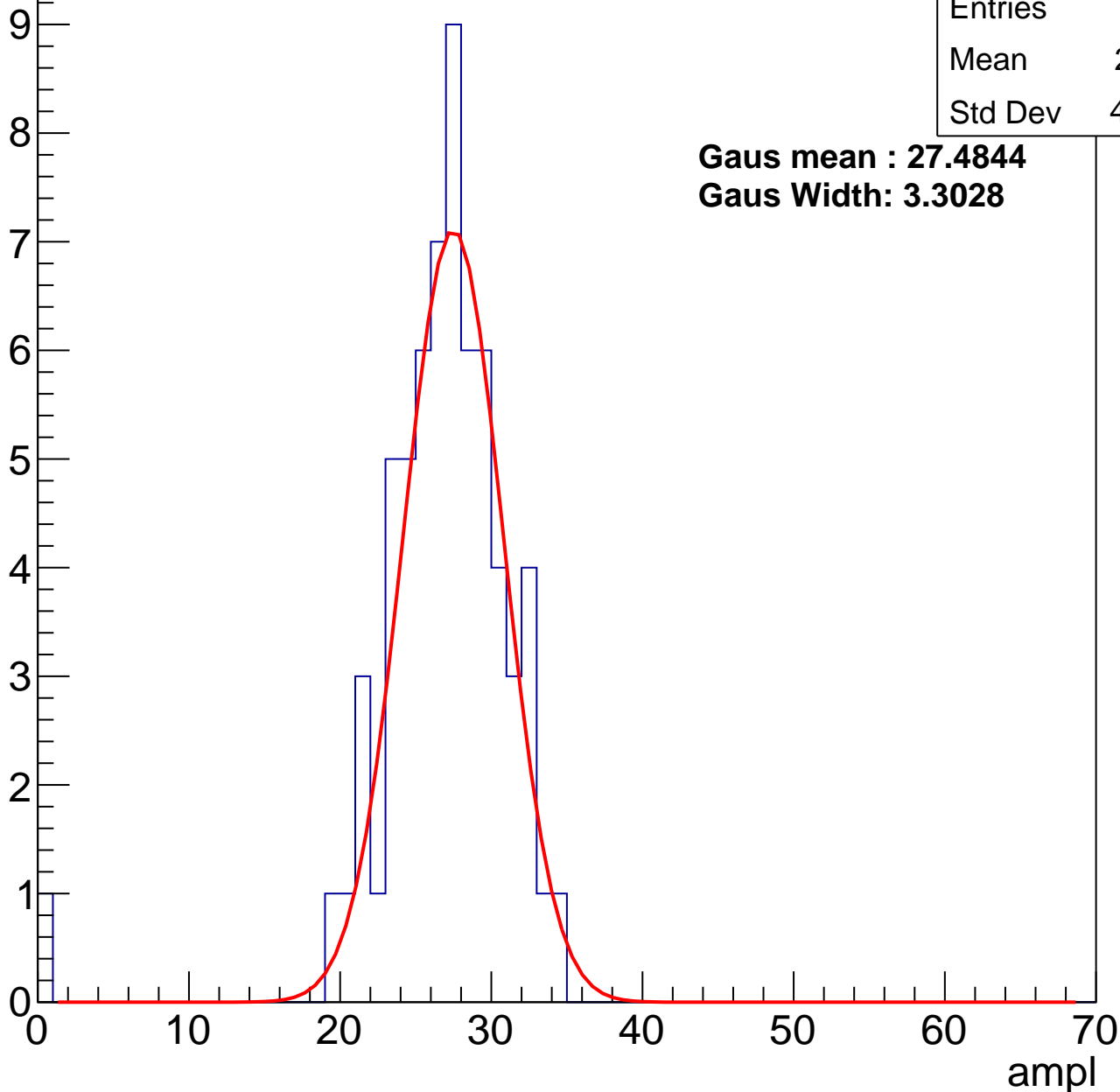
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	26.31
Std Dev	4.677

**Gaus mean : 27.4844**

**Gaus Width: 3.3028**



# B1L002S, U17-ch77, adc1

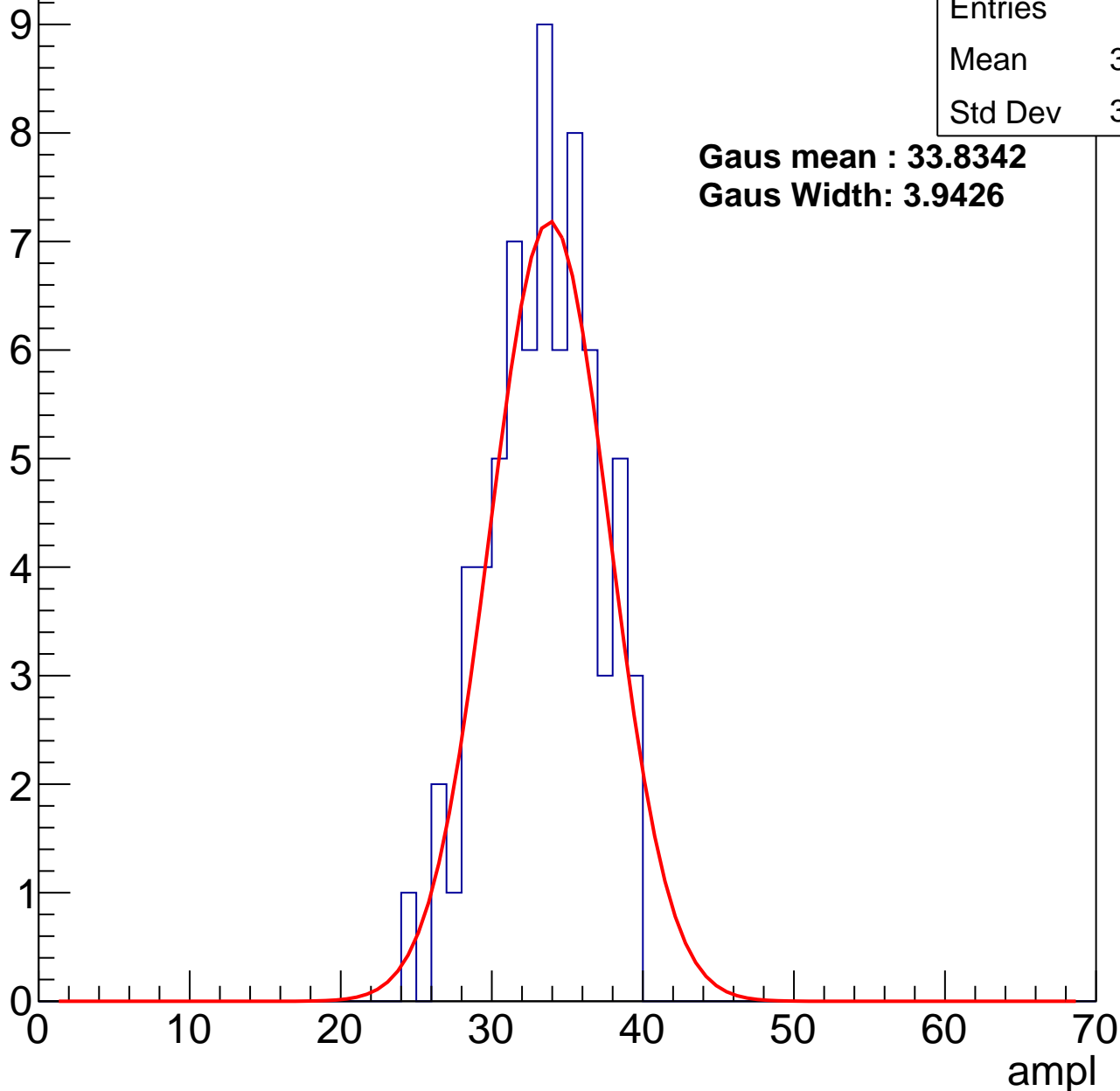
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	32.93
Std Dev	3.445

**Gaus mean : 33.8342**

**Gaus Width: 3.9426**



# B1L002S, U17-ch77, adc2

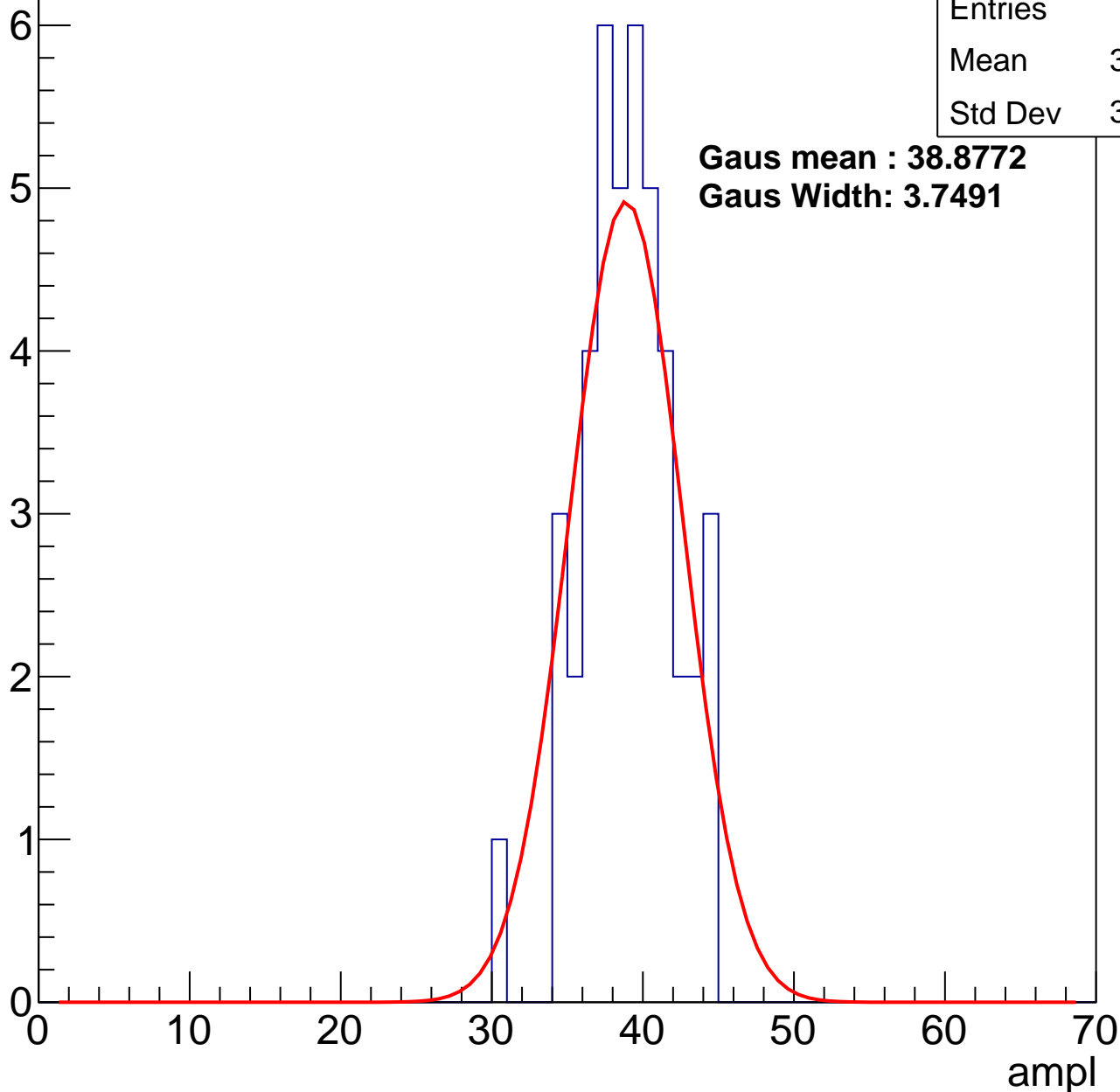
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	38.56
Std Dev	3.014

**Gaus mean : 38.8772**

**Gaus Width: 3.7491**

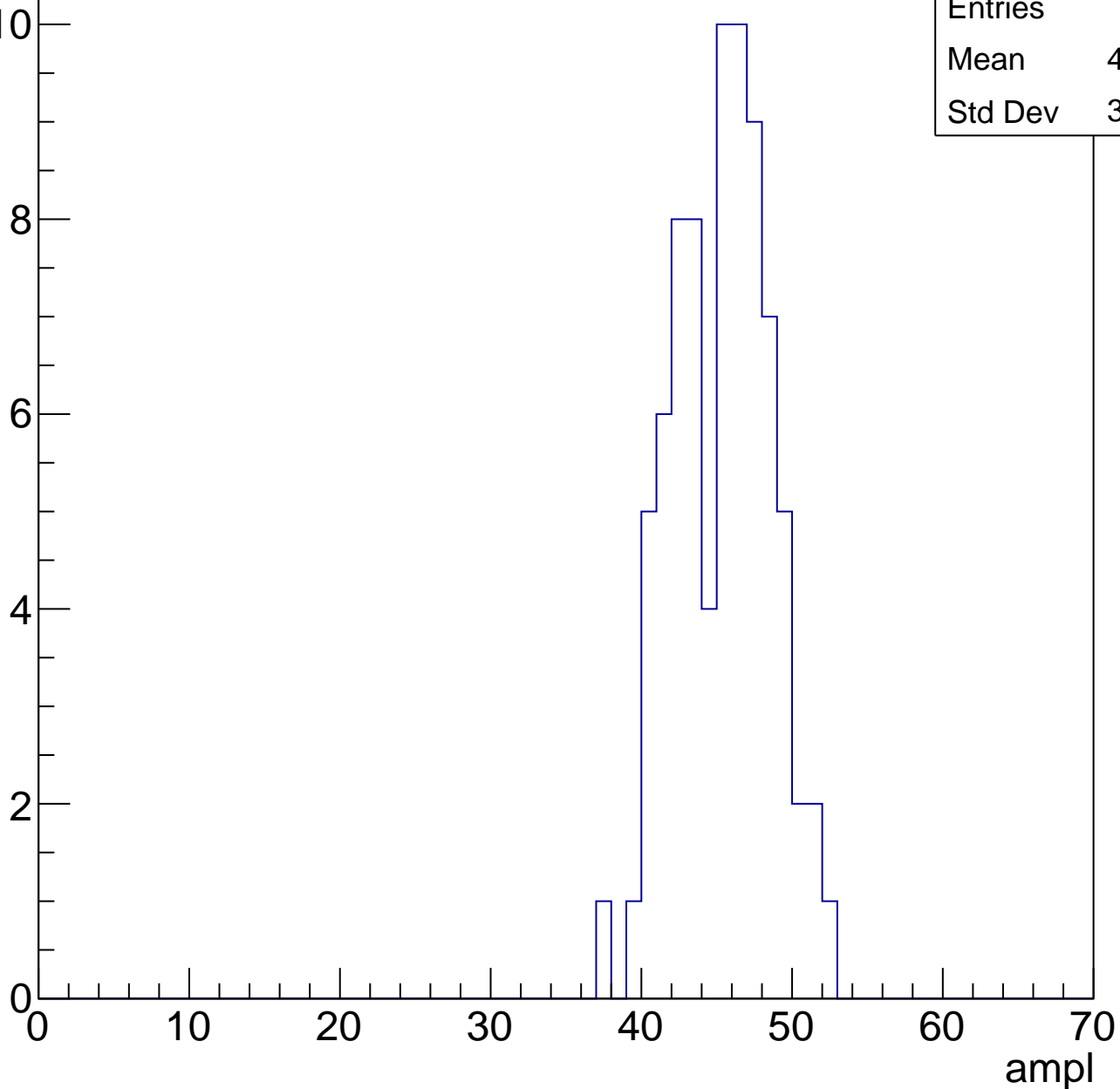


# B1L002S, U17-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	44.89
Std Dev	3.158

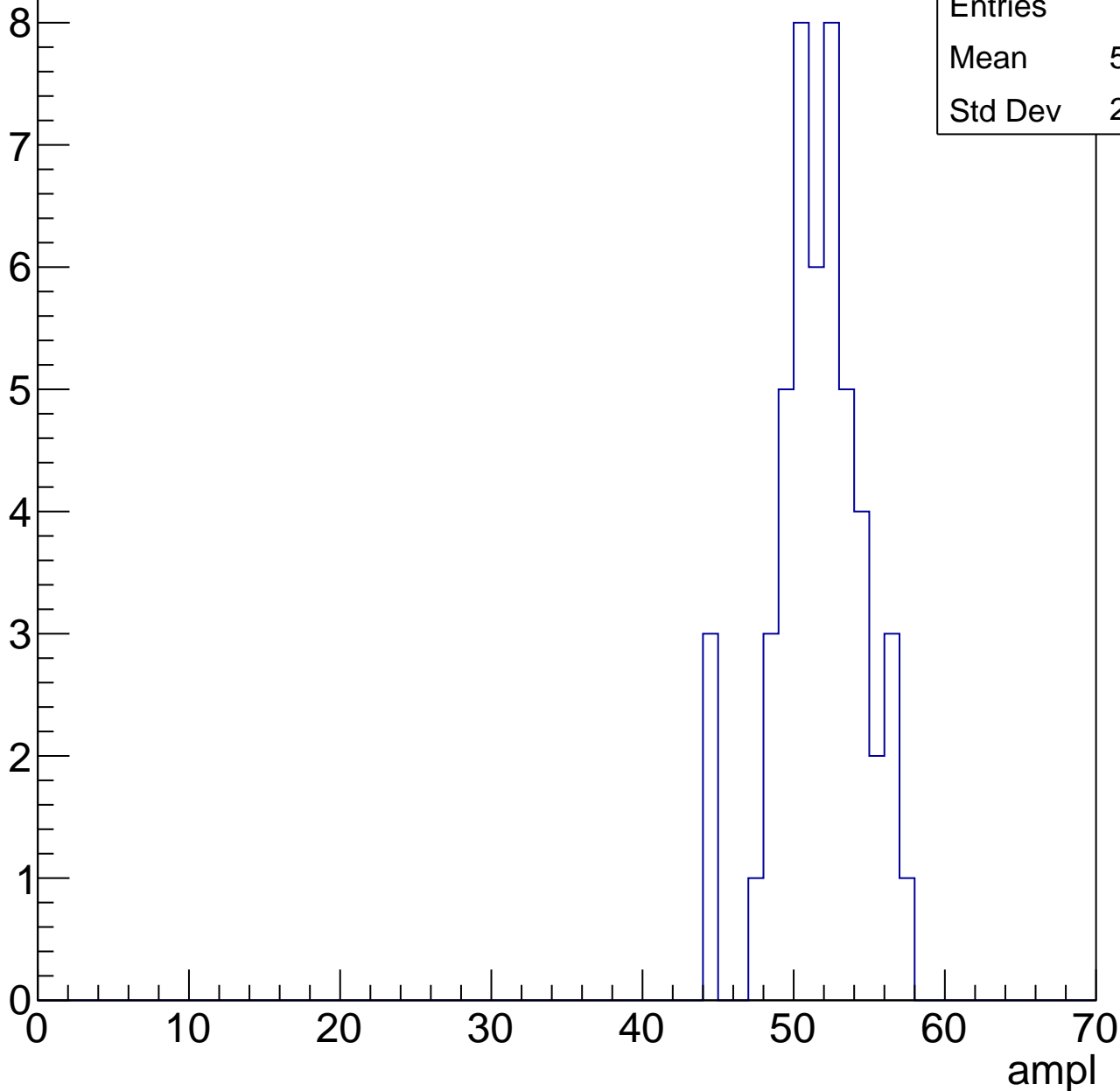


# B1L002S, U17-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	51.14
Std Dev	2.942

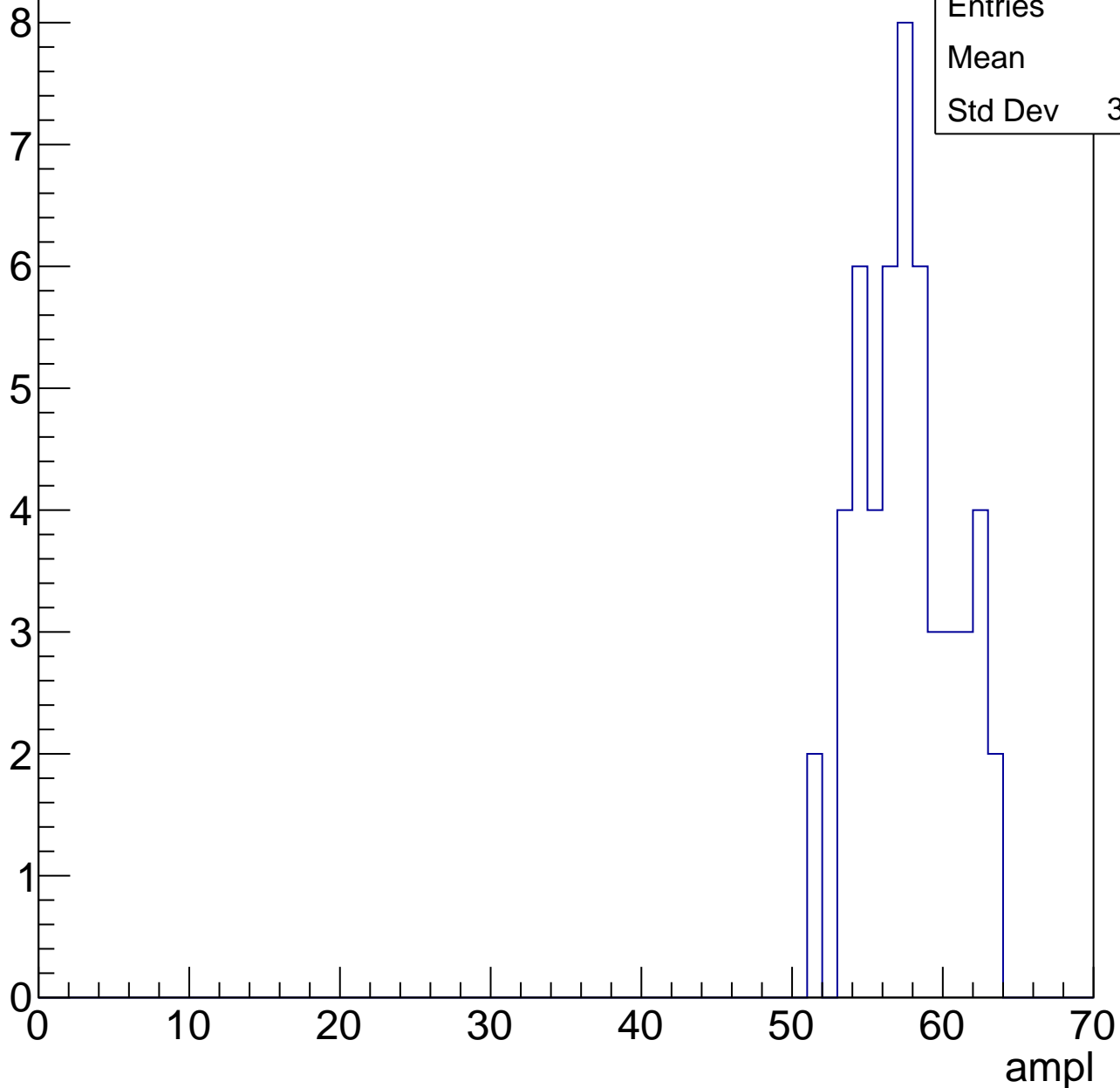


# B1L002S, U17-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.1
Std Dev	3.057

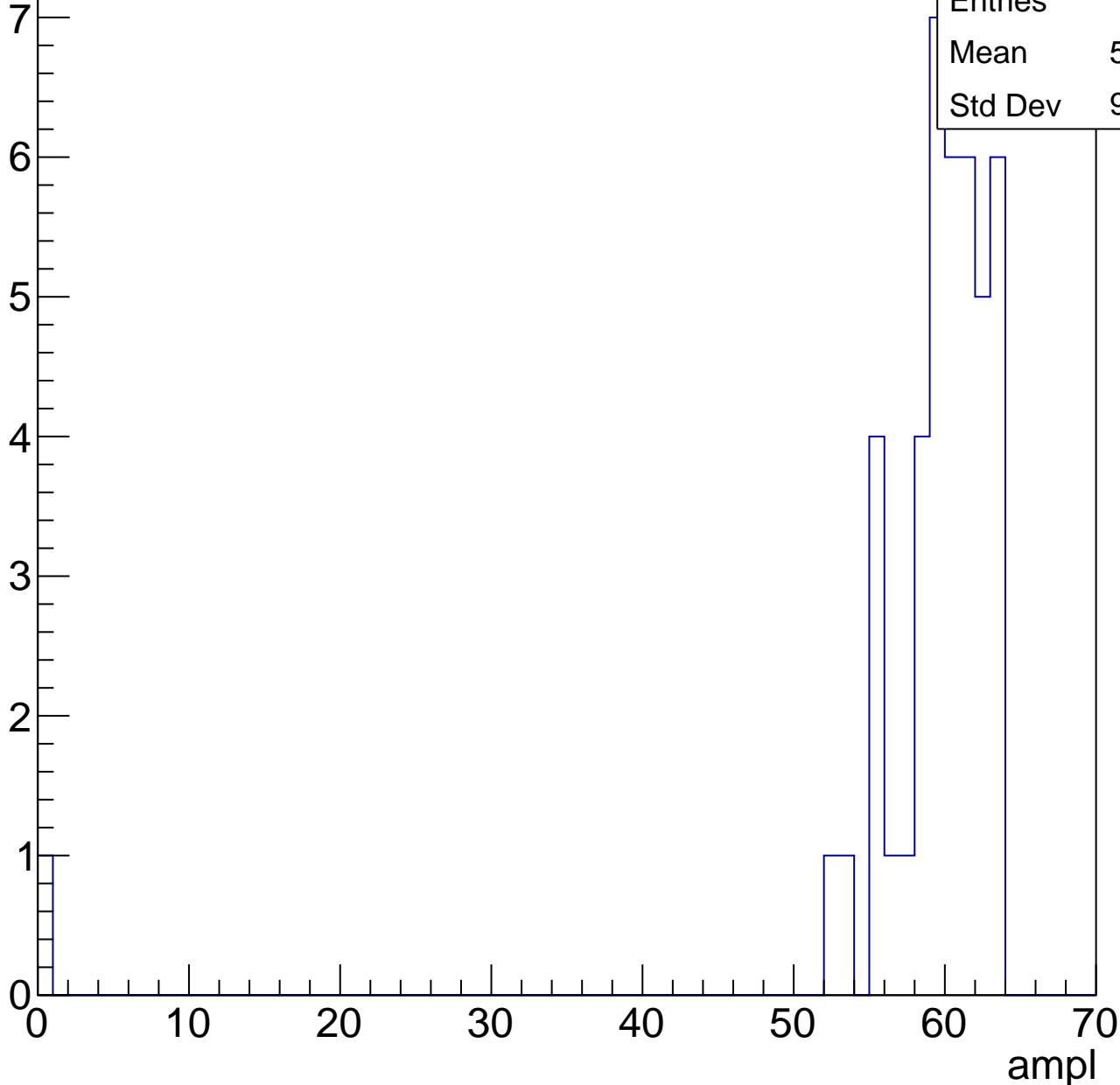


# B1L002S, U17-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	58.07
Std Dev	9.377





# B1L002S, U17-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

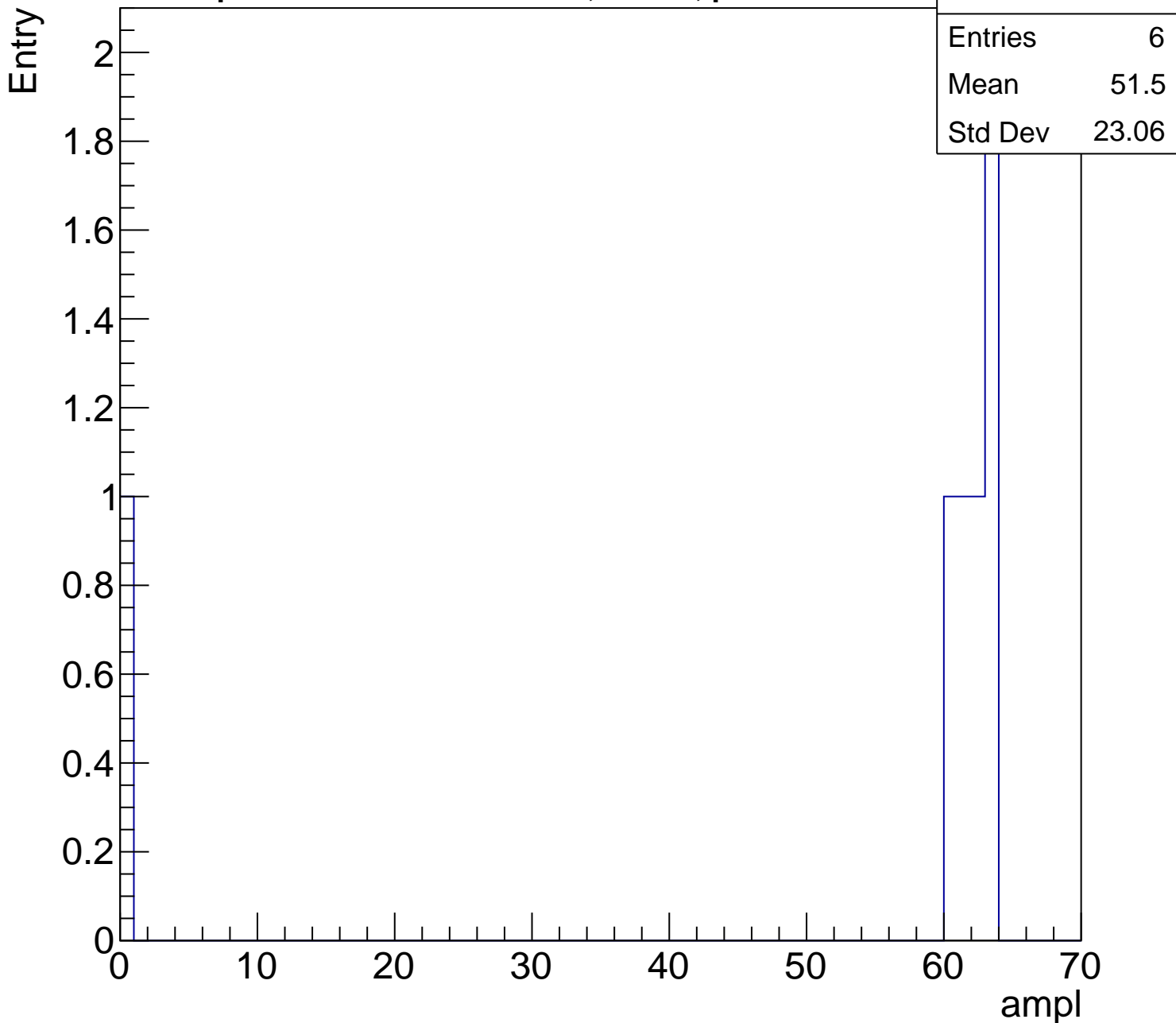
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.06

0 10 20 30 40 50 60 70

ampl



# B1L002S, U17-ch78, adc0

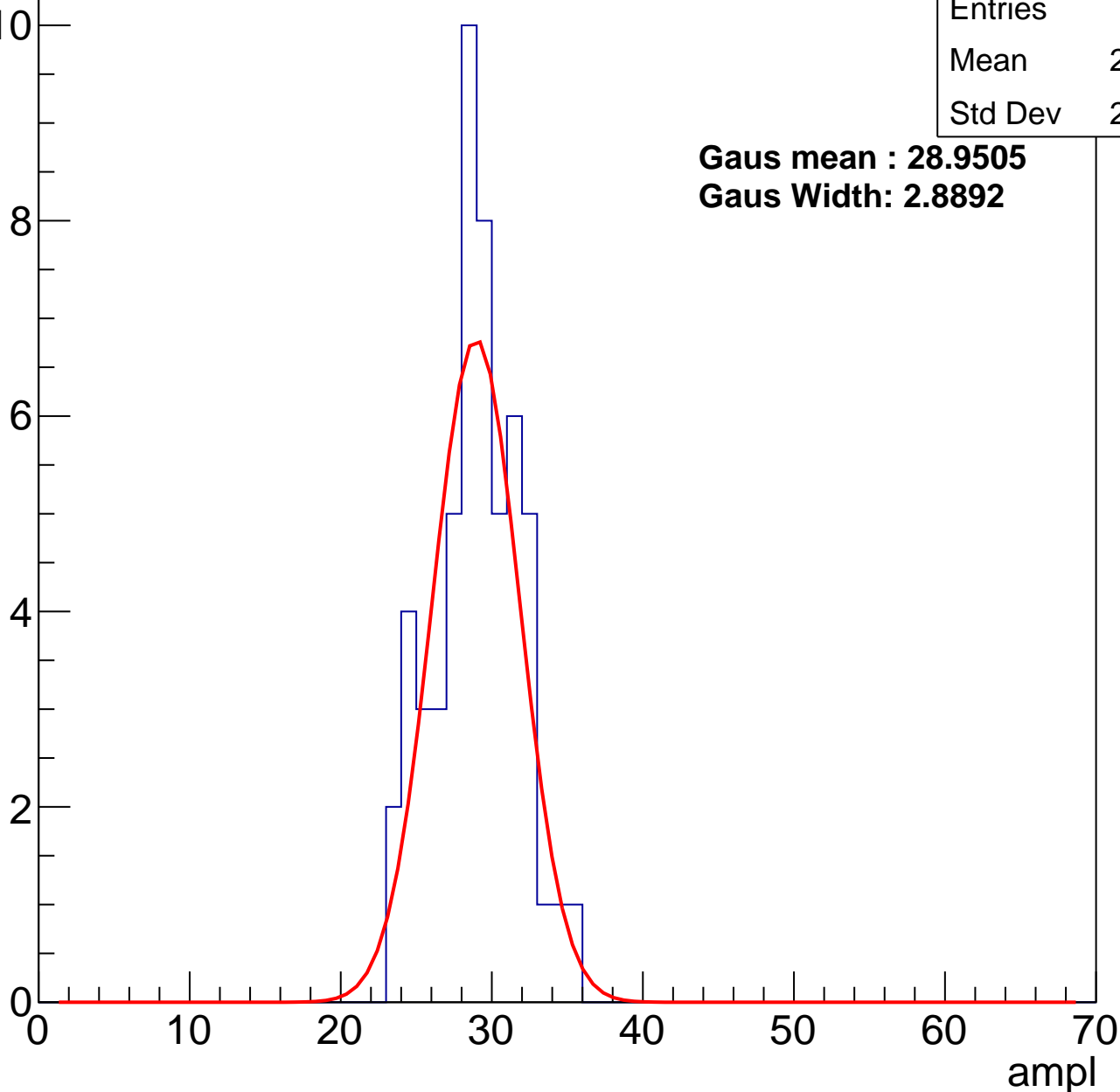
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.52
Std Dev	2.774

**Gaus mean : 28.9505**

**Gaus Width: 2.8892**



# B1L002S, U17-ch78, adc1

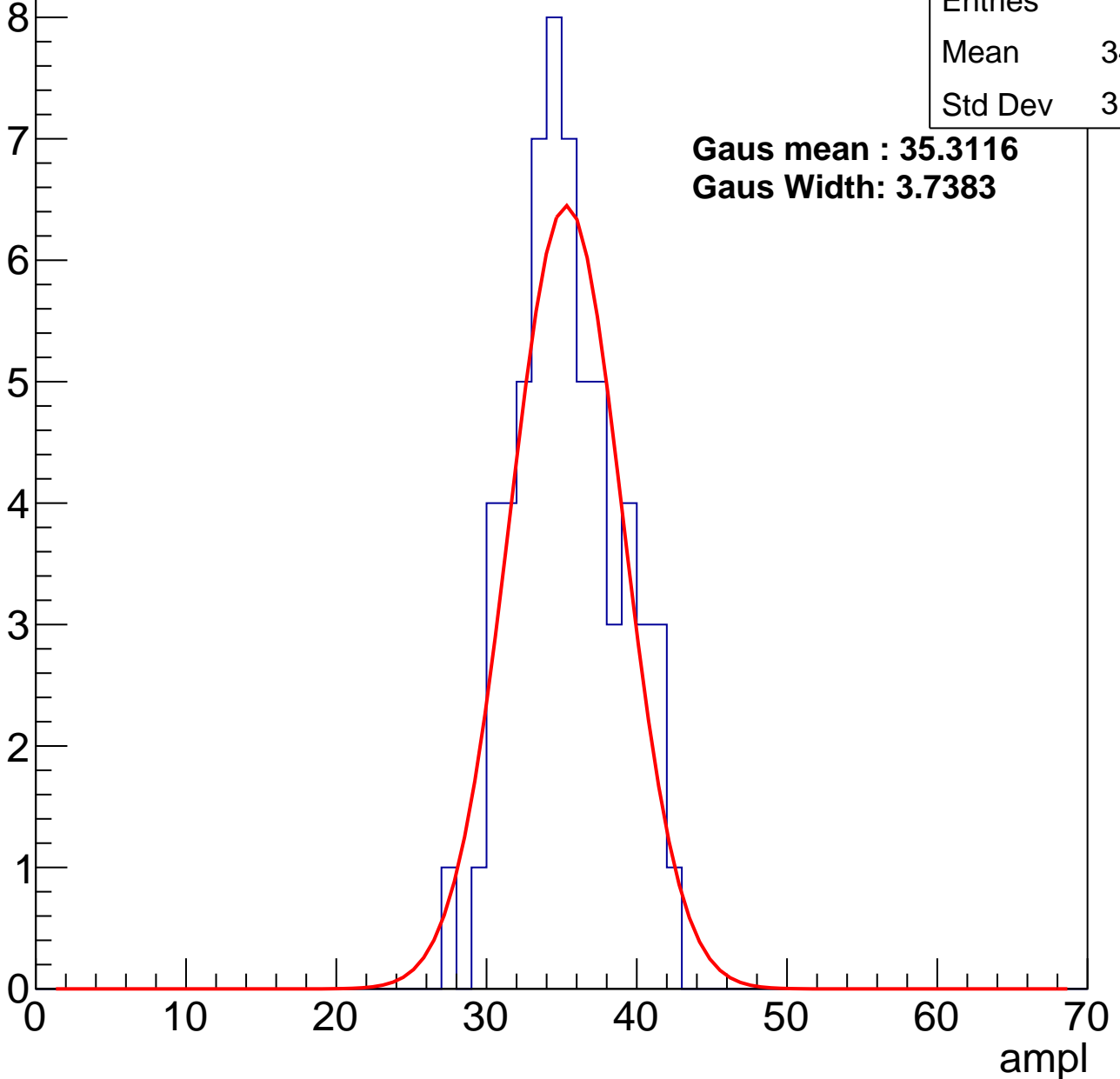
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.89
Std Dev	3.369

**Gaus mean : 35.3116**

**Gaus Width: 3.7383**



# B1L002S, U17-ch78, adc2

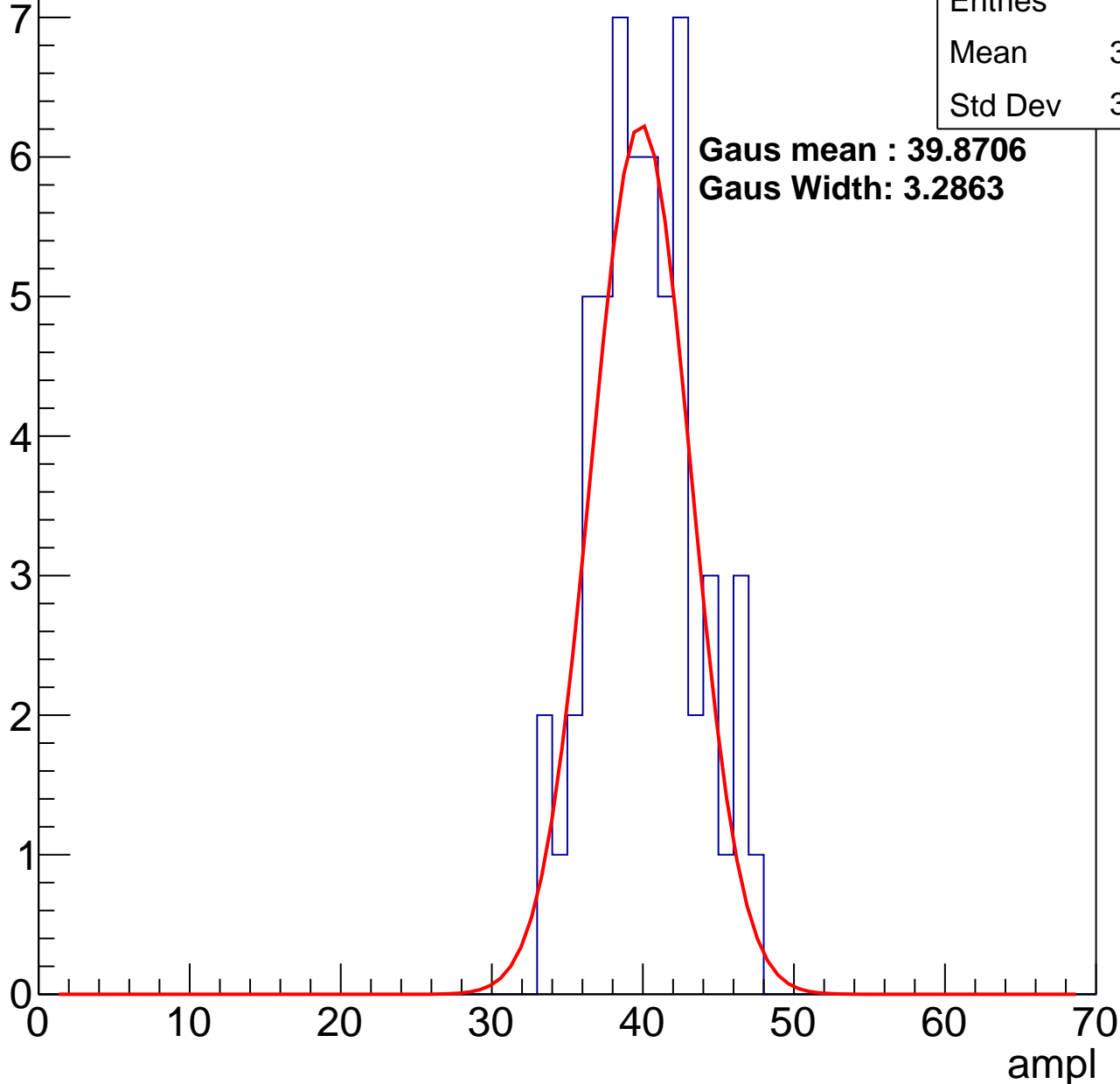
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	39.68
Std Dev	3.323

**Gaus mean : 39.8706**

**Gaus Width: 3.2863**

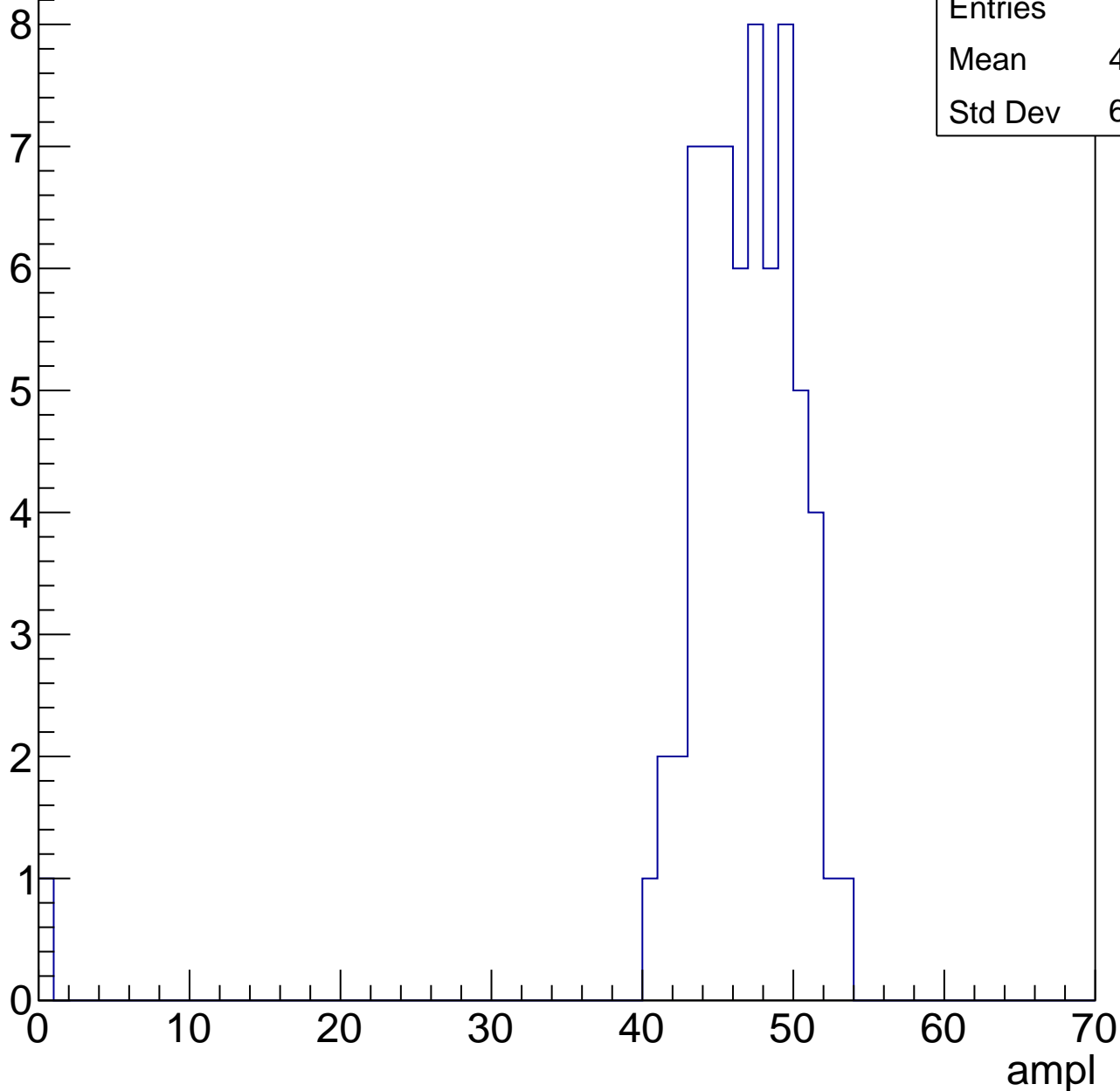


# B1L002S, U17-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	45.77
Std Dev	6.393

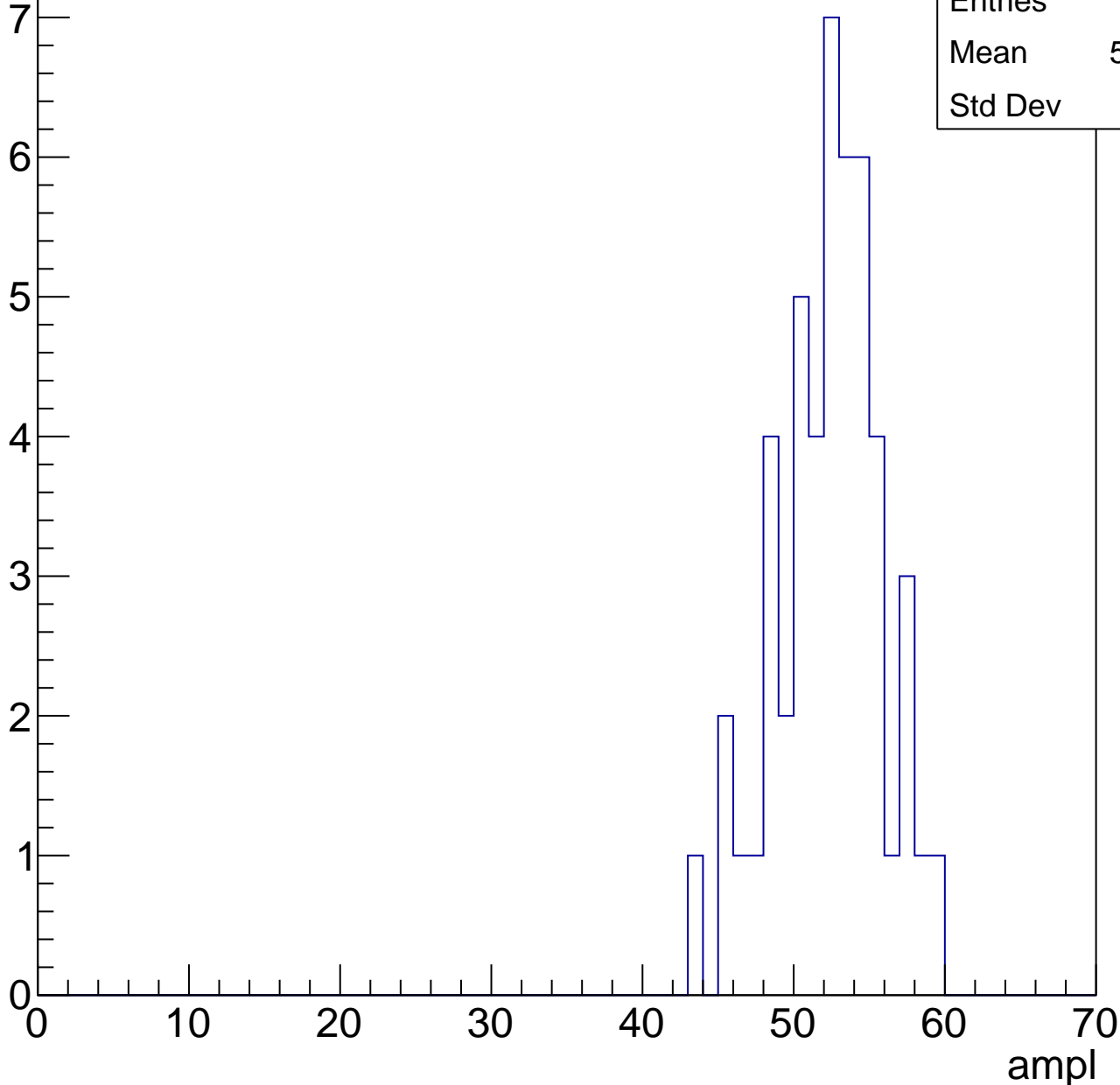


# B1L002S, U17-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

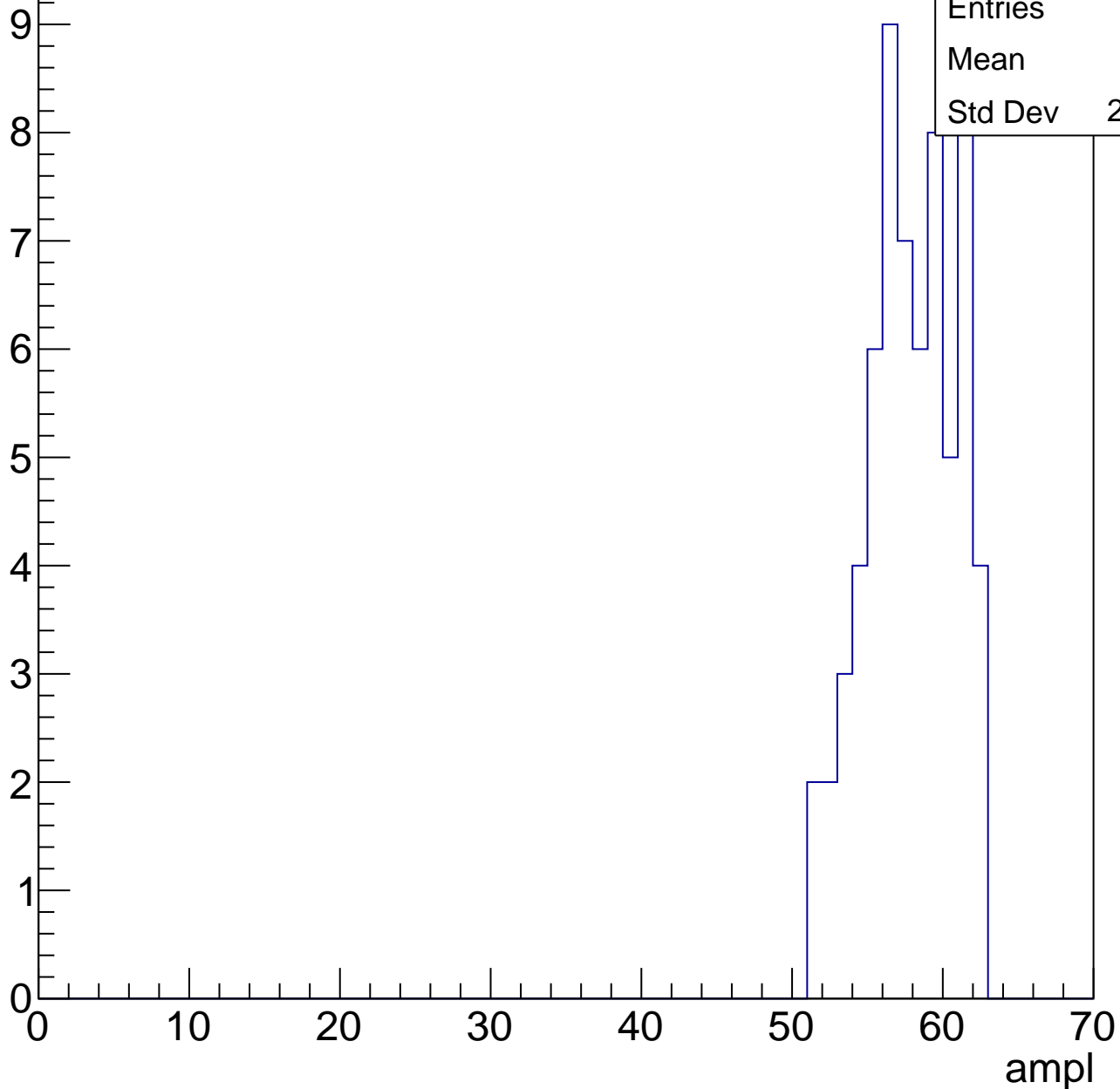
Entries	49
Mean	51.84
Std Dev	3.46



# B1L002S, U17-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



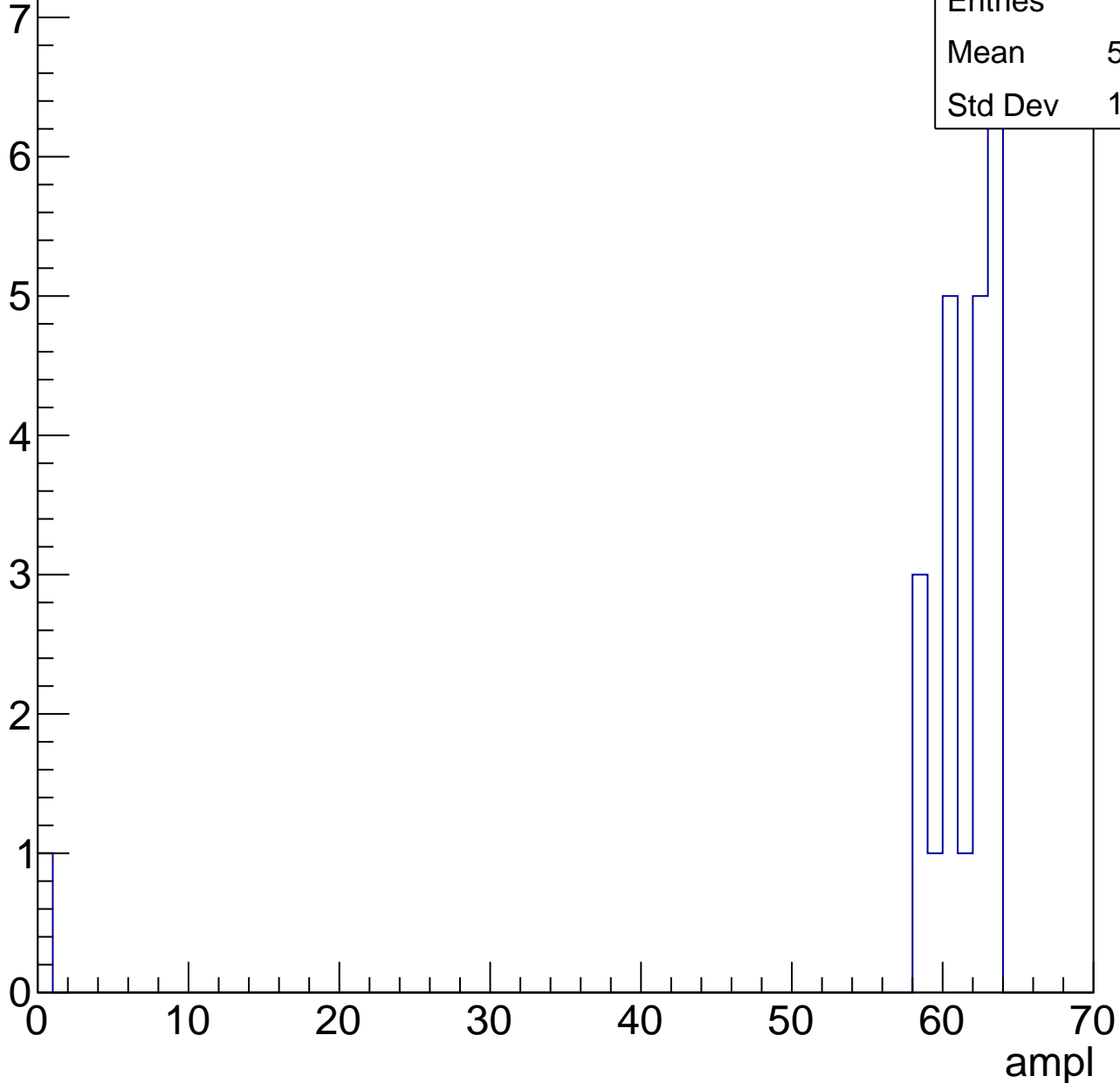
Entries	65
Mean	57.4
Std Dev	2.924

# B1L002S, U17-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.48
Std Dev	12.59

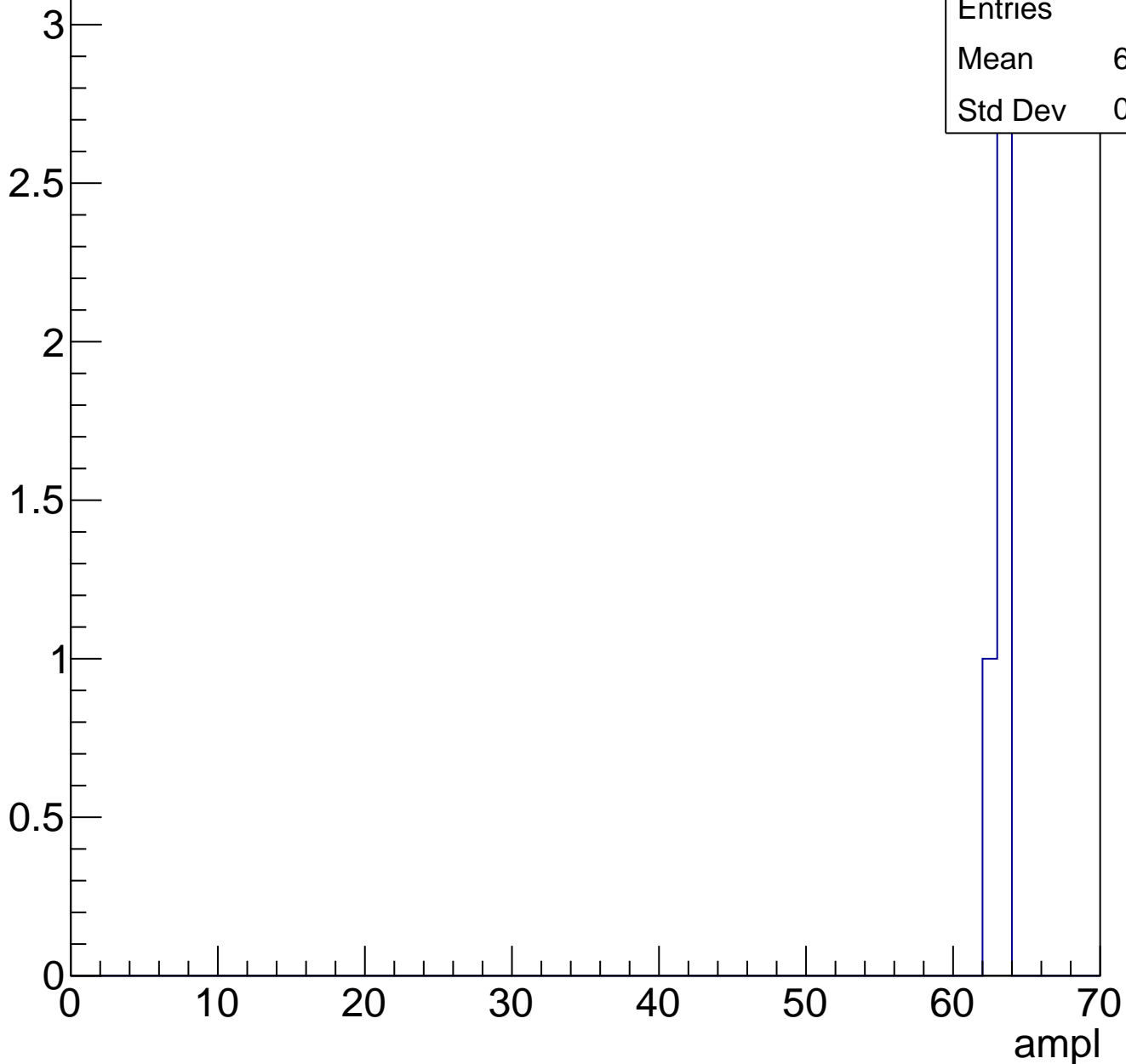




# B1L002S, U17-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch79, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	28.71
Std Dev	3.063

**Gaus mean : 29.5026**

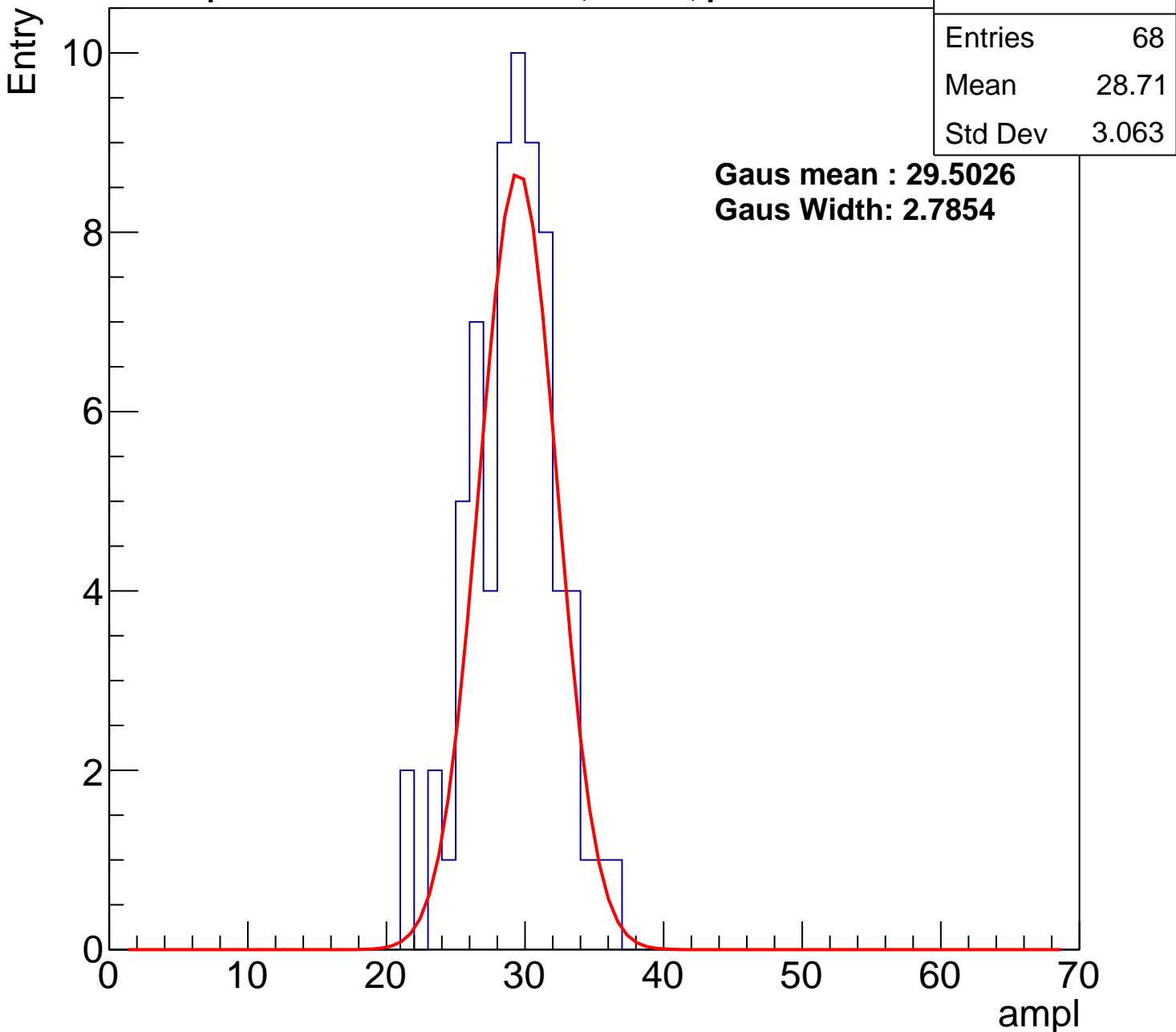
**Gaus Width: 2.7854**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch79, adc1

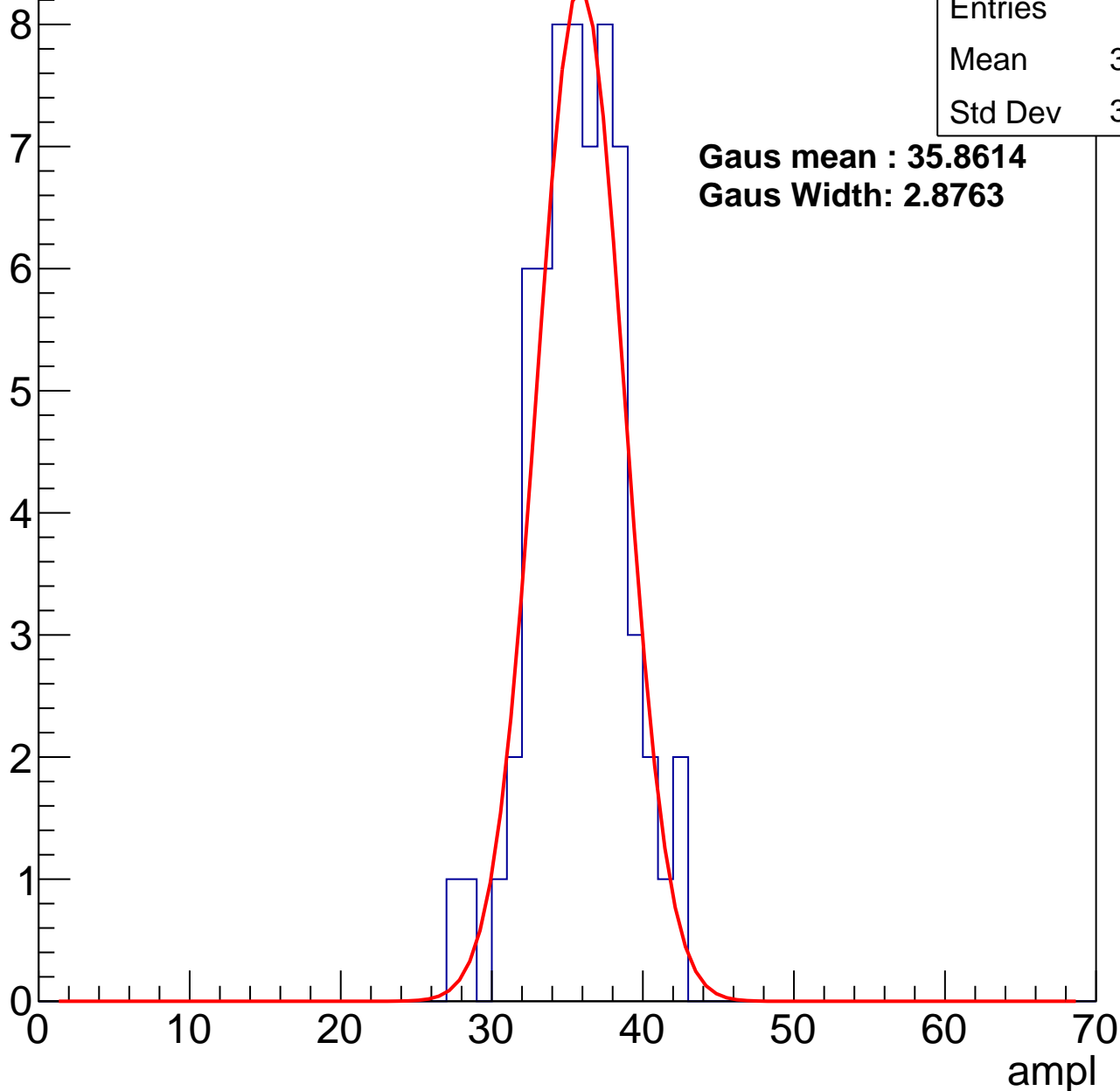
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.32
Std Dev	3.044

**Gaus mean : 35.8614**

**Gaus Width: 2.8763**



# B1L002S, U17-ch79, adc2

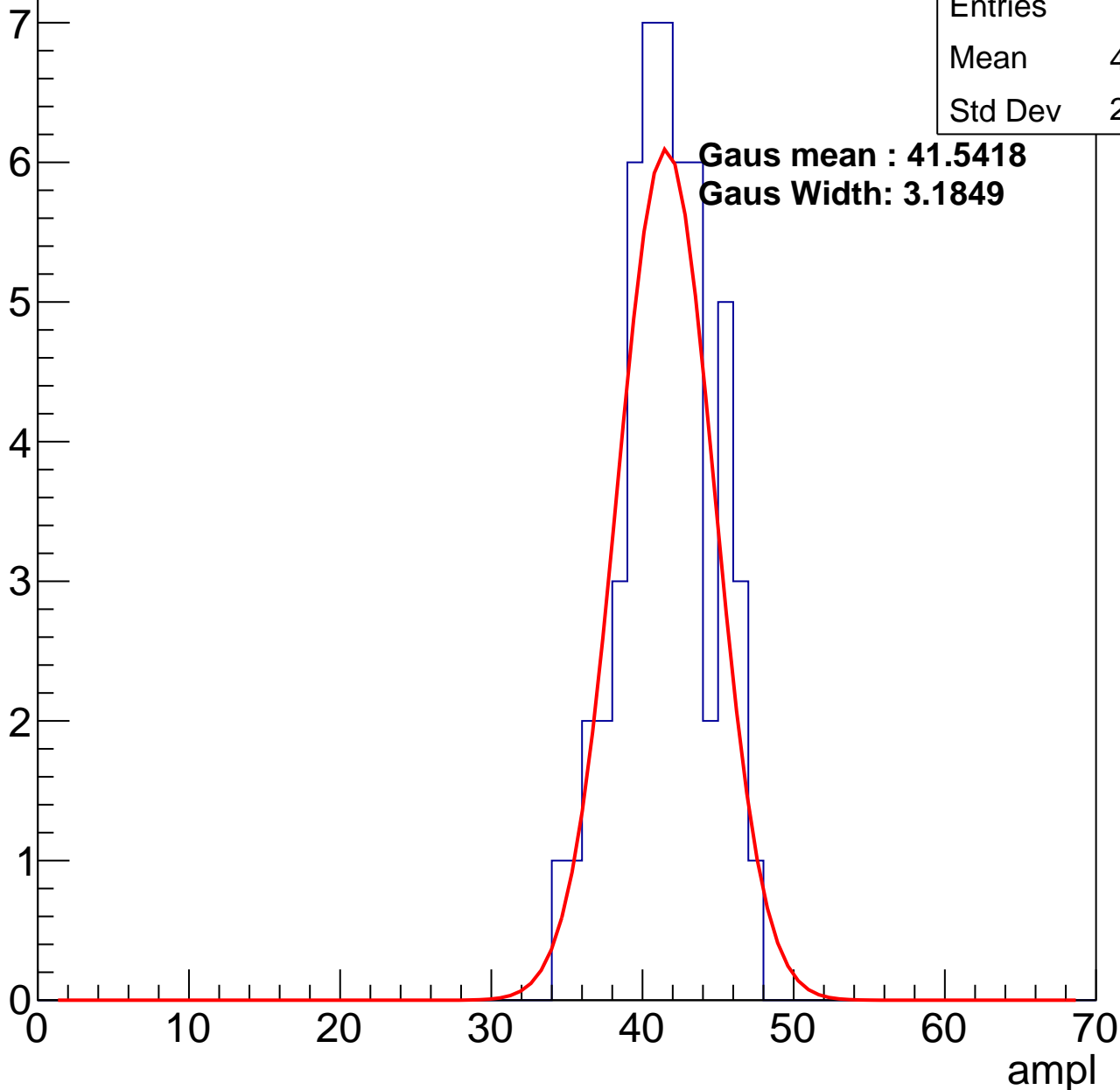
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	41.12
Std Dev	2.985

**Gaus mean : 41.5418**

**Gaus Width: 3.1849**

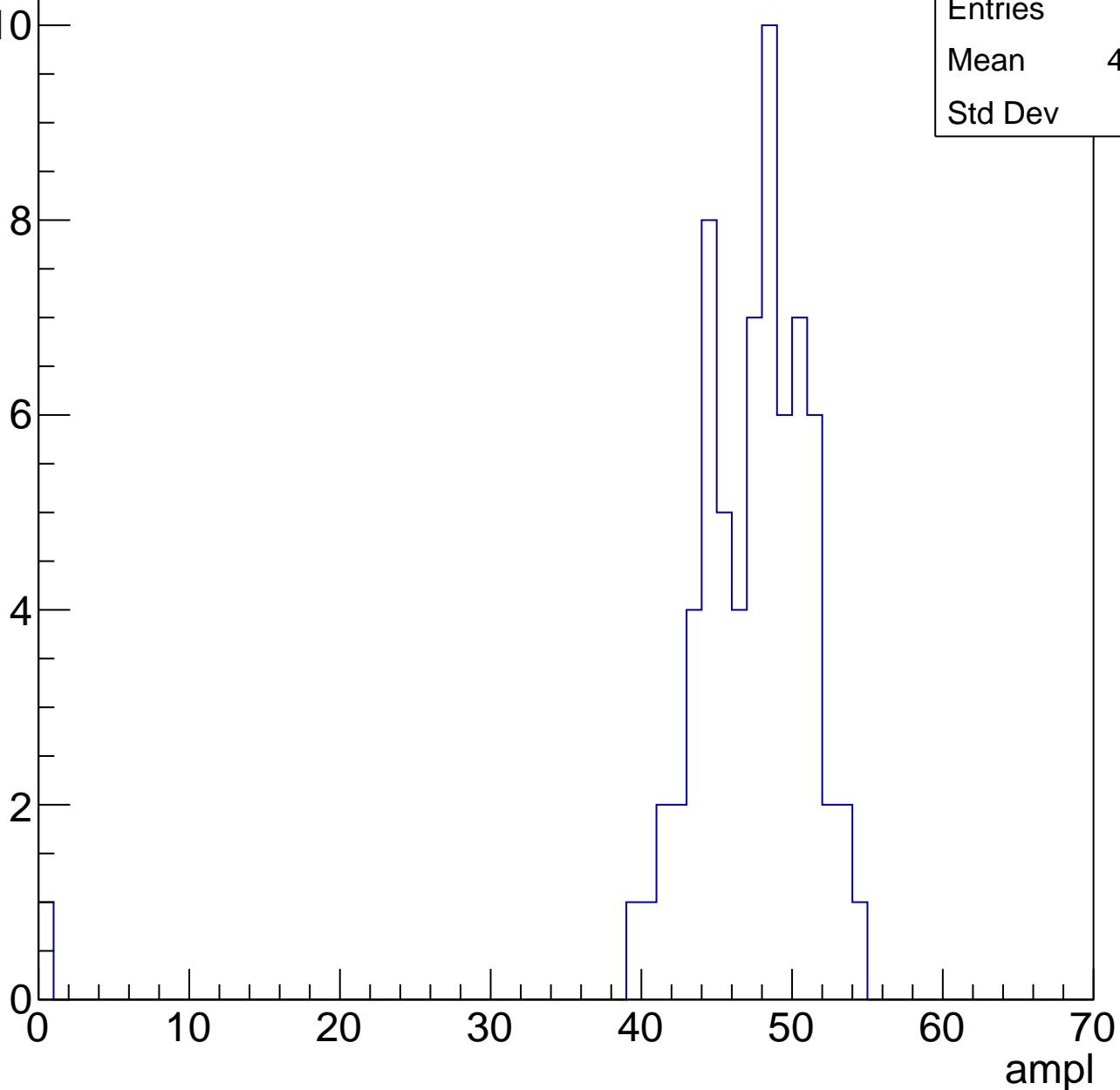


# B1L002S, U17-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.39
Std Dev	6.53

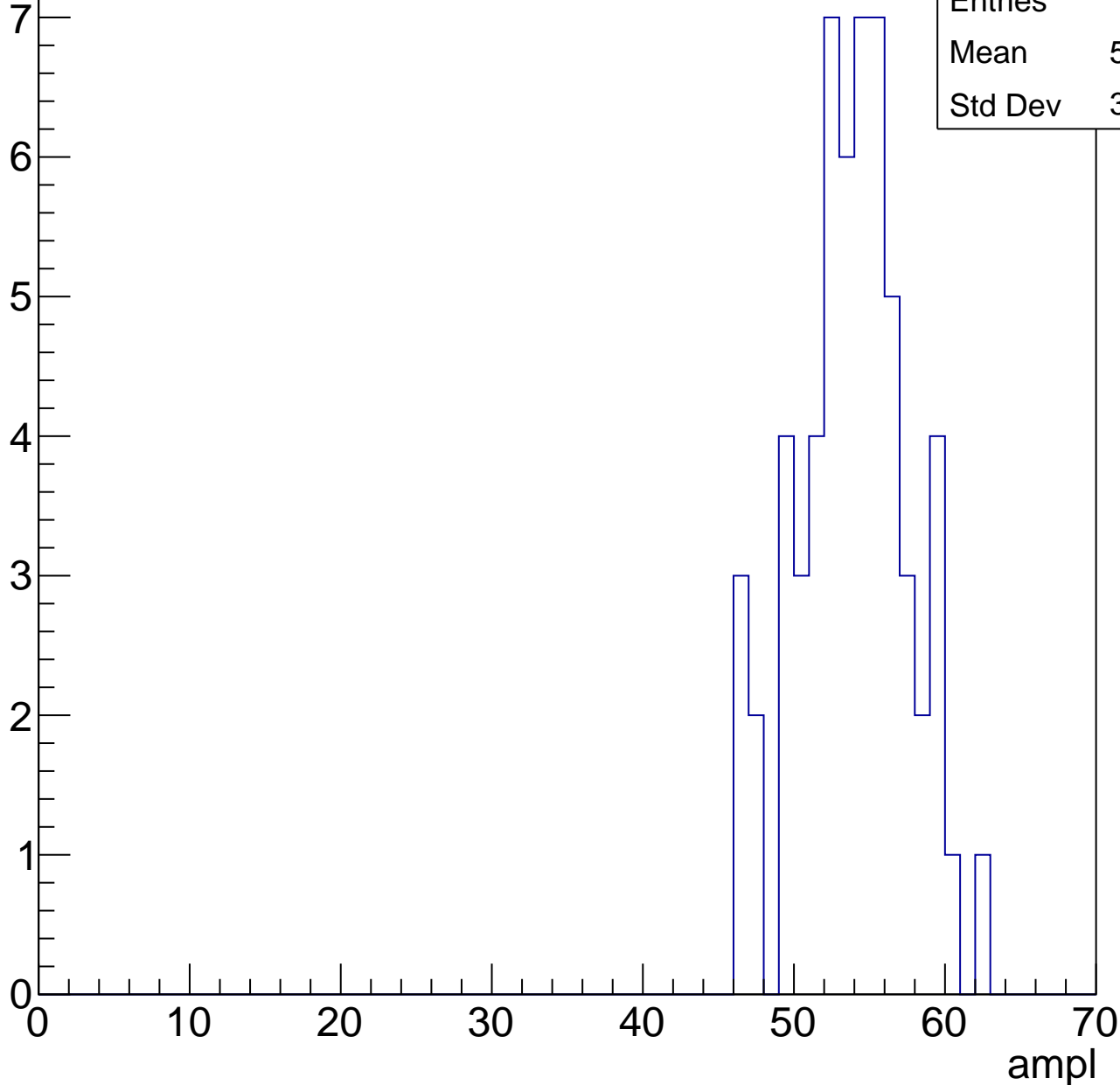


# B1L002S, U17-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

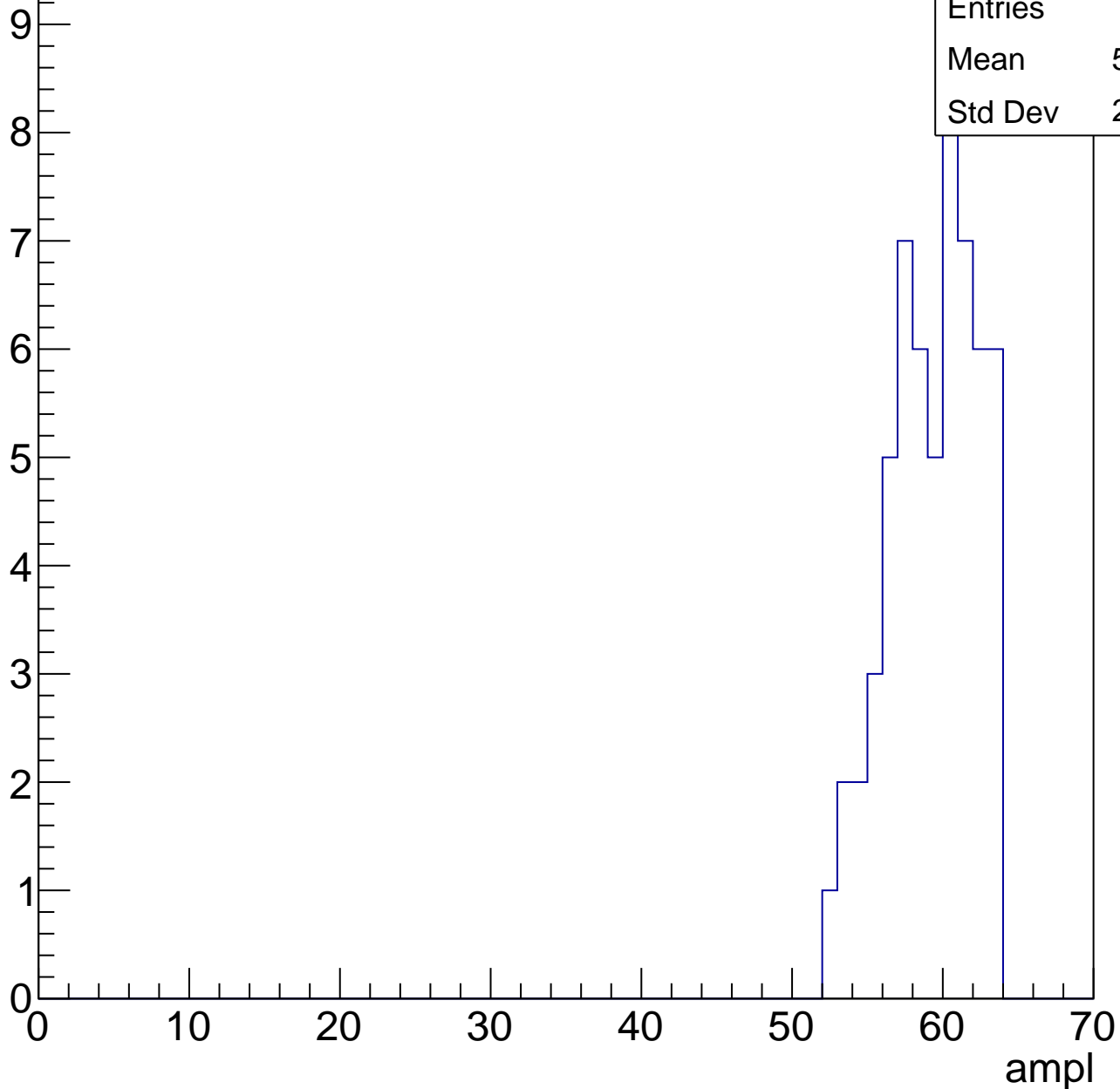
Entries	59
Mean	53.42
Std Dev	3.623



# B1L002S, U17-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

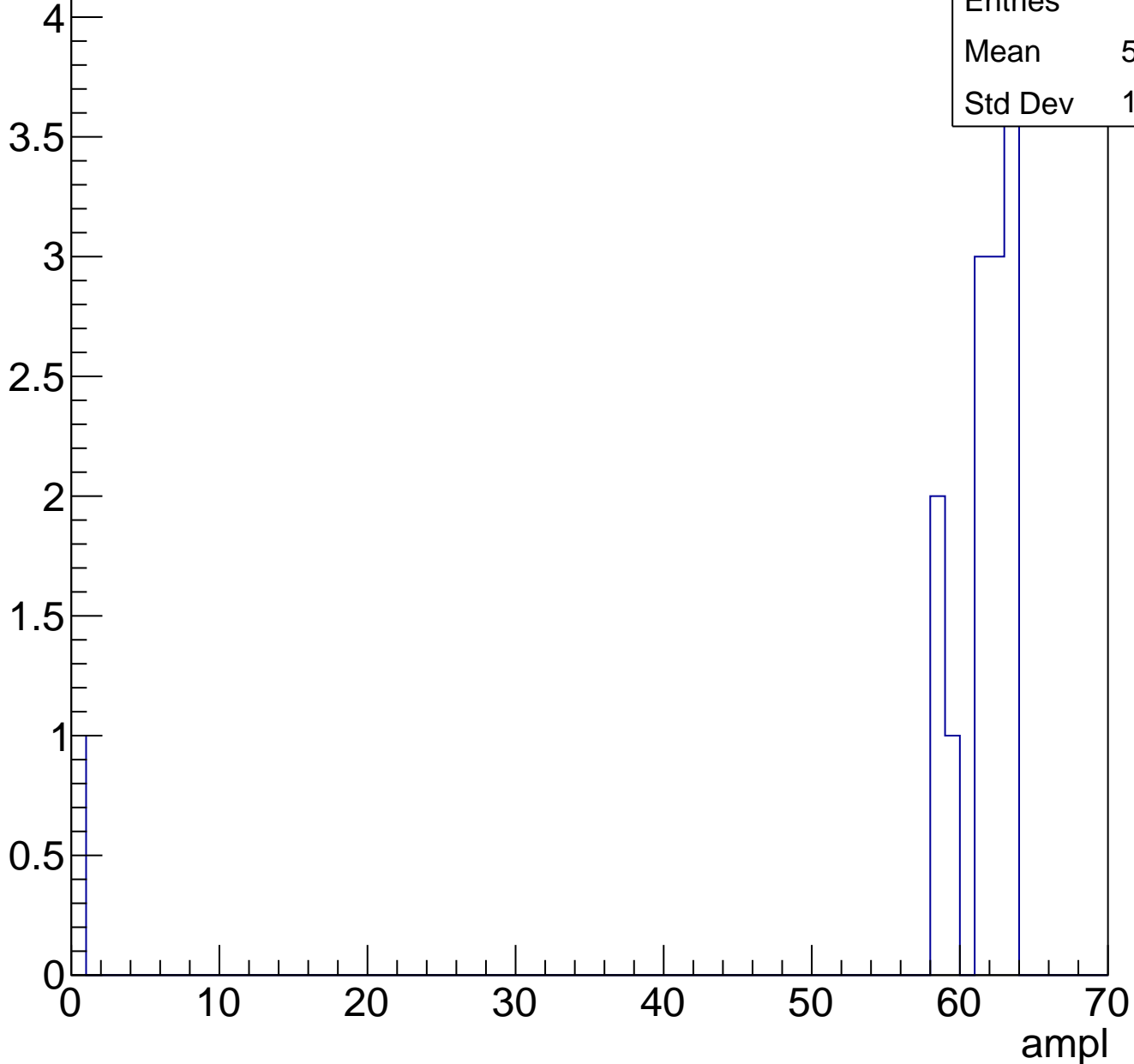


Entries	59
Mean	58.81
Std Dev	2.861

# B1L002S, U17-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U17-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch80, adc0

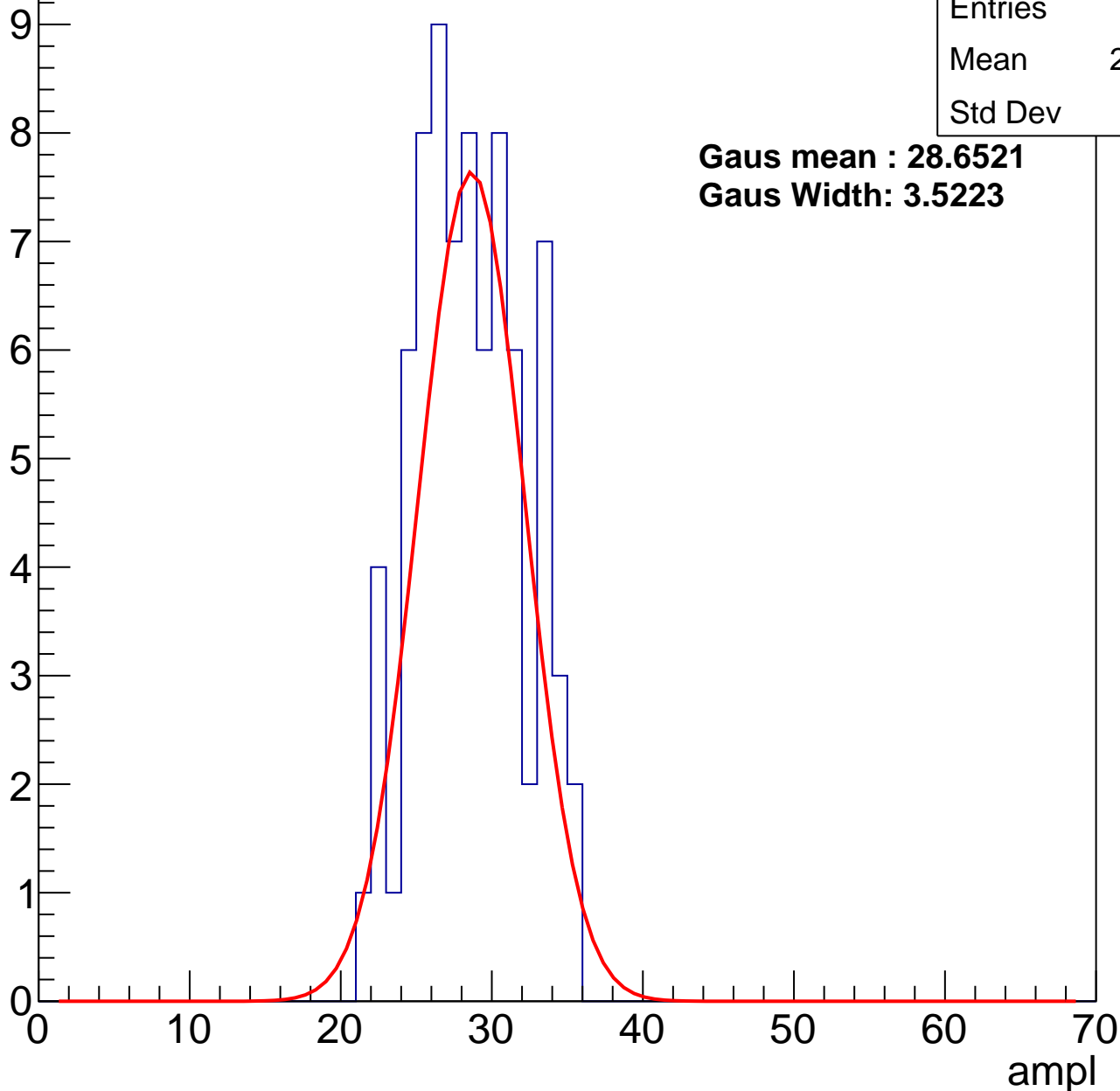
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	28.08
Std Dev	3.46

**Gaus mean : 28.6521**

**Gaus Width: 3.5223**



# B1L002S, U17-ch80, adc1

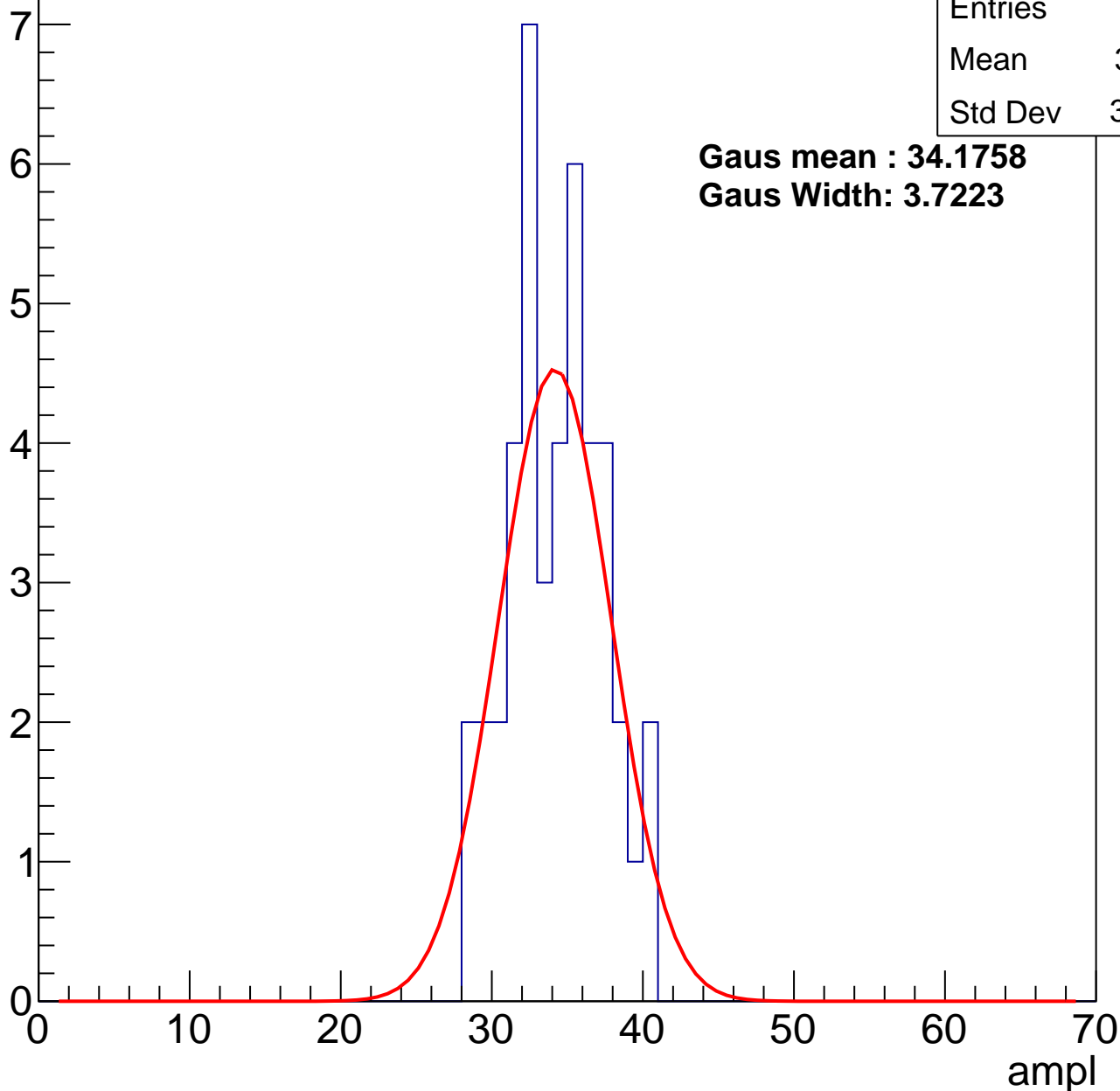
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	33.81
Std Dev	3.075

**Gaus mean : 34.1758**

**Gaus Width: 3.7223**



# B1L002S, U17-ch80, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.86
Std Dev	3.503

**Gaus mean : 39.8599**

**Gaus Width: 3.7809**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

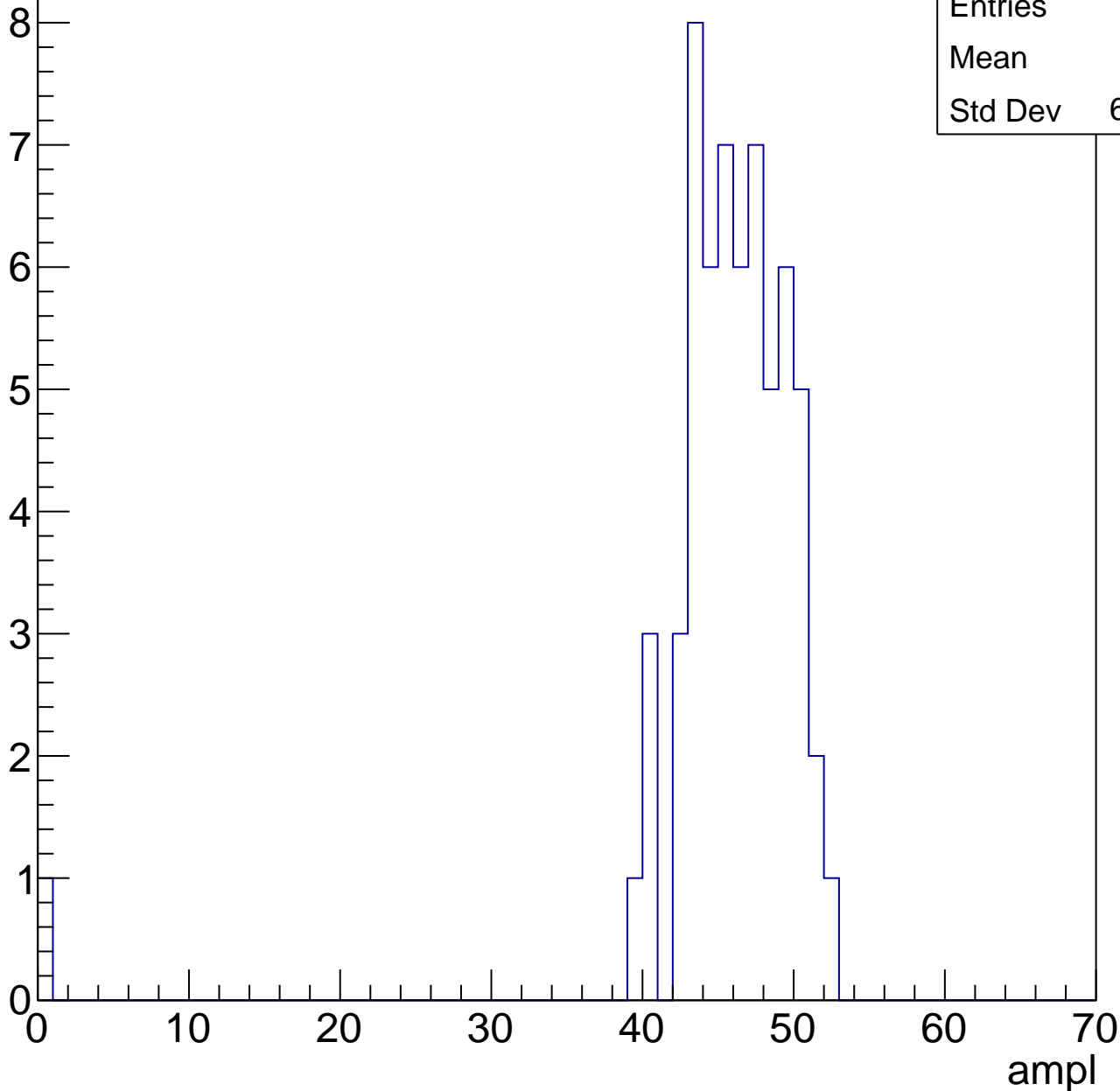


# B1L002S, U17-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.1
Std Dev	6.555

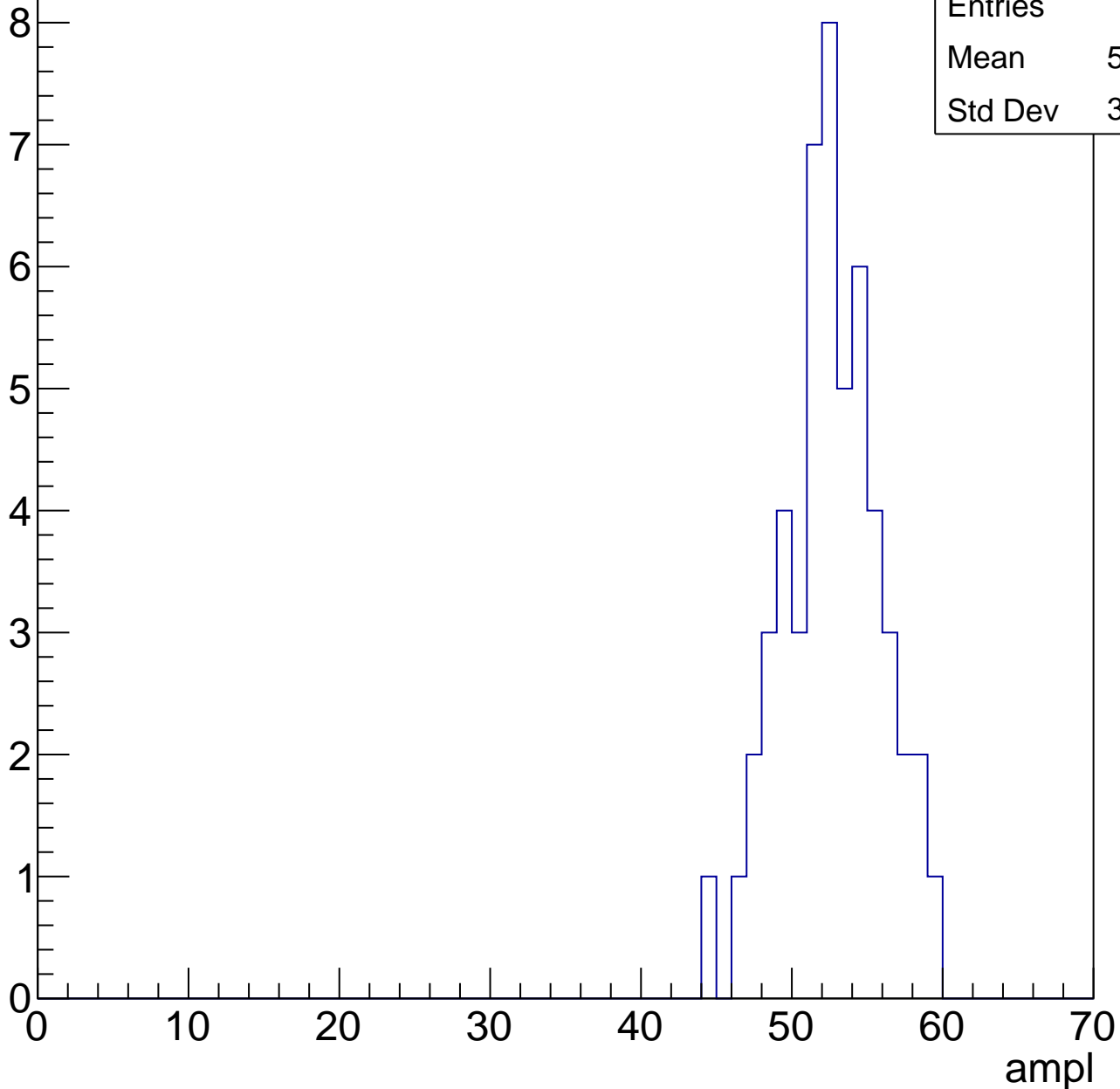


# B1L002S, U17-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

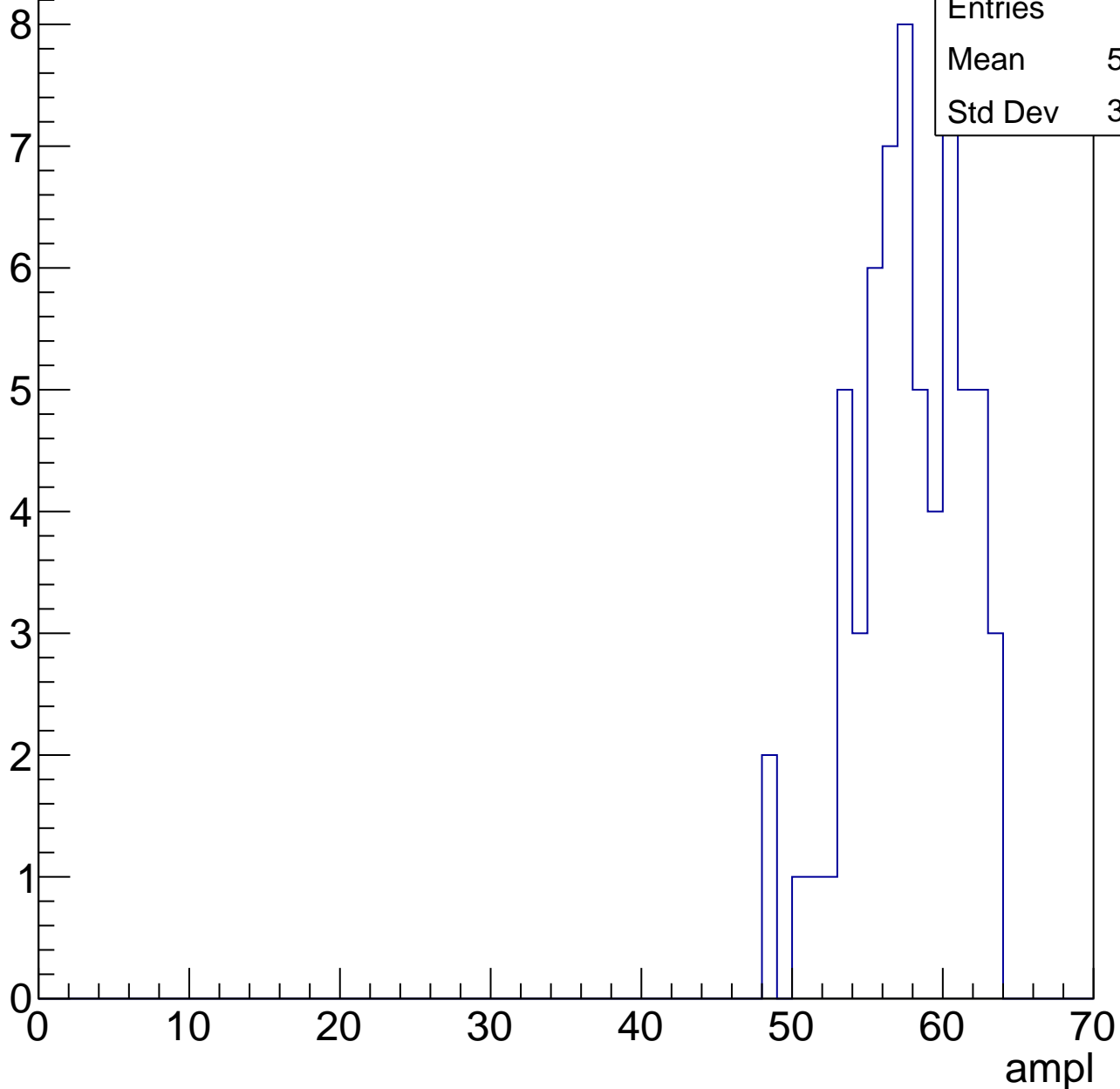
Entries	52
Mean	52.17
Std Dev	3.209



# B1L002S, U17-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



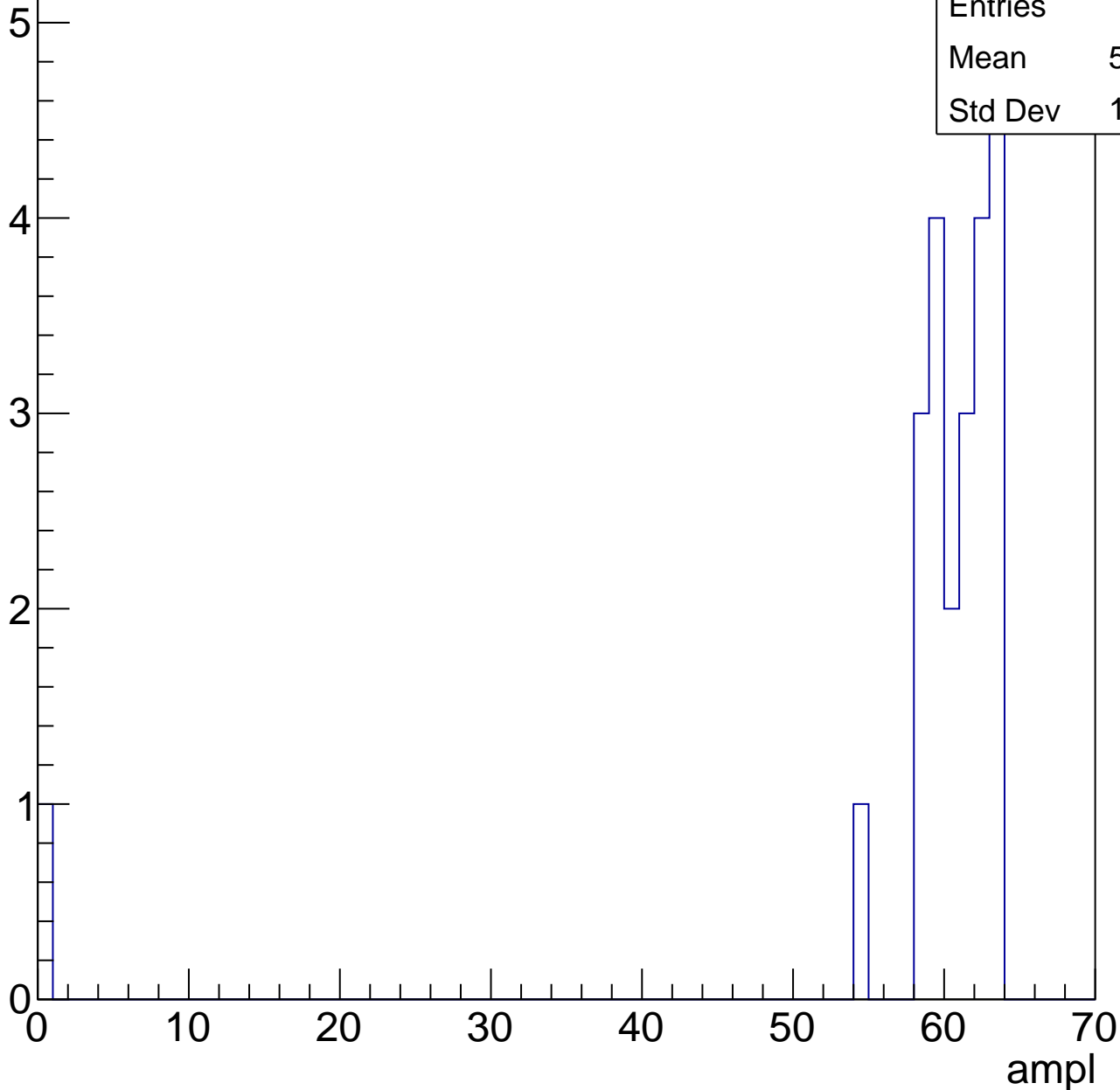
Entries	64
Mean	57.25
Std Dev	3.562

# B1L002S, U17-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	57.83
Std Dev	12.52





# B1L002S, U17-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

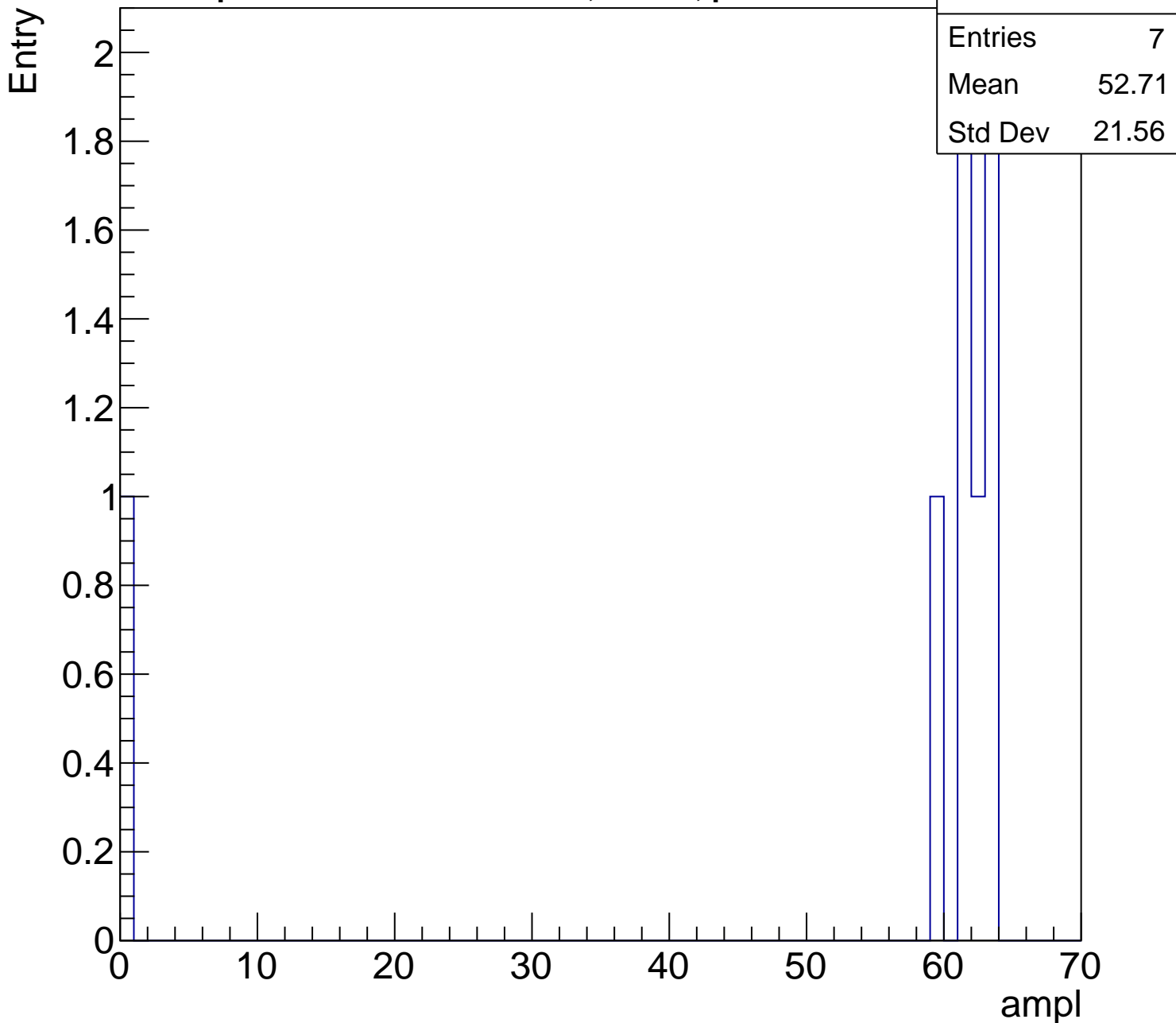
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.71
Std Dev	21.56

0 10 20 30 40 50 60 70

ampl



# B1L002S, U17-ch81, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	28.17
Std Dev	3.548

**Gaus mean : 28.0122**

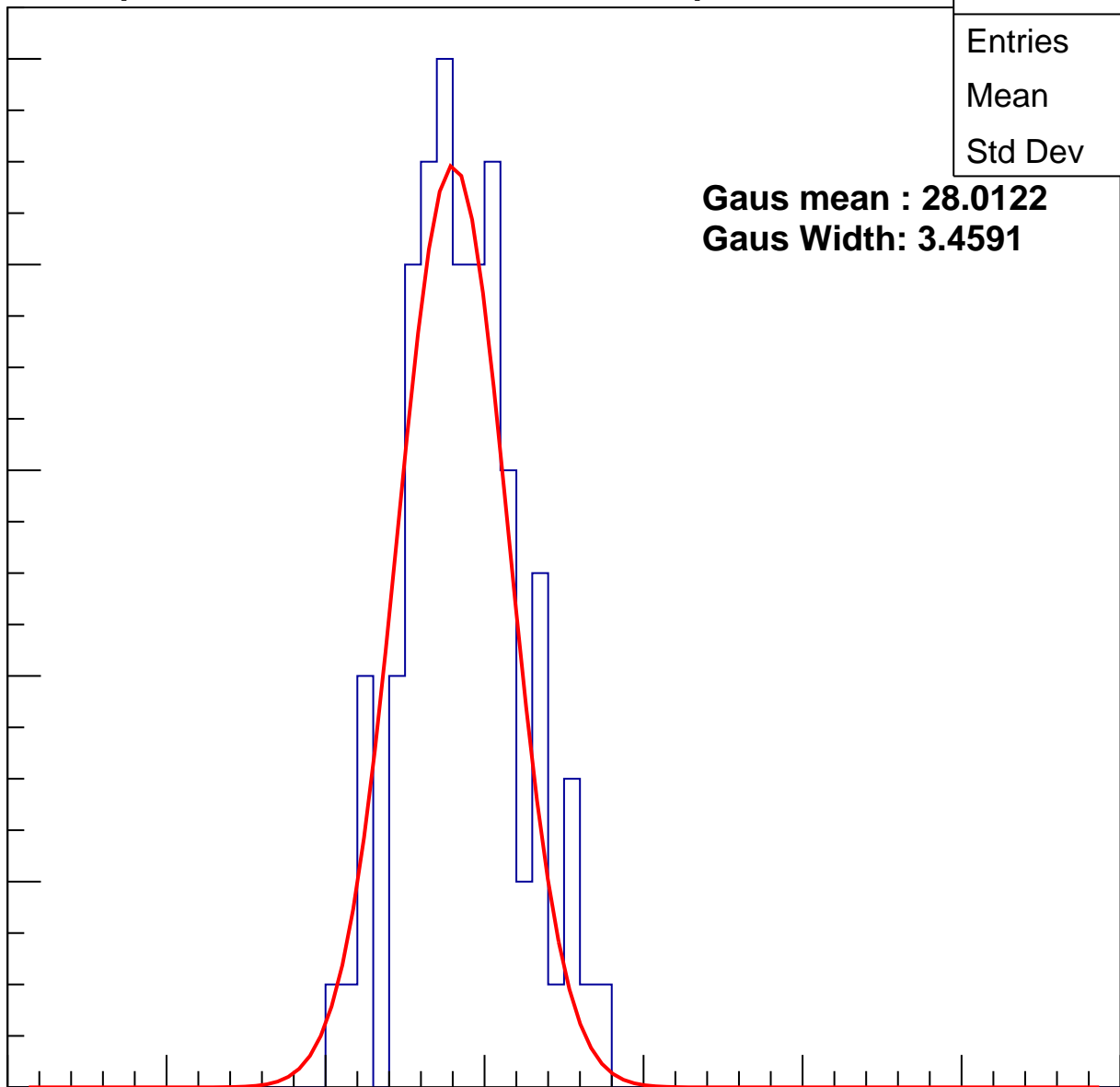
**Gaus Width: 3.4591**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch81, adc1

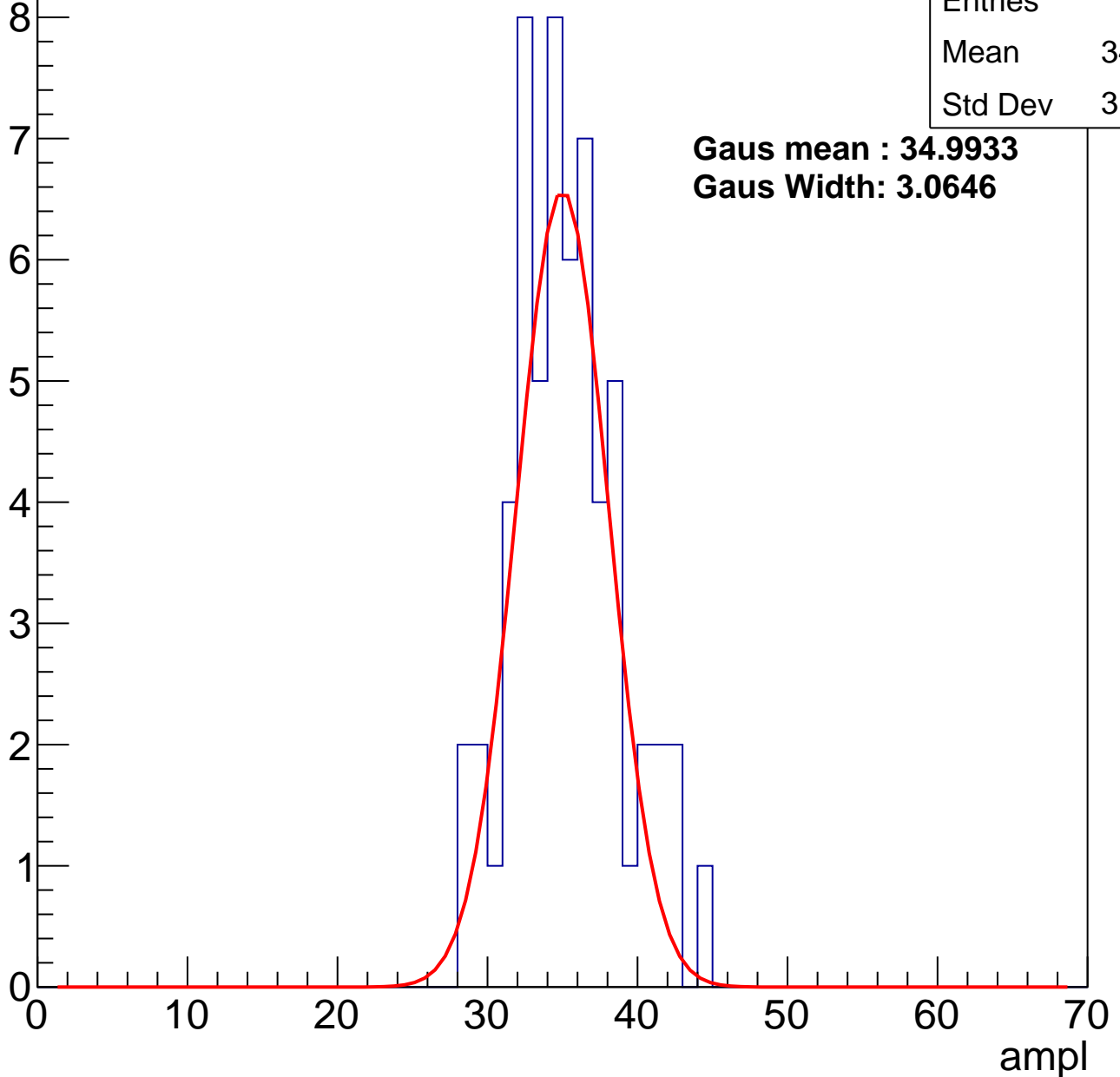
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.83
Std Dev	3.517

**Gaus mean : 34.9933**

**Gaus Width: 3.0646**



# B1L002S, U17-ch81, adc2

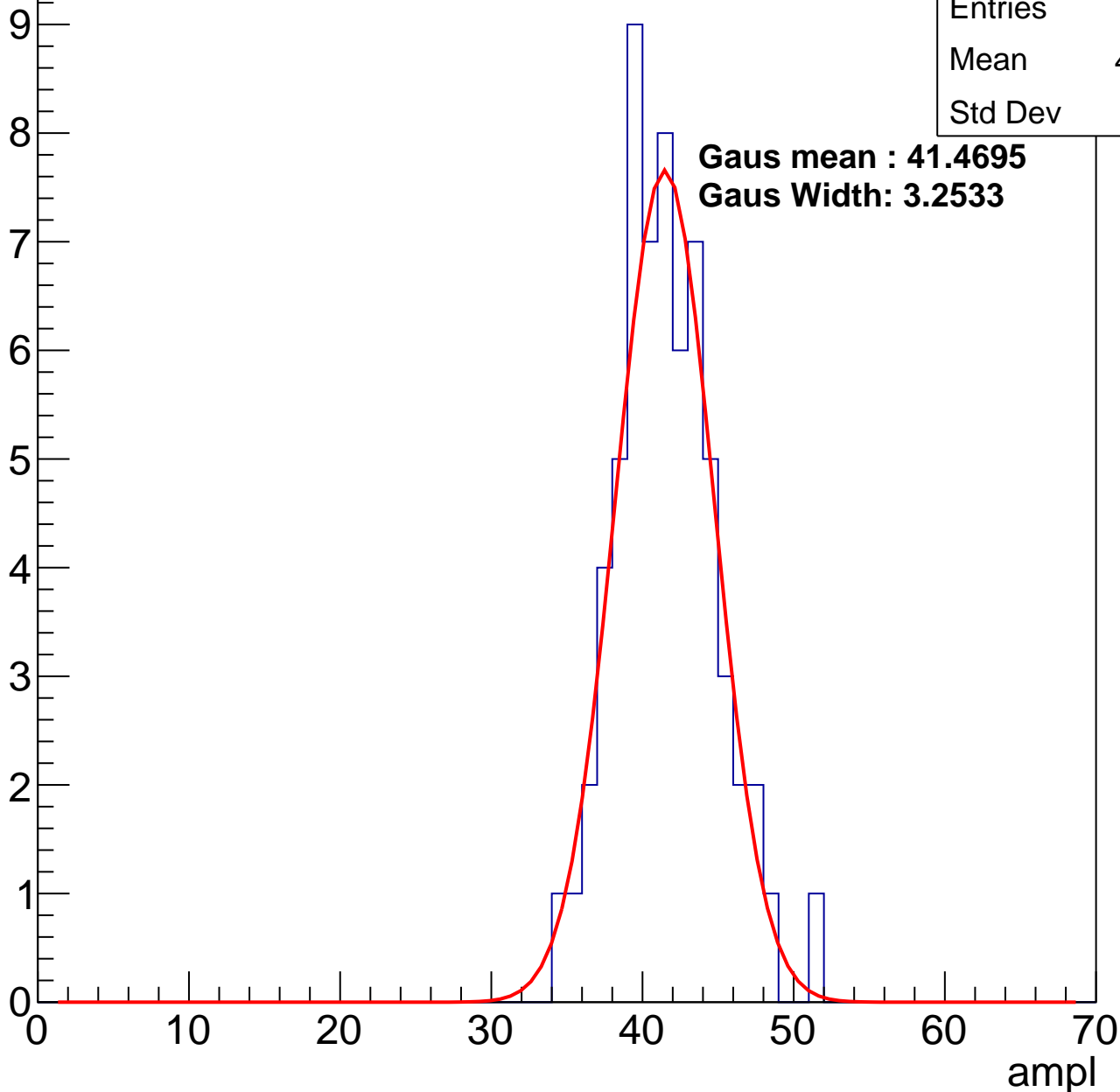
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.11
Std Dev	3.27

**Gaus mean : 41.4695**

**Gaus Width: 3.2533**

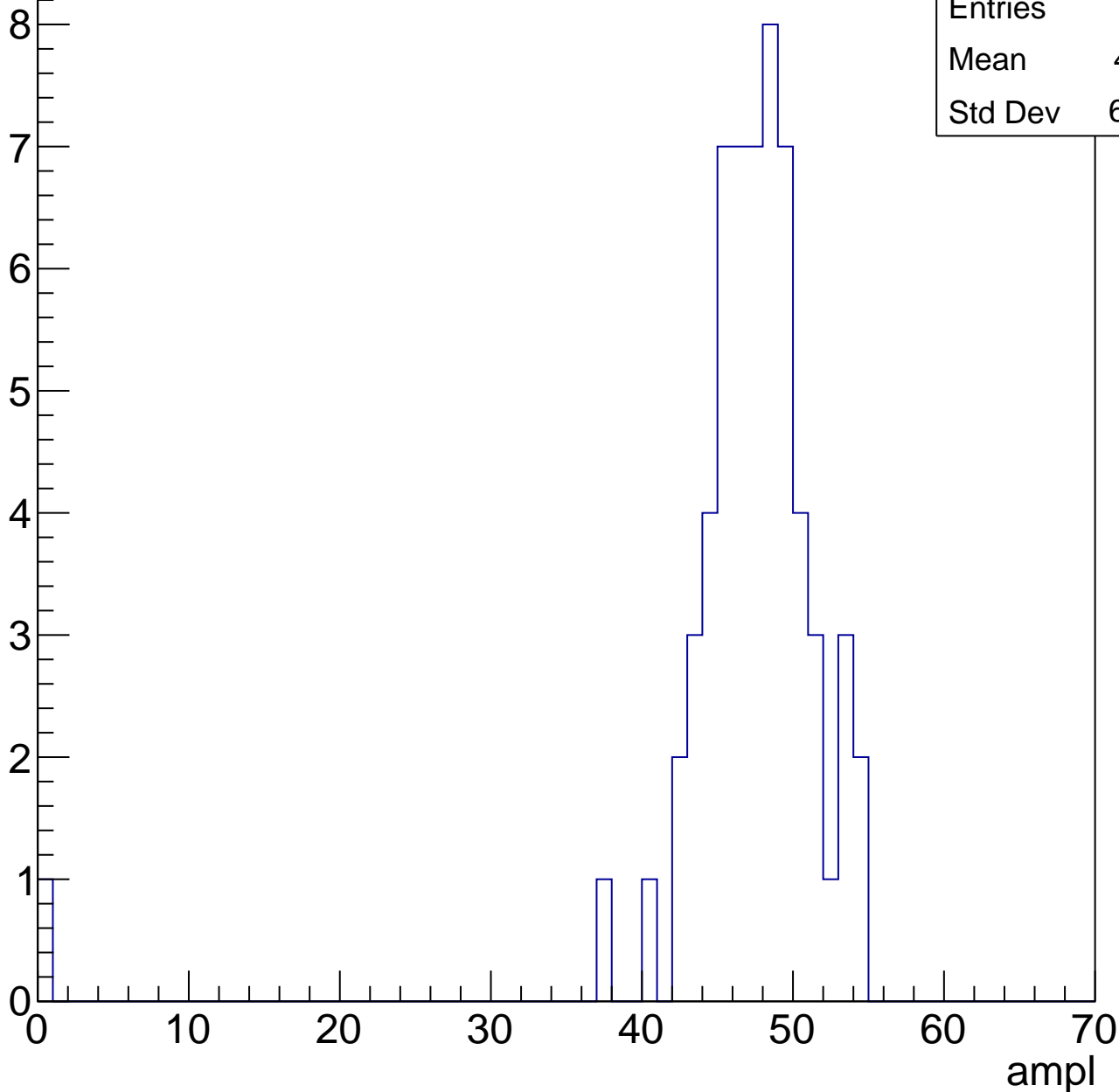


# B1L002S, U17-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.41
Std Dev	6.843

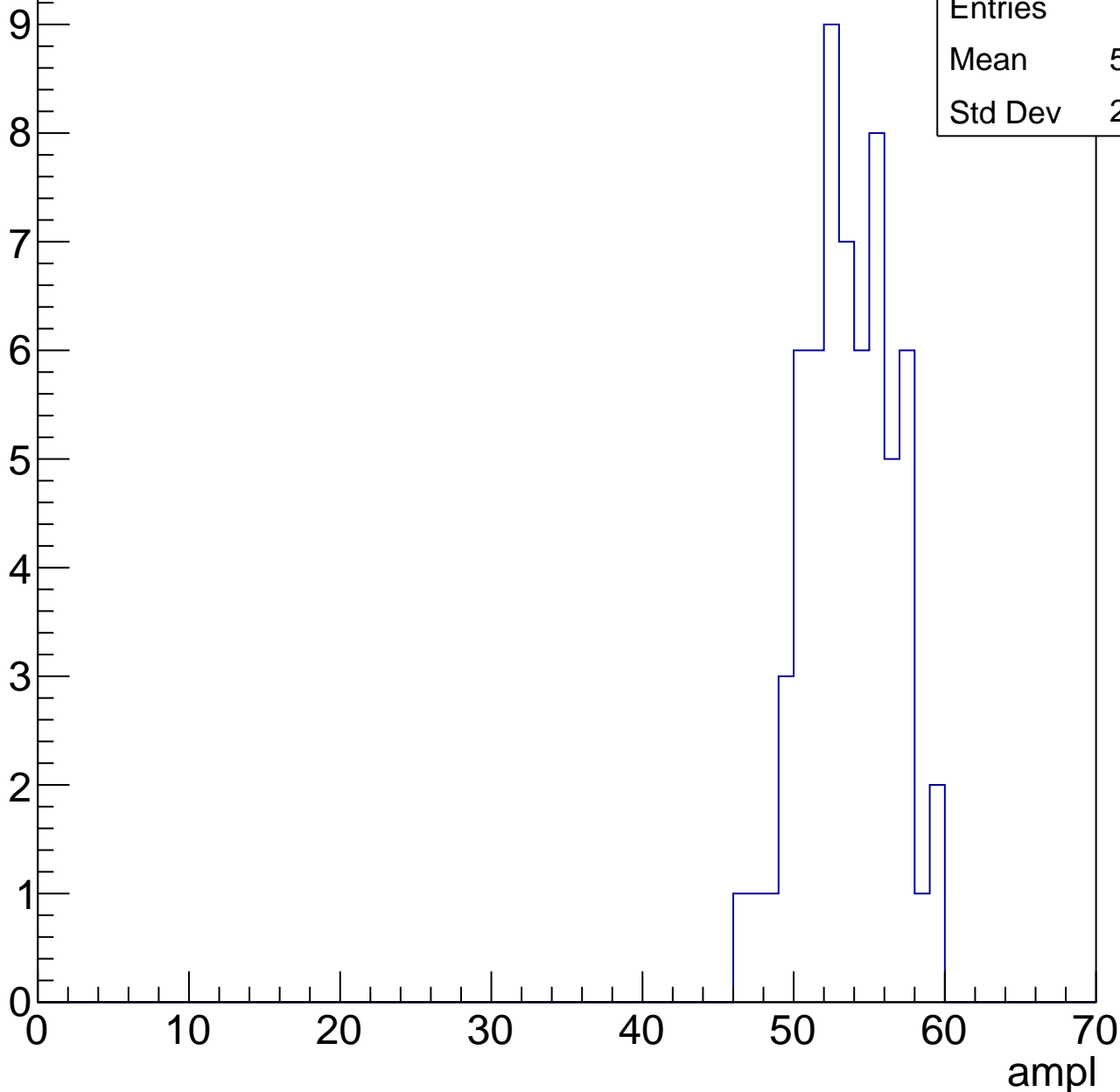


# B1L002S, U17-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	53.15
Std Dev	2.895



# B1L002S, U17-ch81, adc5

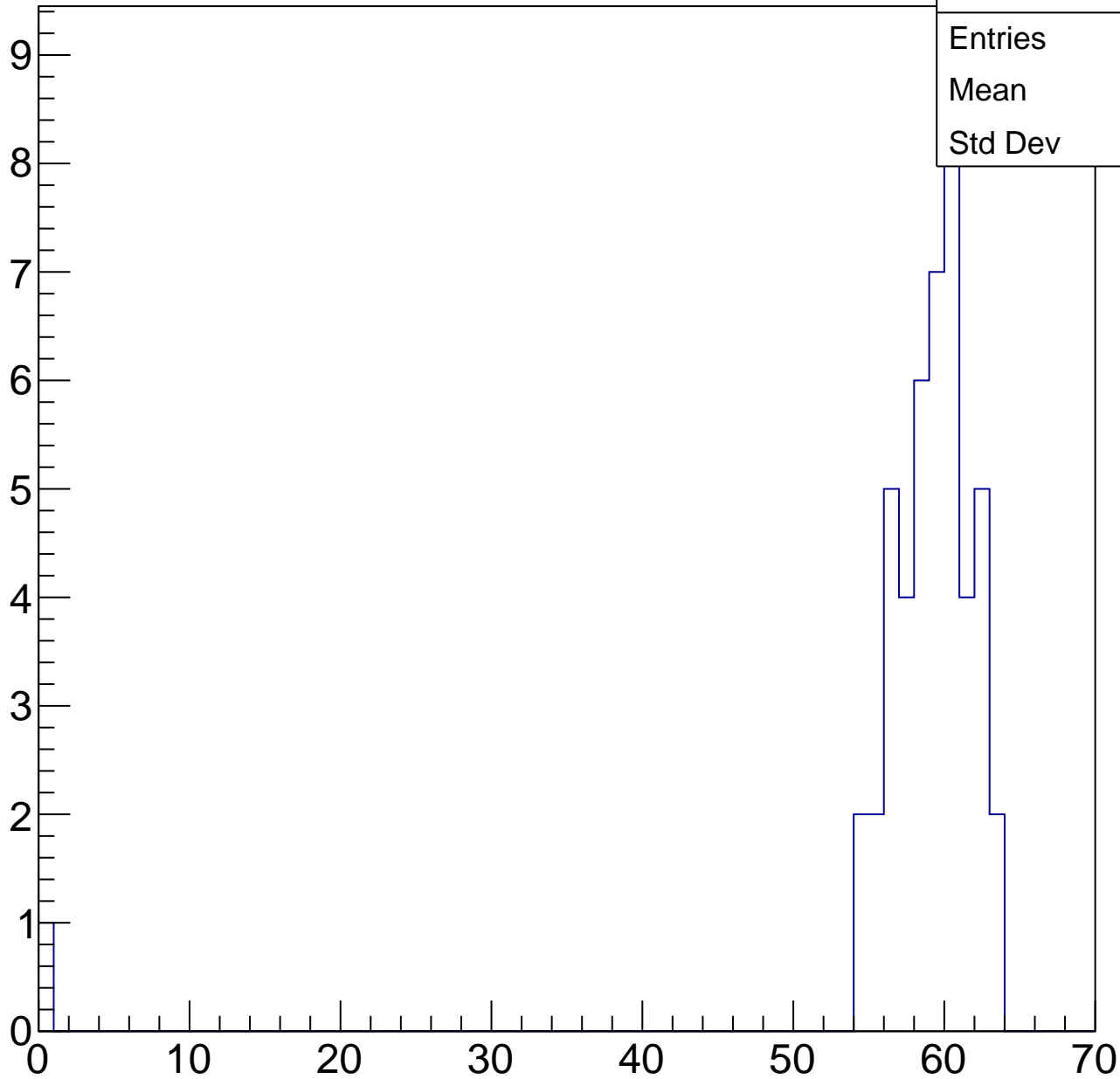
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	57.6
Std Dev	8.8

ampl

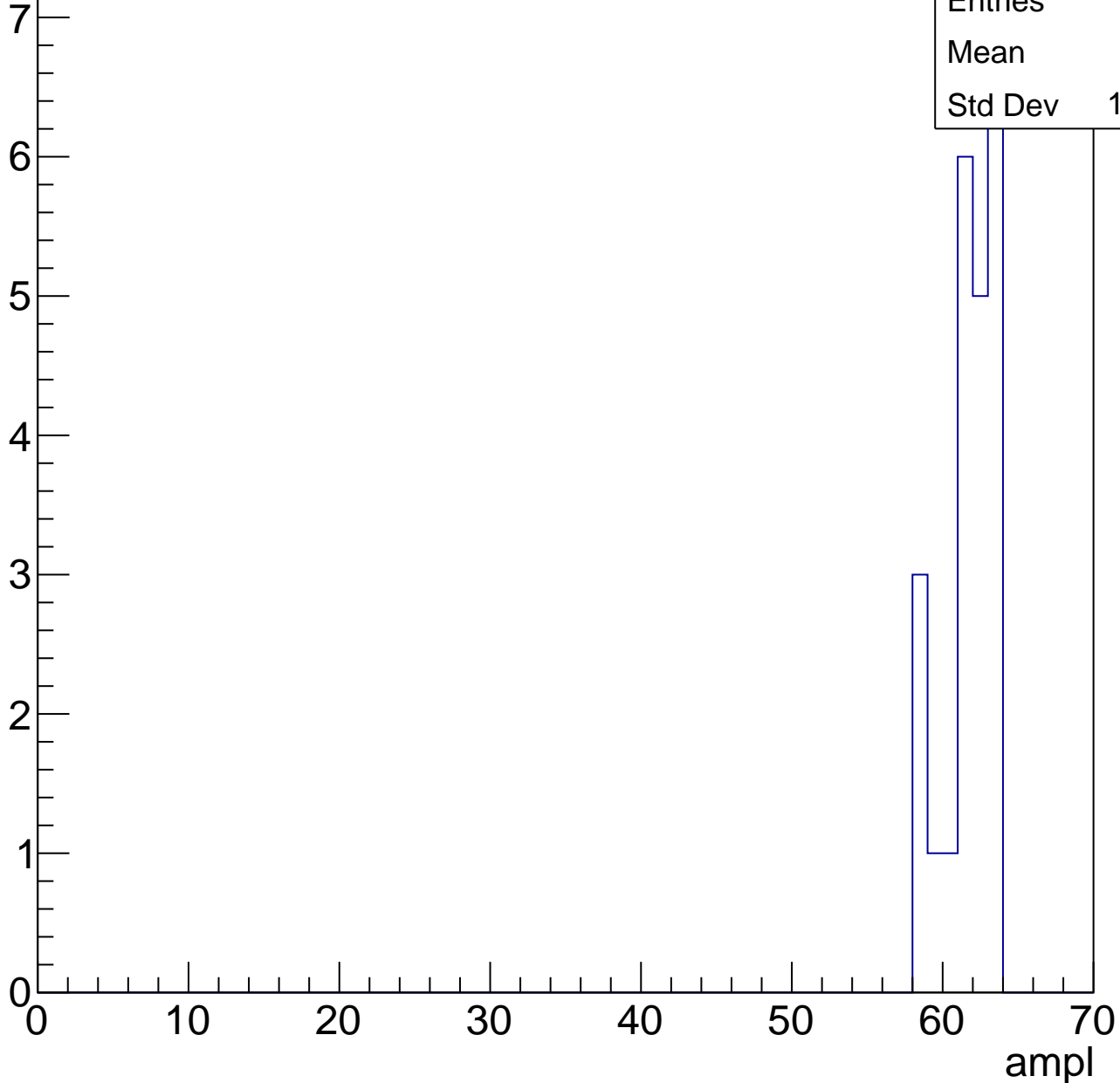


# B1L002S, U17-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.3
Std Dev	1.653

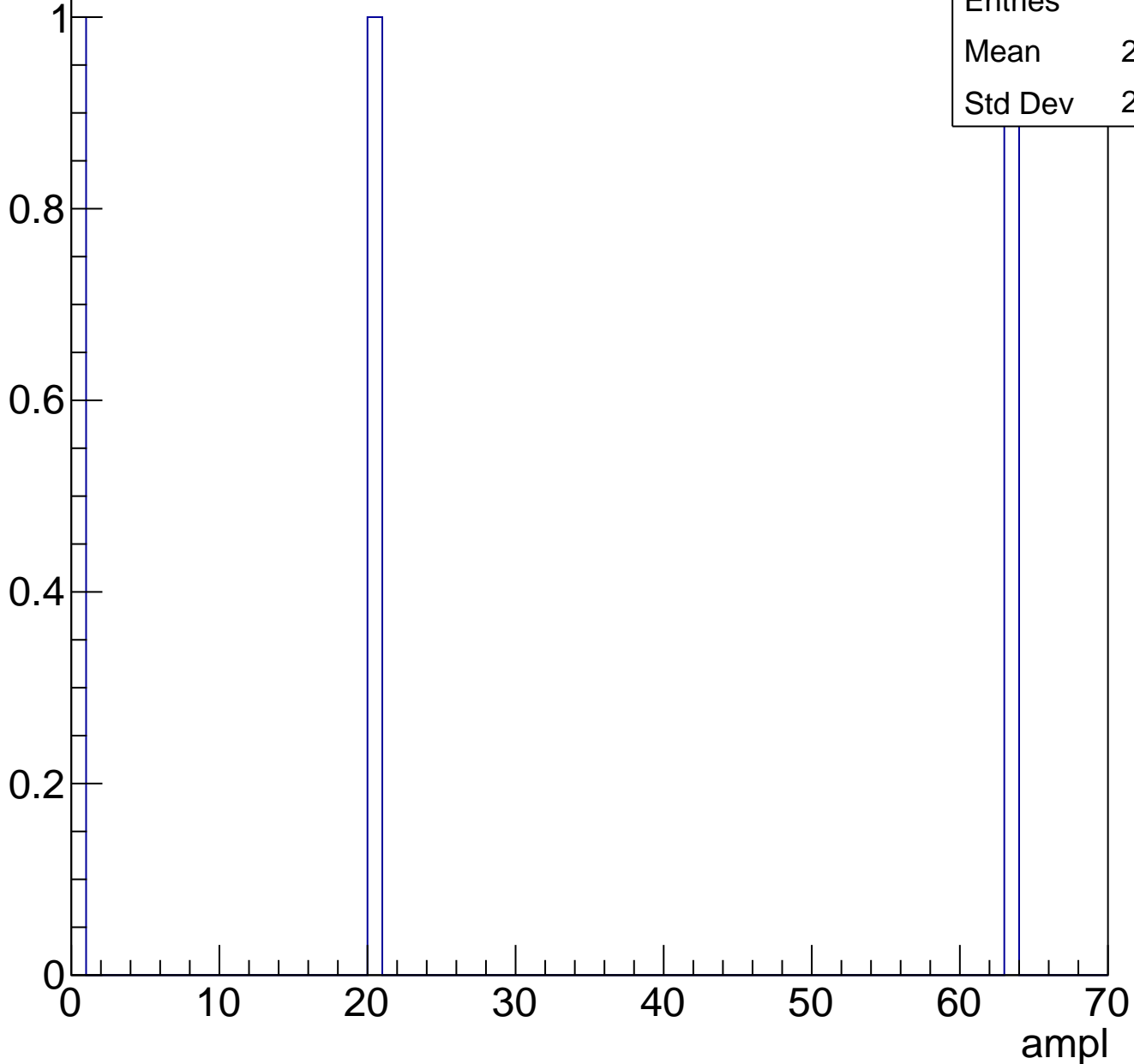




# B1L002S, U17-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	26.28

# B1L002S, U17-ch82, adc0

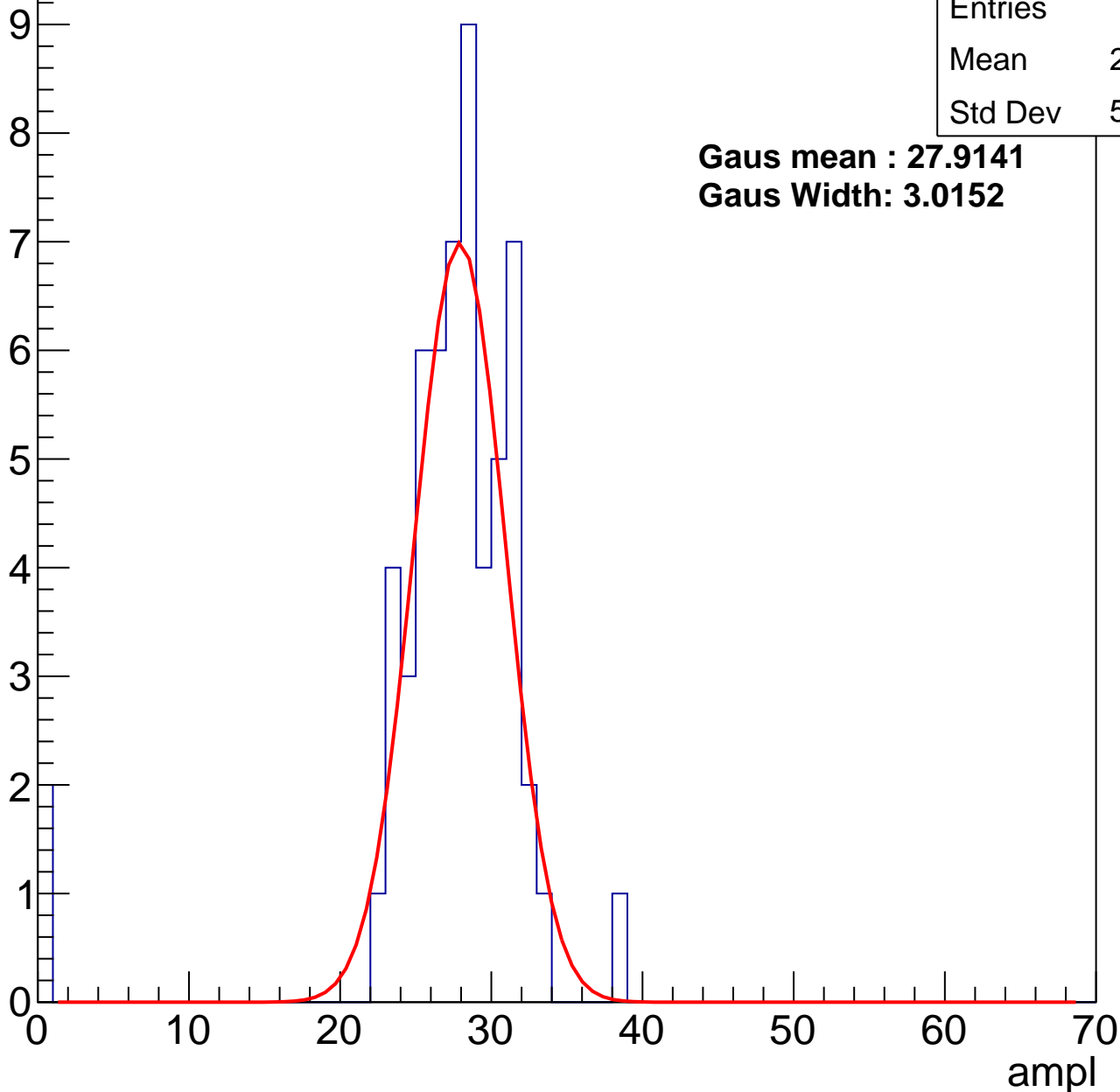
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	26.74
Std Dev	5.856

**Gaus mean : 27.9141**

**Gaus Width: 3.0152**



# B1L002S, U17-ch82, adc1

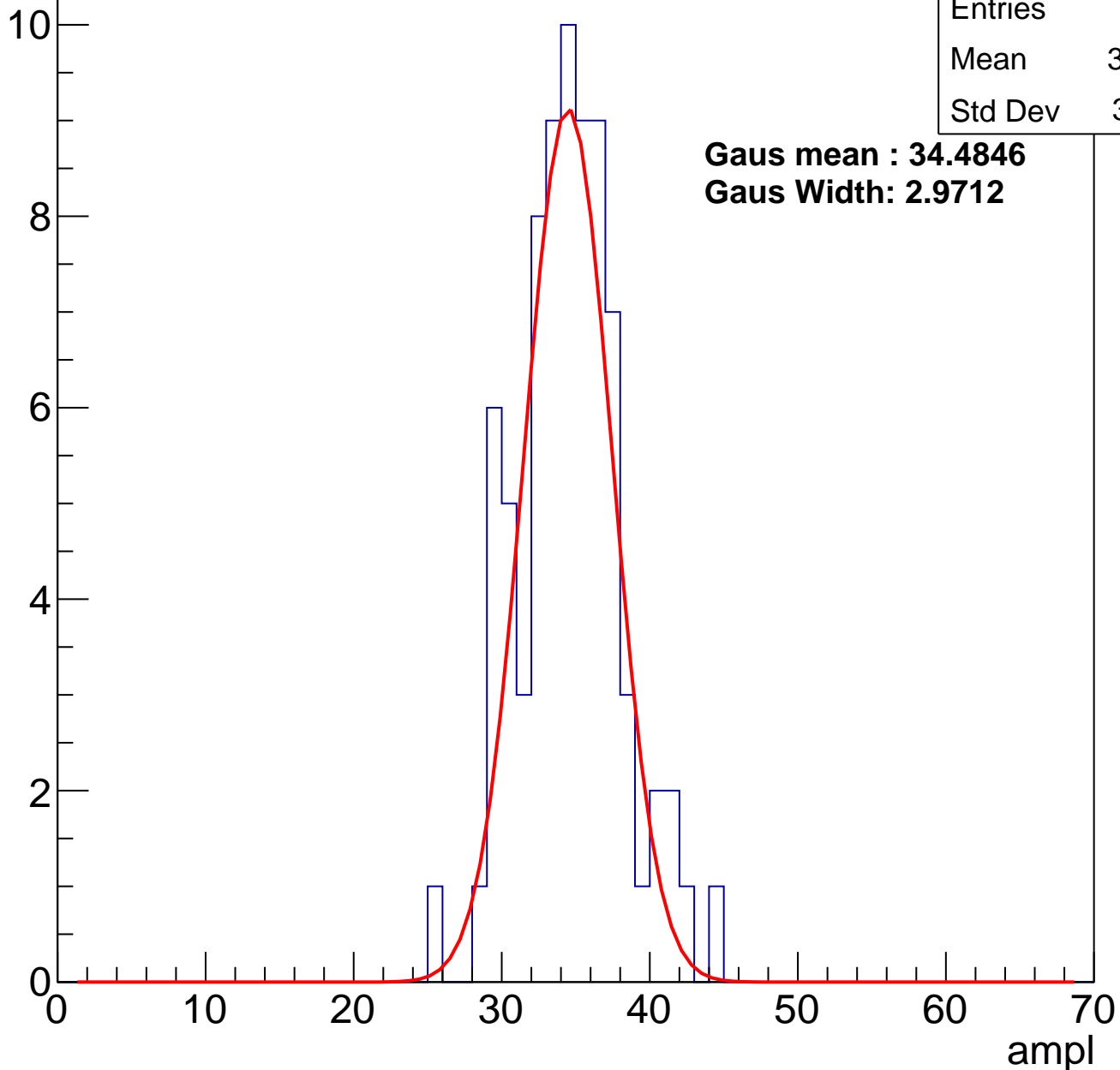
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	34.13
Std Dev	3.451

**Gaus mean : 34.4846**

**Gaus Width: 2.9712**

Entry



# B1L002S, U17-ch82, adc2

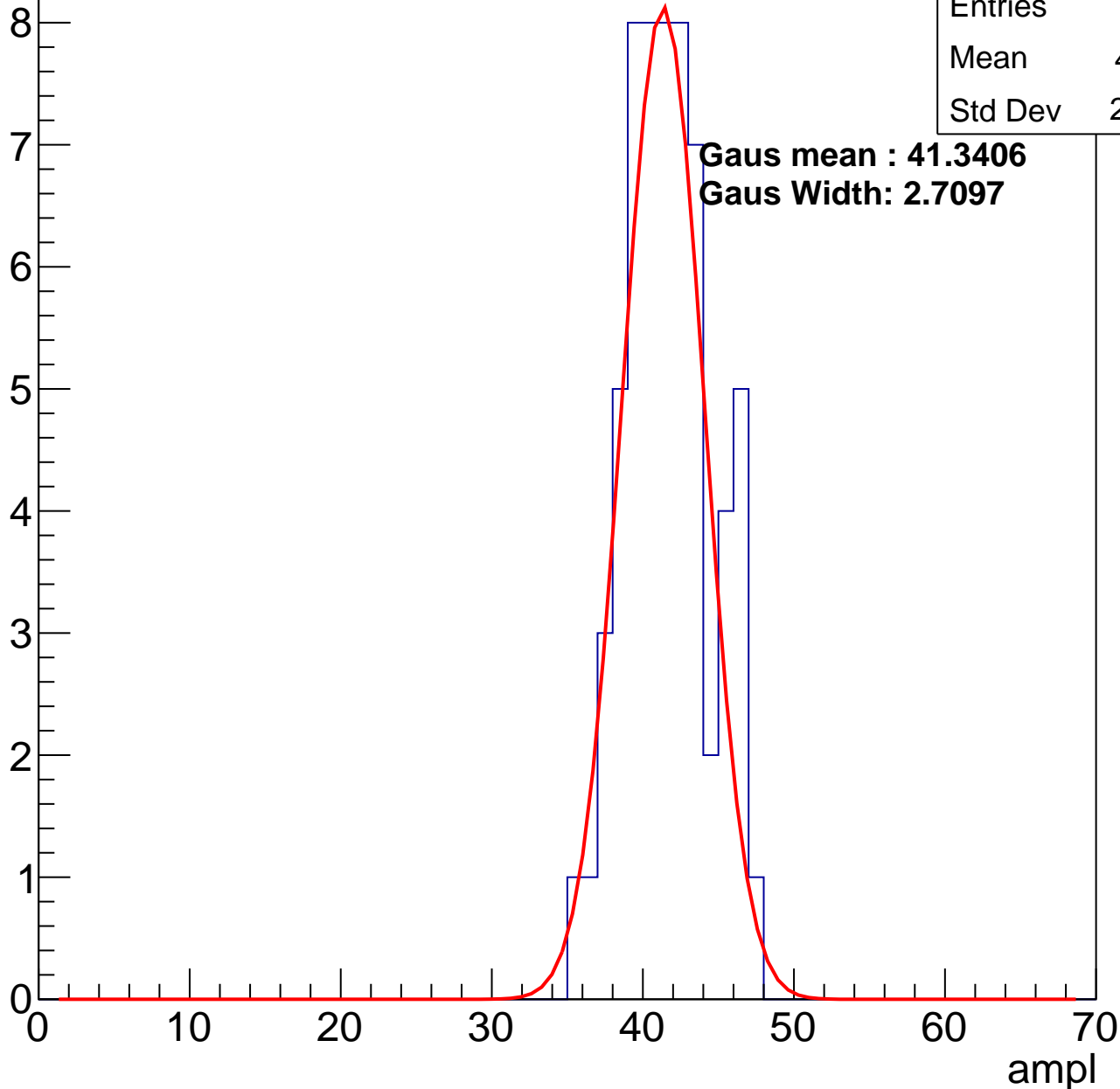
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.21
Std Dev	2.776

**Gaus mean : 41.3406**

**Gaus Width: 2.7097**

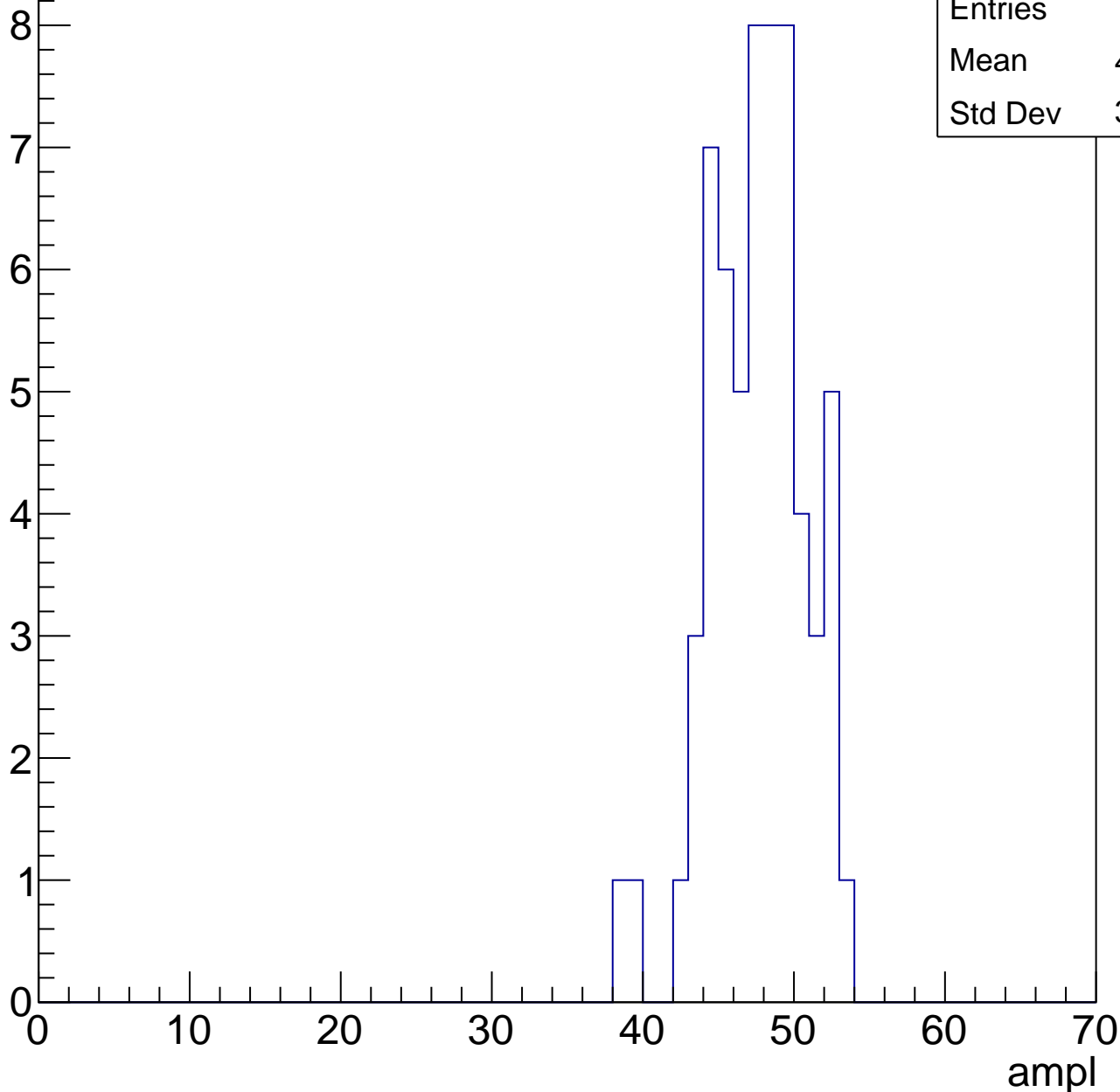


# B1L002S, U17-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	47.11
Std Dev	3.121

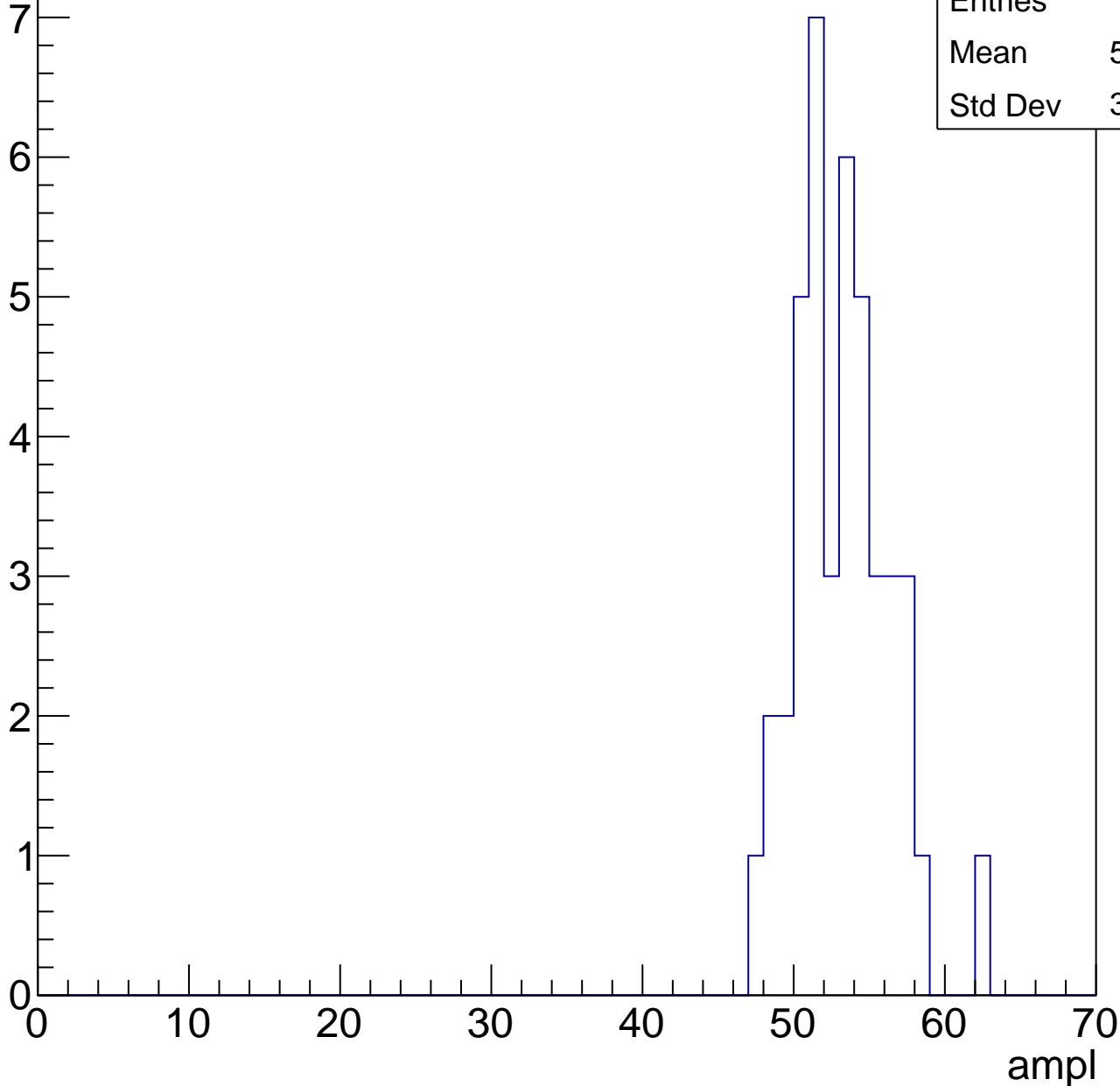


# B1L002S, U17-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	52.76
Std Dev	3.046

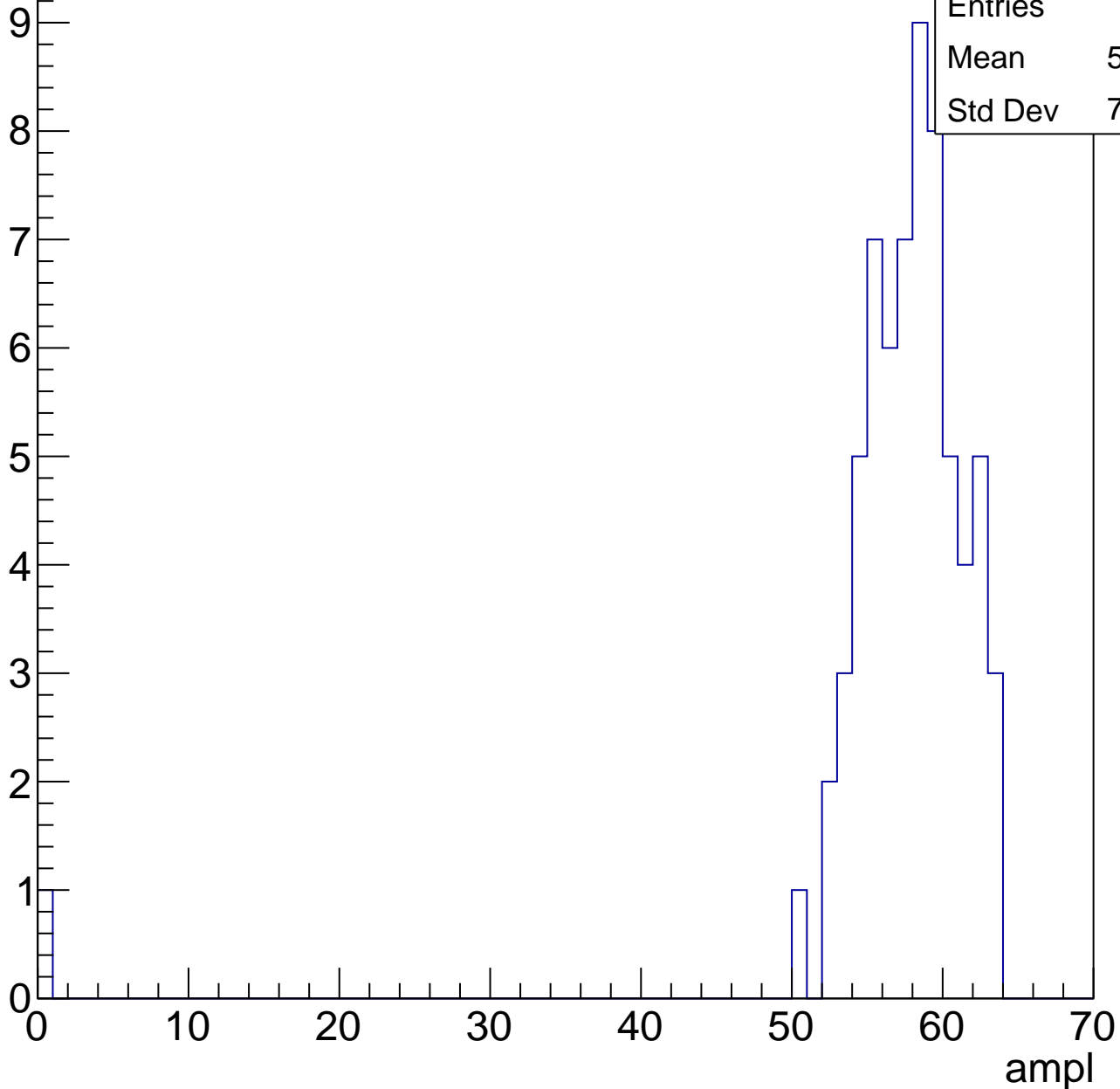


# B1L002S, U17-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	56.67
Std Dev	7.638

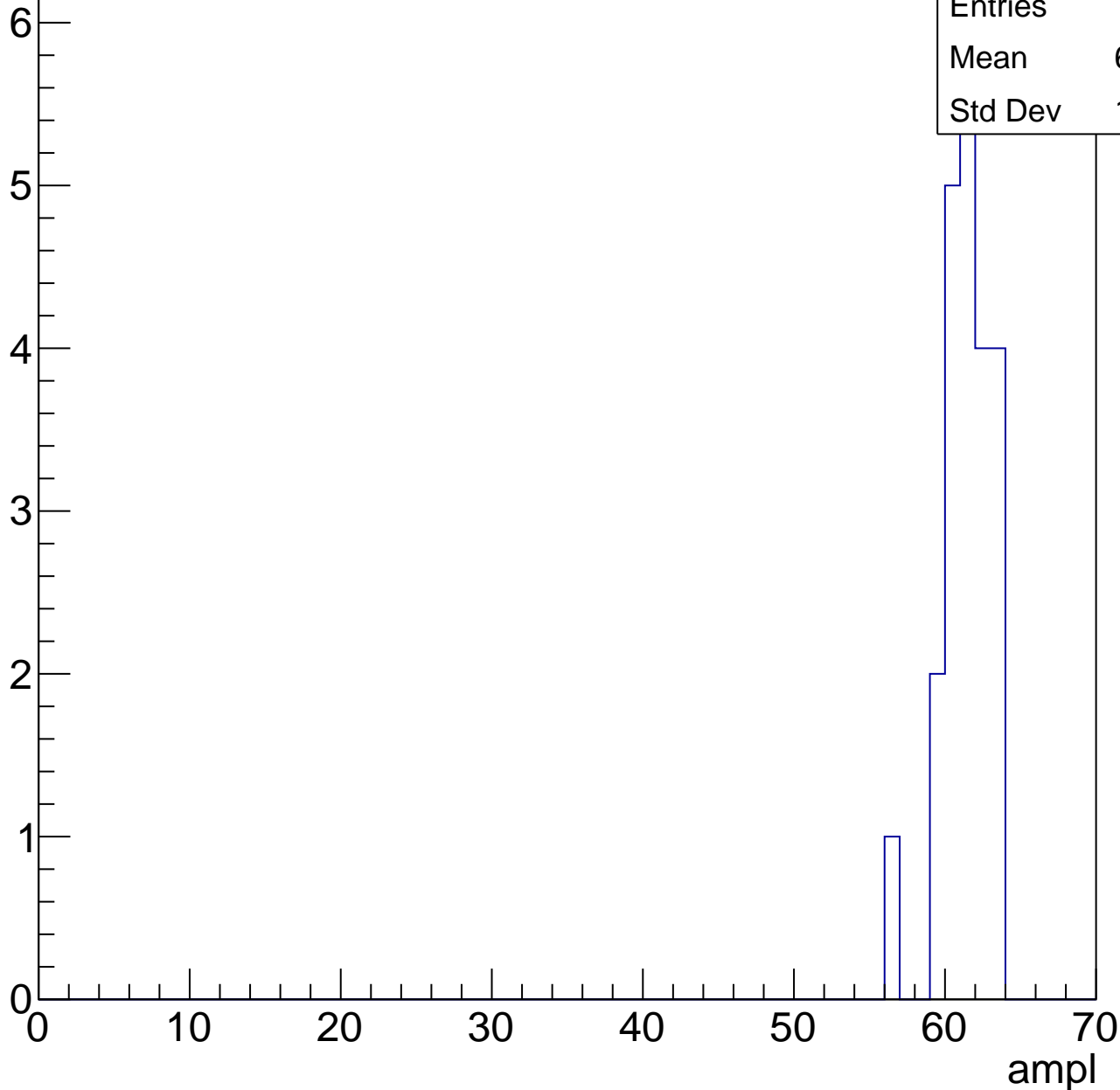


# B1L002S, U17-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	60.91
Std Dev	1.621

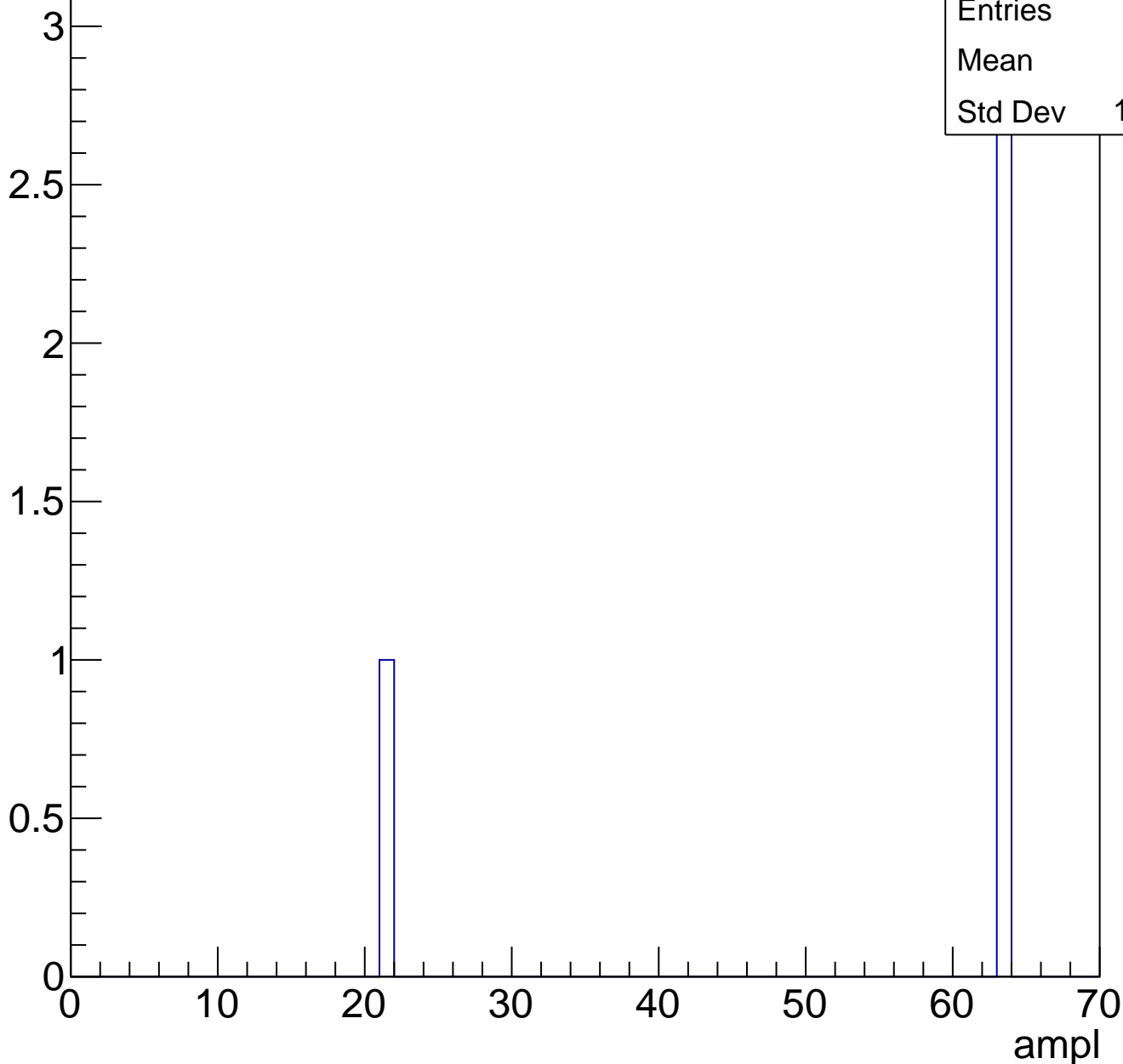




# B1L002S, U17-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch83, adc0

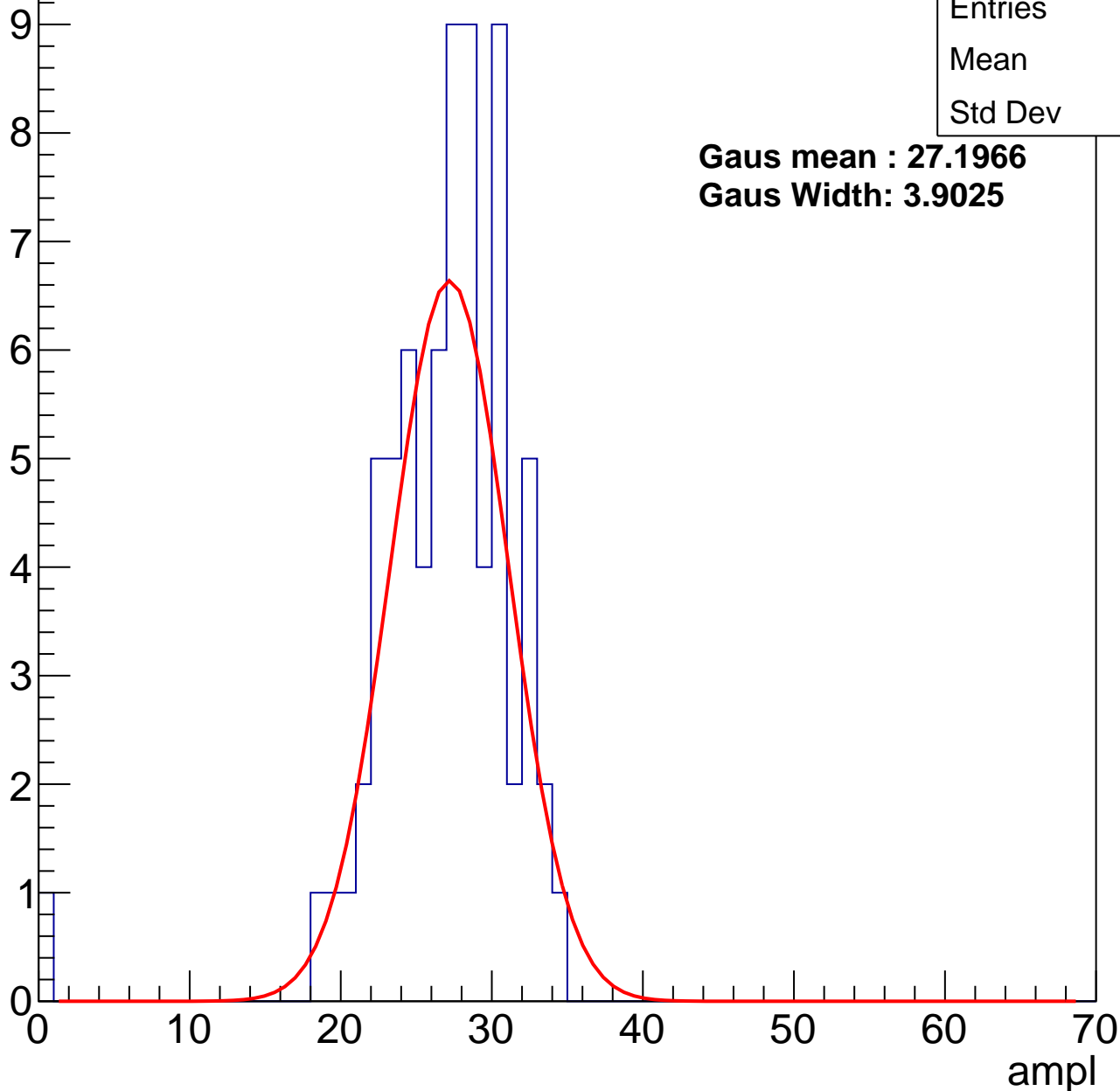
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	26.4
Std Dev	4.73

**Gaus mean : 27.1966**

**Gaus Width: 3.9025**



# B1L002S, U17-ch83, adc1

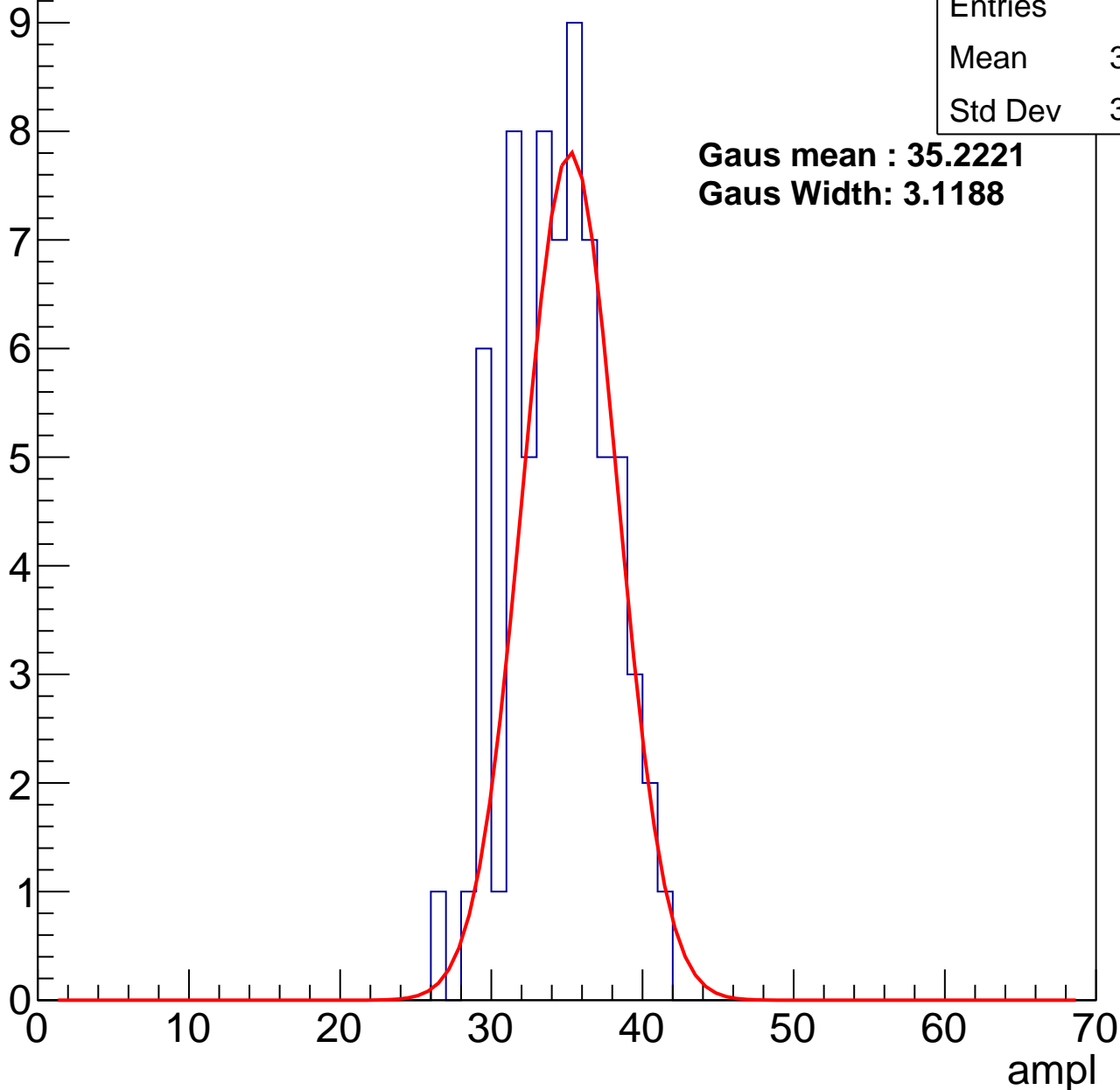
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.03
Std Dev	3.239

**Gaus mean : 35.2221**

**Gaus Width: 3.1188**



# B1L002S, U17-ch83, adc2

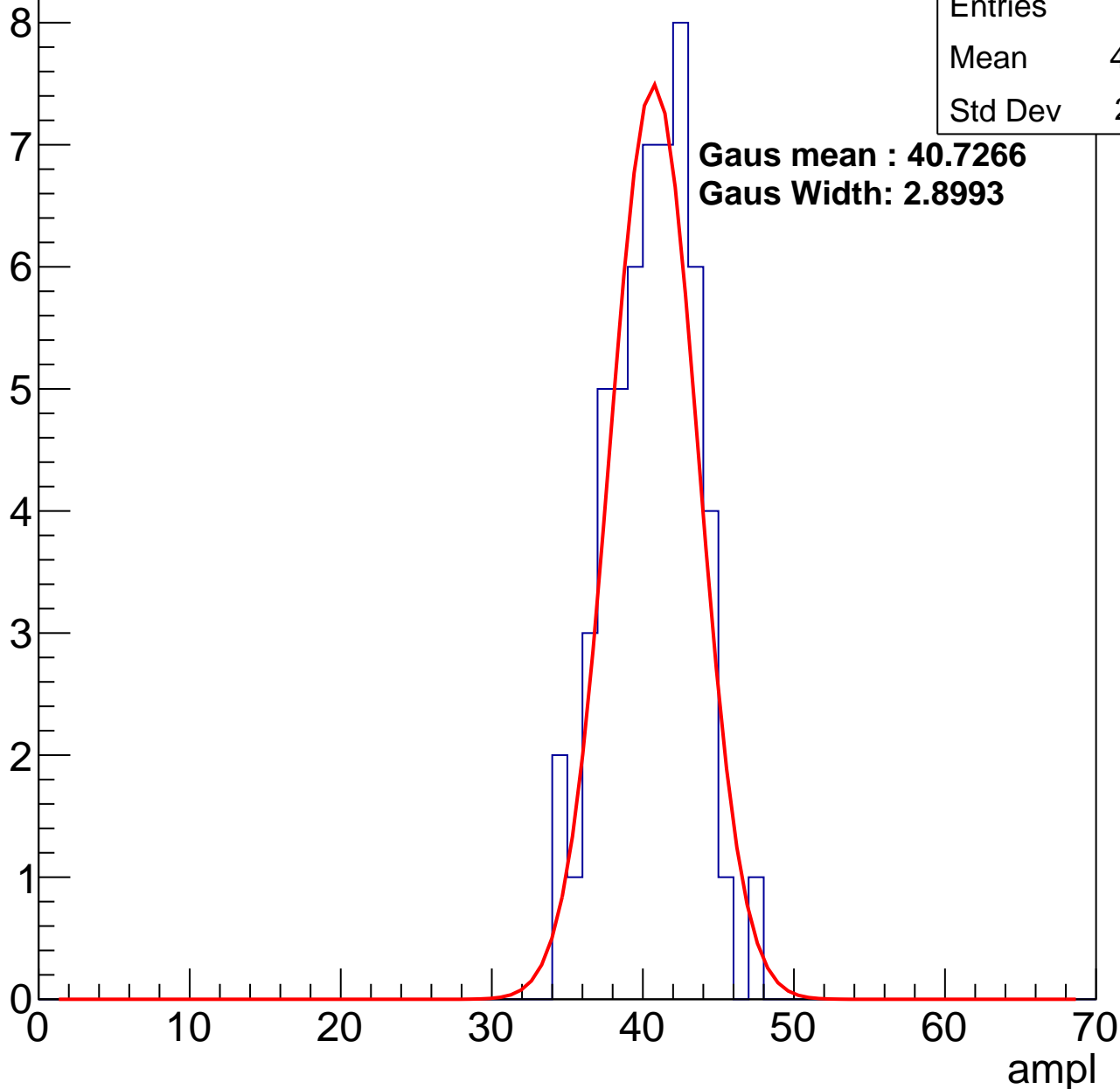
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.16
Std Dev	2.821

**Gaus mean : 40.7266**

**Gaus Width: 2.8993**



# B1L002S, U17-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

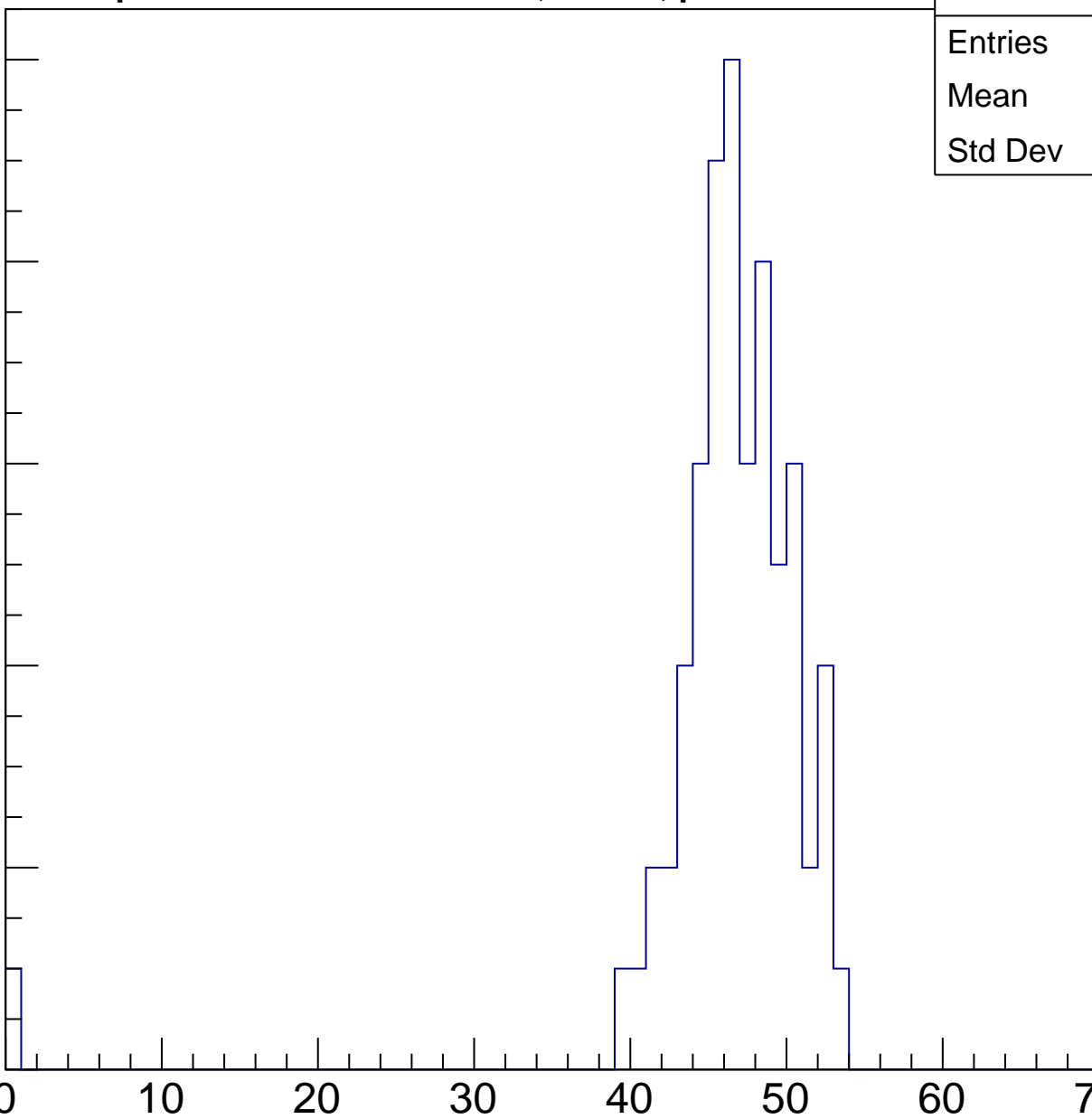
Entries	68
Mean	45.88
Std Dev	6.386

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

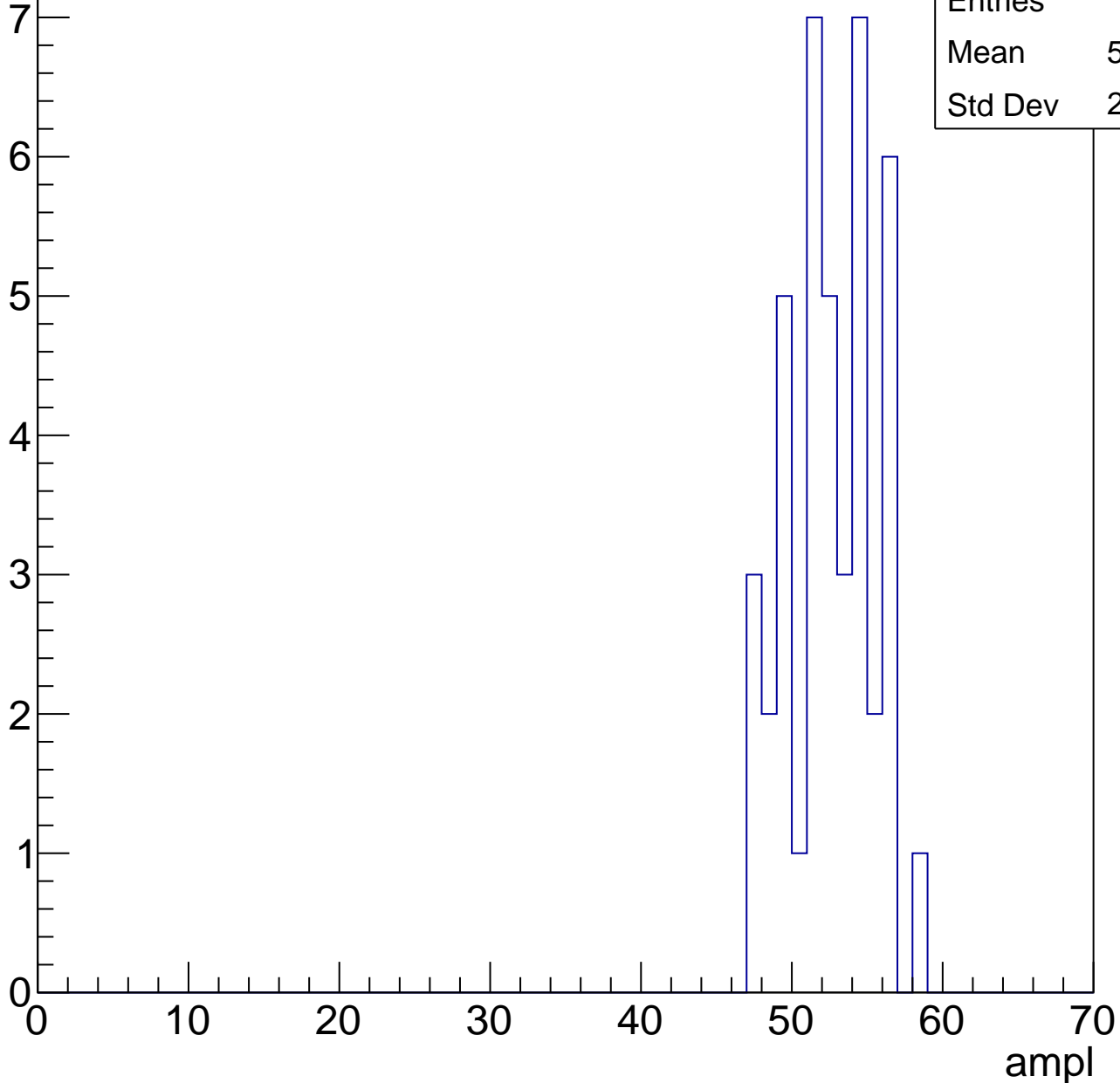


# B1L002S, U17-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

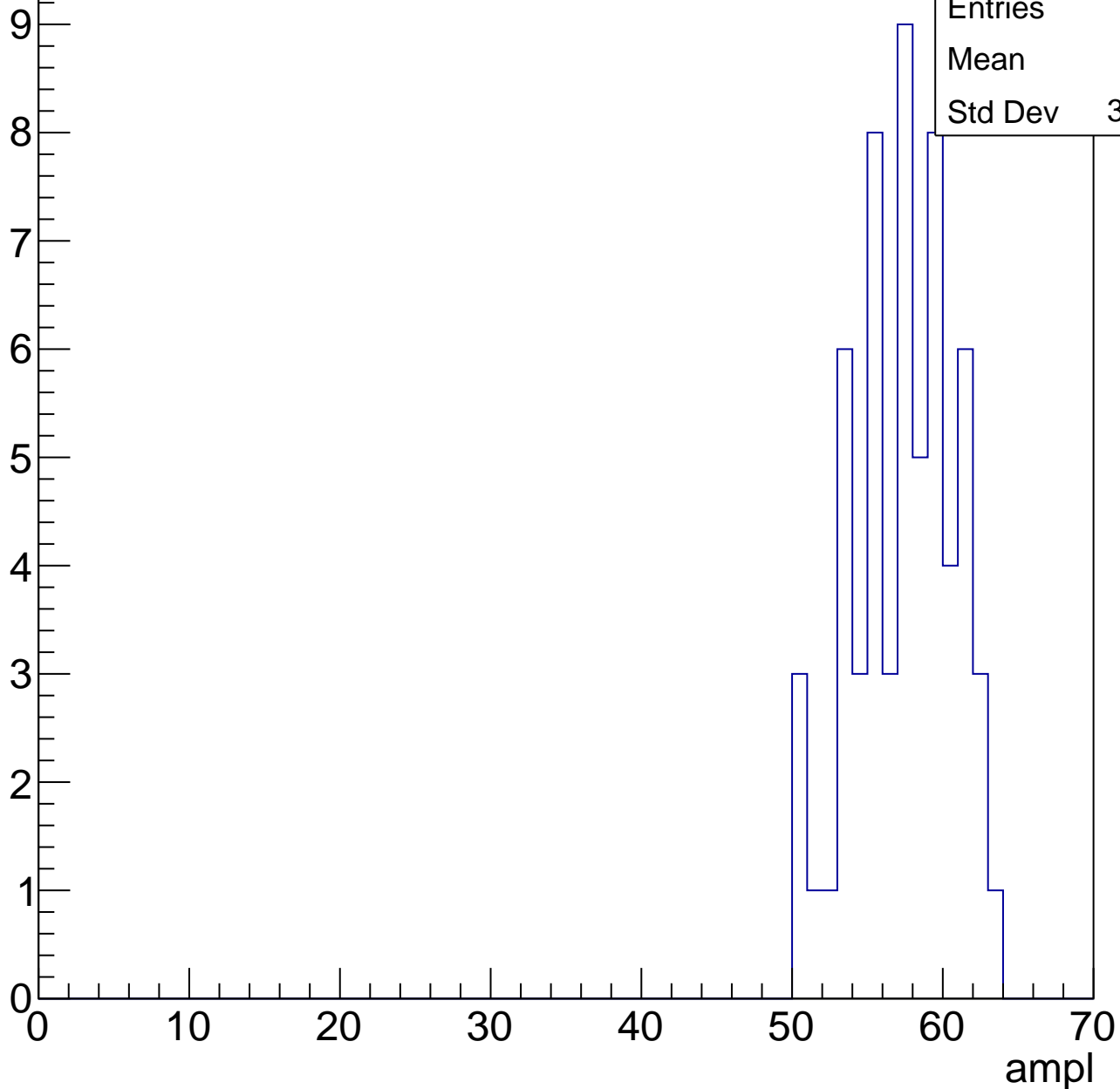
Entries	42
Mean	52.14
Std Dev	2.858



# B1L002S, U17-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

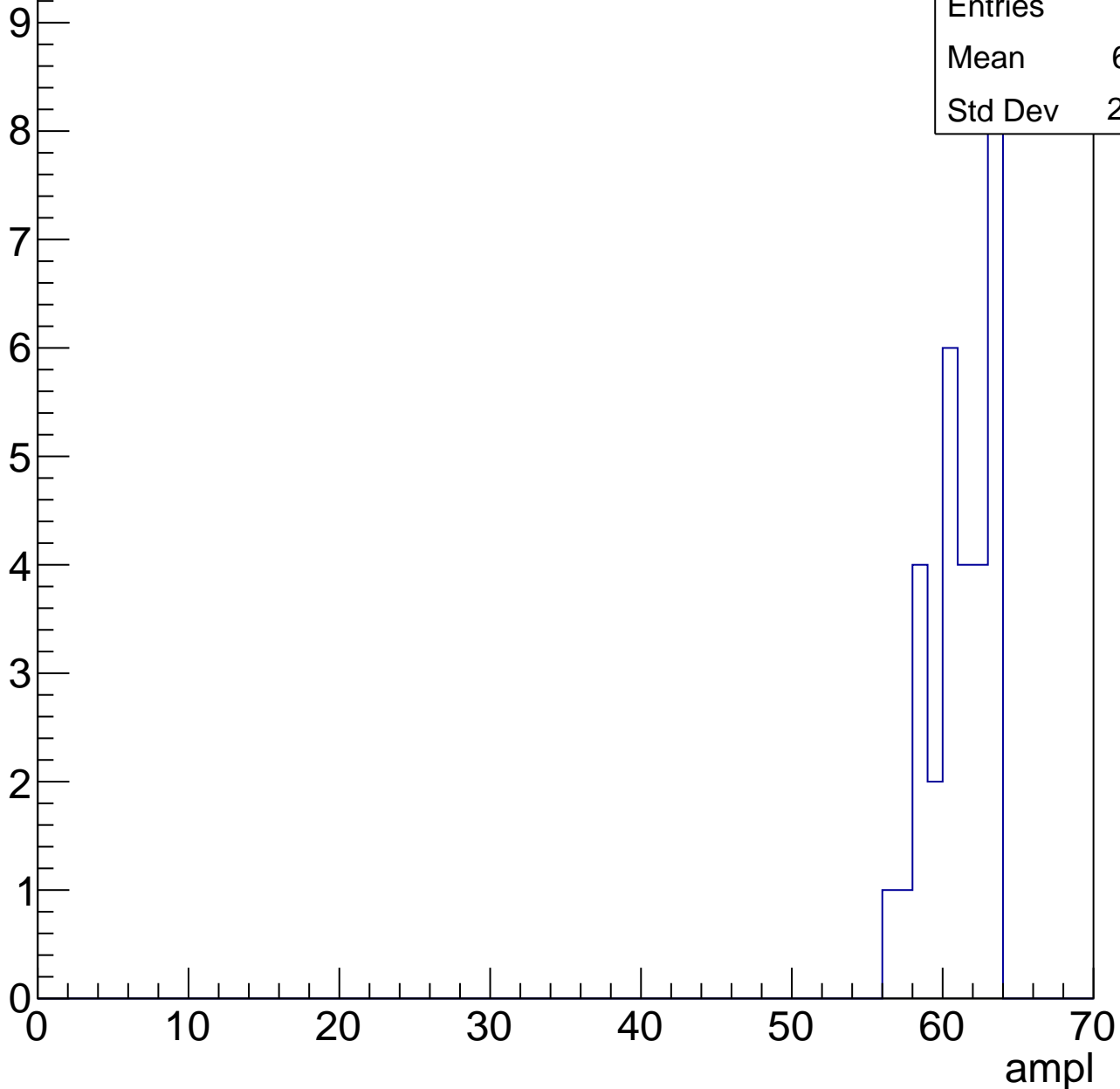


# B1L002S, U17-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	60.71
Std Dev	2.035

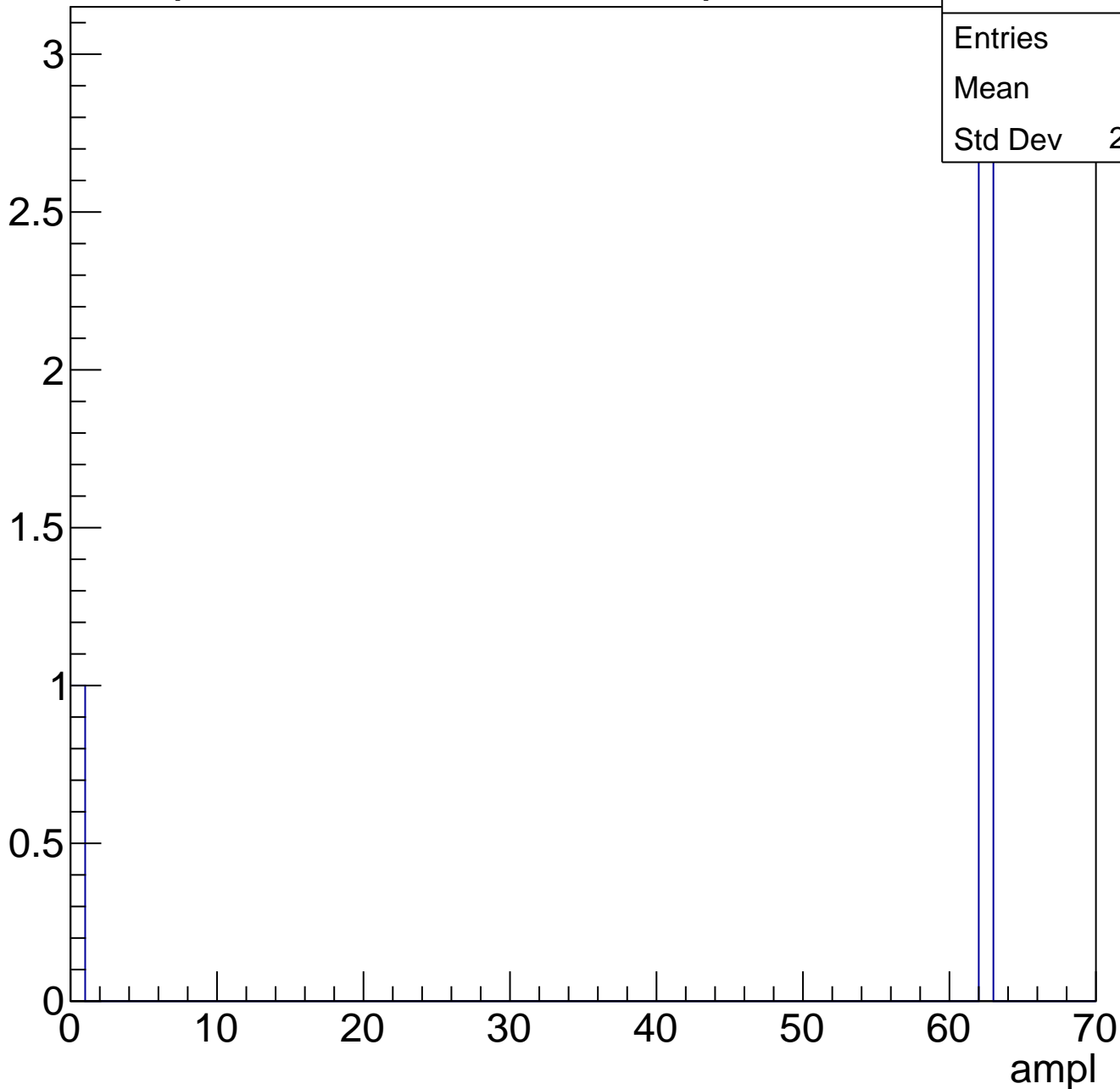




# B1L002S, U17-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch84, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	27.75
Std Dev	4.356

**Gaus mean : 28.5479**

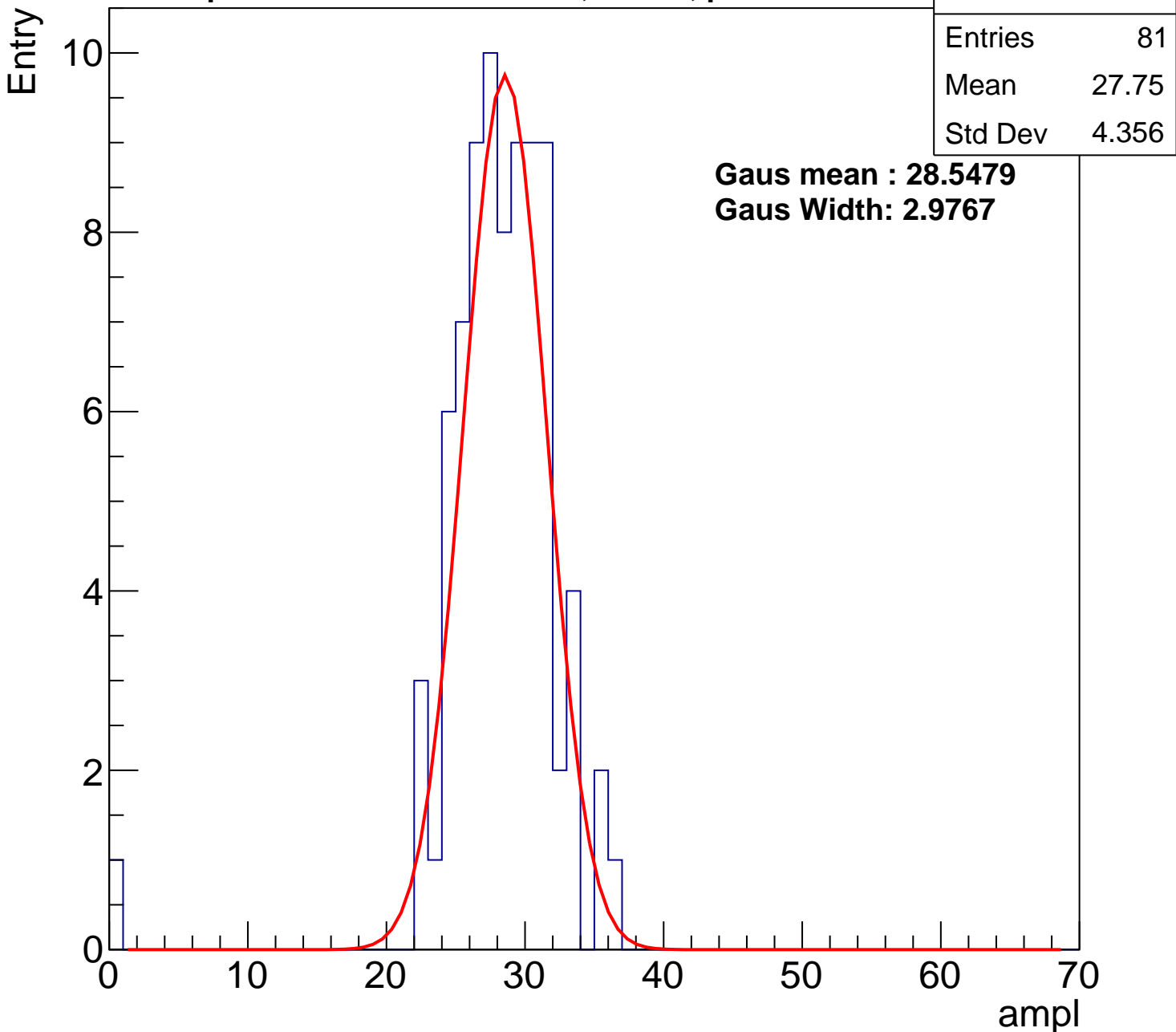
**Gaus Width: 2.9767**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch84, adc1

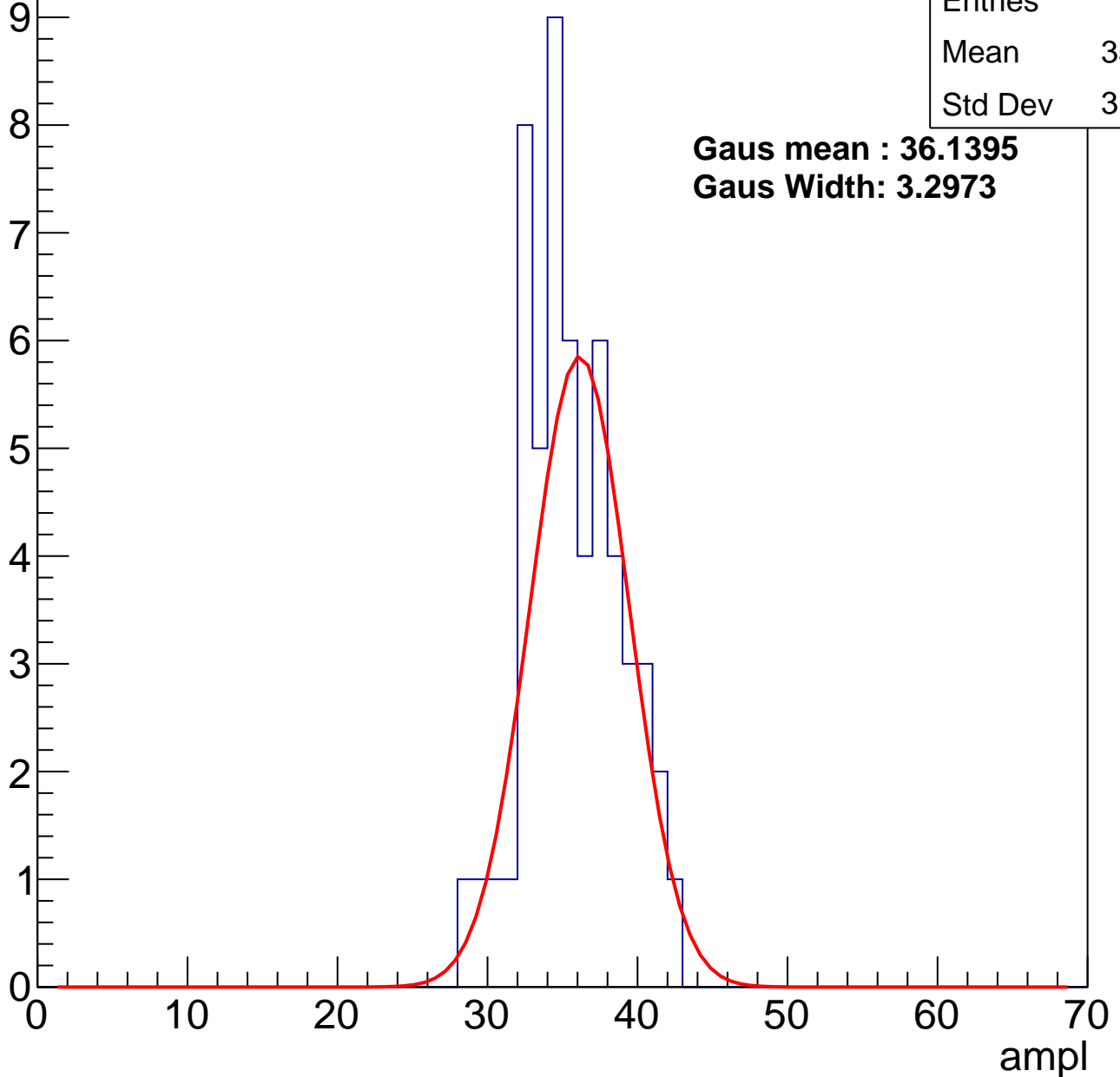
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	35.16
Std Dev	3.115

**Gaus mean : 36.1395**

**Gaus Width: 3.2973**



# B1L002S, U17-ch84, adc2

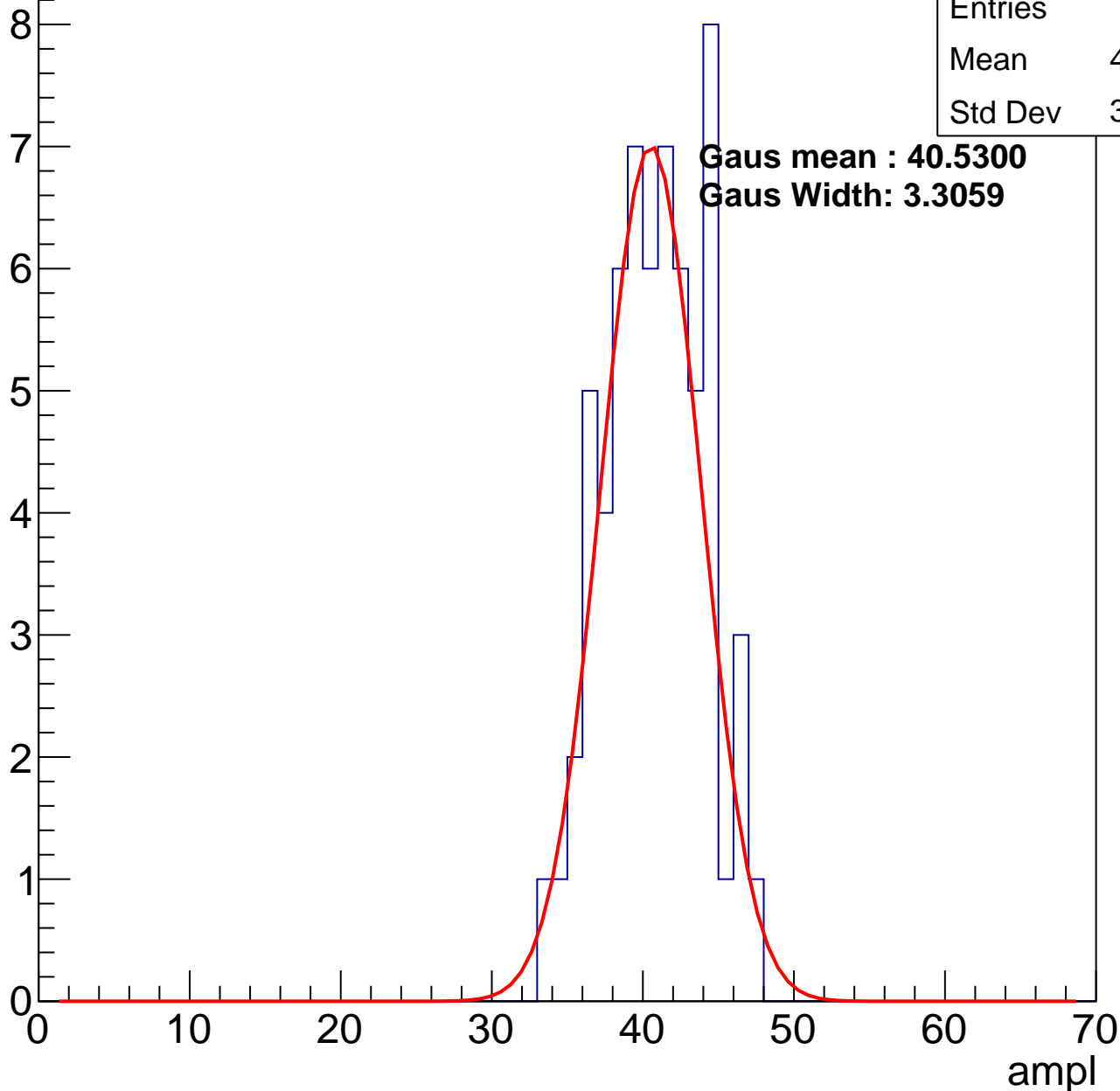
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.35
Std Dev	3.237

**Gaus mean : 40.5300**

**Gaus Width: 3.3059**

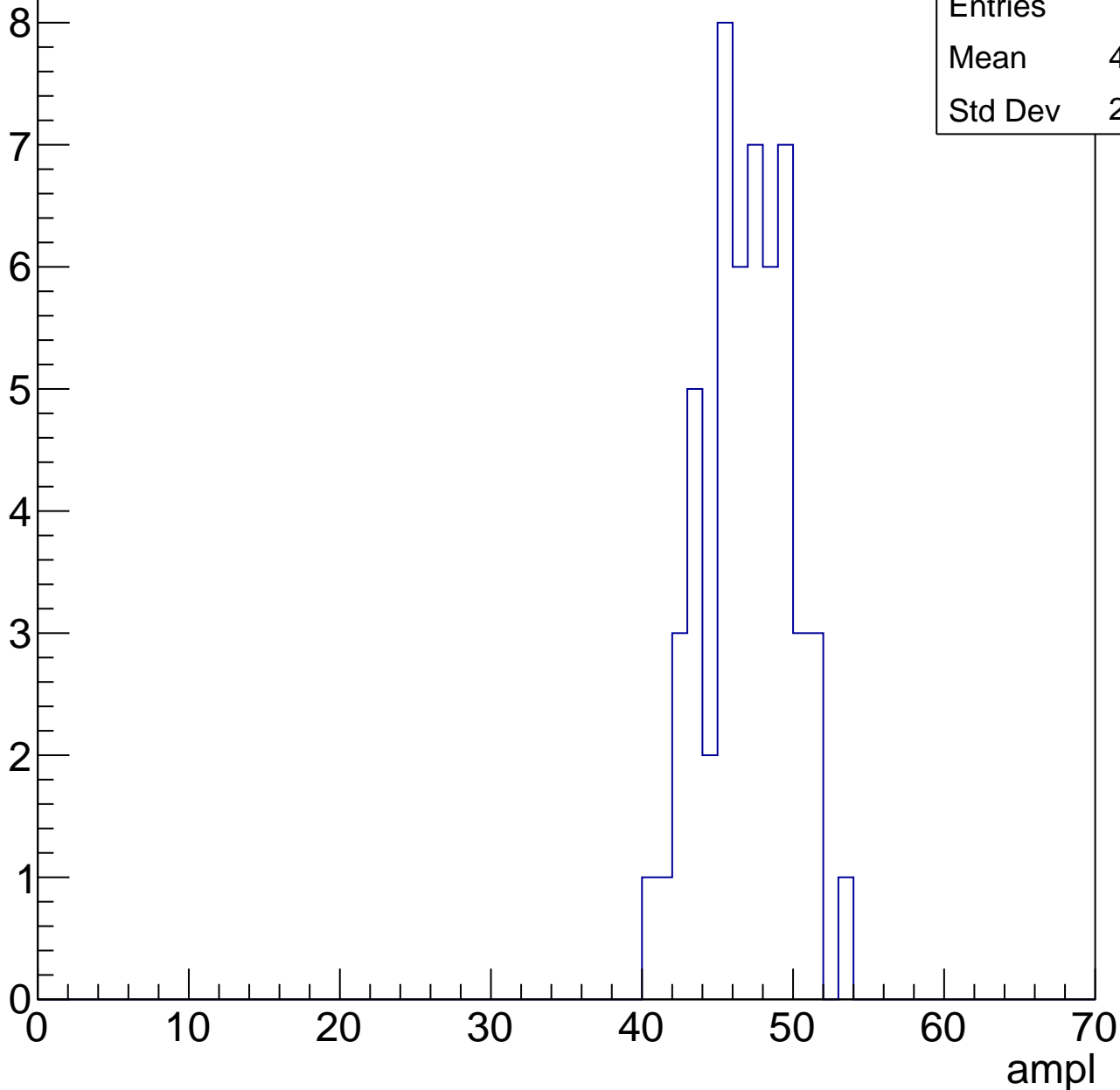


# B1L002S, U17-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

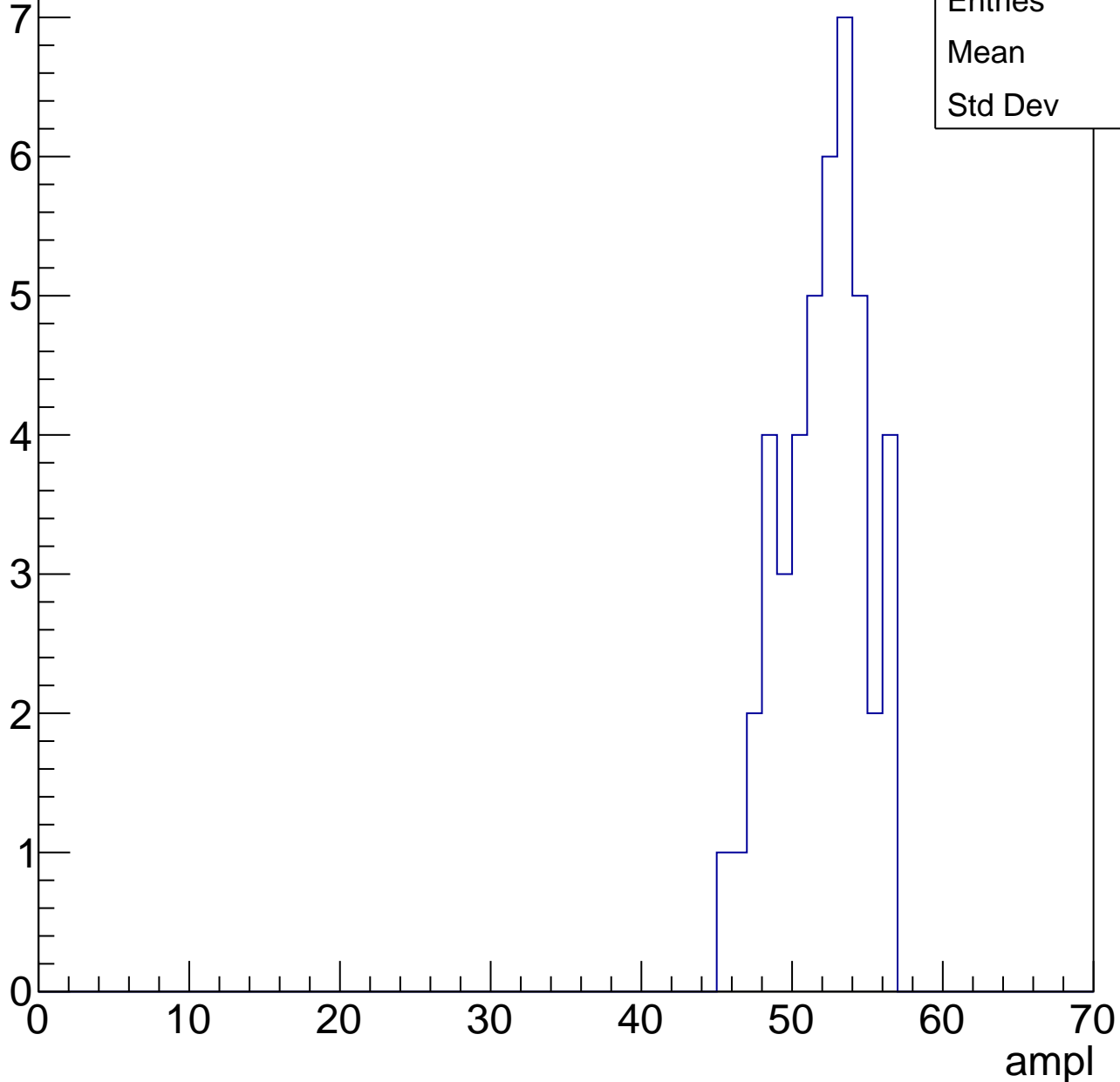
Entries	53
Mean	46.45
Std Dev	2.832



# B1L002S, U17-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



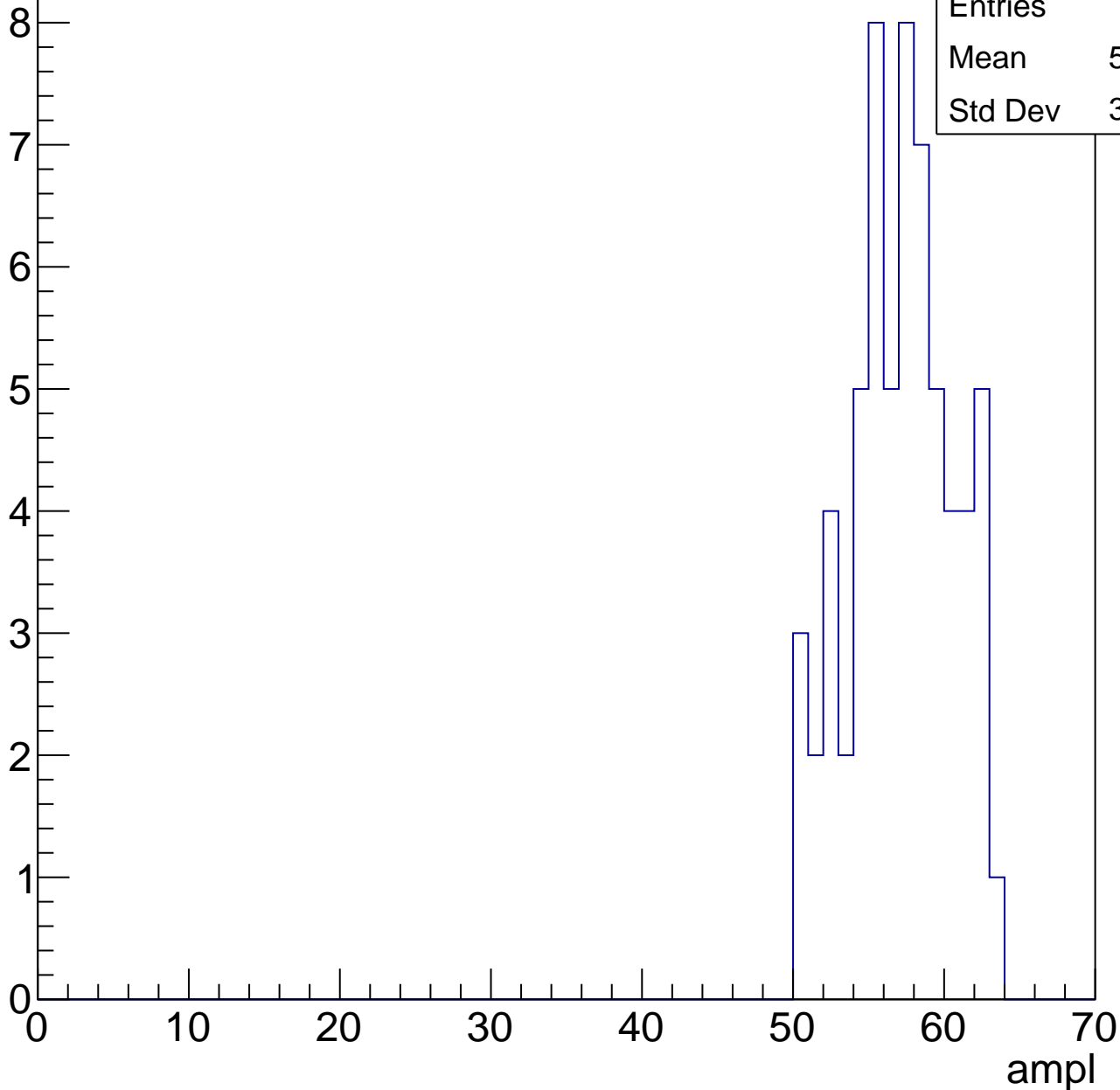
Entries	44
Mean	51.5
Std Dev	2.8

# B1L002S, U17-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	56.67
Std Dev	3.367

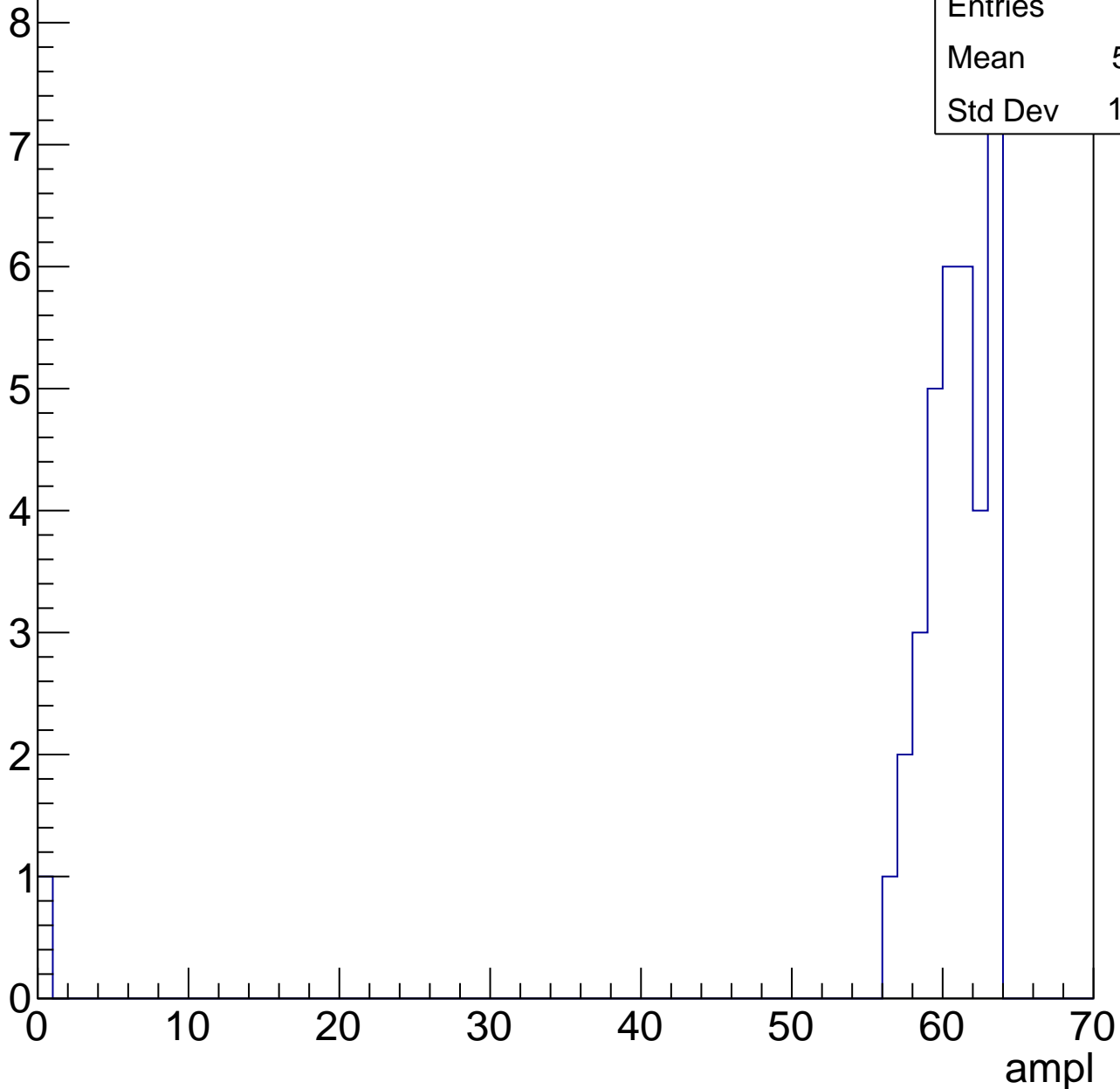


# B1L002S, U17-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.81
Std Dev	10.13





# B1L002S, U17-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch85, adc0

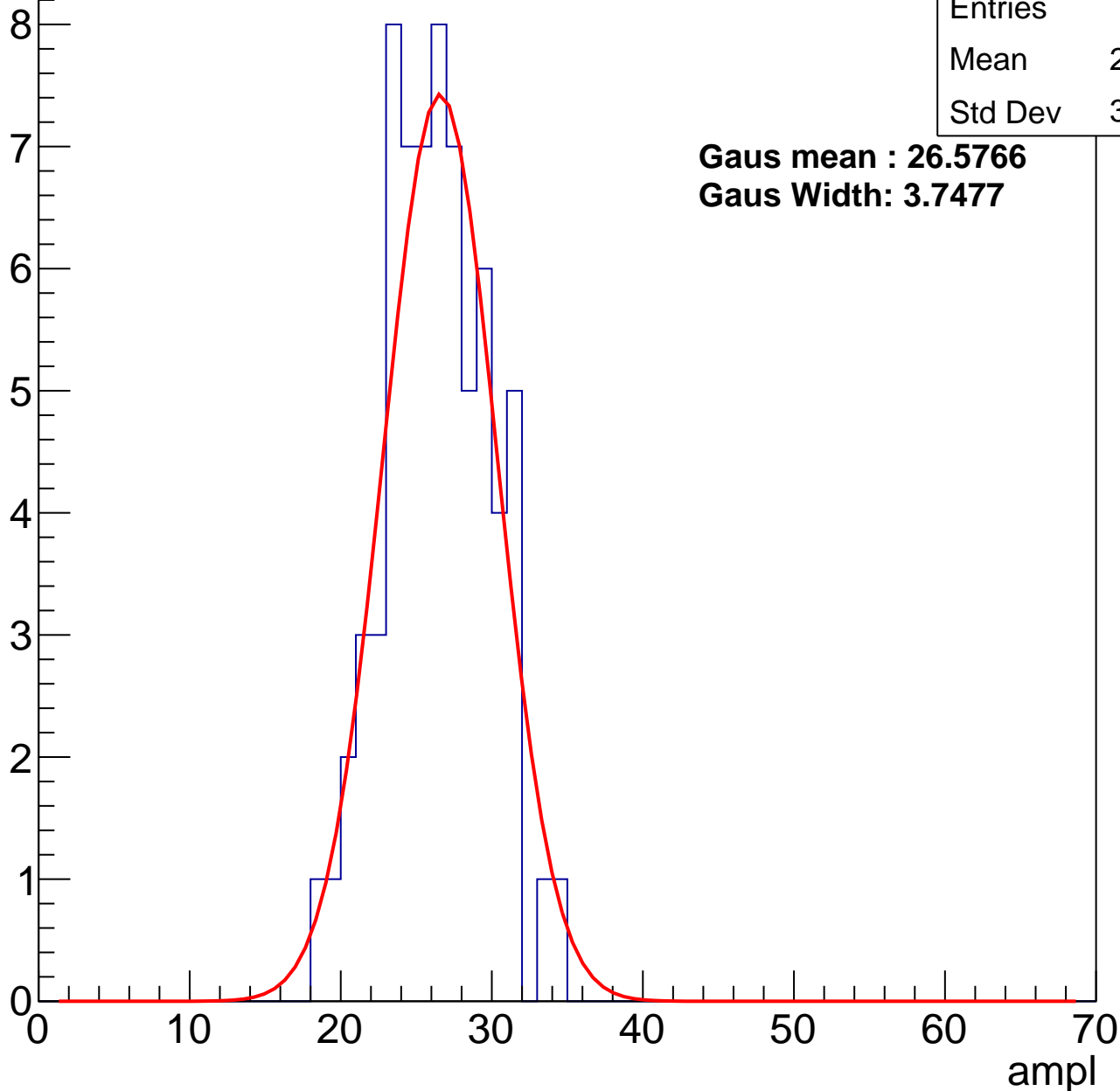
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	25.88
Std Dev	3.399

**Gaus mean : 26.5766**

**Gaus Width: 3.7477**



# B1L002S, U17-ch85, adc1

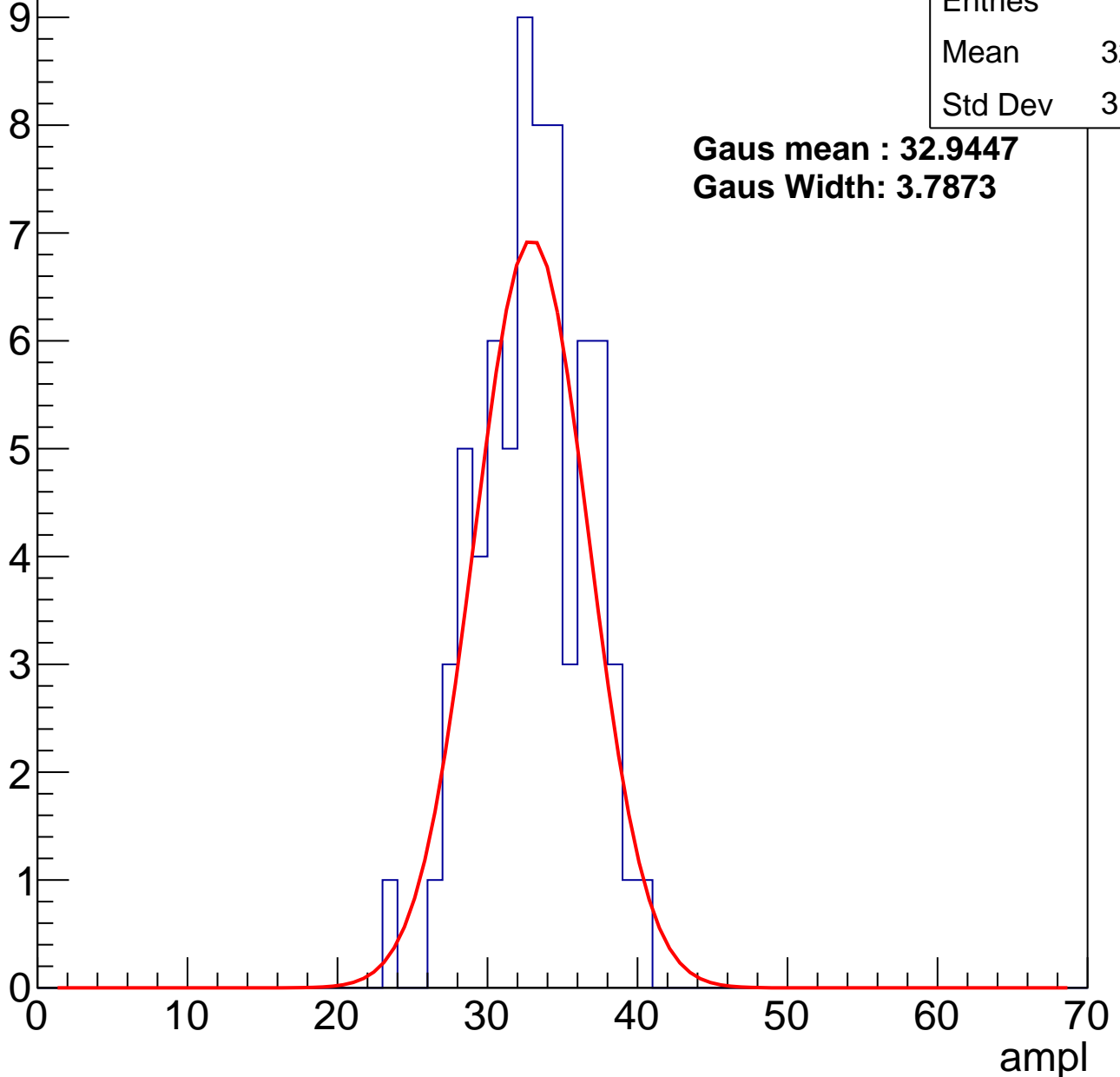
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	32.59
Std Dev	3.478

**Gaus mean : 32.9447**

**Gaus Width: 3.7873**



# B1L002S, U17-ch85, adc2

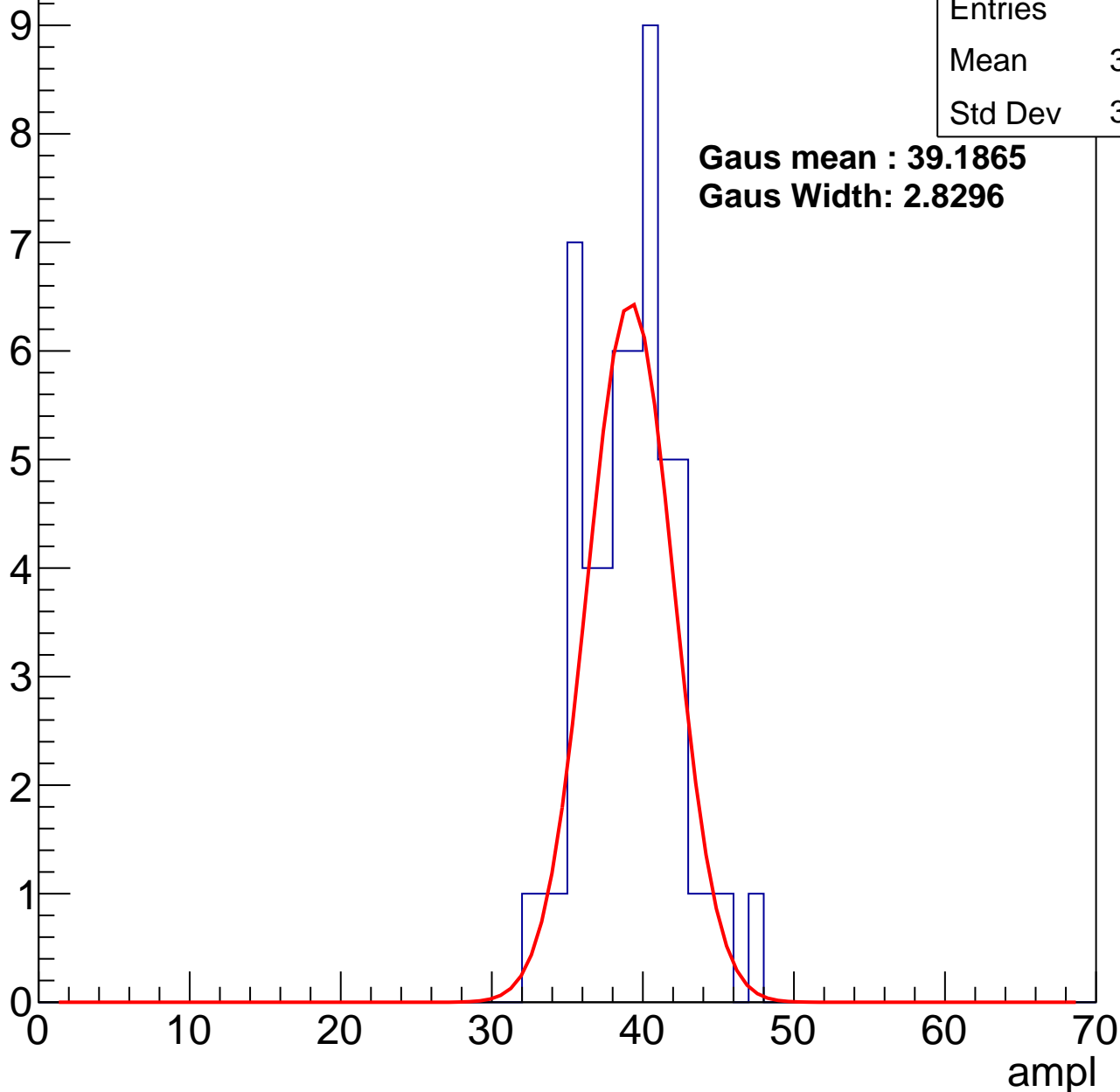
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	38.72
Std Dev	3.043

**Gaus mean : 39.1865**

**Gaus Width: 2.8296**

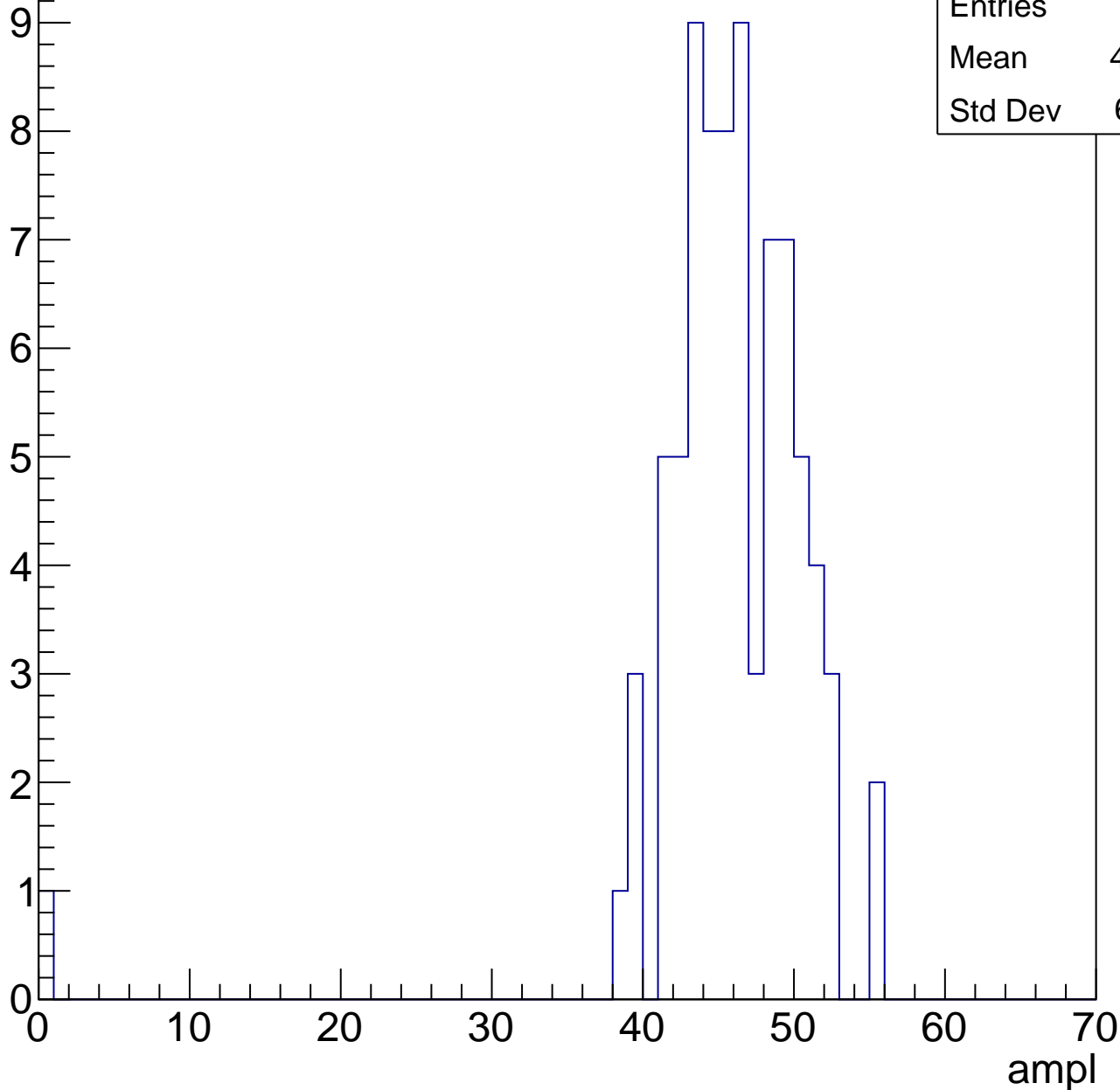


# B1L002S, U17-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	45.29
Std Dev	6.281

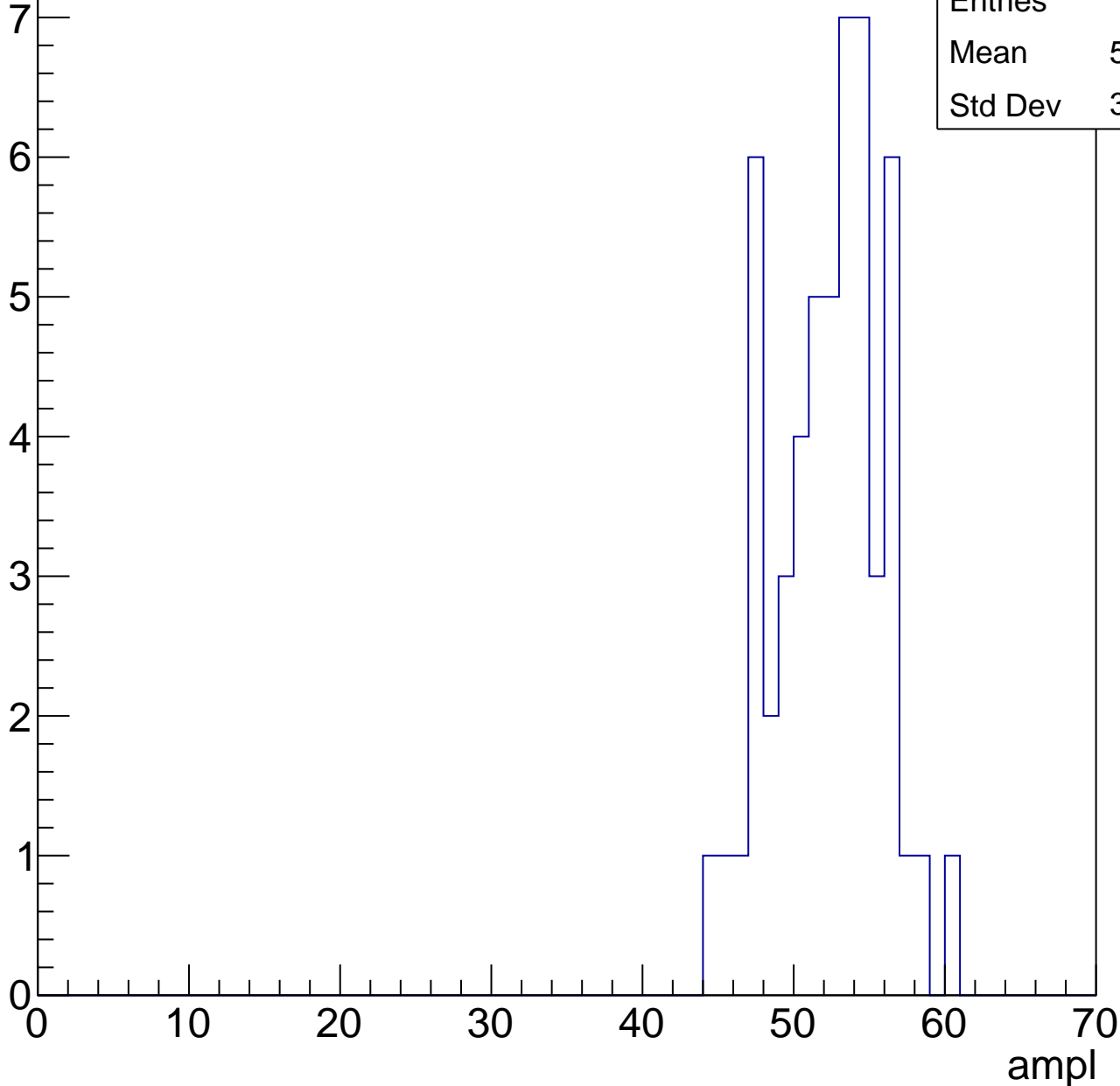


# B1L002S, U17-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

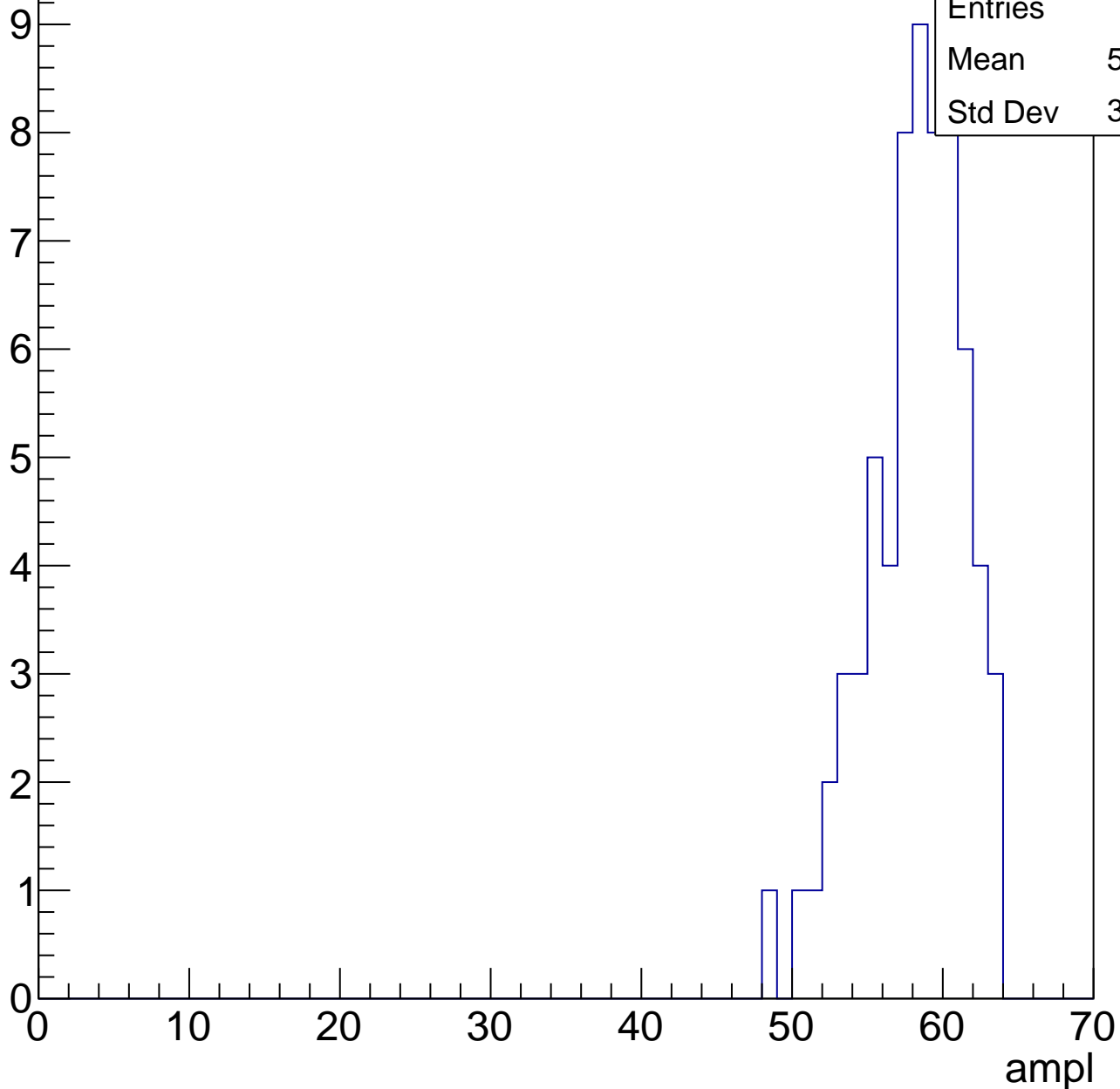
Entries	54
Mean	51.85
Std Dev	3.498



# B1L002S, U17-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



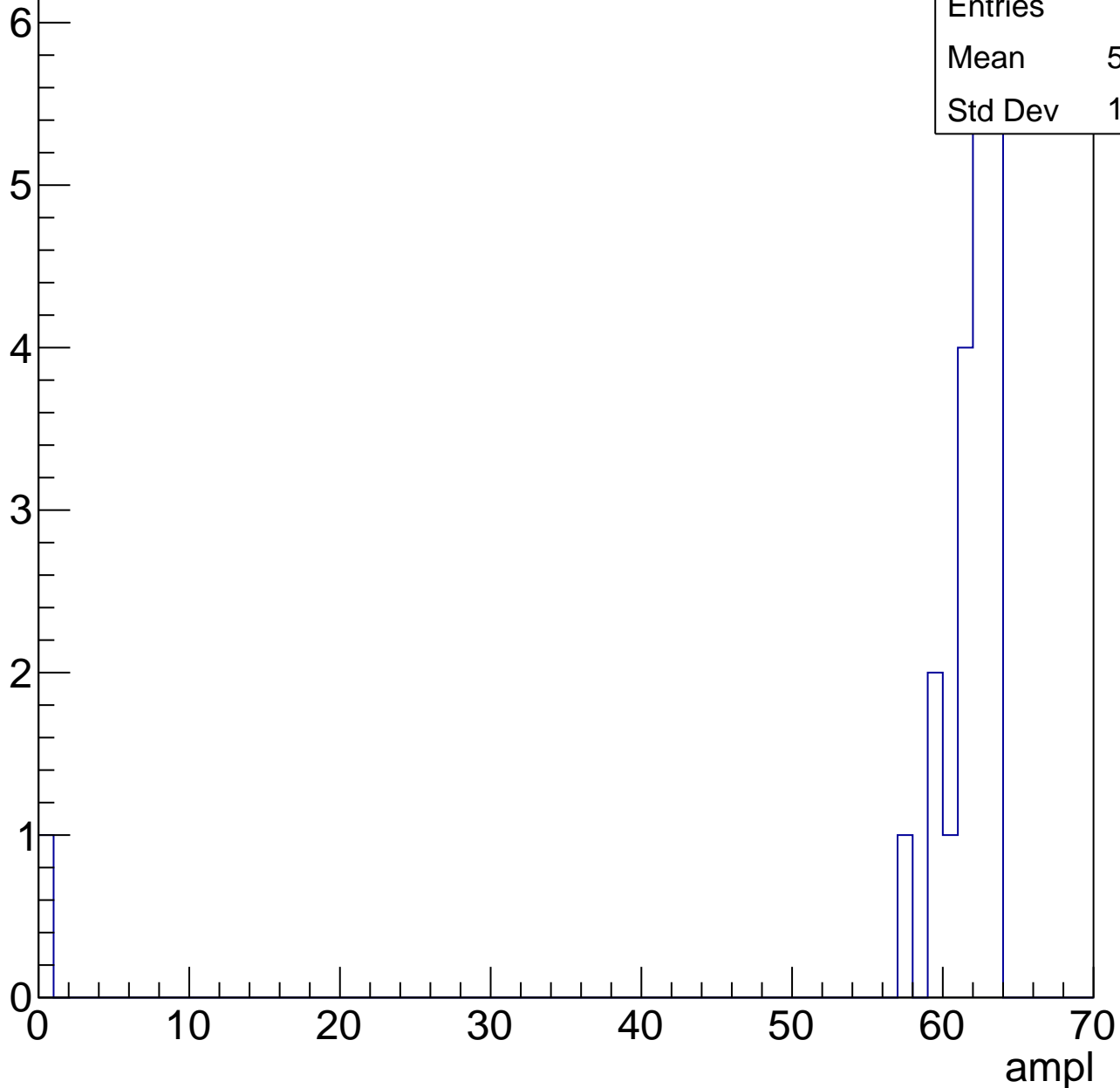
Entries	66
Mean	57.67
Std Dev	3.263

# B1L002S, U17-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.52
Std Dev	13.18





# B1L002S, U17-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch86, adc0

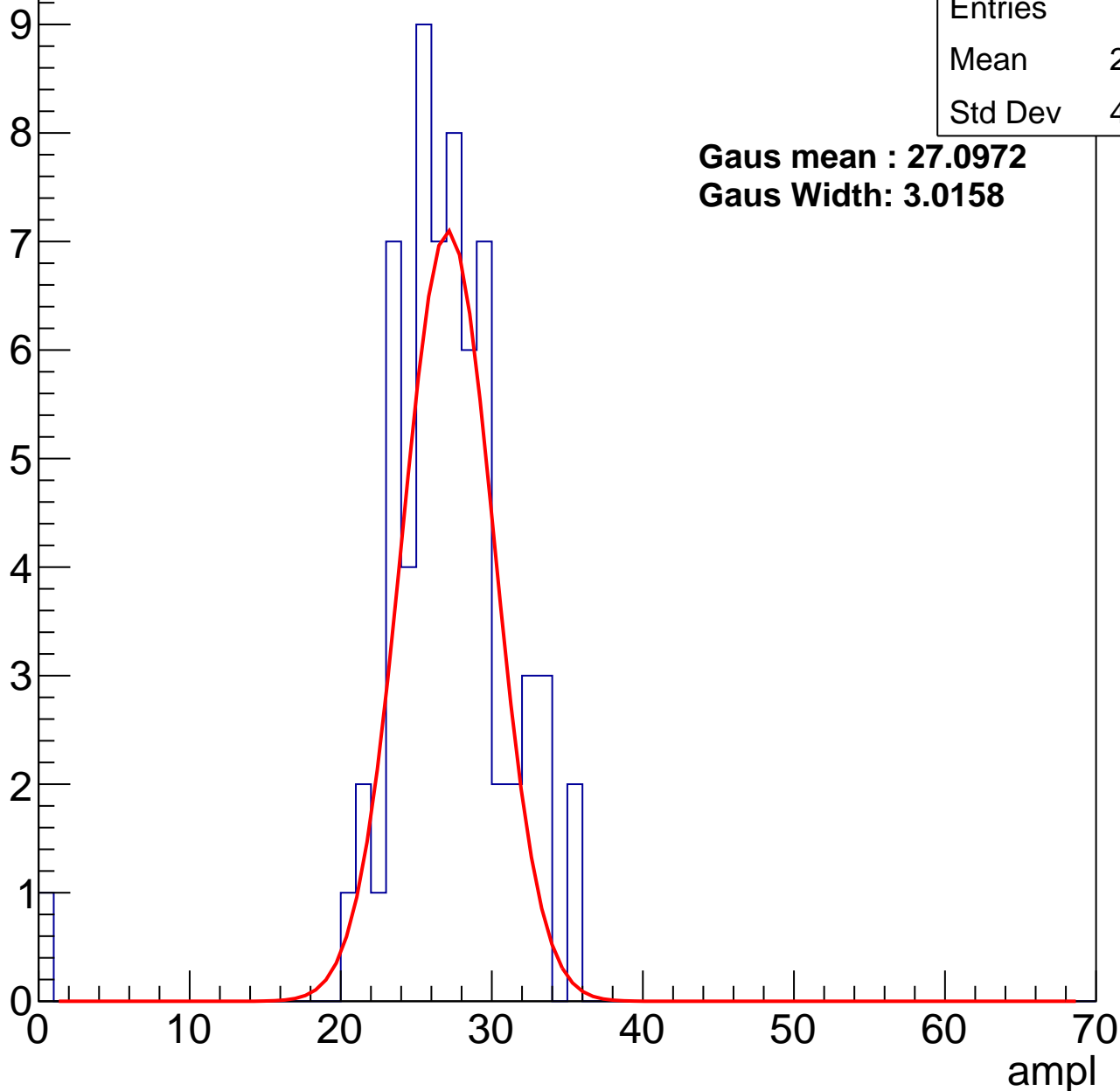
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	26.49
Std Dev	4.714

**Gaus mean : 27.0972**

**Gaus Width: 3.0158**



# B1L002S, U17-ch86, adc1

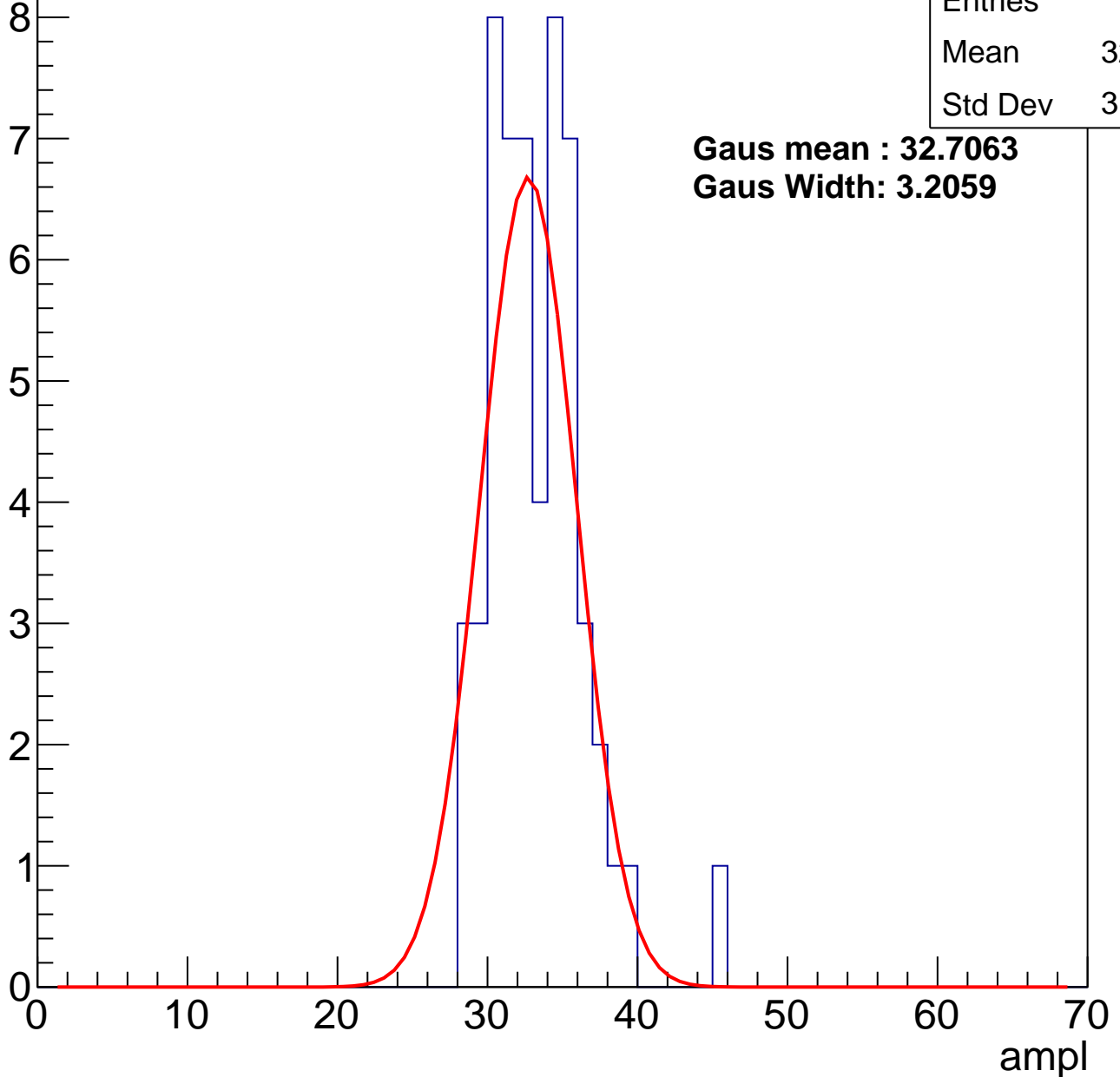
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	32.82
Std Dev	3.099

**Gaus mean : 32.7063**

**Gaus Width: 3.2059**



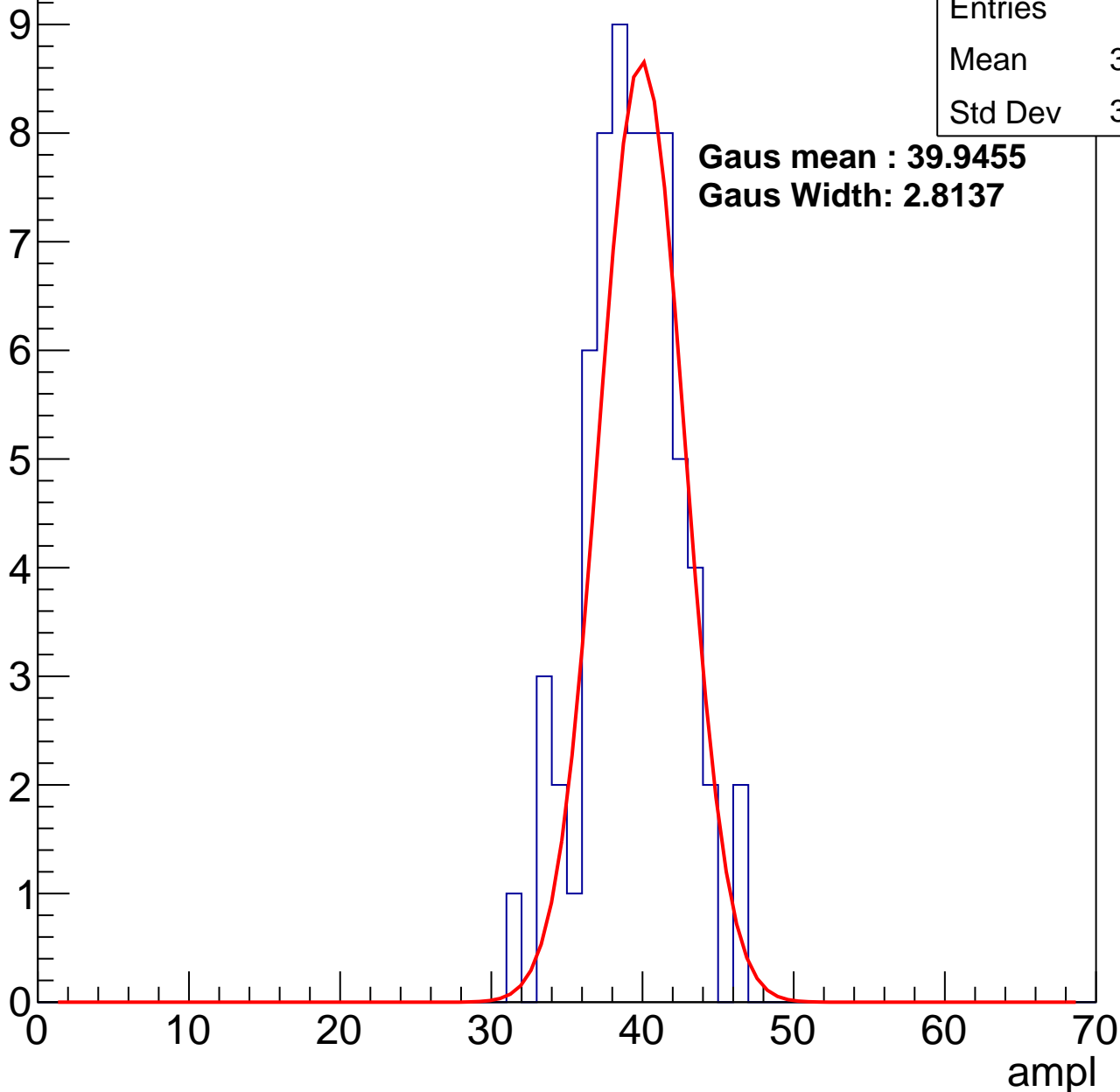
# B1L002S, U17-ch86, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	38.94
Std Dev	3.066

**Gaus mean : 39.9455**  
**Gaus Width: 2.8137**

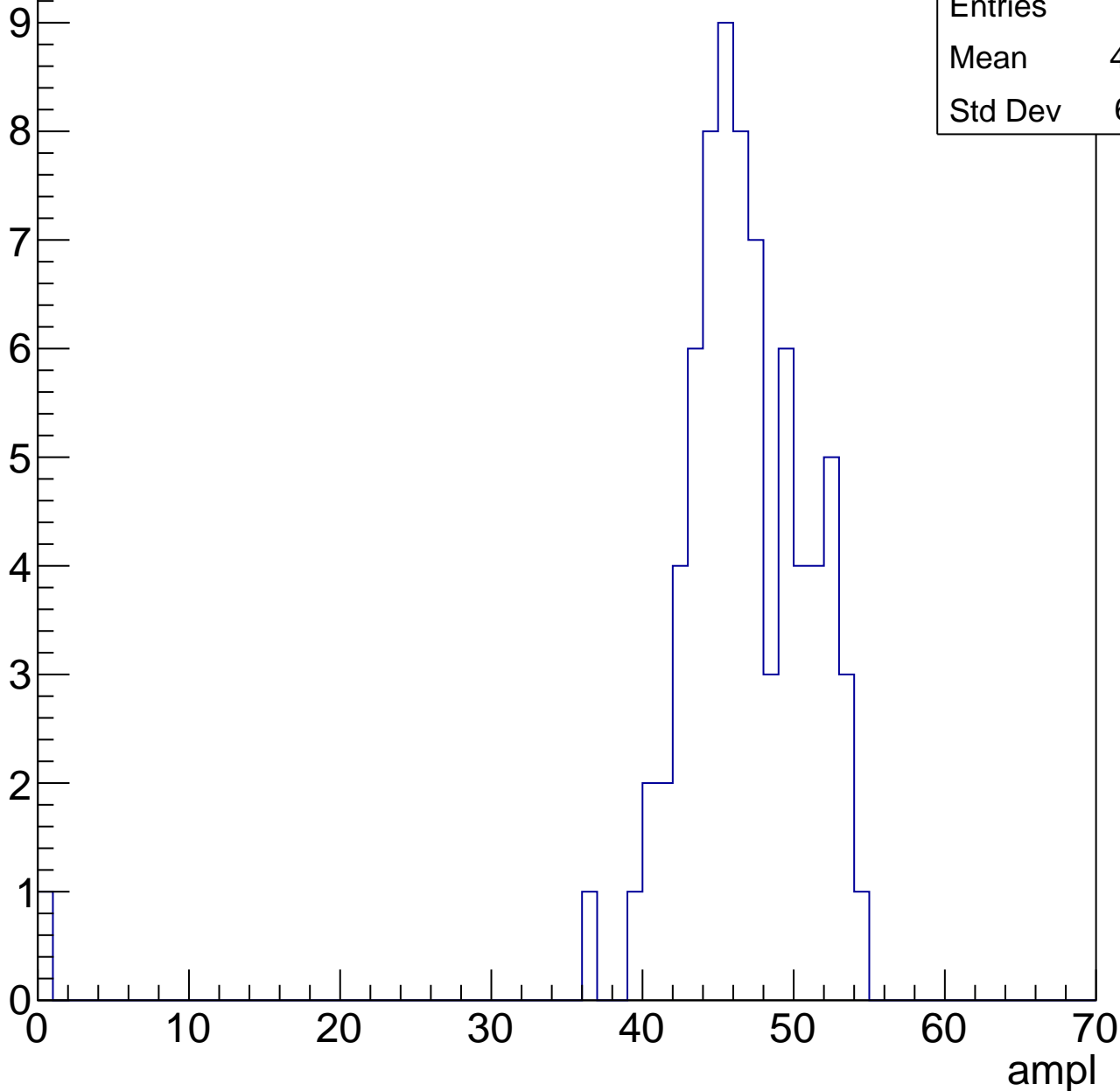


# B1L002S, U17-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	45.76
Std Dev	6.501

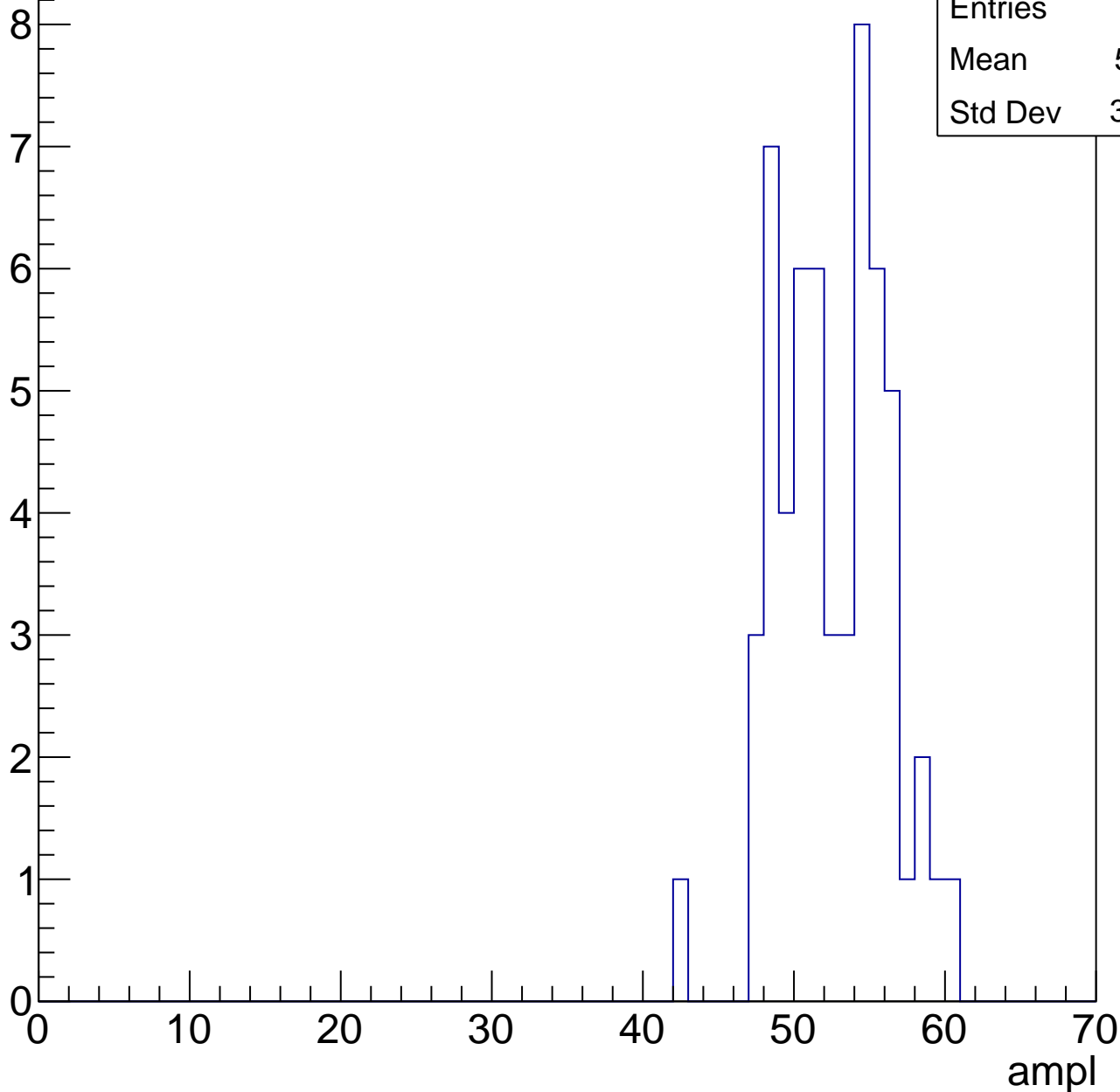


# B1L002S, U17-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

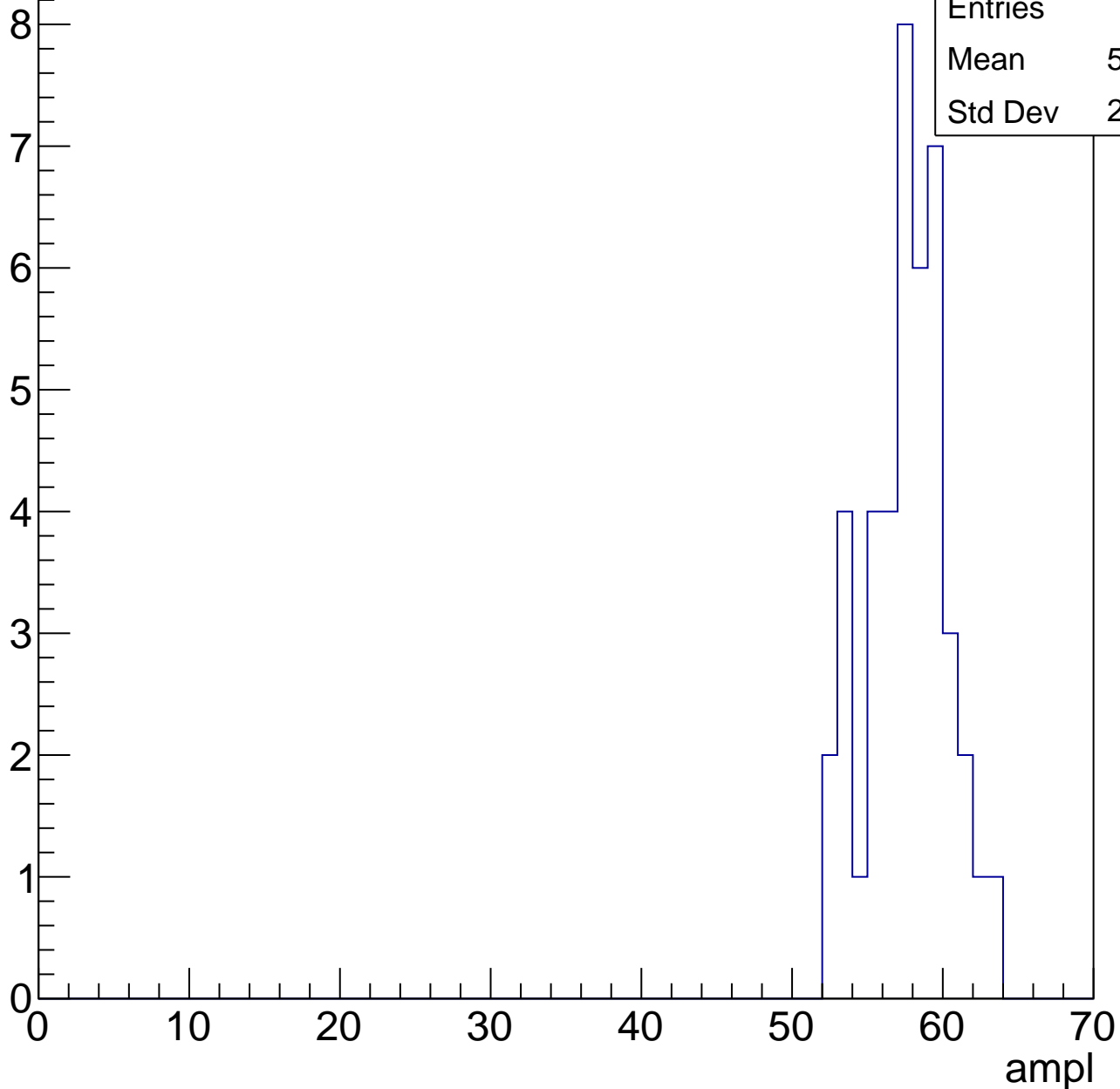
Entries	57
Mean	52.11
Std Dev	3.582



# B1L002S, U17-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



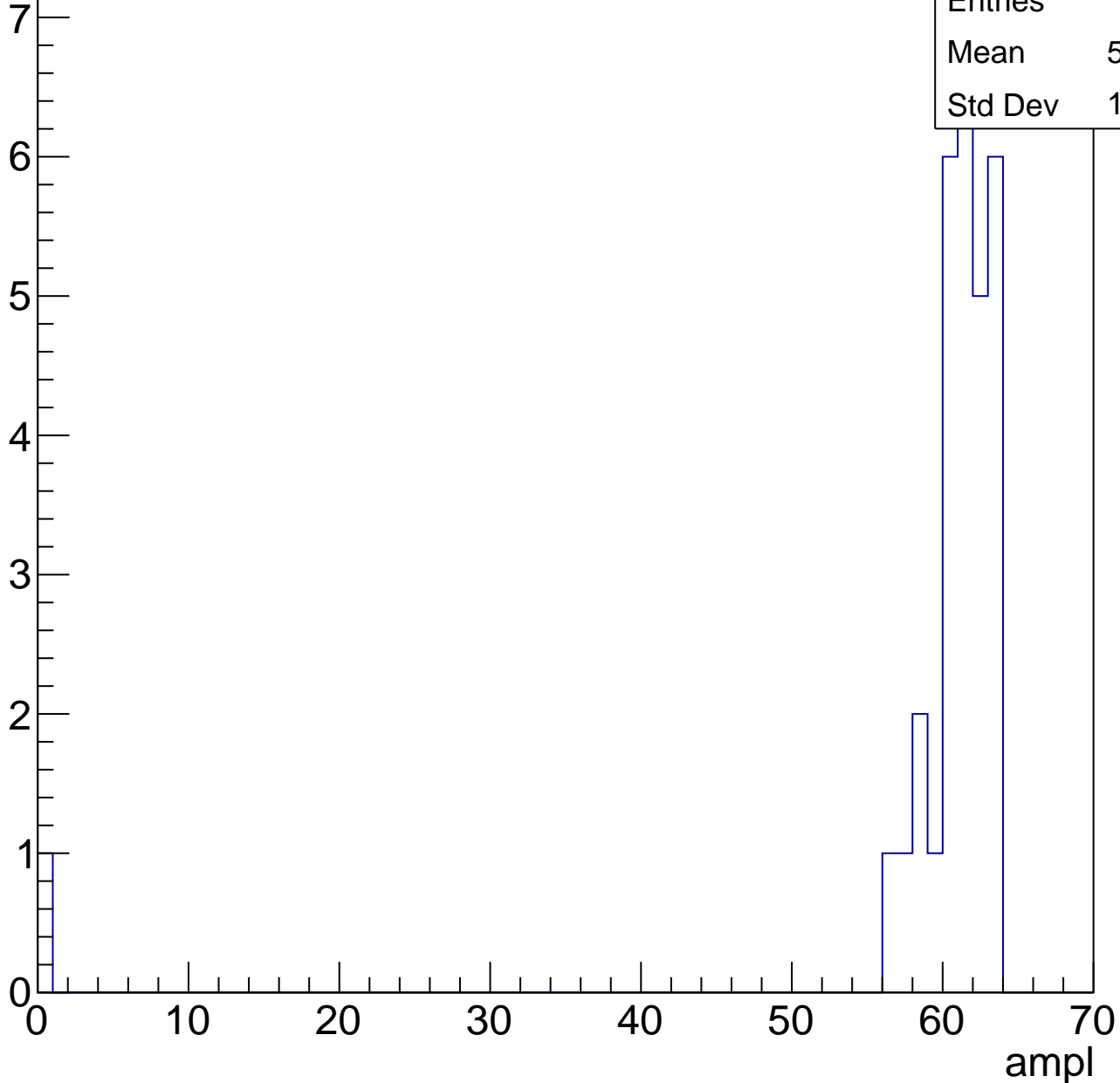
Entries	43
Mean	57.16
Std Dev	2.623

# B1L002S, U17-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.77
Std Dev	11.06

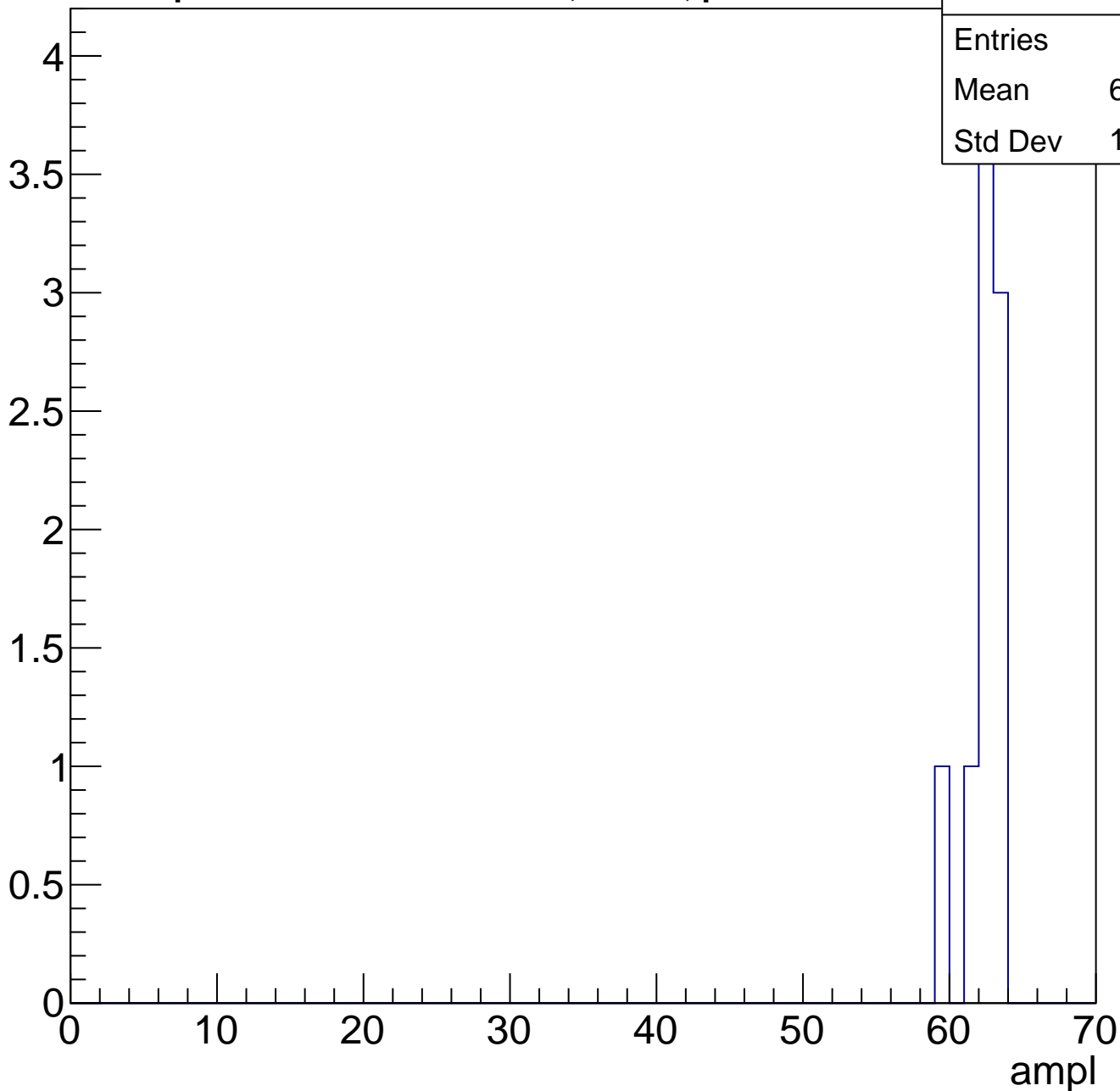




# B1L002S, U17-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch87, adc0

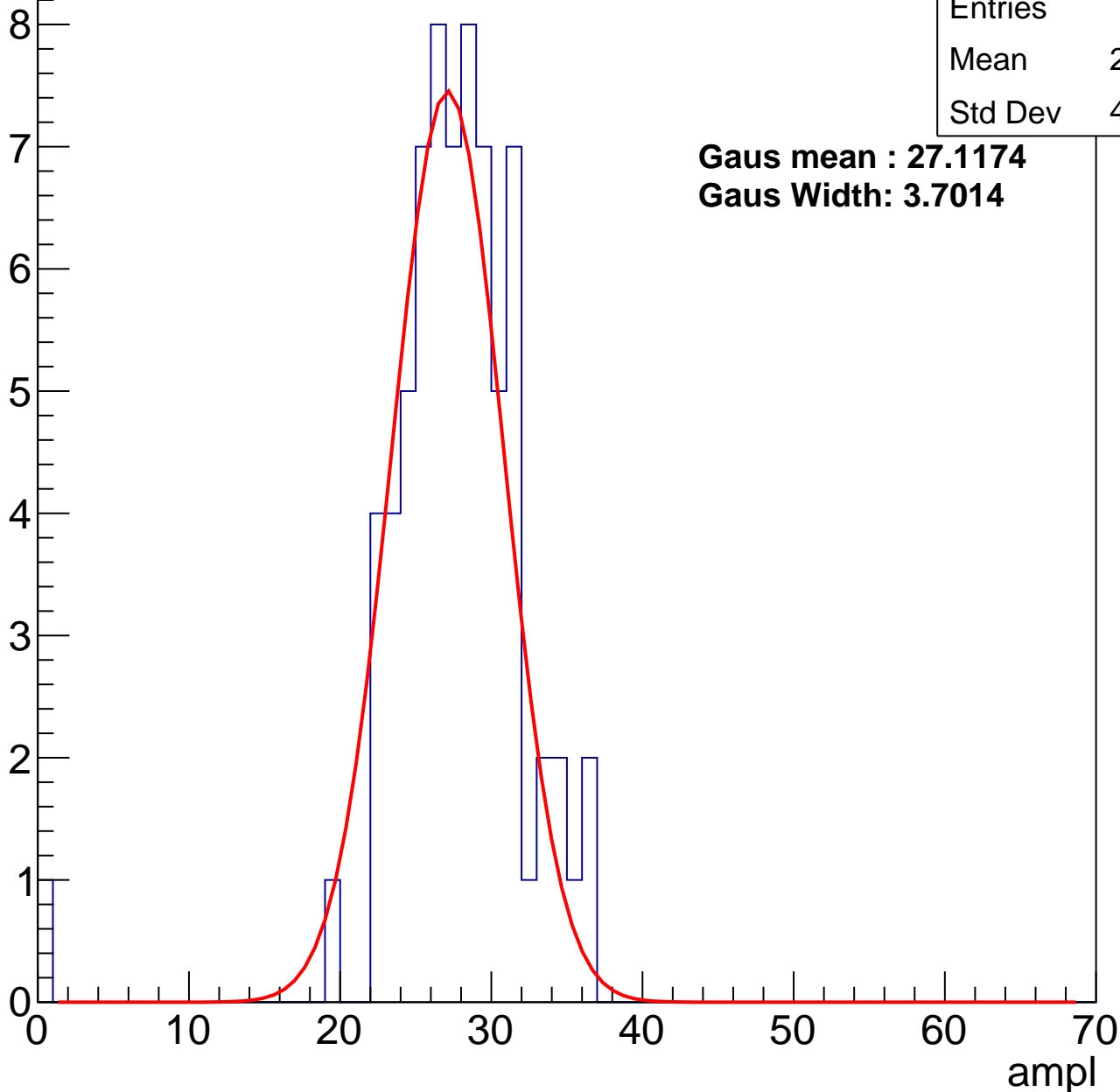
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.19
Std Dev	4.786

**Gaus mean : 27.1174**

**Gaus Width: 3.7014**



# B1L002S, U17-ch87, adc1

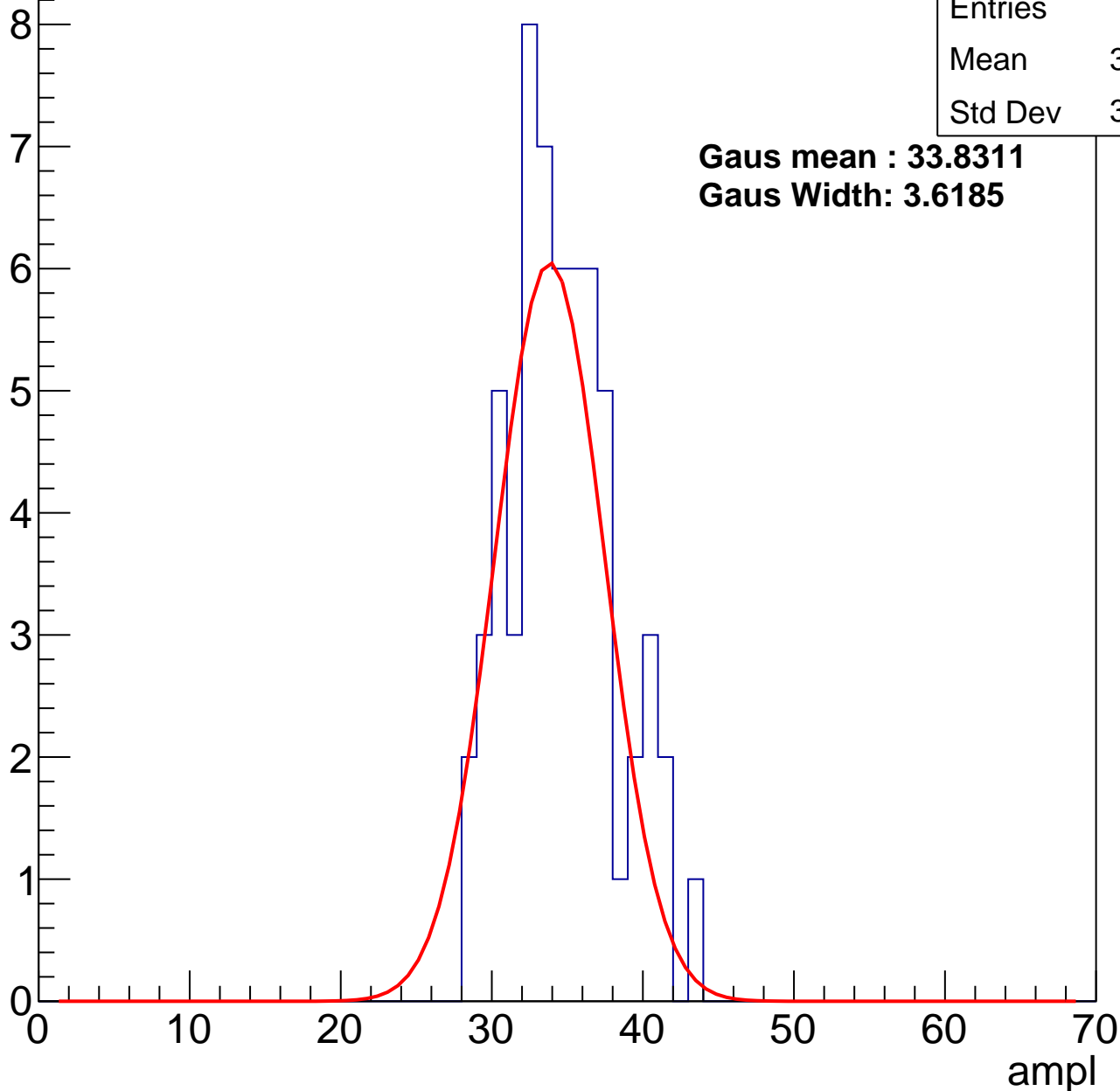
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.15
Std Dev	3.463

**Gaus mean : 33.8311**

**Gaus Width: 3.6185**



# B1L002S, U17-ch87, adc2

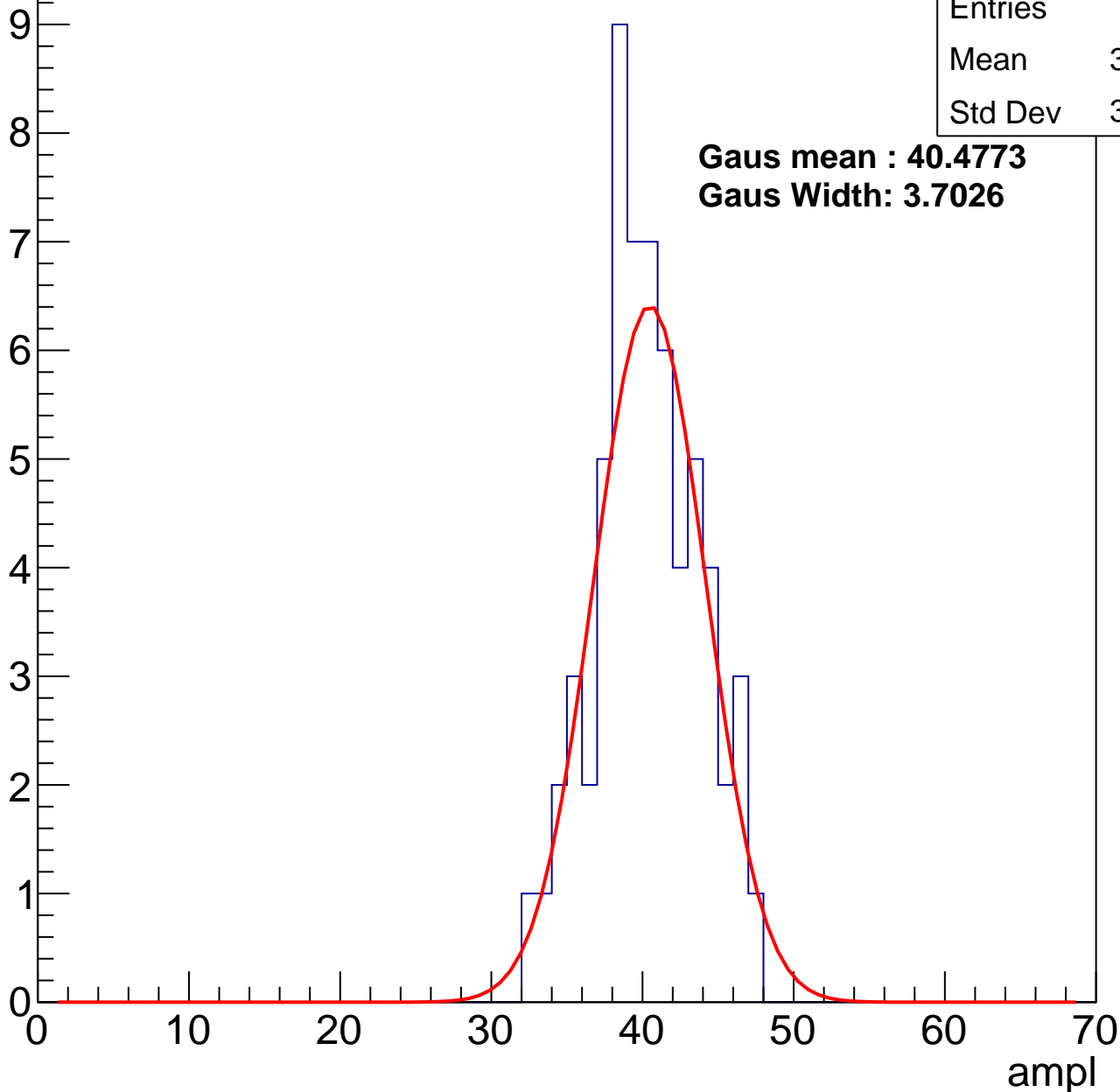
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.84
Std Dev	3.399

**Gaus mean : 40.4773**

**Gaus Width: 3.7026**

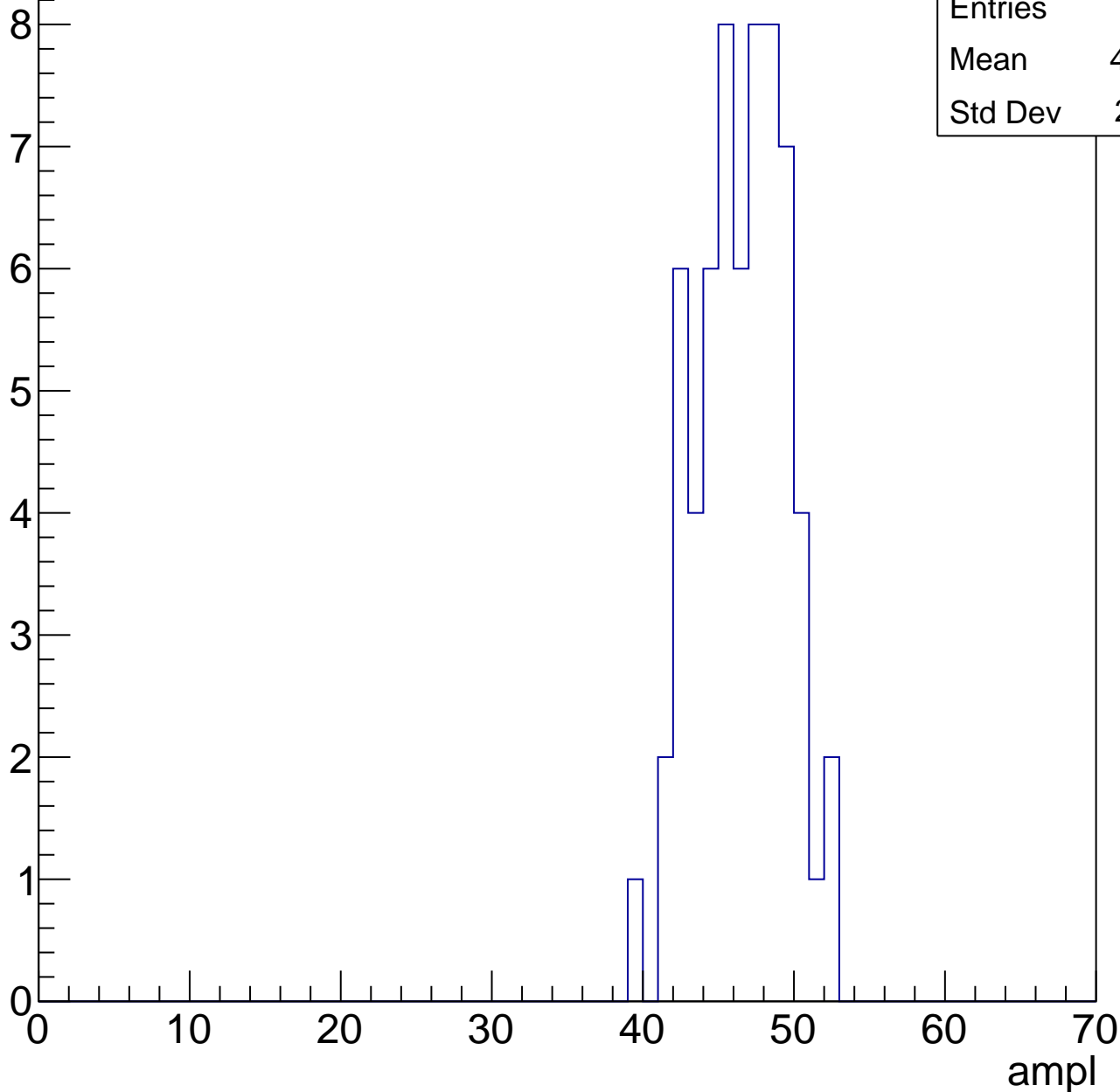


# B1L002S, U17-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

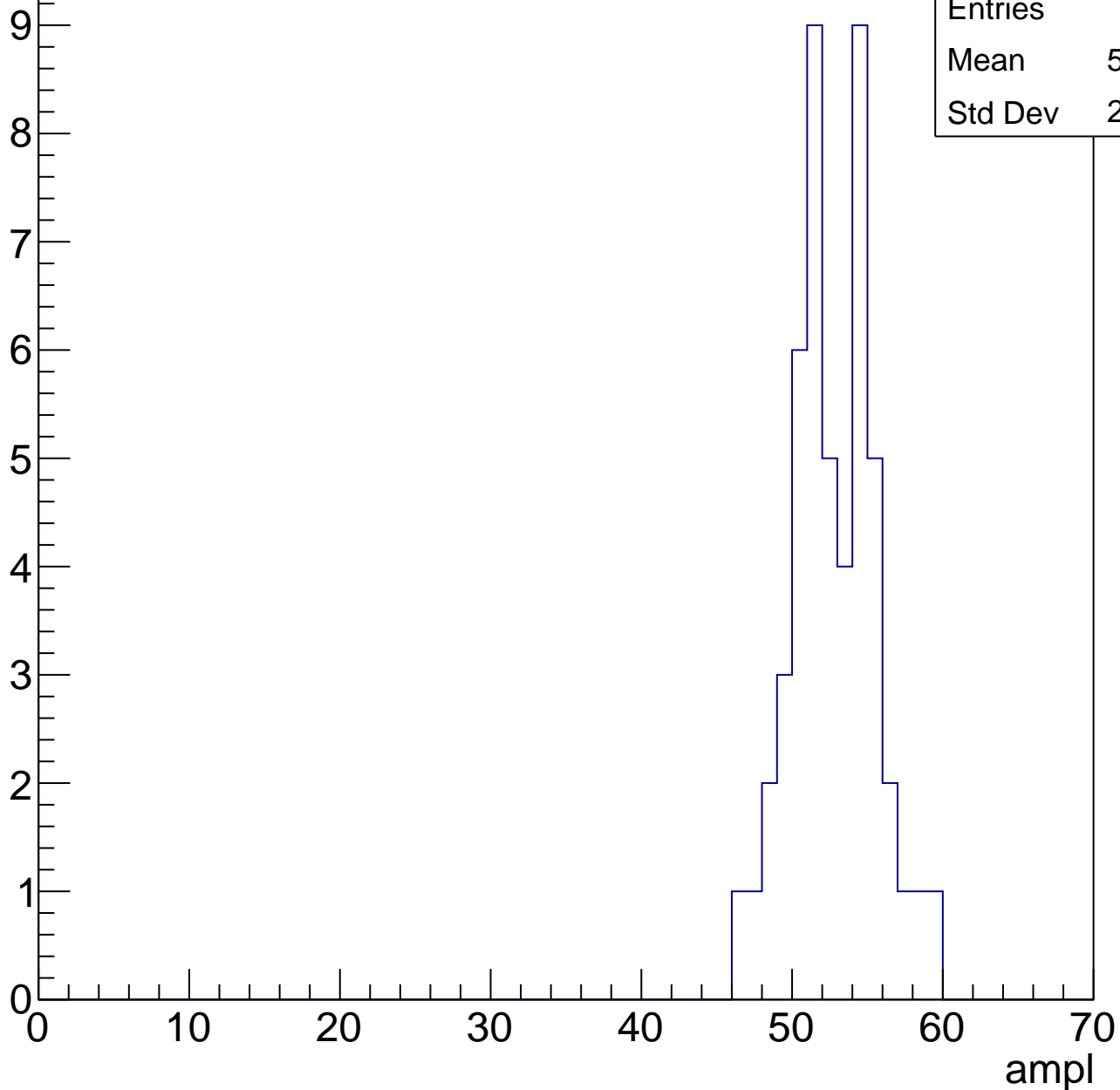
Entries	63
Mean	46.08
Std Dev	2.891



# B1L002S, U17-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

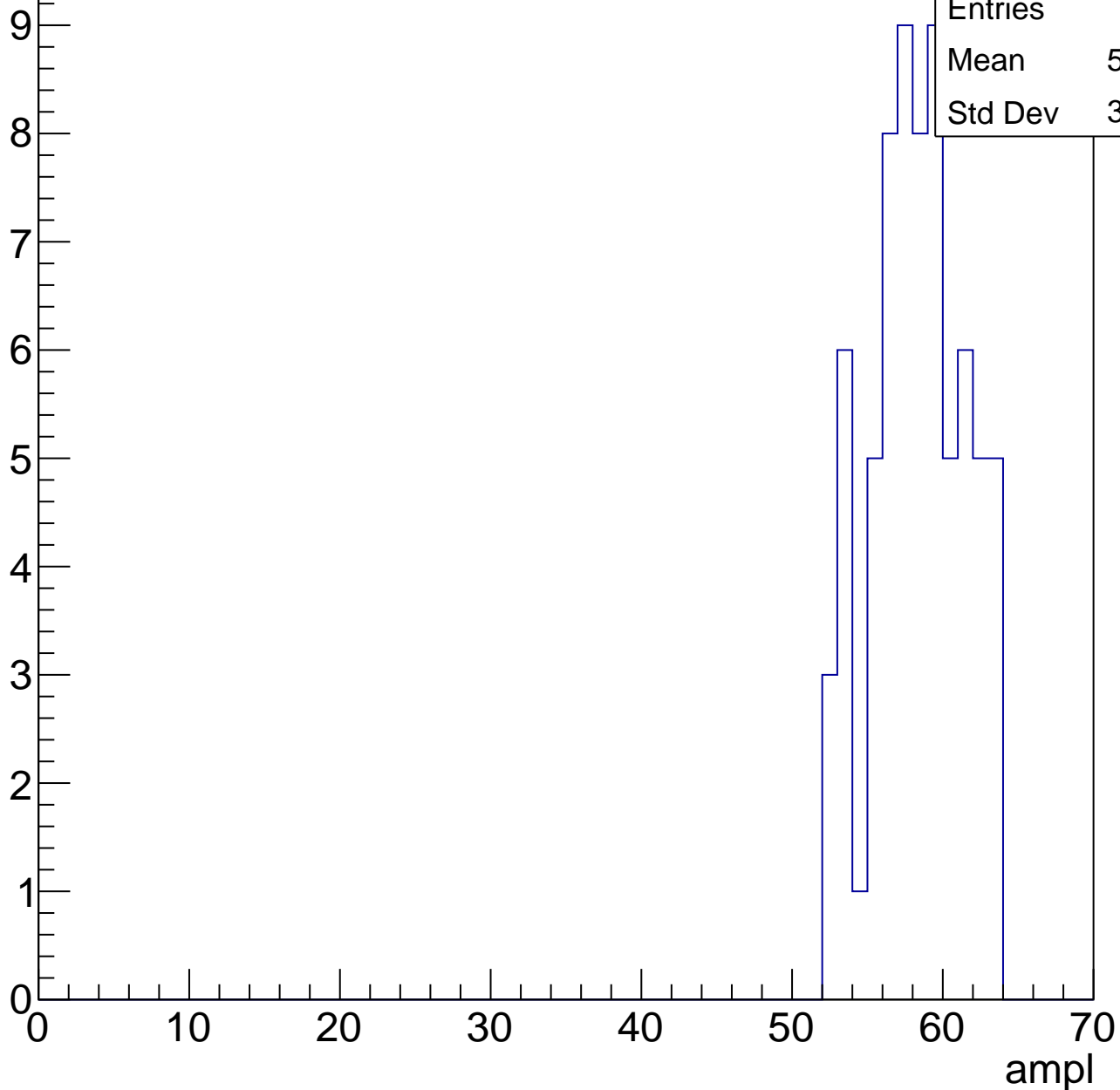


Entries	50
Mean	52.28
Std Dev	2.743

# B1L002S, U17-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

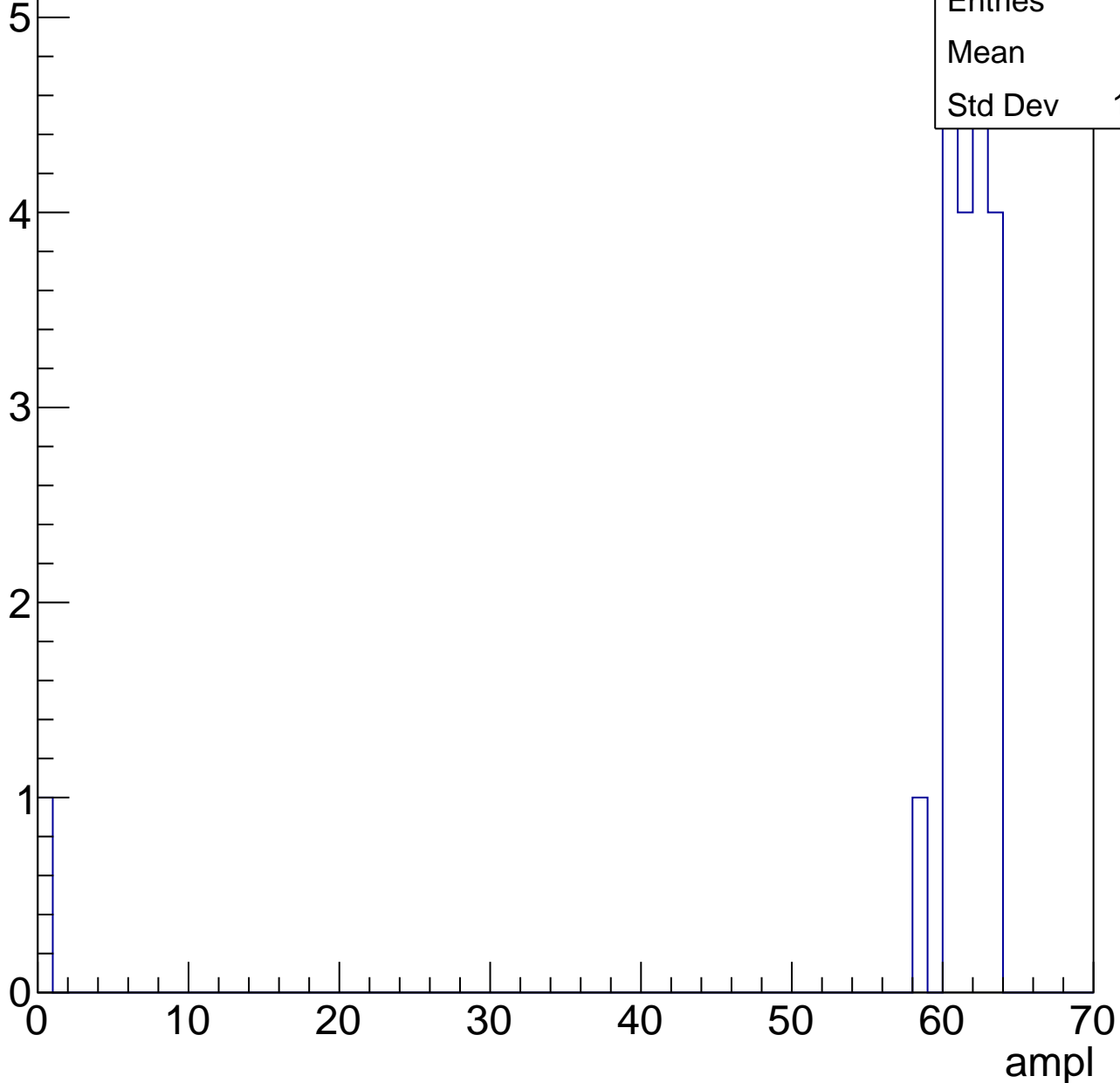


# B1L002S, U17-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.2
Std Dev	13.41





# B1L002S, U17-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch88, adc0

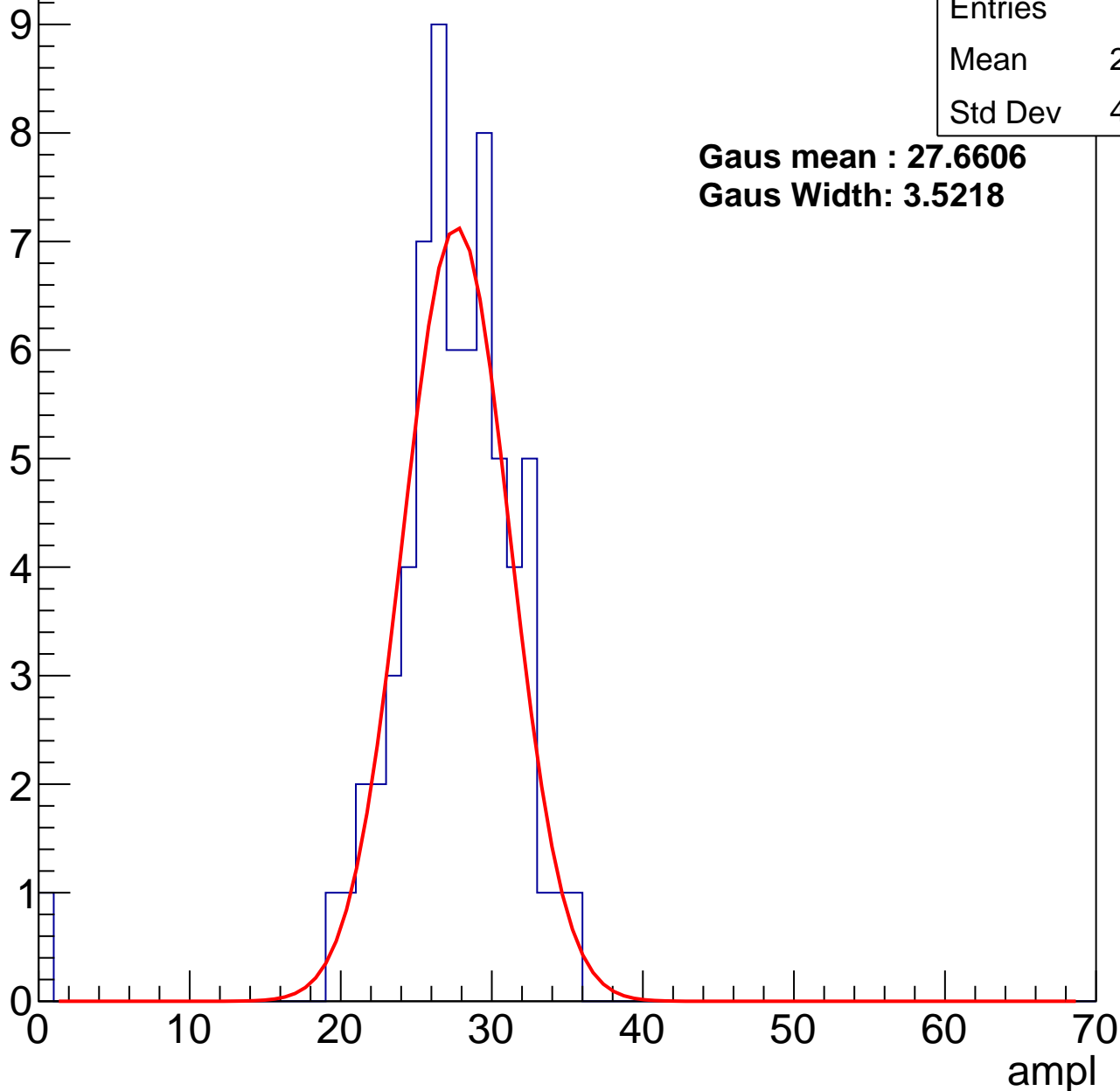
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	26.82
Std Dev	4.738

**Gaus mean : 27.6606**

**Gaus Width: 3.5218**



# B1L002S, U17-ch88, adc1

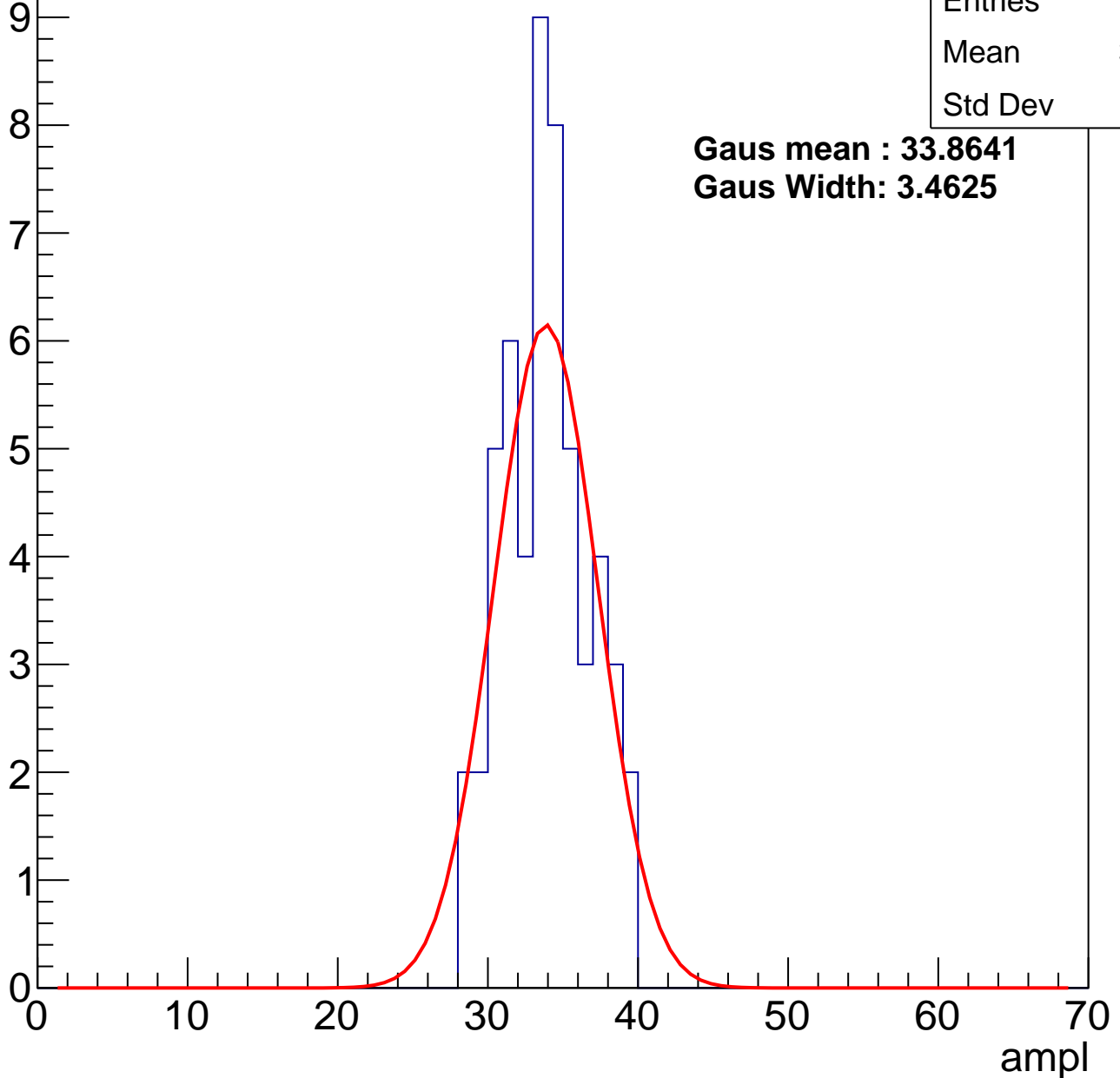
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	33.4
Std Dev	2.79

**Gaus mean : 33.8641**

**Gaus Width: 3.4625**



# B1L002S, U17-ch88, adc2

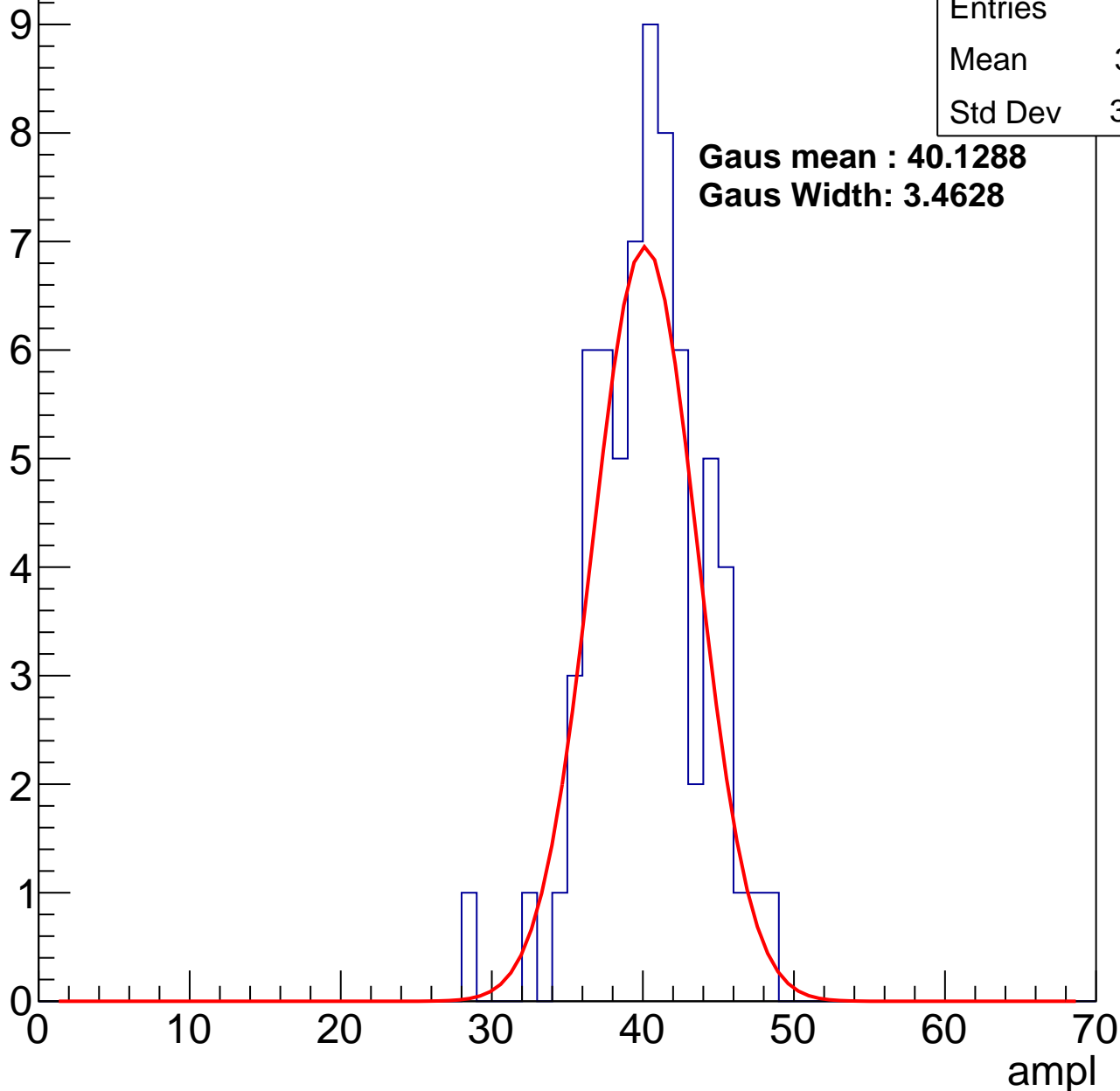
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.81
Std Dev	3.609

**Gaus mean : 40.1288**

**Gaus Width: 3.4628**

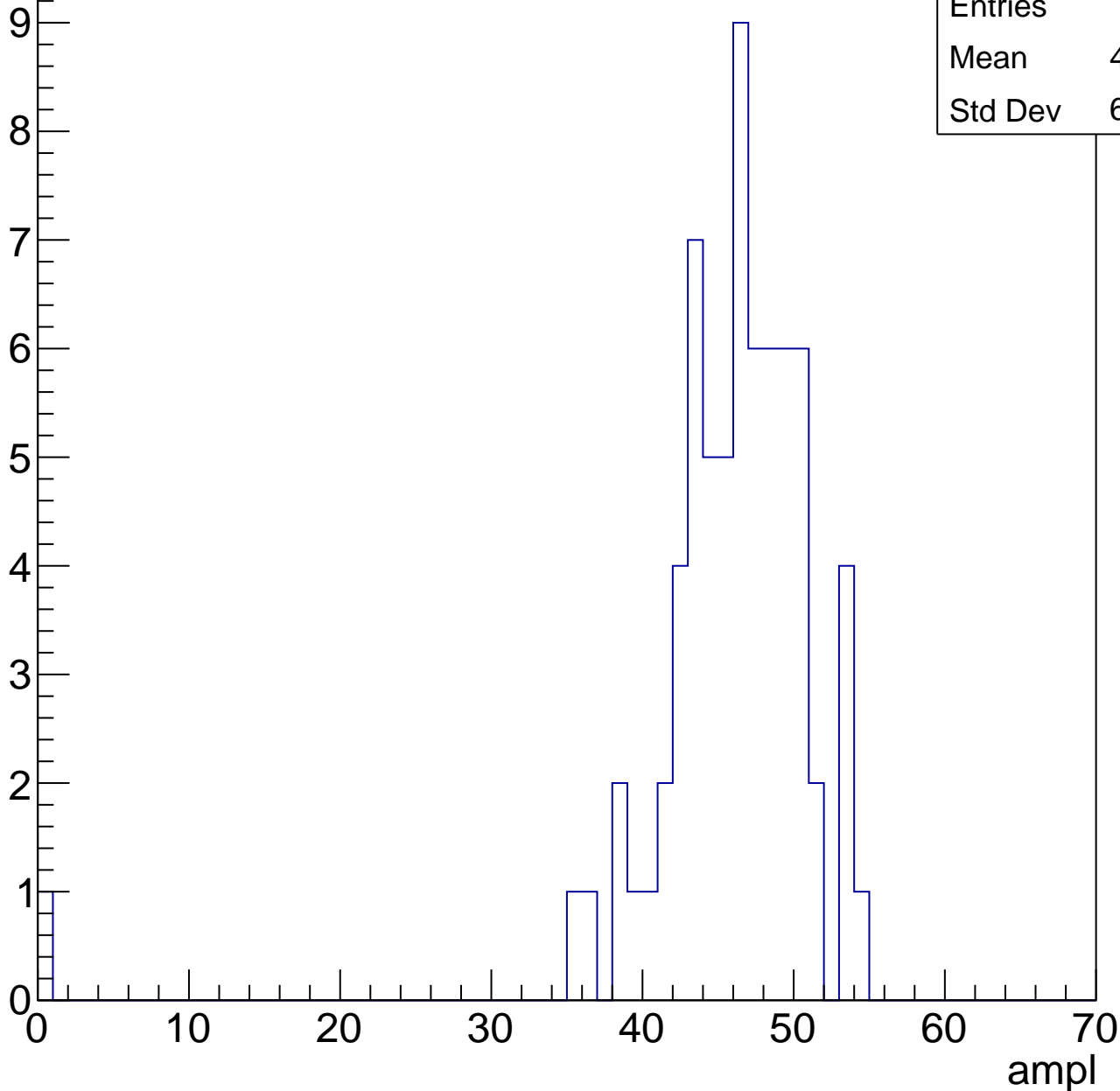


# B1L002S, U17-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	45.26
Std Dev	6.765

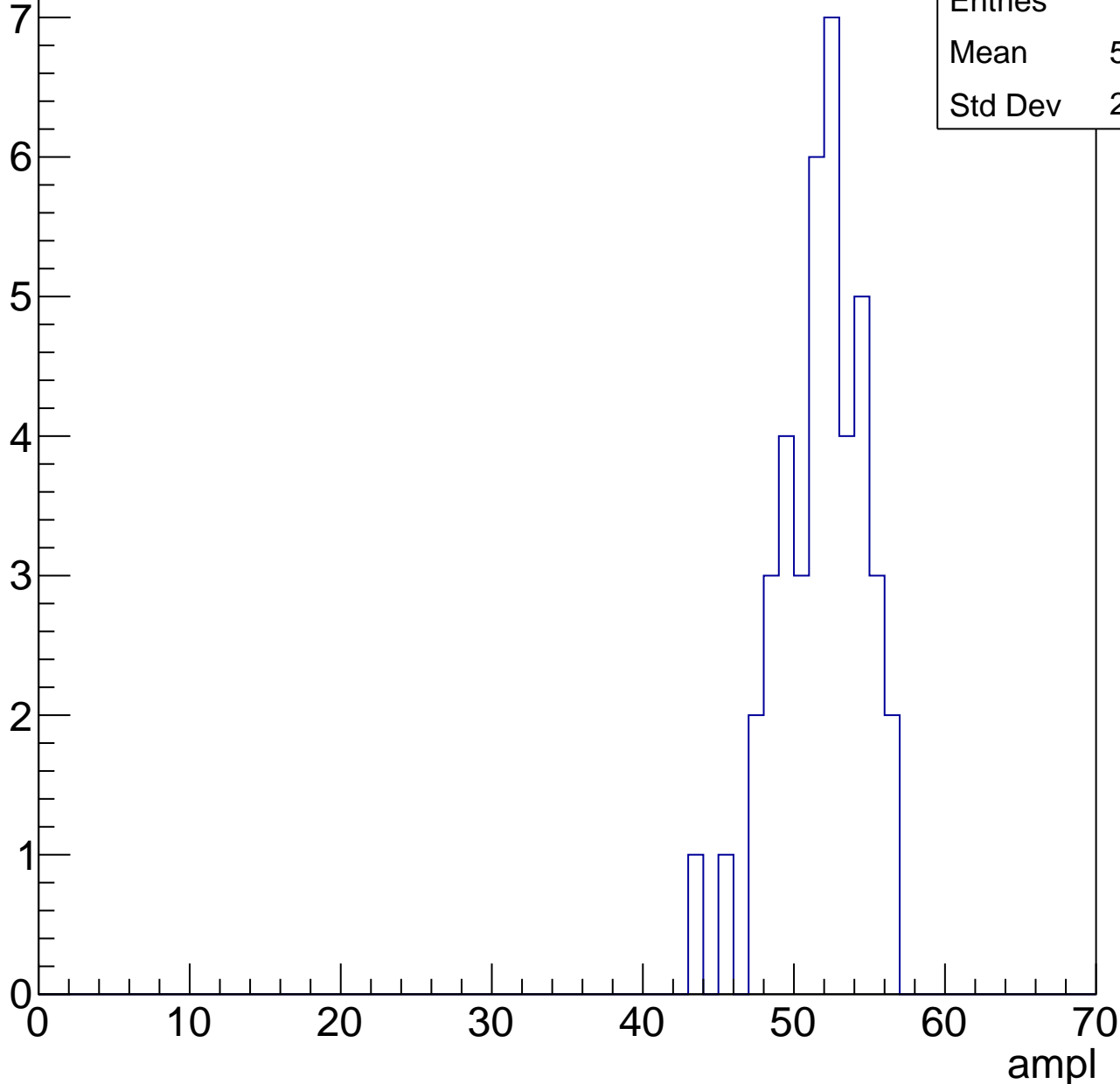


# B1L002S, U17-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	51.24
Std Dev	2.886

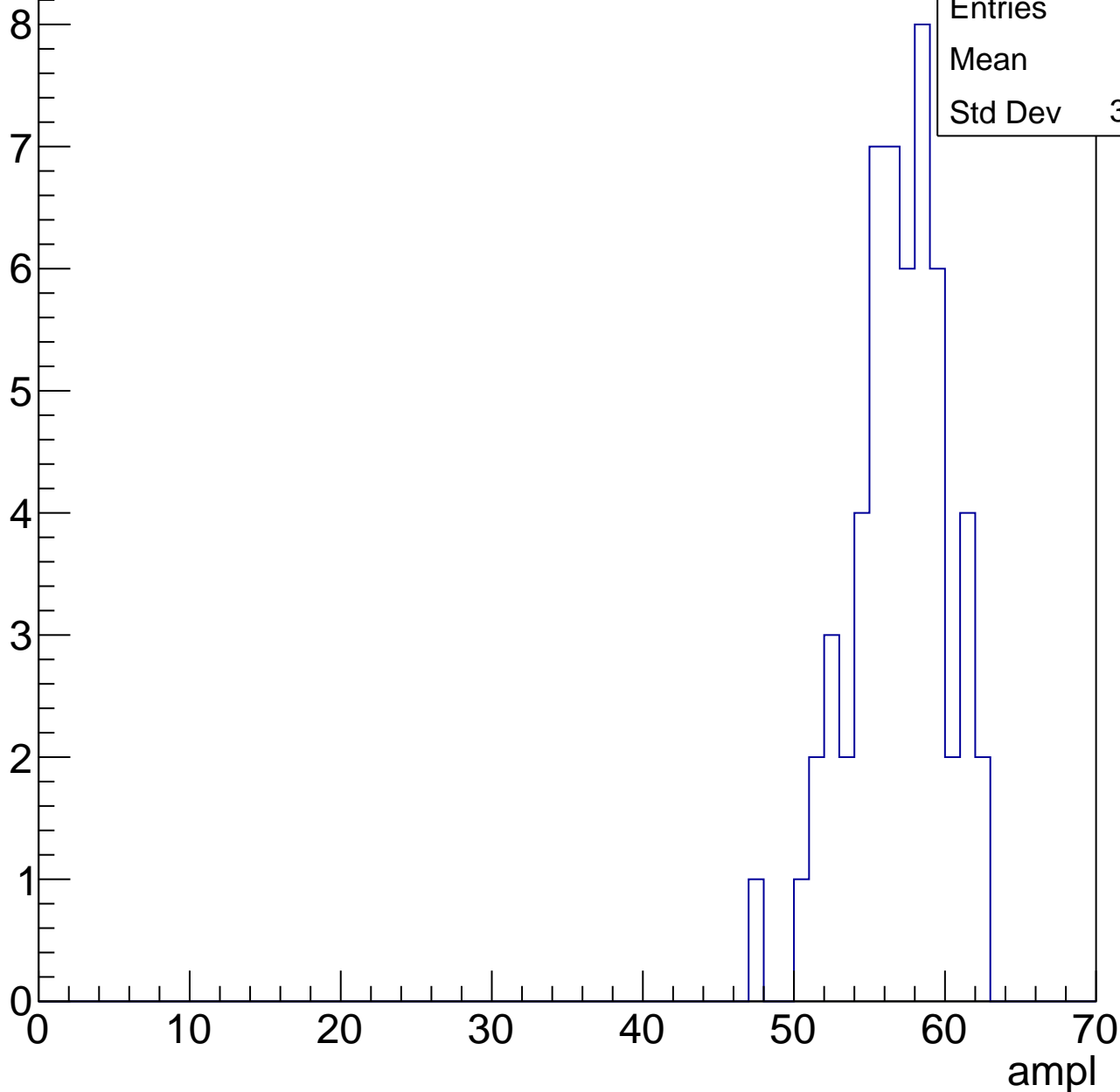


# B1L002S, U17-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

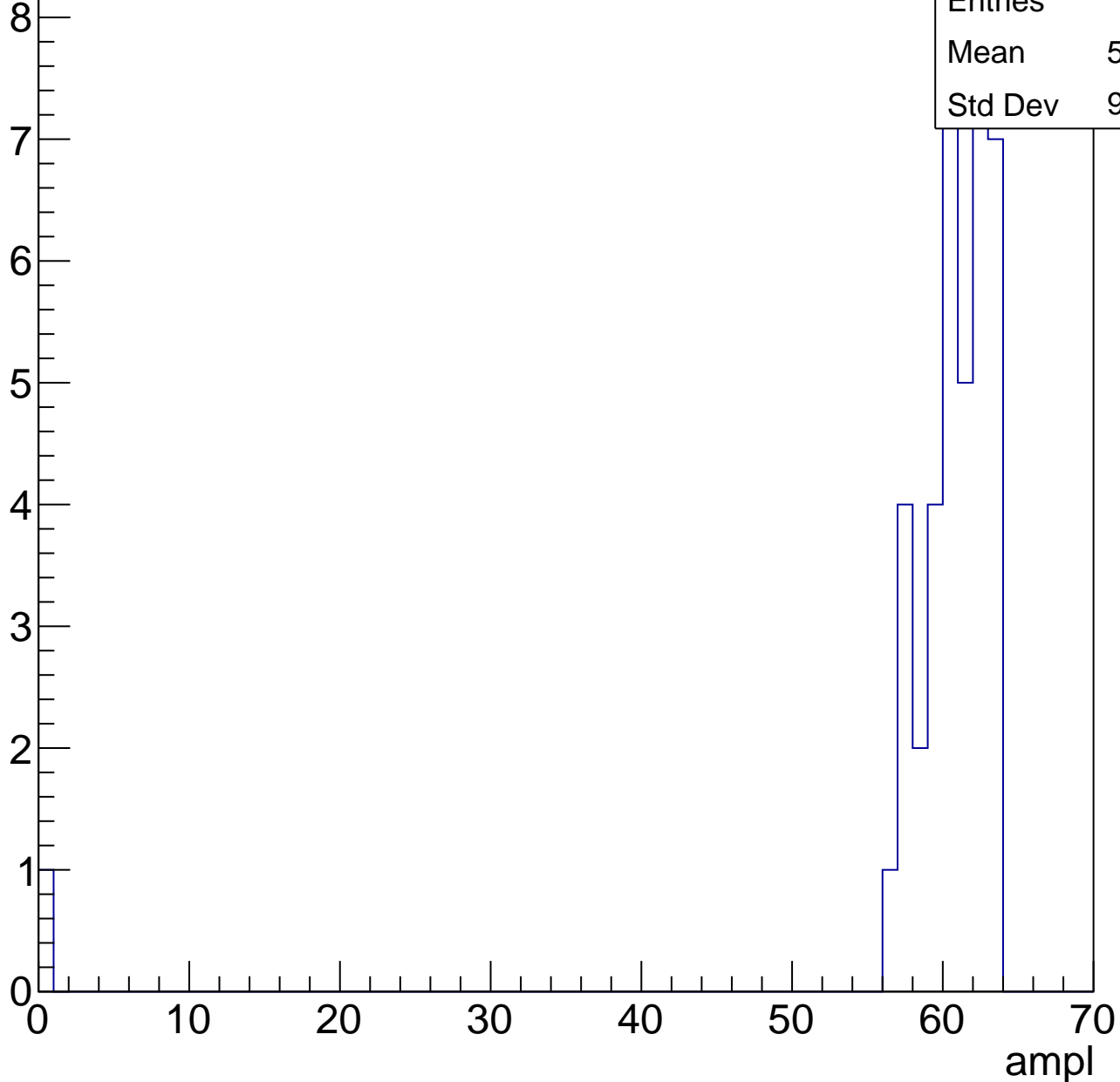
Entries	55
Mean	56.4
Std Dev	3.143



# B1L002S, U17-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

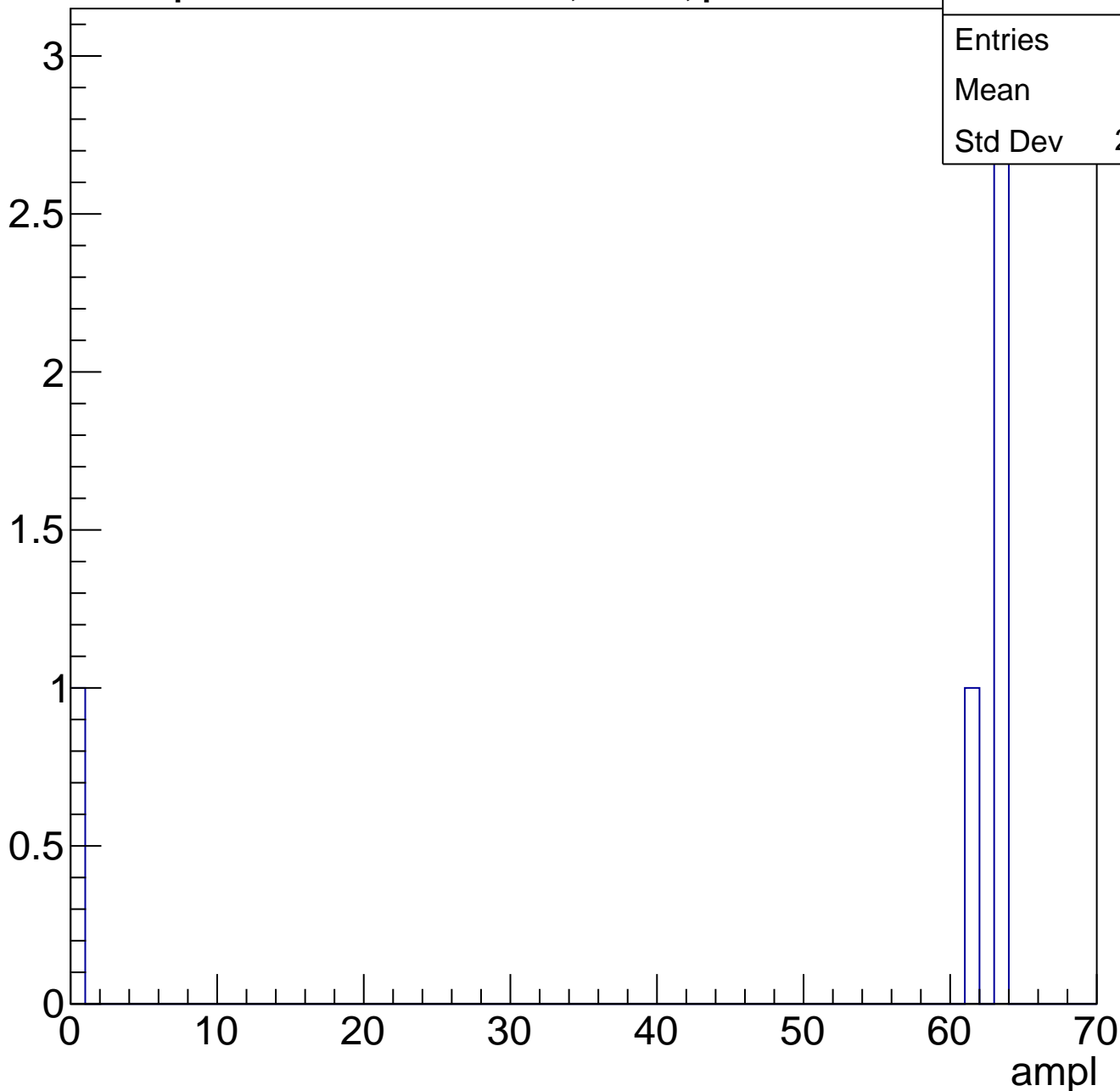




# B1L002S, U17-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch89, adc0

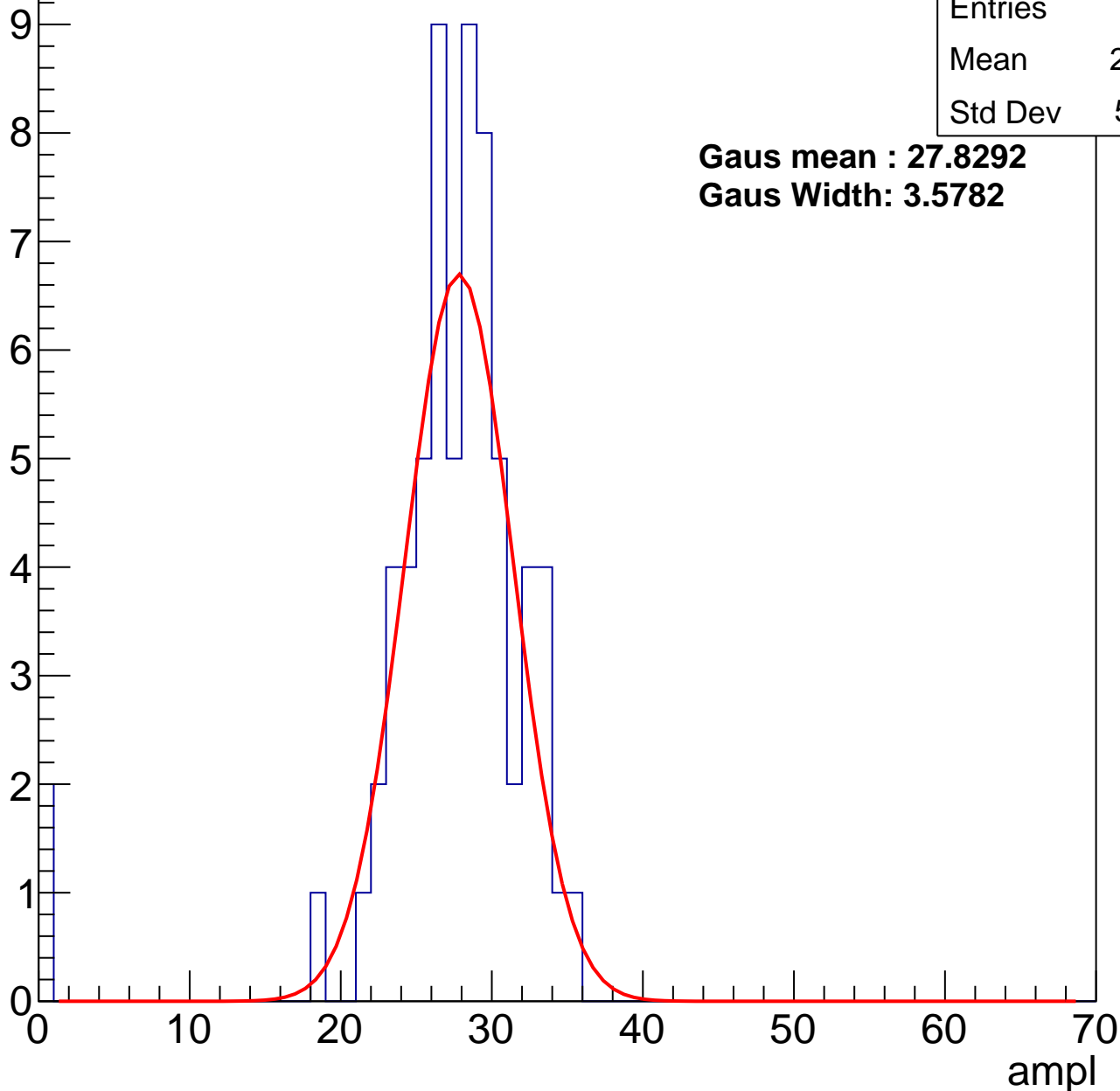
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	26.72
Std Dev	5.761

**Gaus mean : 27.8292**

**Gaus Width: 3.5782**



# B1L002S, U17-ch89, adc1

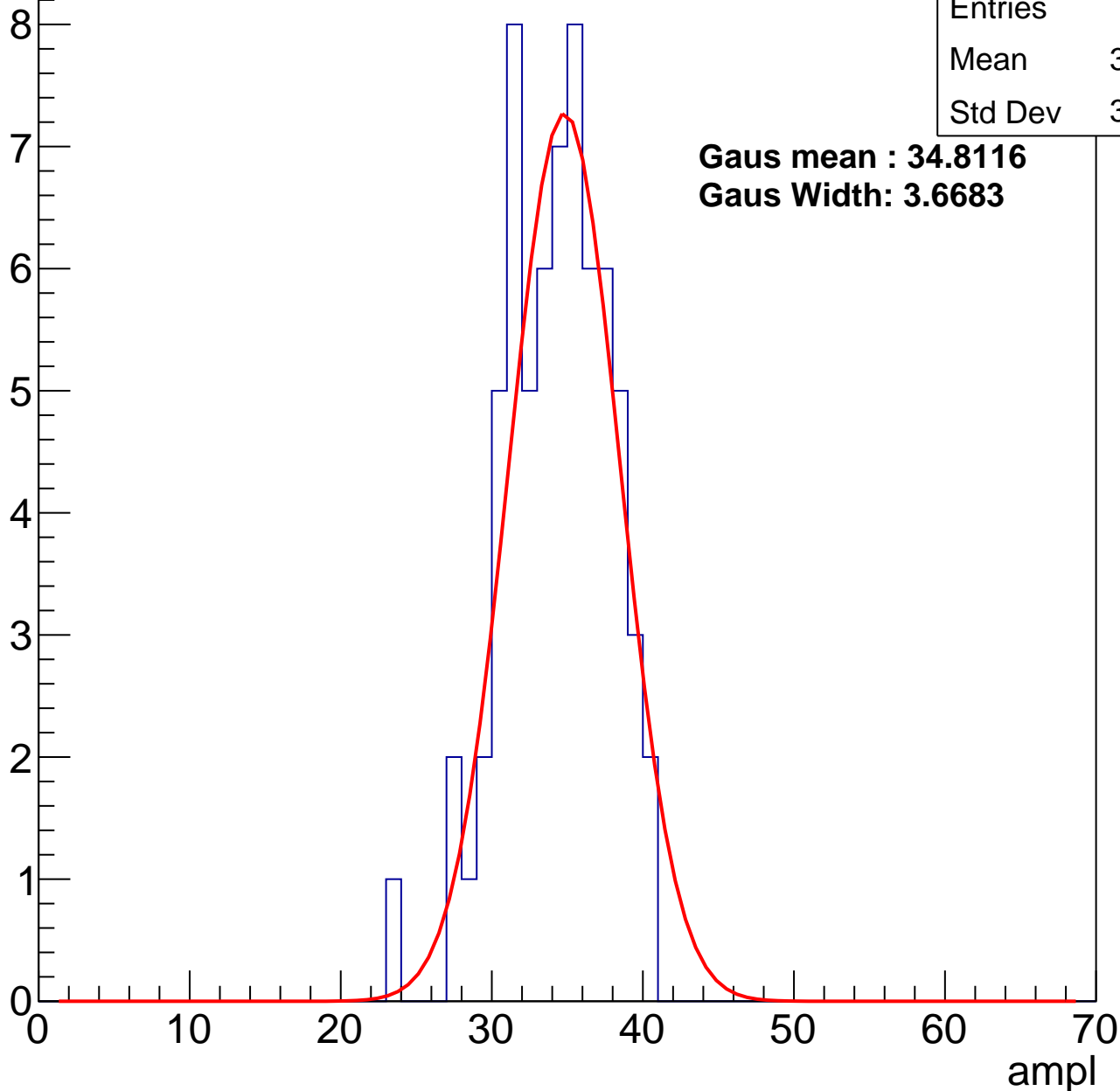
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.76
Std Dev	3.439

**Gaus mean : 34.8116**

**Gaus Width: 3.6683**



# B1L002S, U17-ch89, adc2

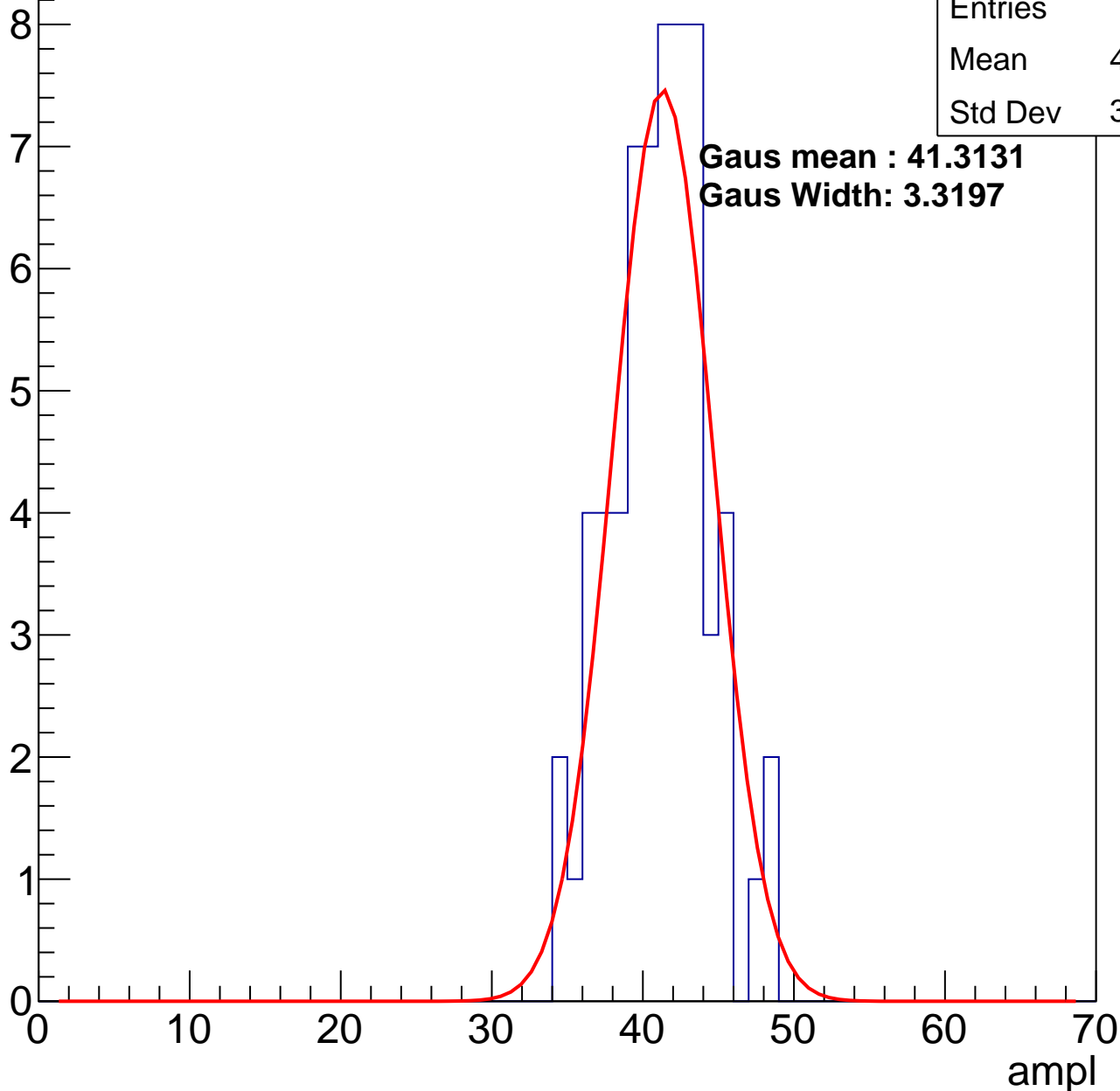
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.68
Std Dev	3.156

**Gaus mean : 41.3131**

**Gaus Width: 3.3197**

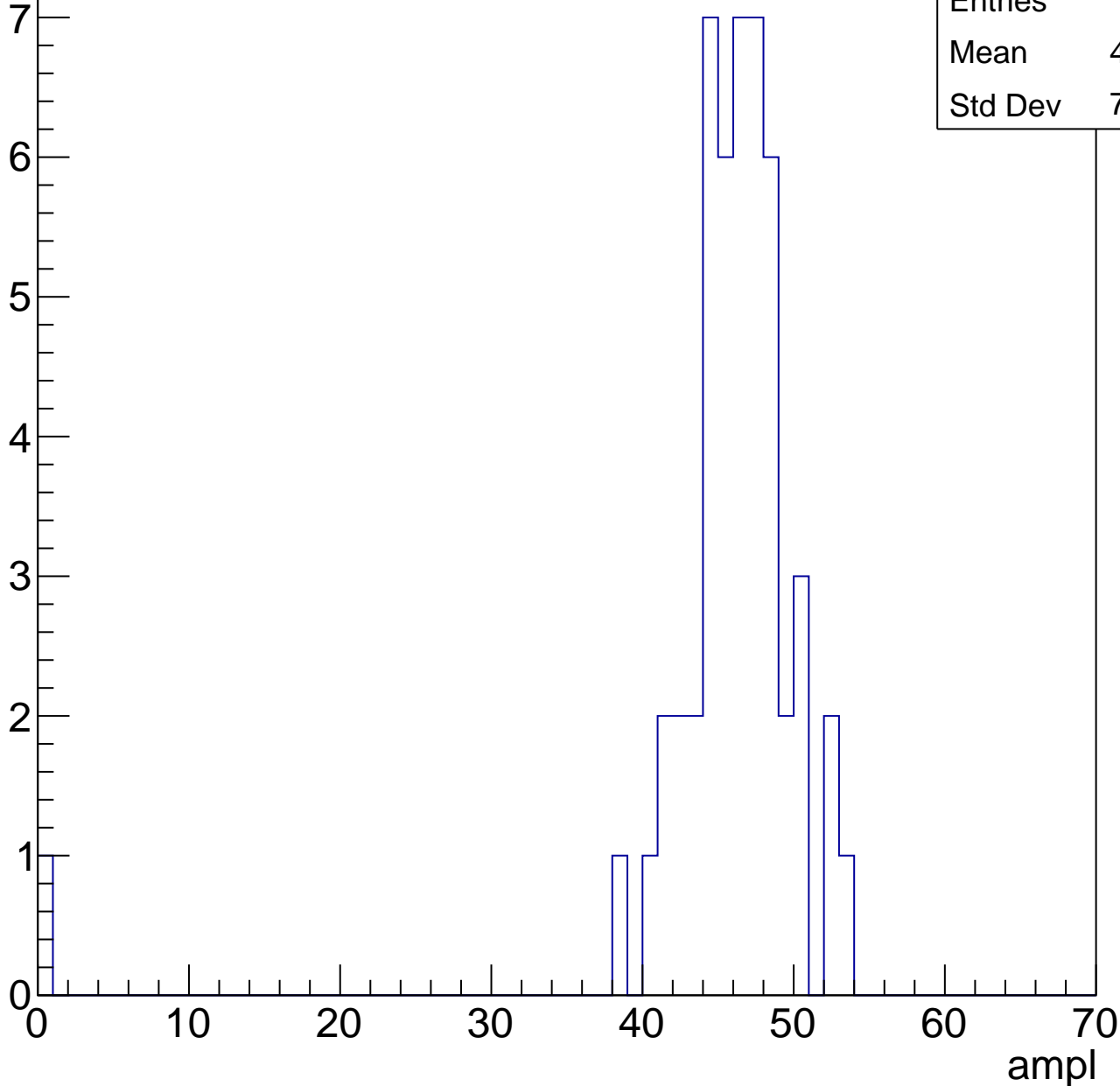


# B1L002S, U17-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

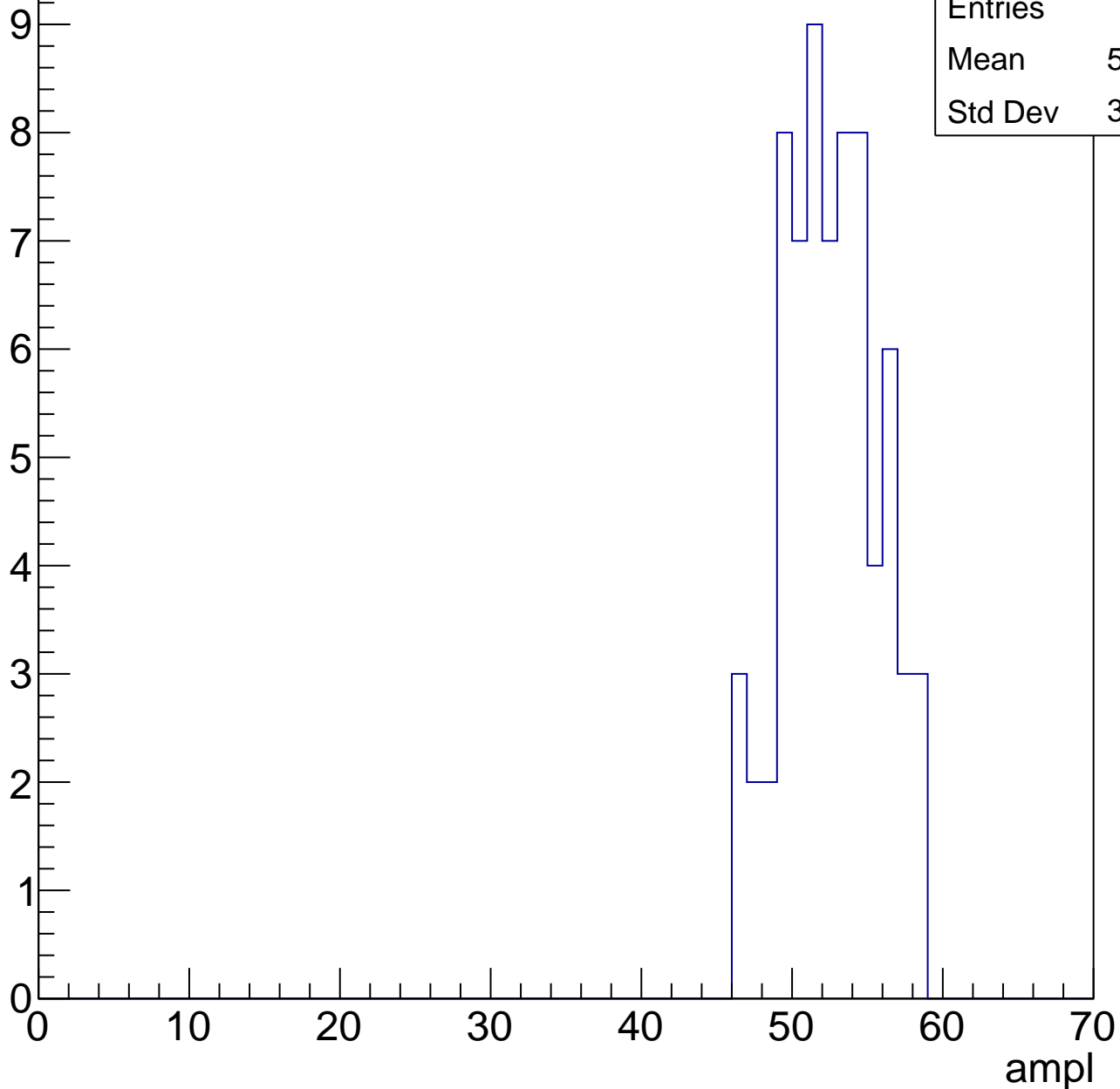
Entries	50
Mean	45.04
Std Dev	7.102



# B1L002S, U17-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch89, adc5

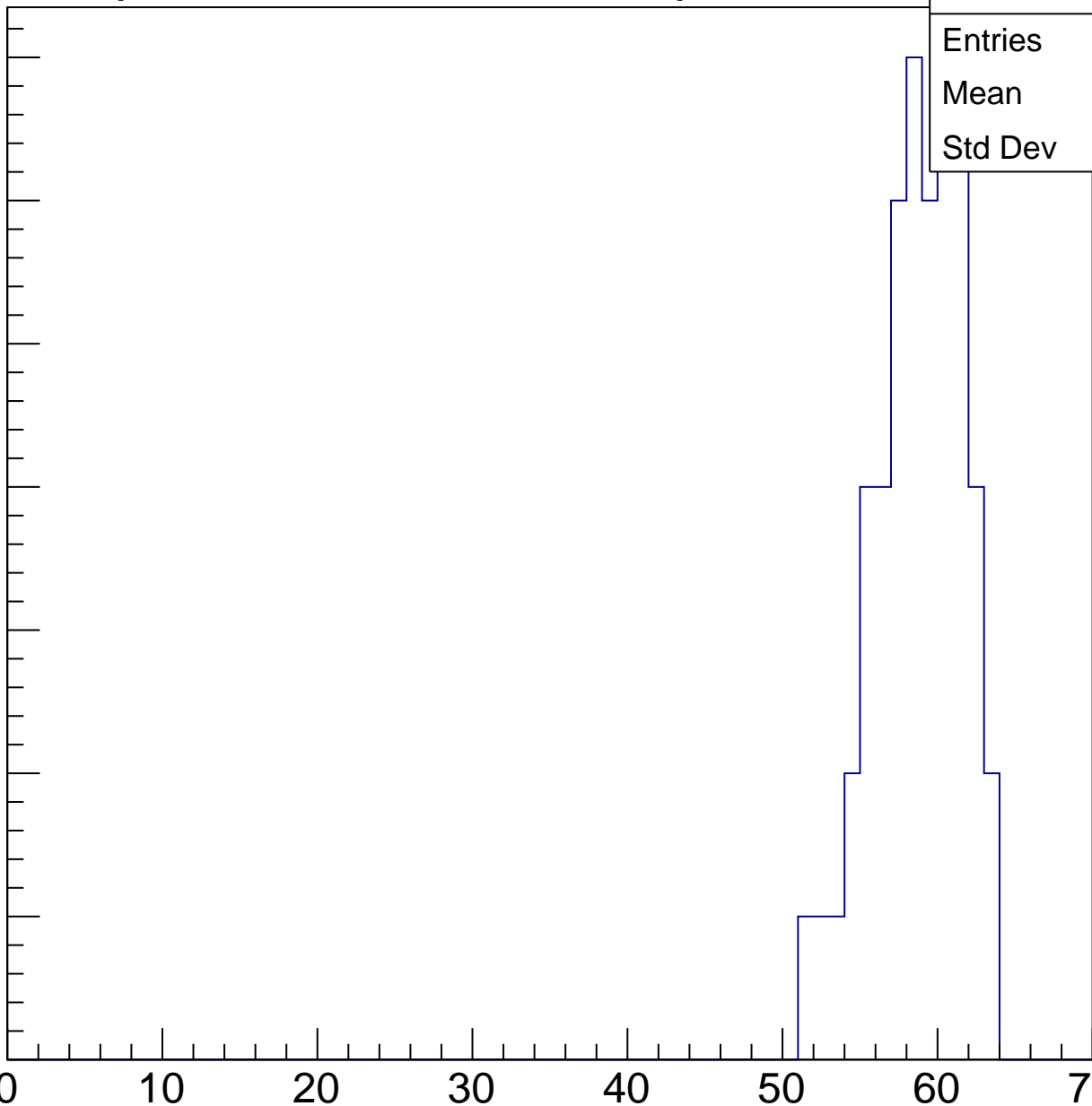
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	58.29
Std Dev	2.797

ampl

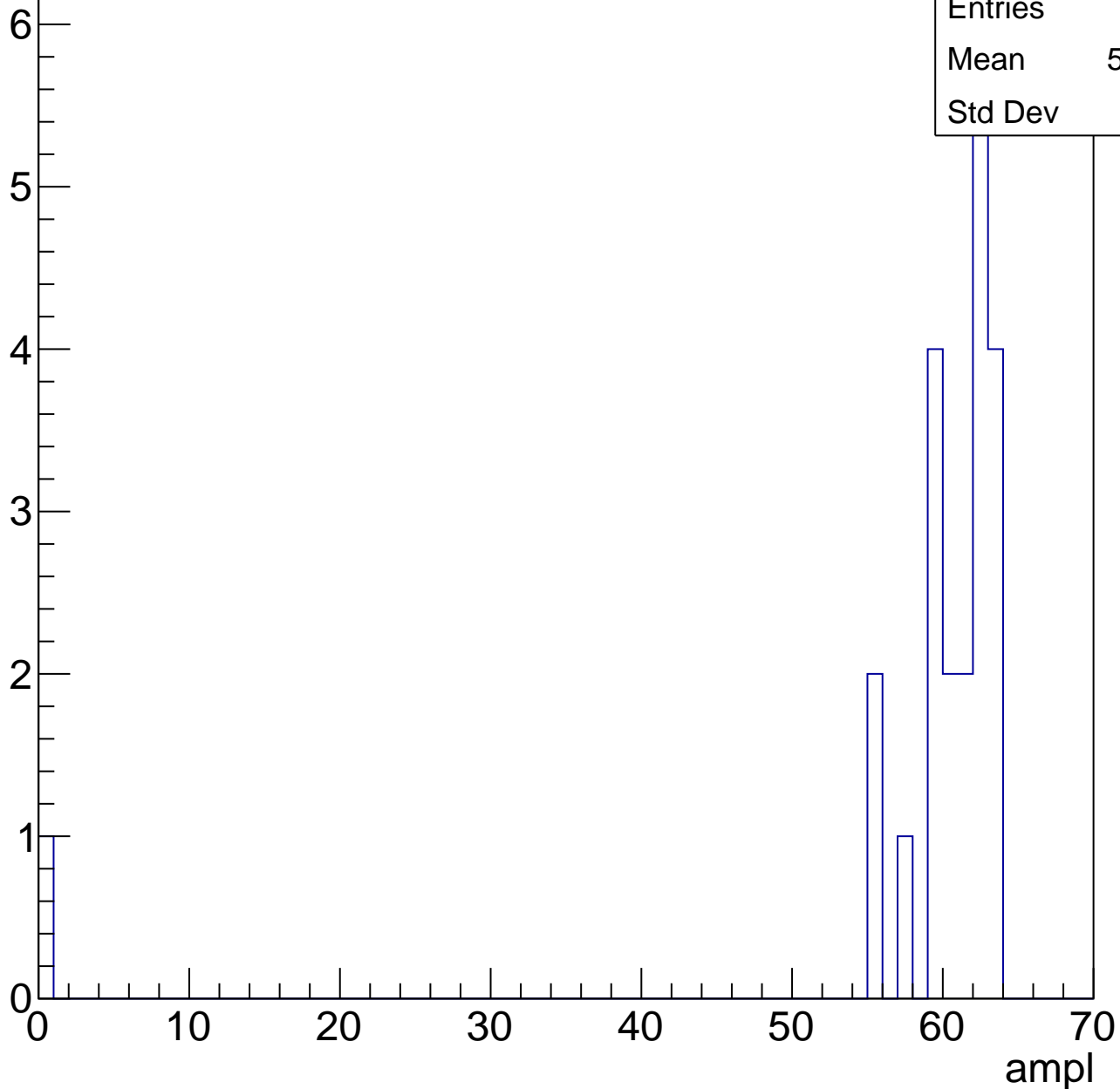


# B1L002S, U17-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	57.68
Std Dev	12.8

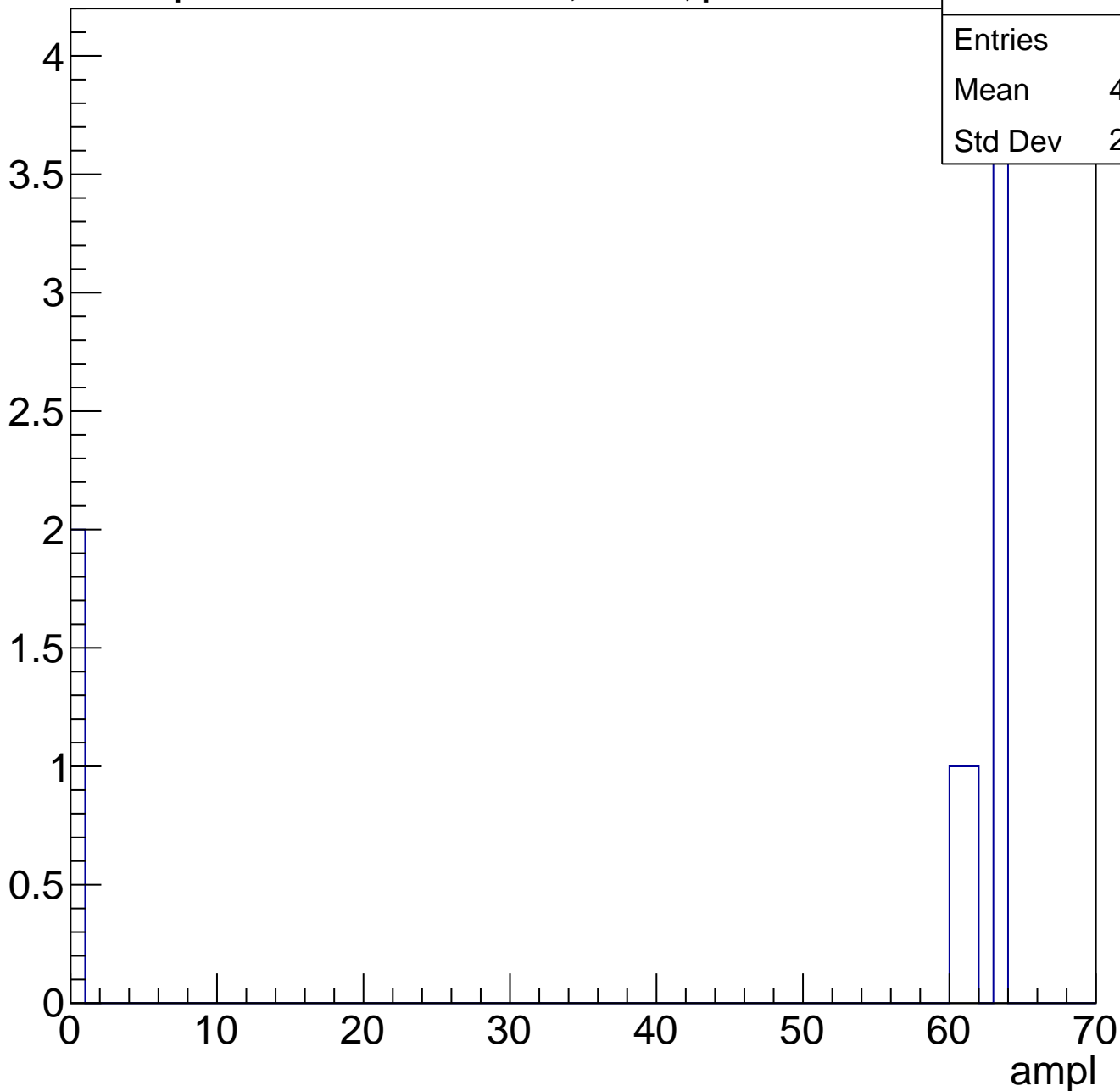




# B1L002S, U17-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch90, adc0

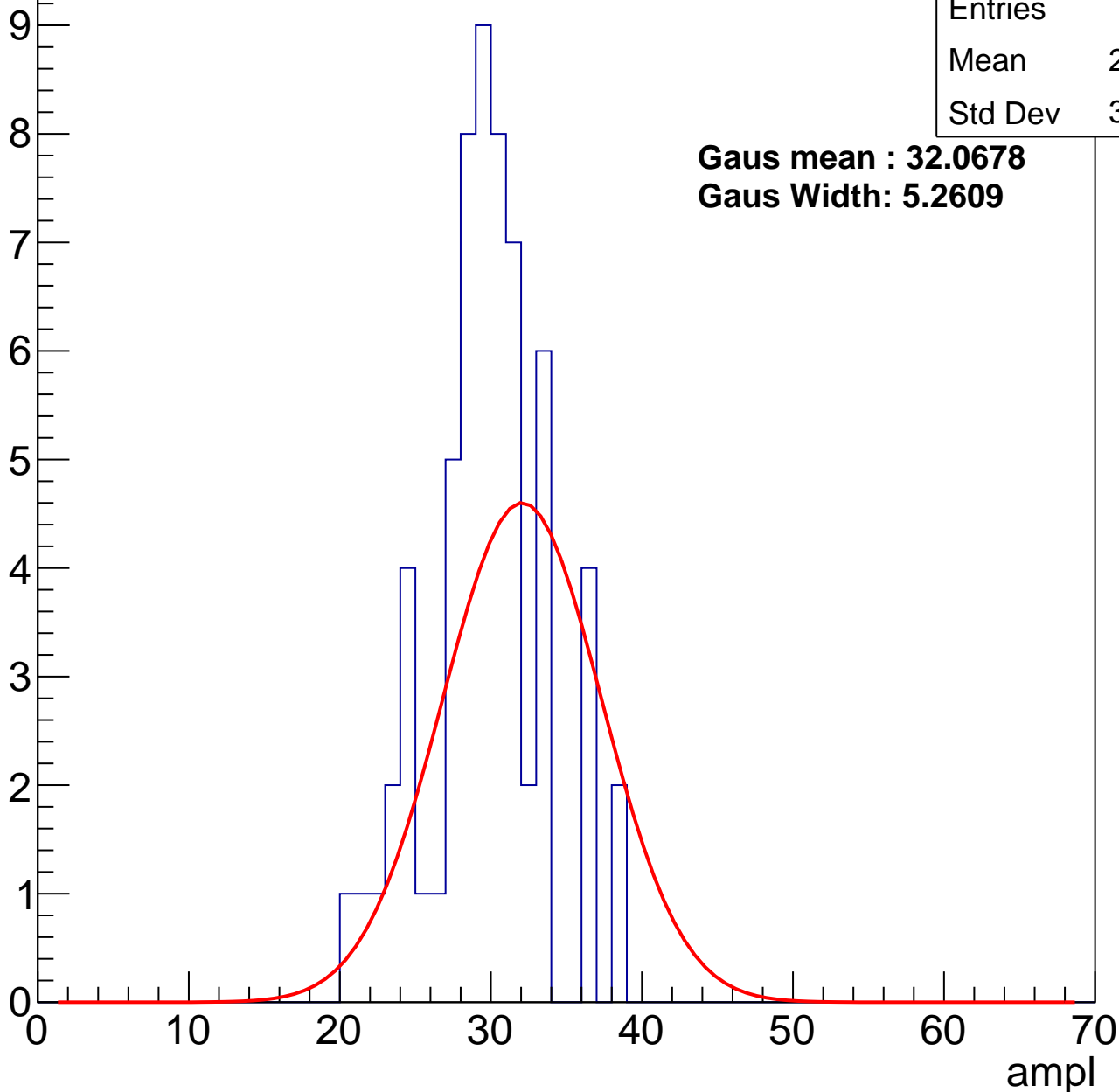
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.27
Std Dev	3.857

**Gaus mean : 32.0678**

**Gaus Width: 5.2609**



# B1L002S, U17-ch90, adc1

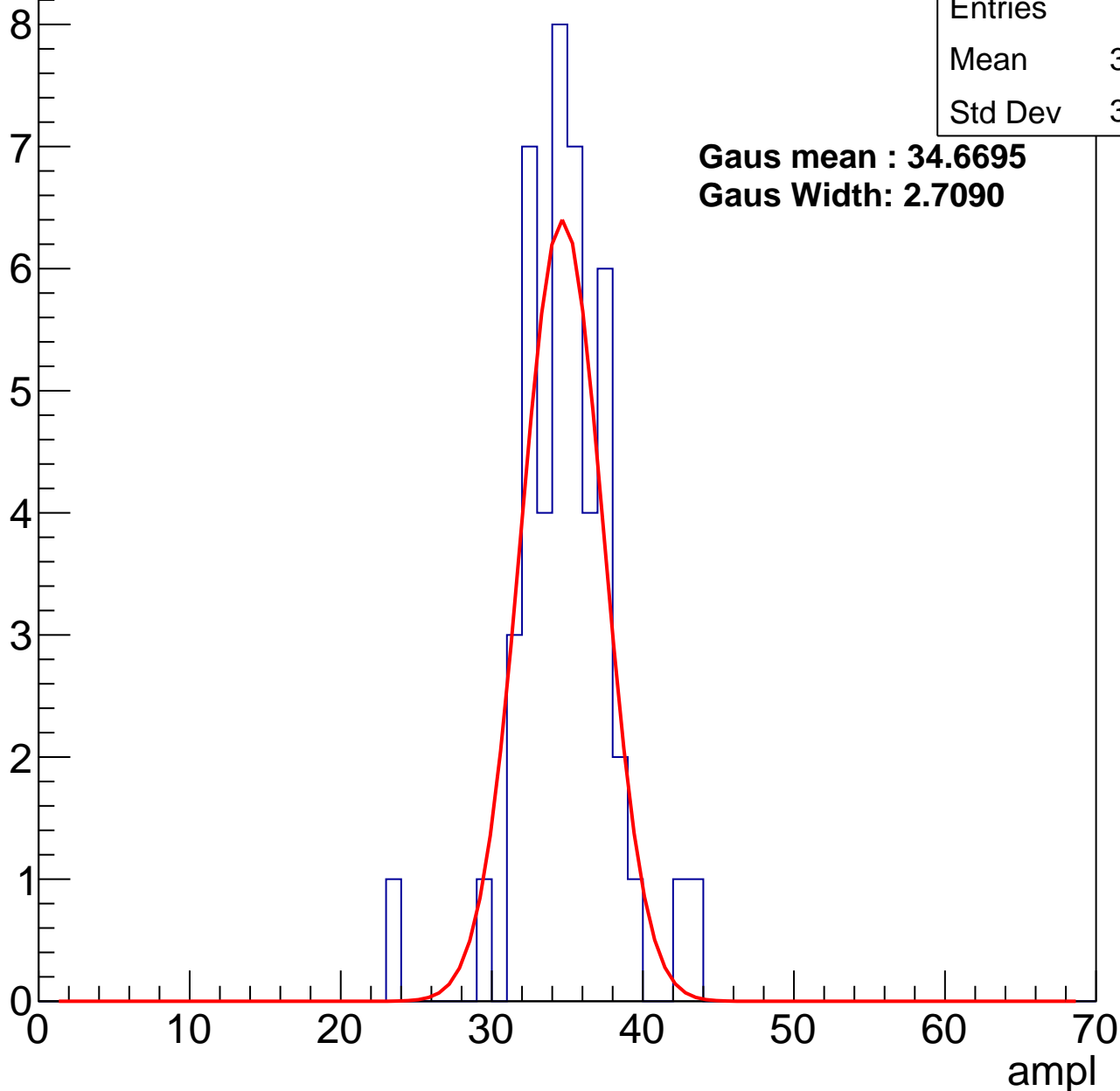
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	34.43
Std Dev	3.214

**Gaus mean : 34.6695**

**Gaus Width: 2.7090**



# B1L002S, U17-ch90, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

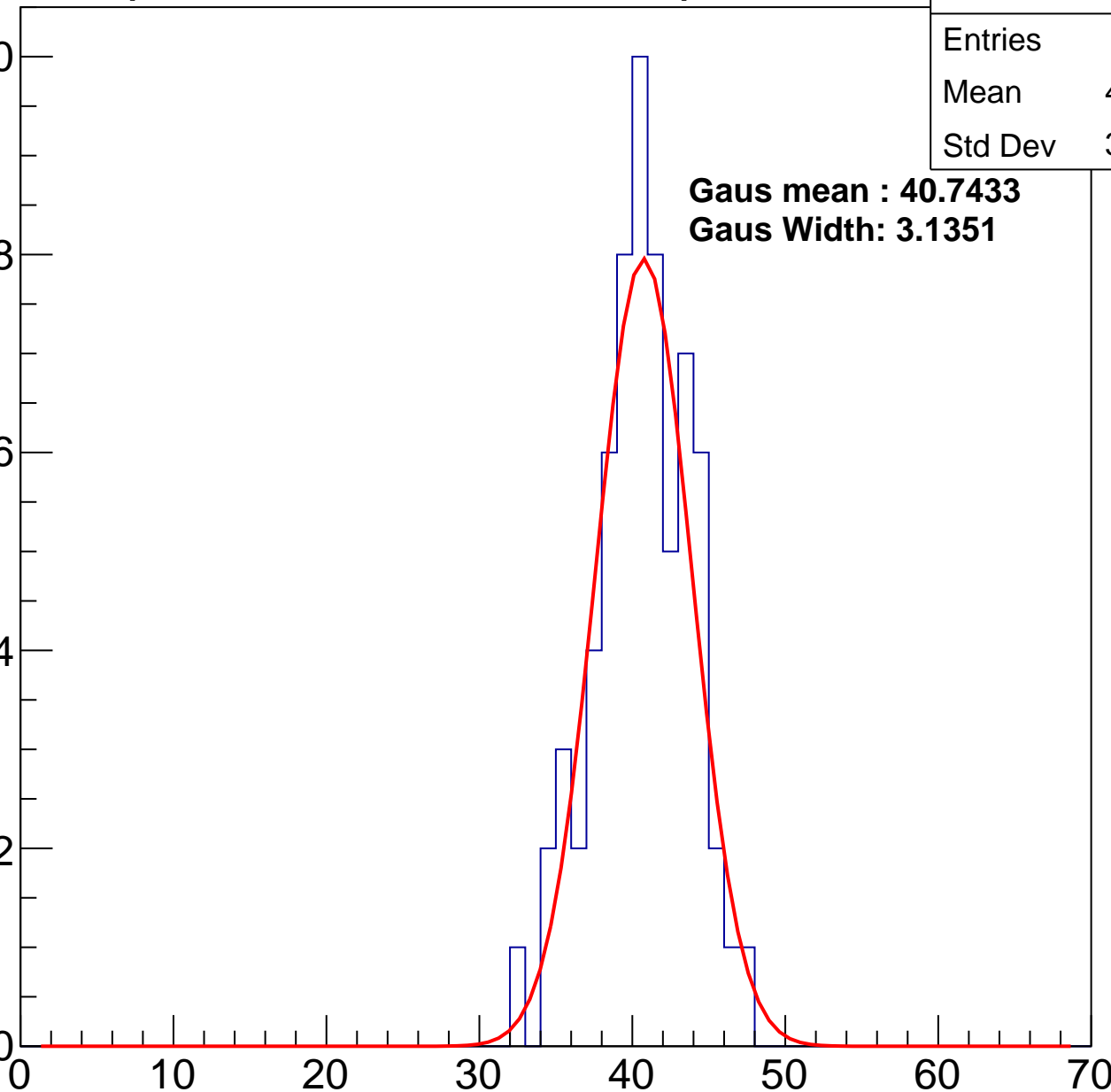
Entries	66
Mean	40.17
Std Dev	3.092

**Gaus mean : 40.7433**

**Gaus Width: 3.1351**

10  
8  
6  
4  
2  
0

ampl

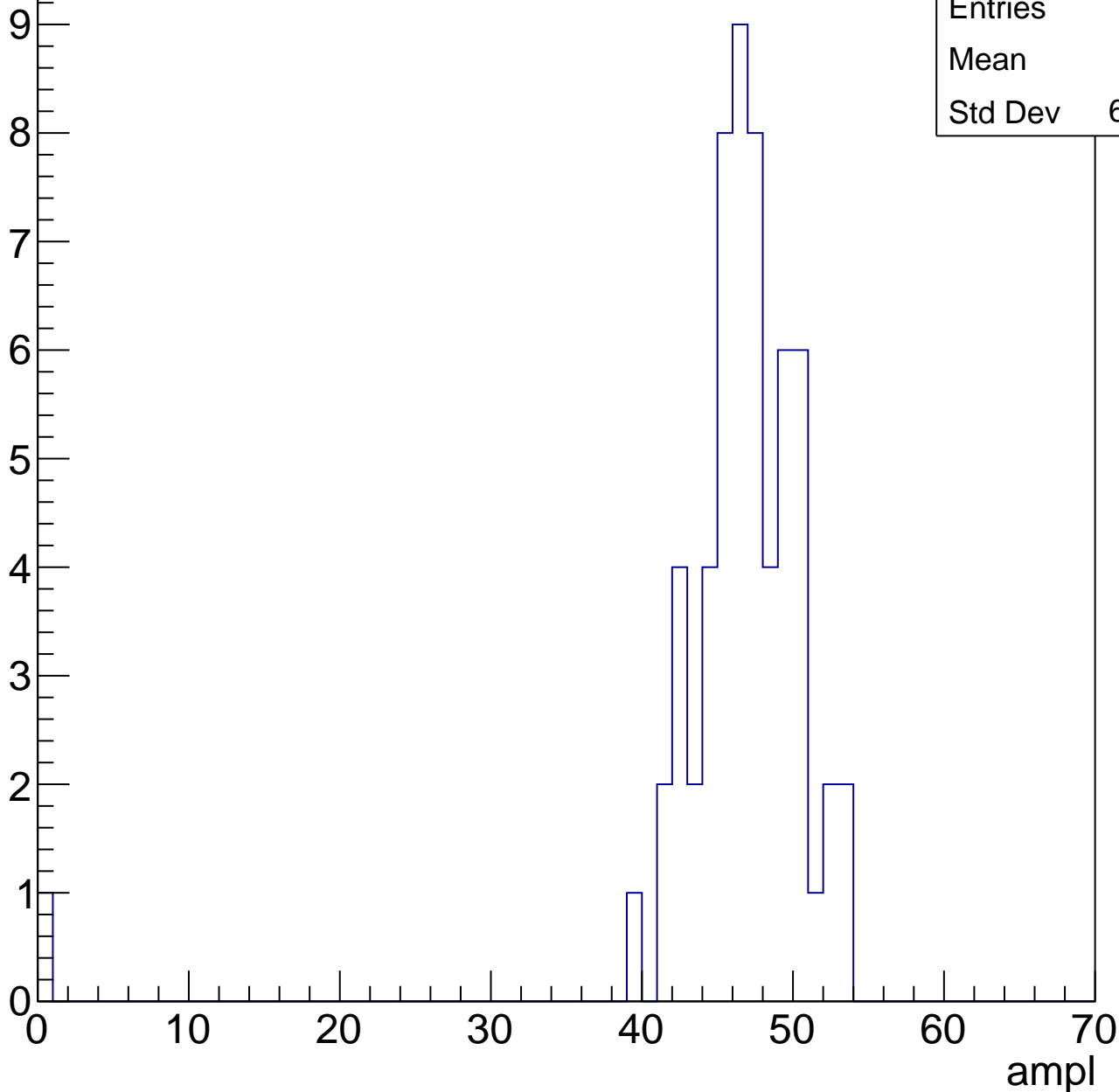


# B1L002S, U17-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.8
Std Dev	6.693

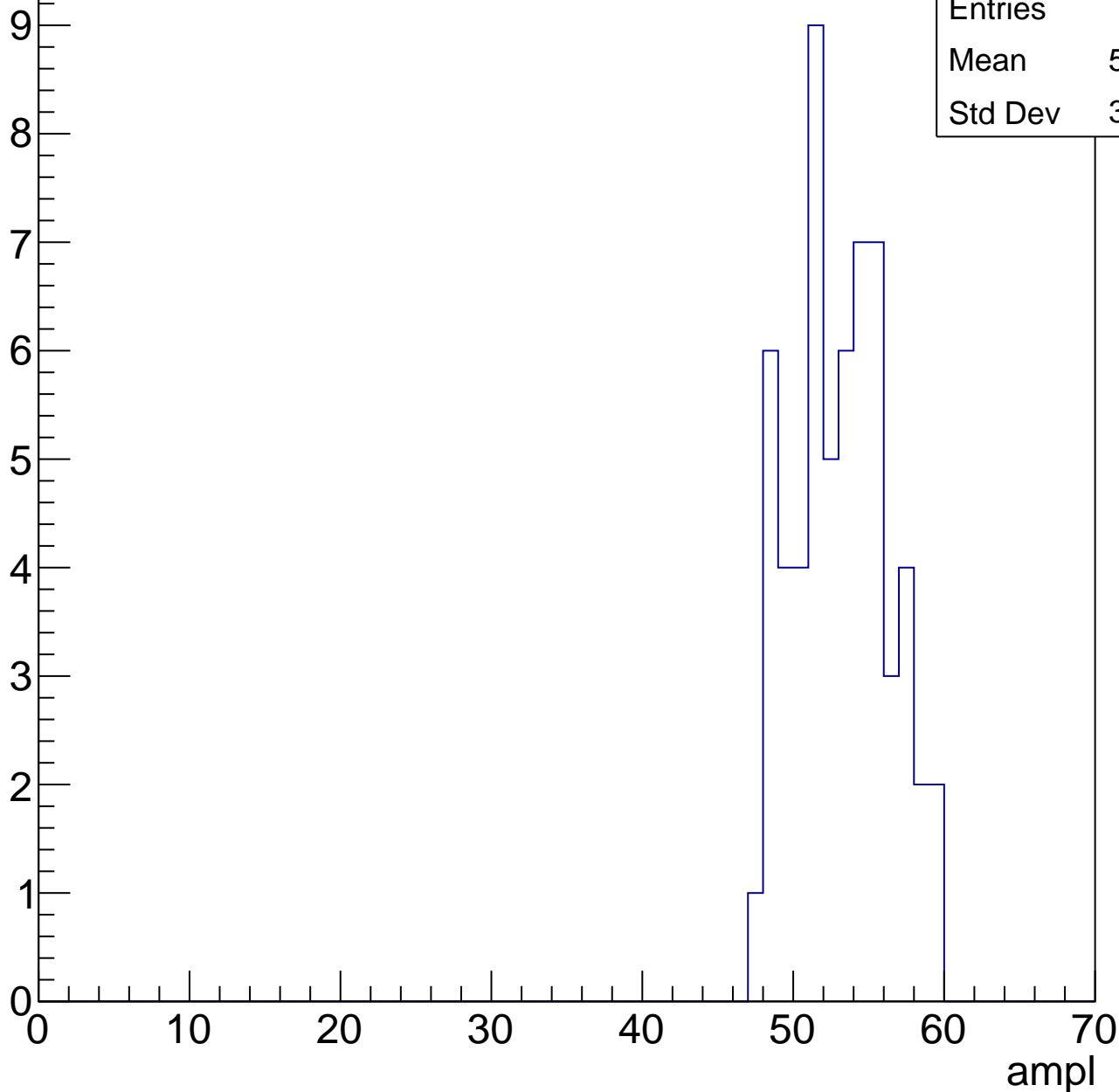


# B1L002S, U17-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.68
Std Dev	3.079

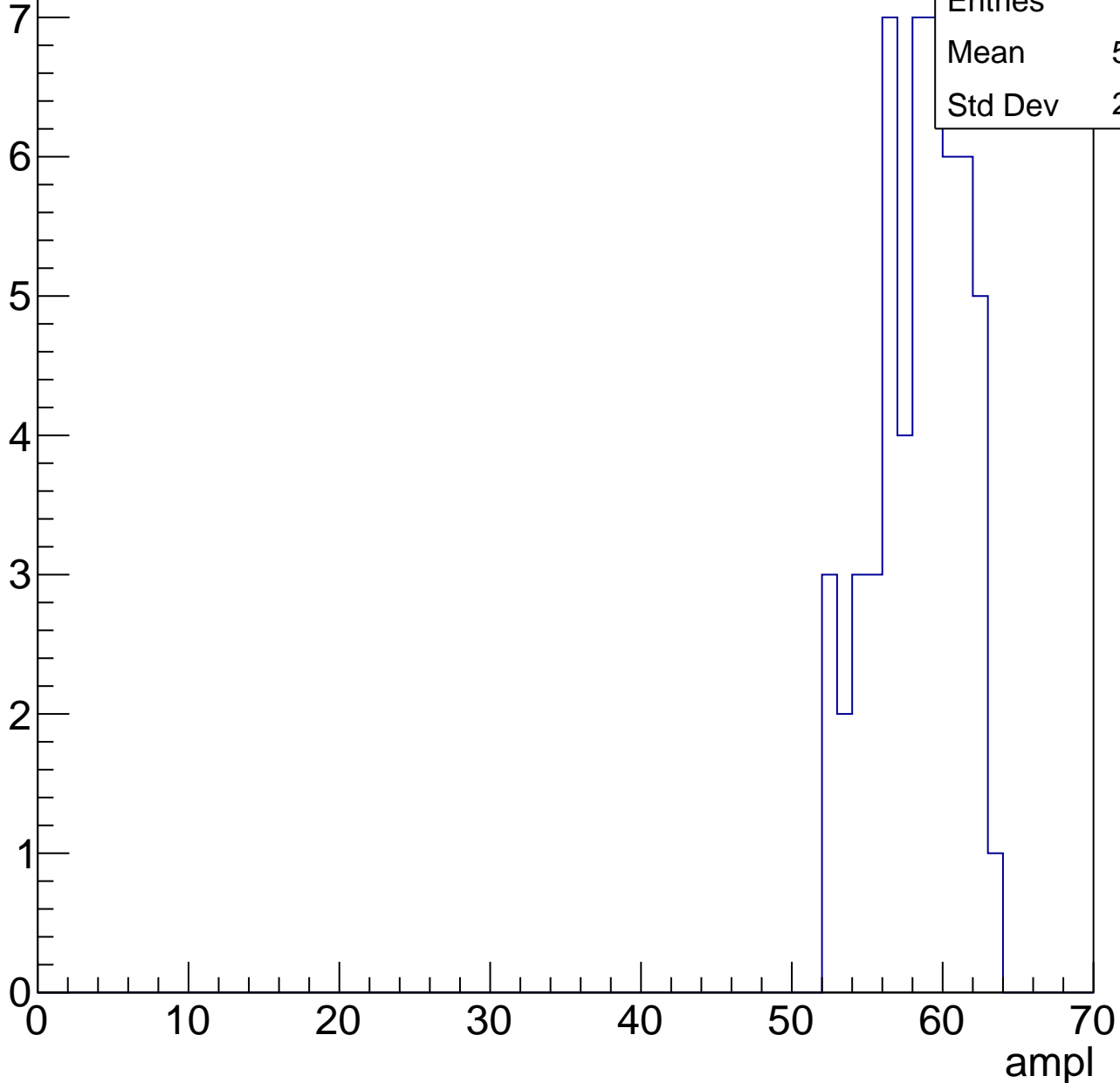


# B1L002S, U17-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.91
Std Dev	2.901

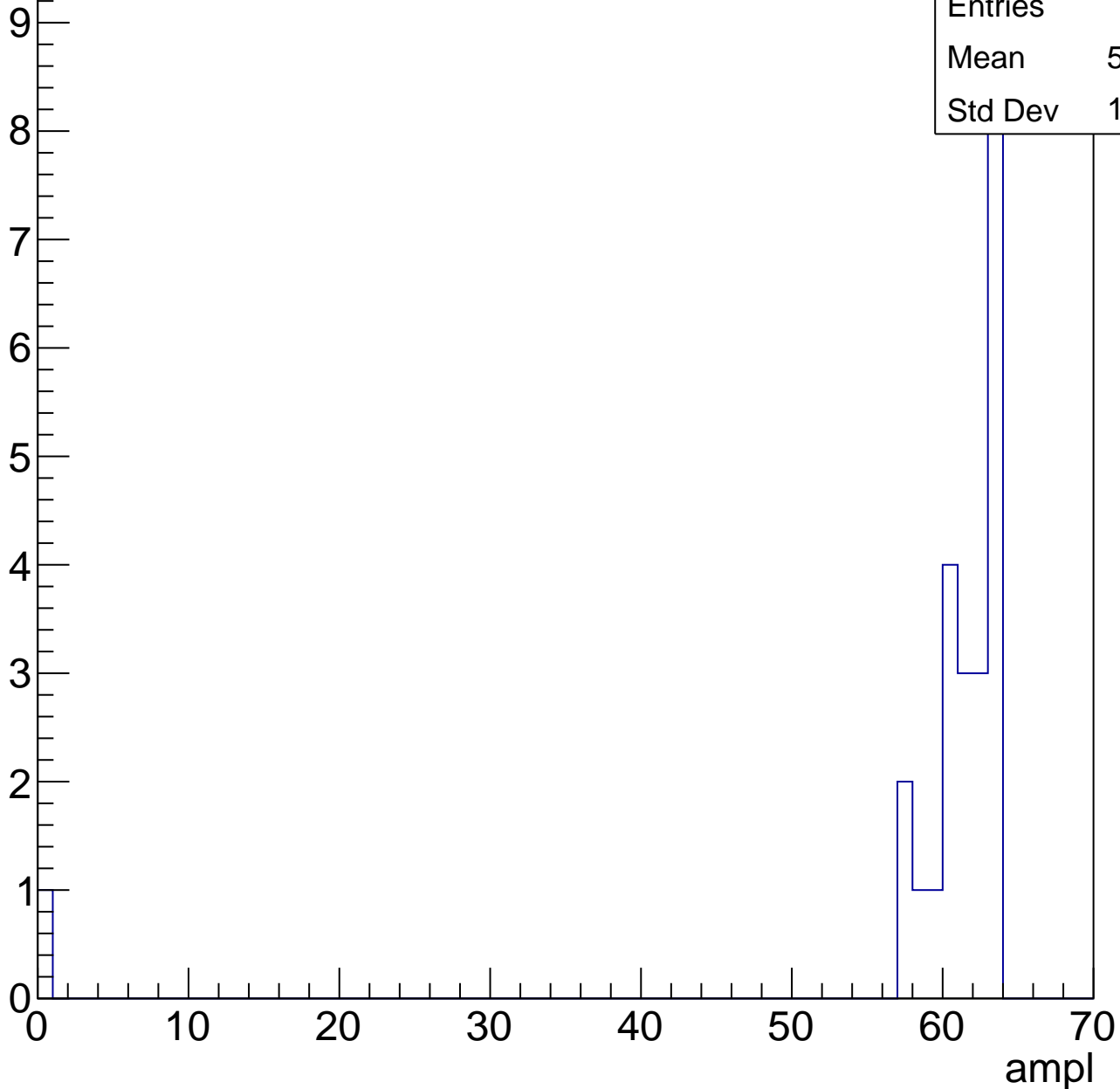


# B1L002S, U17-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.62
Std Dev	12.37





# B1L002S, U17-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

# B1L002S, U17-ch91, adc0

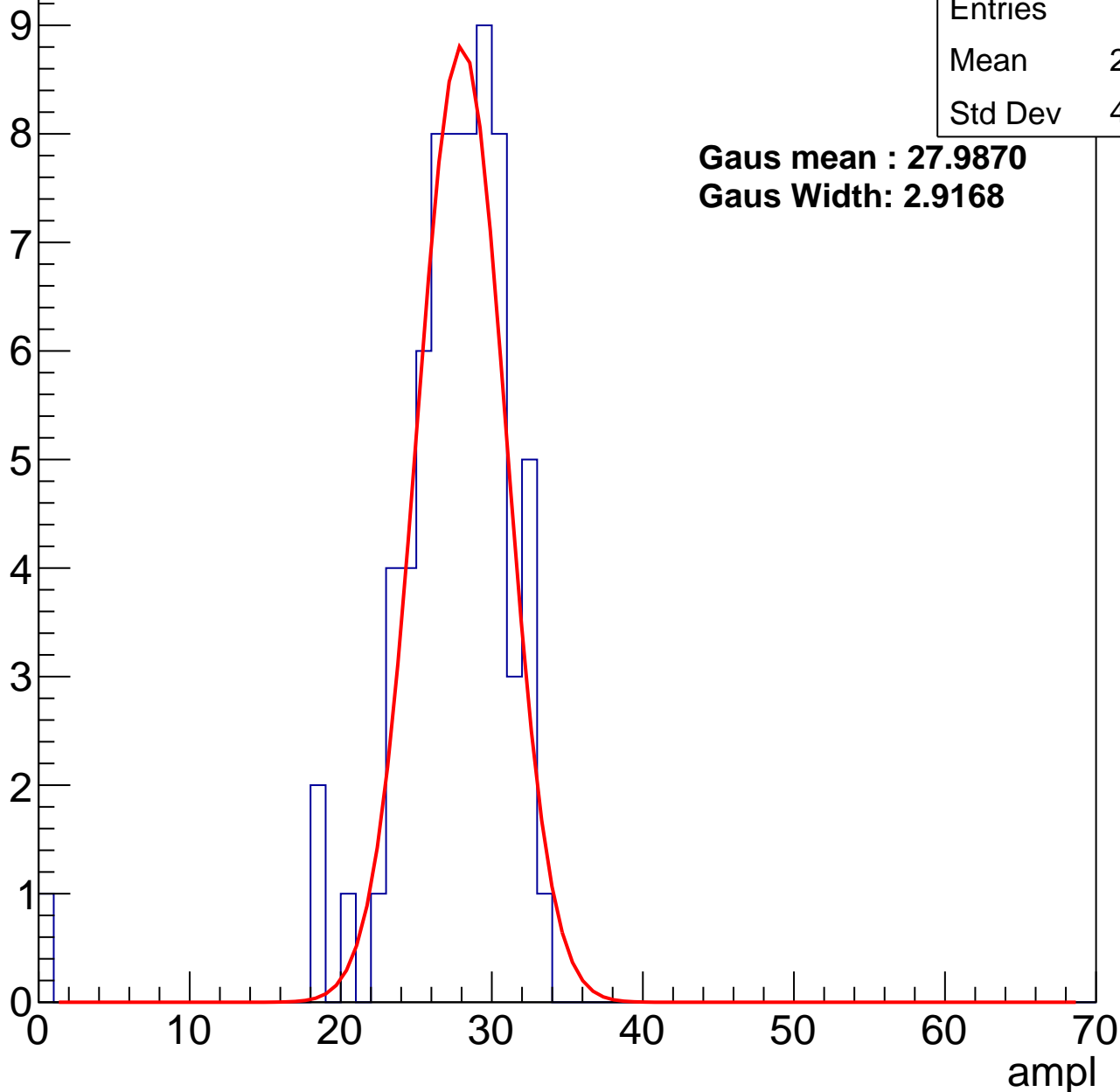
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	26.83
Std Dev	4.536

**Gaus mean : 27.9870**

**Gaus Width: 2.9168**



# B1L002S, U17-ch91, adc1

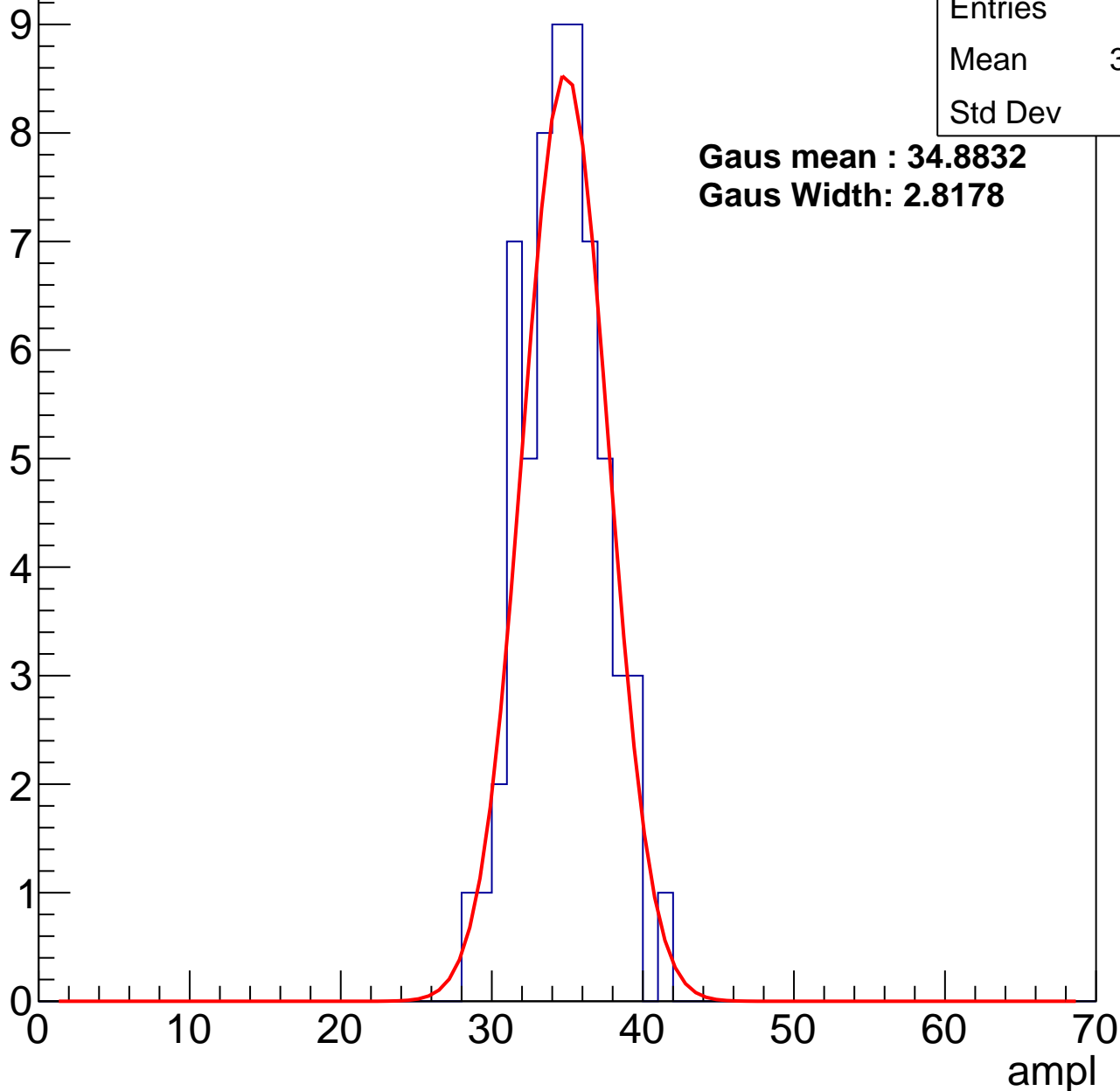
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.23
Std Dev	2.67

**Gaus mean : 34.8832**

**Gaus Width: 2.8178**



# B1L002S, U17-ch91, adc2

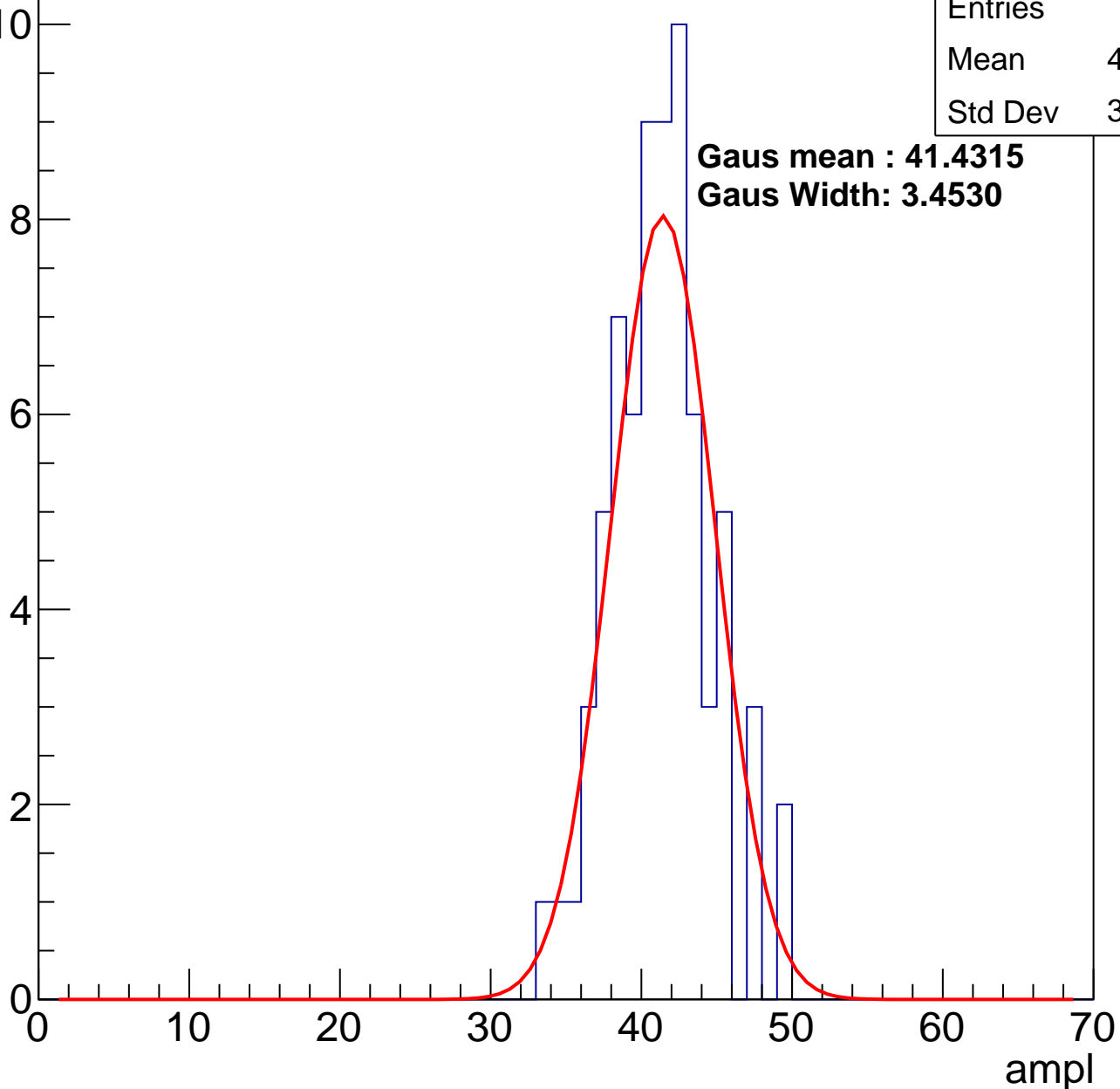
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.82
Std Dev	3.303

**Gaus mean : 41.4315**

**Gaus Width: 3.4530**



# B1L002S, U17-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

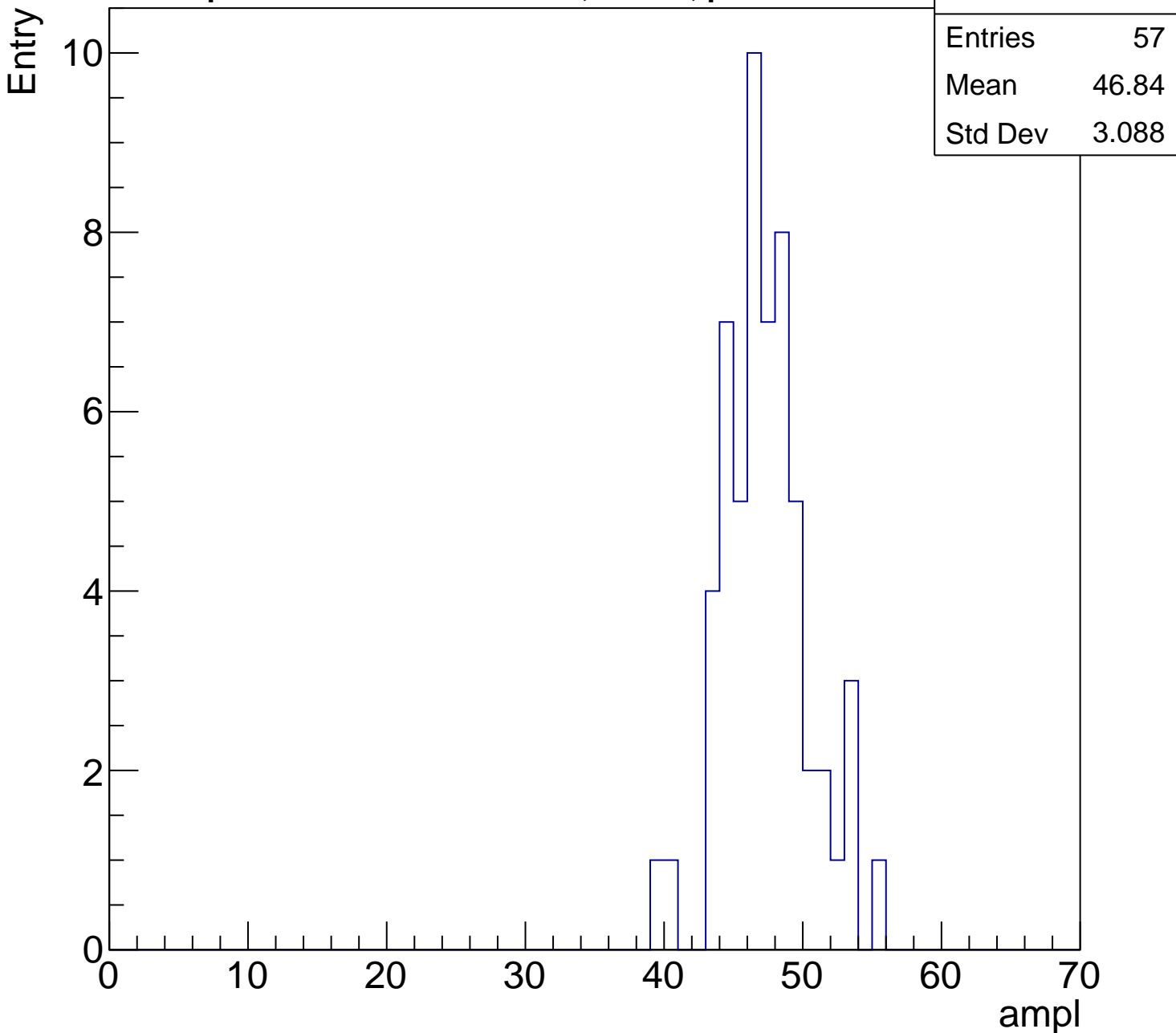
Entries	57
Mean	46.84
Std Dev	3.088

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

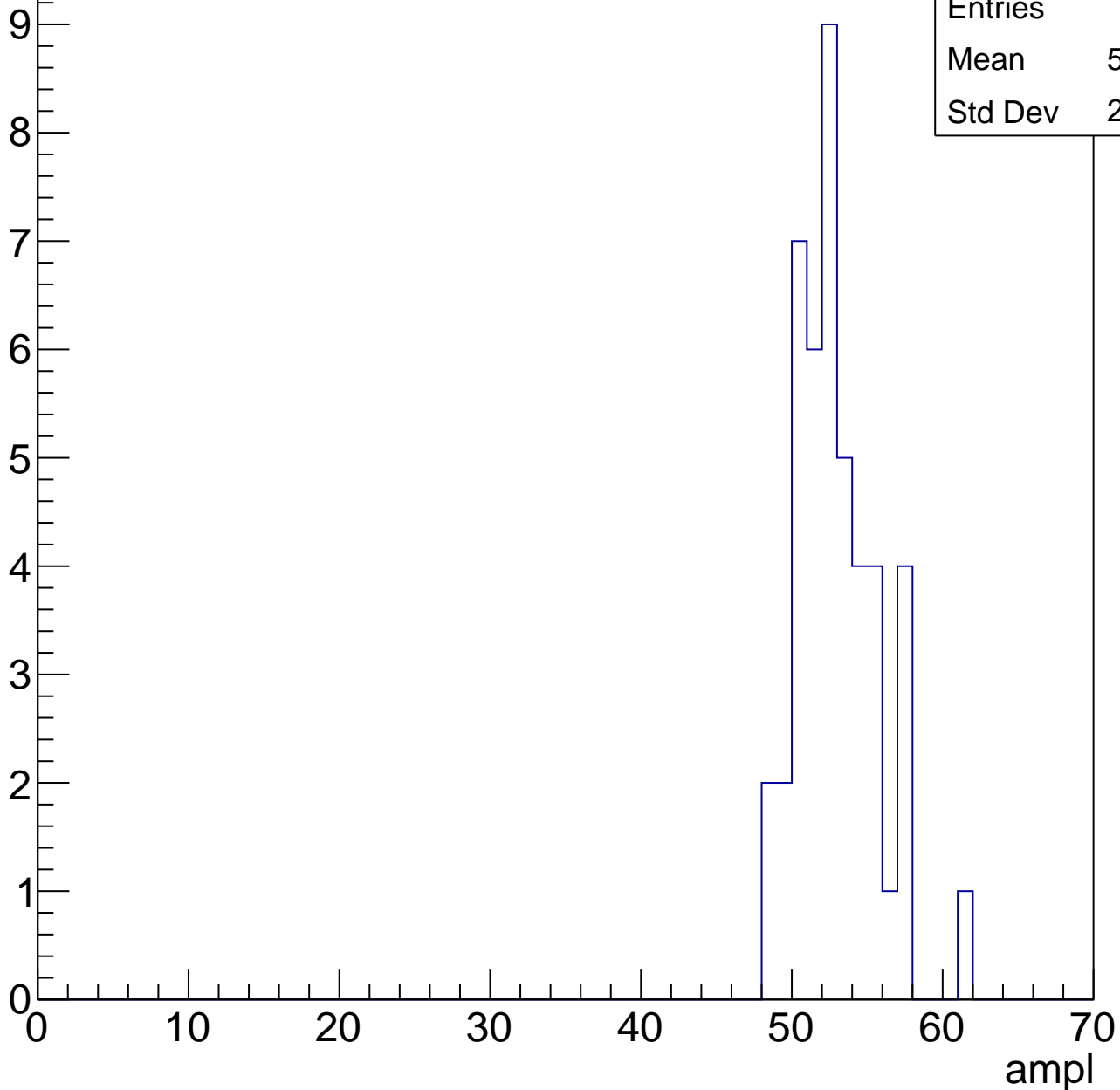


# B1L002S, U17-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	52.53
Std Dev	2.688

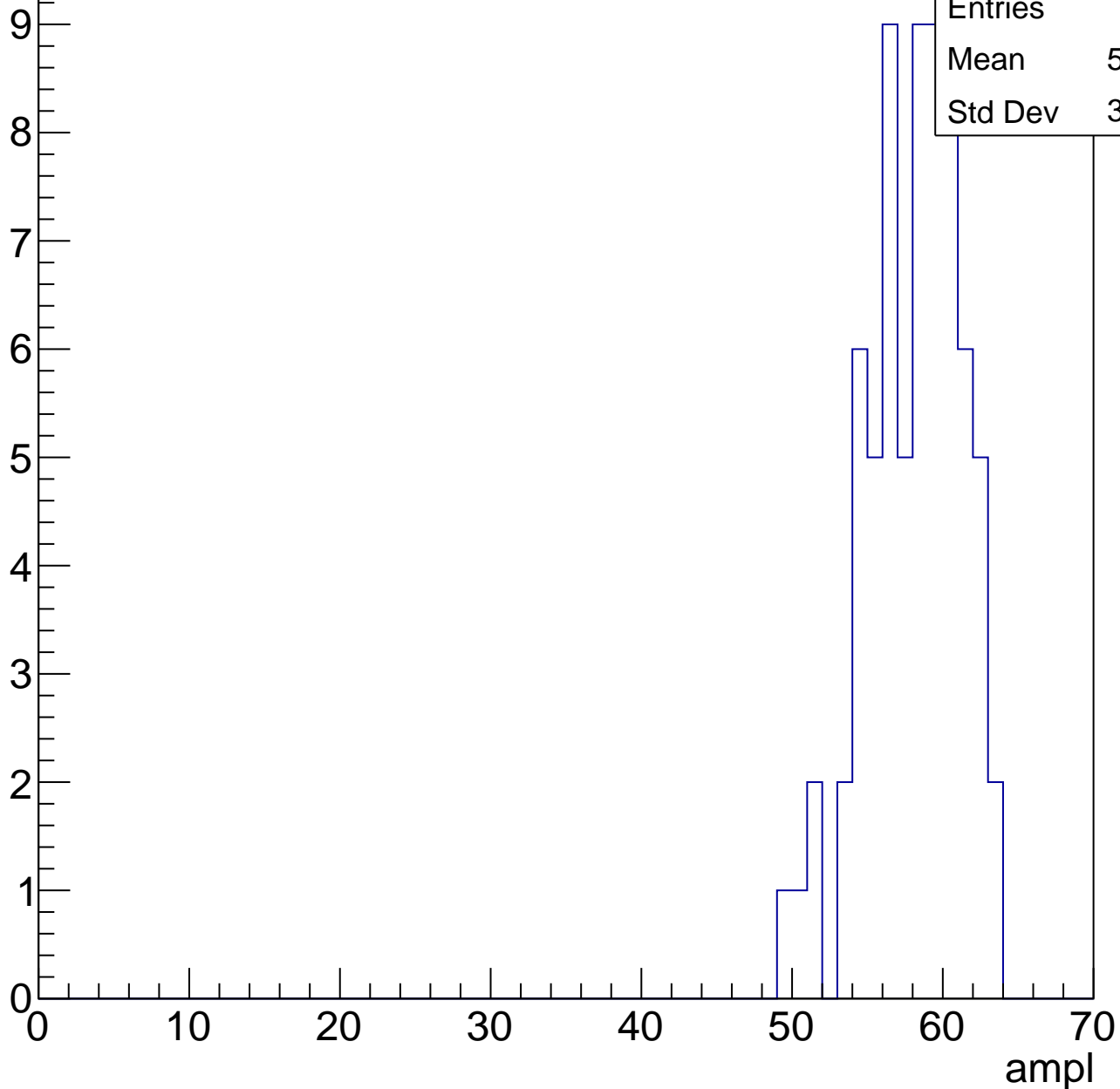


# B1L002S, U17-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	57.57
Std Dev	3.133

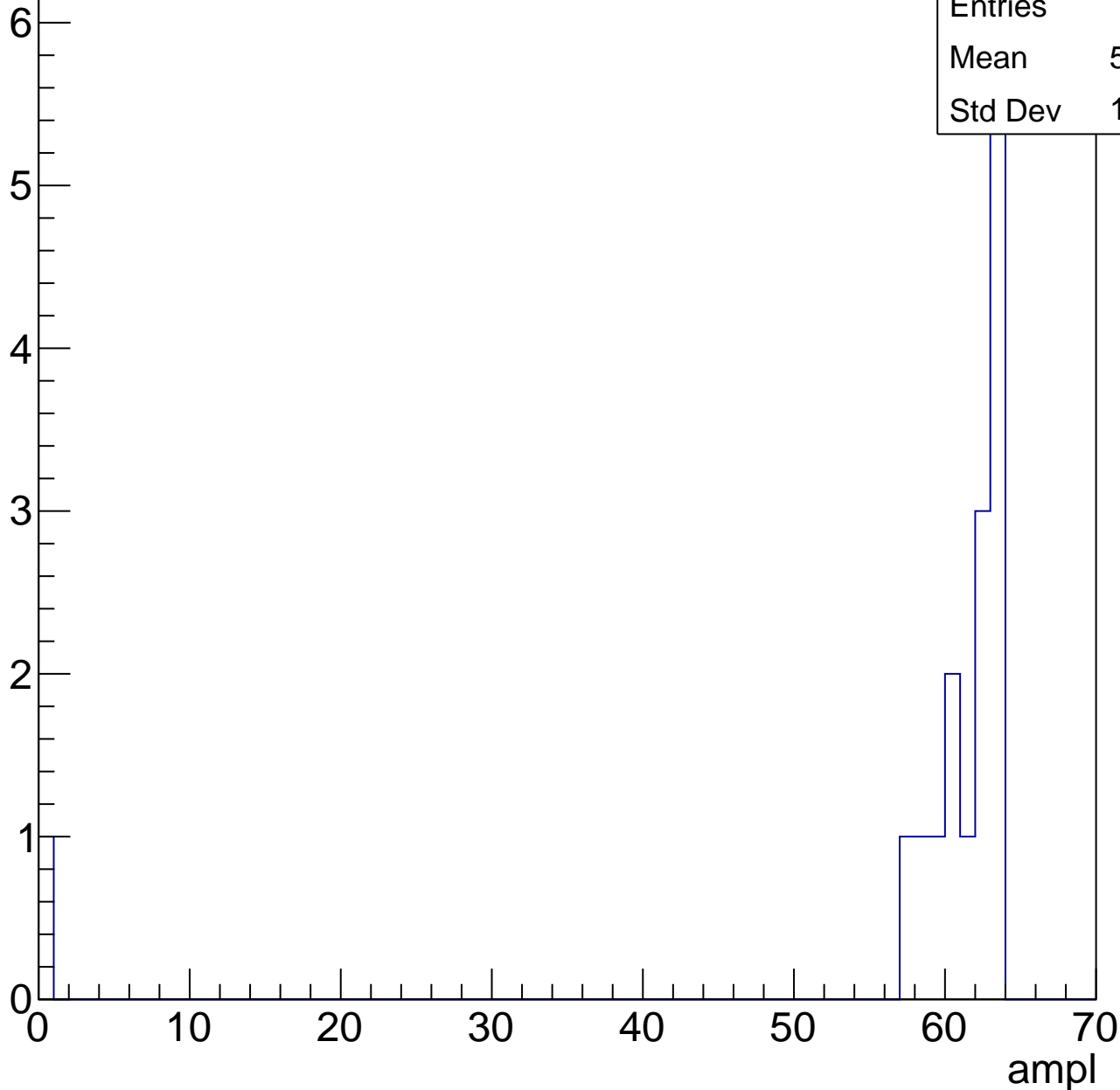


# B1L002S, U17-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.44
Std Dev	14.95

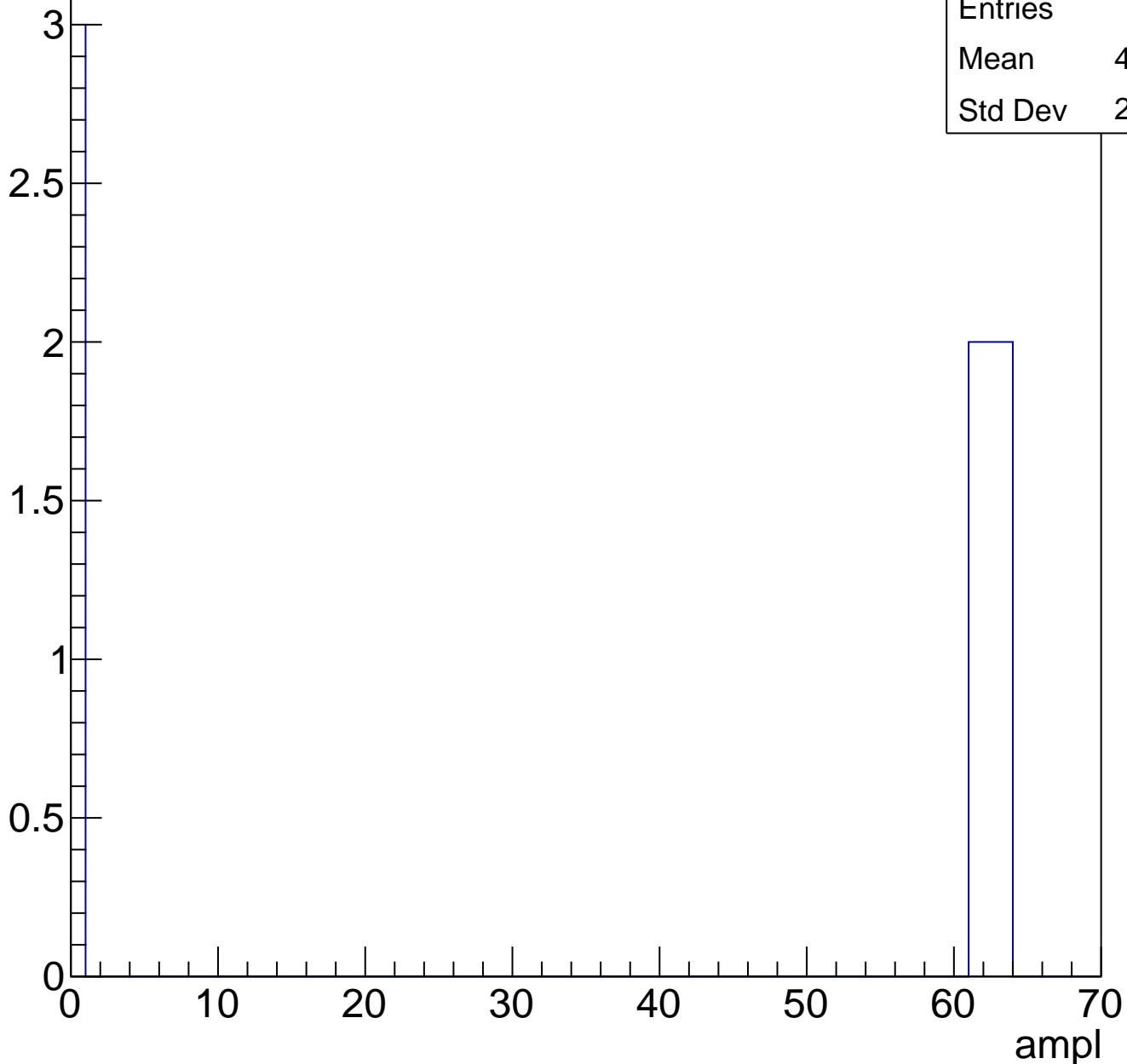




# B1L002S, U17-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	9
Mean	41.33
Std Dev	29.23

# B1L002S, U17-ch92, adc0

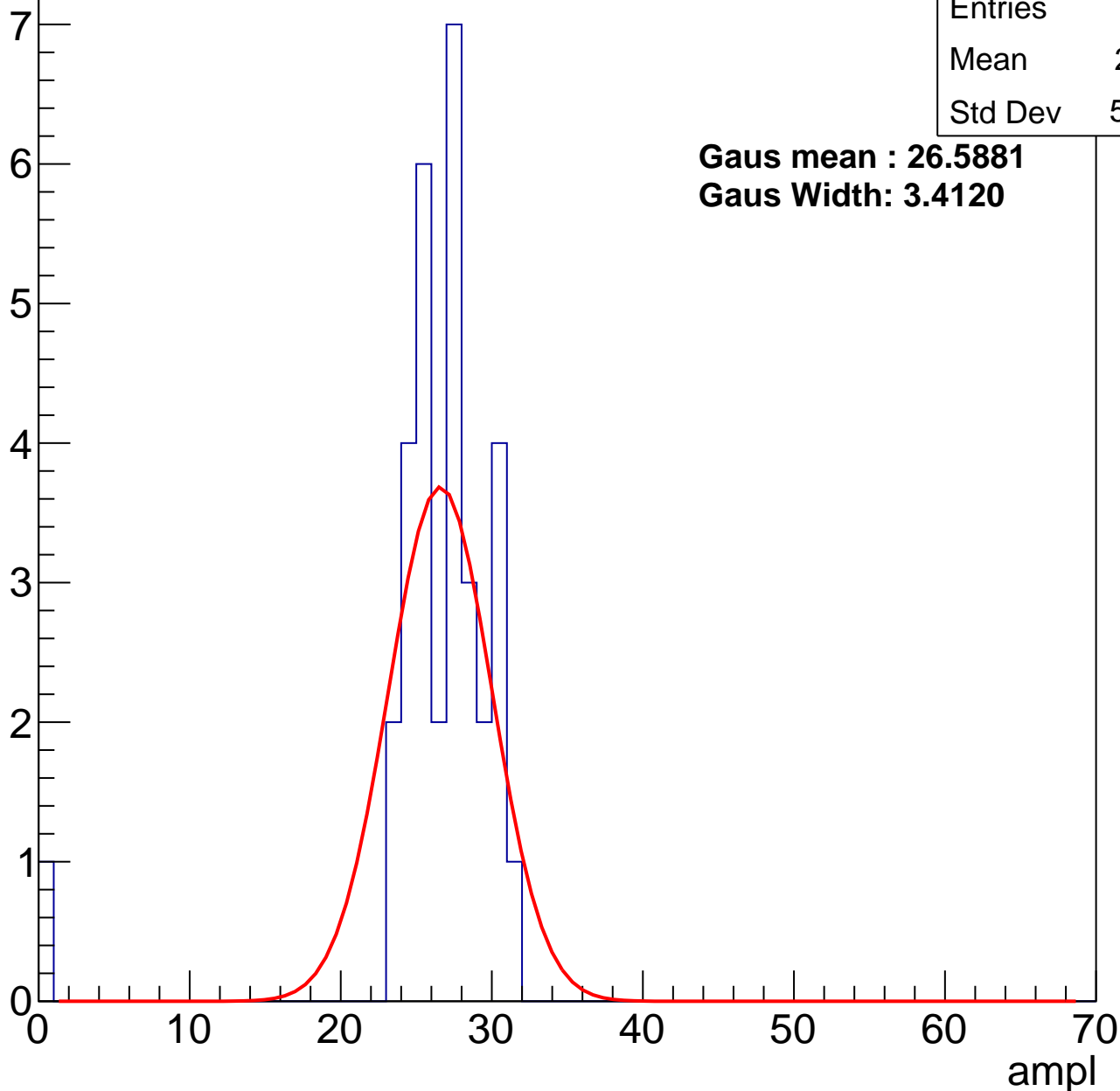
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	25.81
Std Dev	5.126

**Gaus mean : 26.5881**

**Gaus Width: 3.4120**



# B1L002S, U17-ch92, adc1

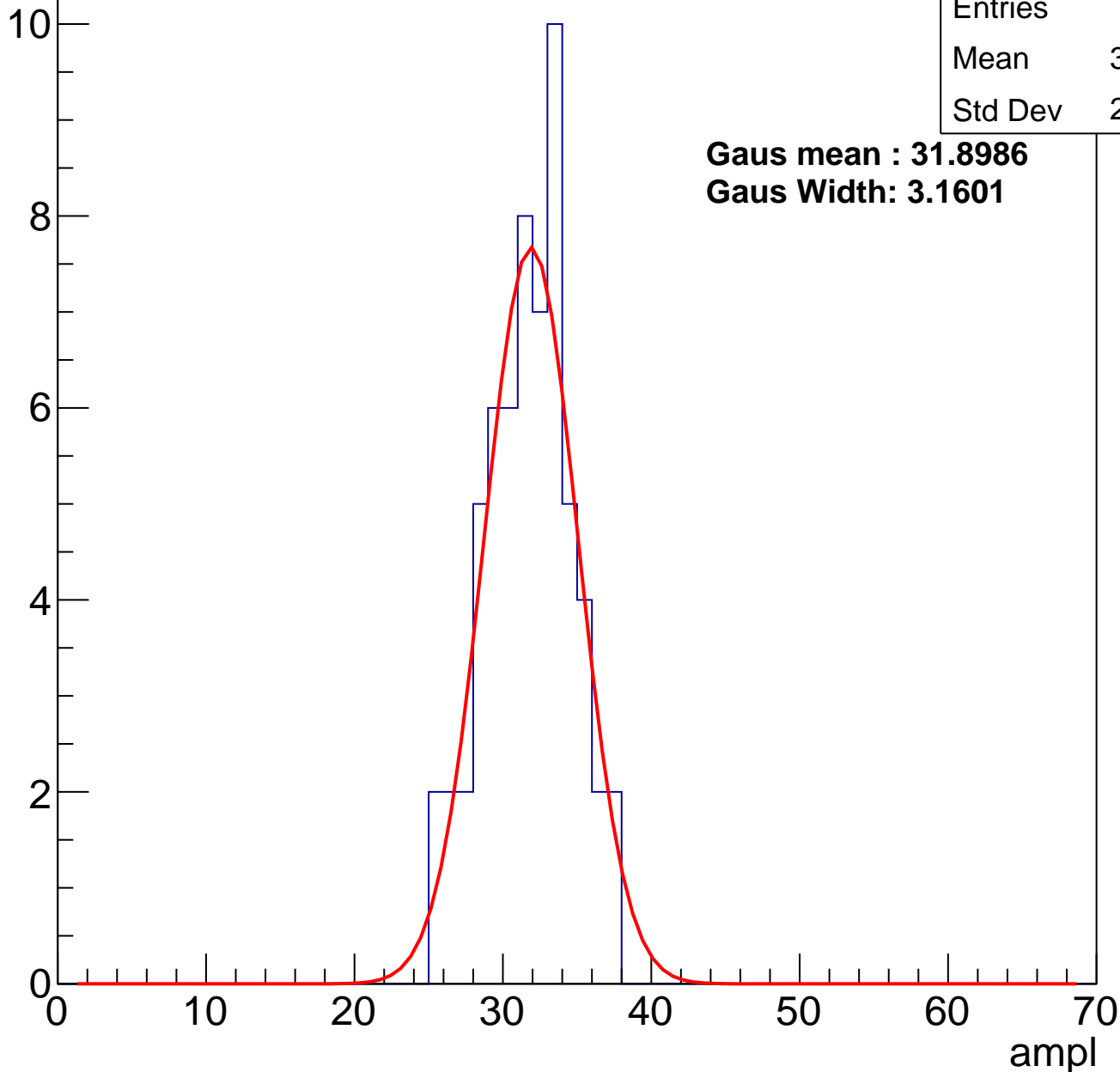
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	31.28
Std Dev	2.869

**Gaus mean : 31.8986**

**Gaus Width: 3.1601**

Entry



# B1L002S, U17-ch92, adc2

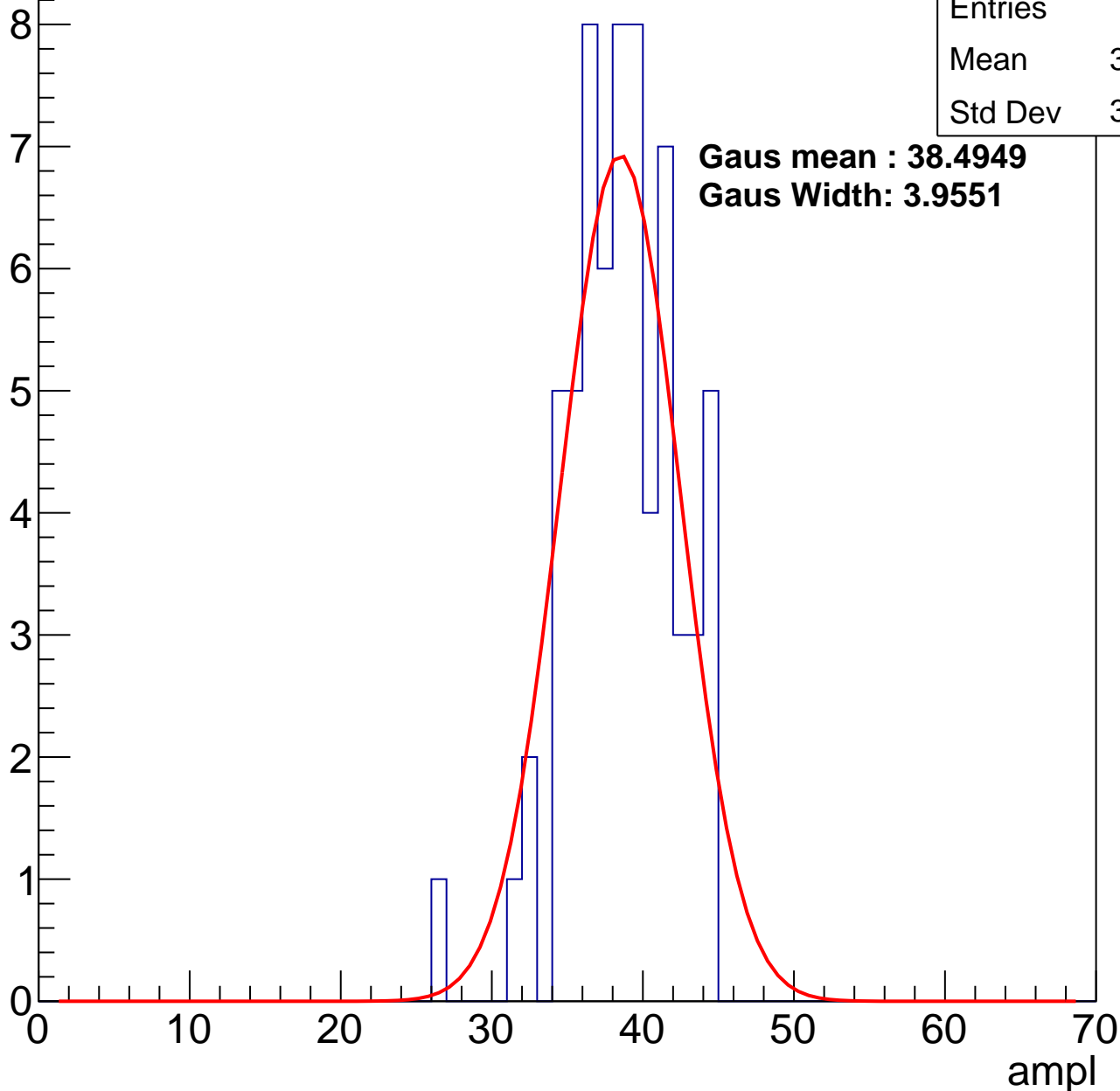
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	38.09
Std Dev	3.519

**Gaus mean : 38.4949**

**Gaus Width: 3.9551**

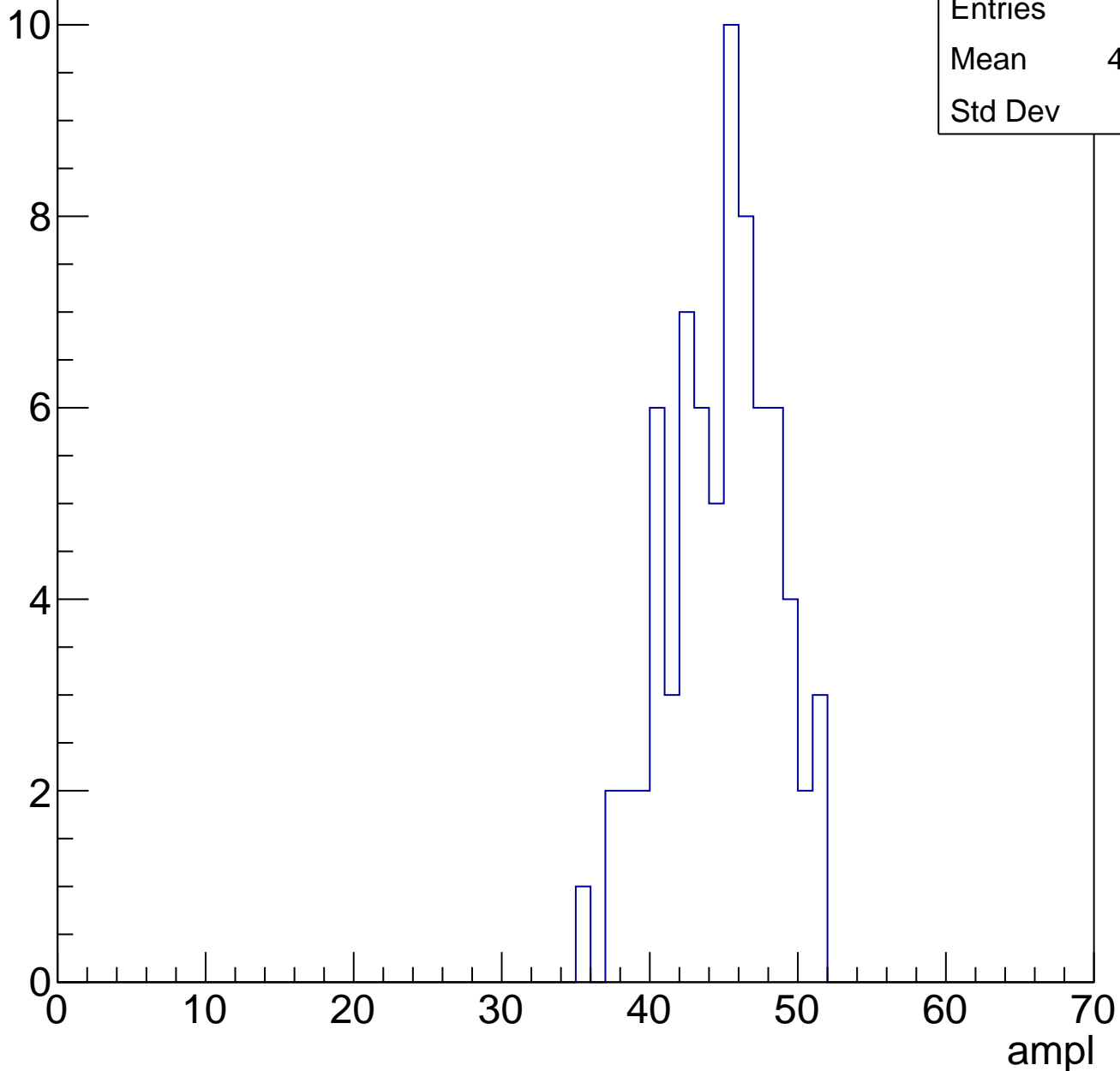


# B1L002S, U17-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	44.32
Std Dev	3.63

Entry

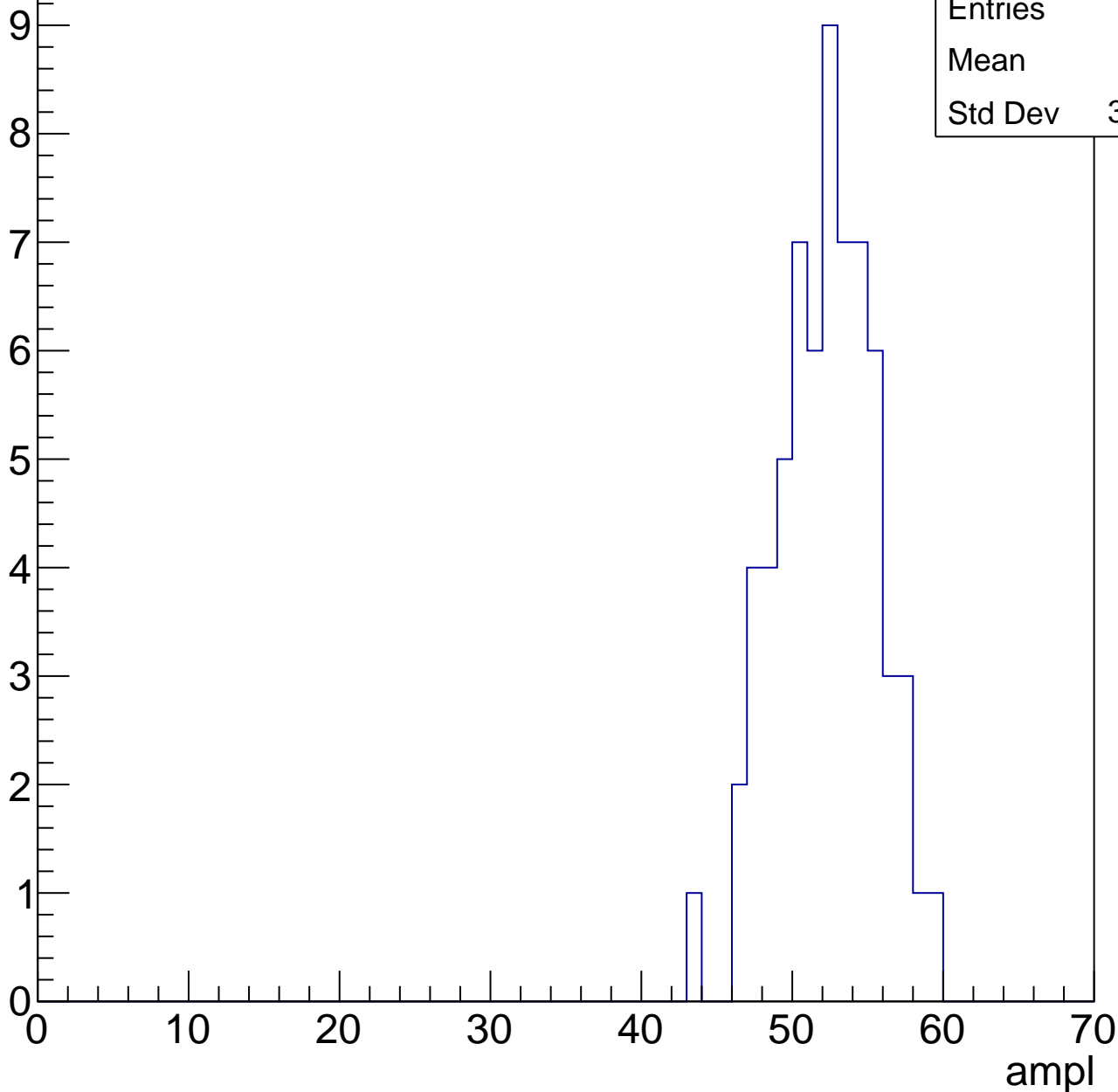


# B1L002S, U17-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

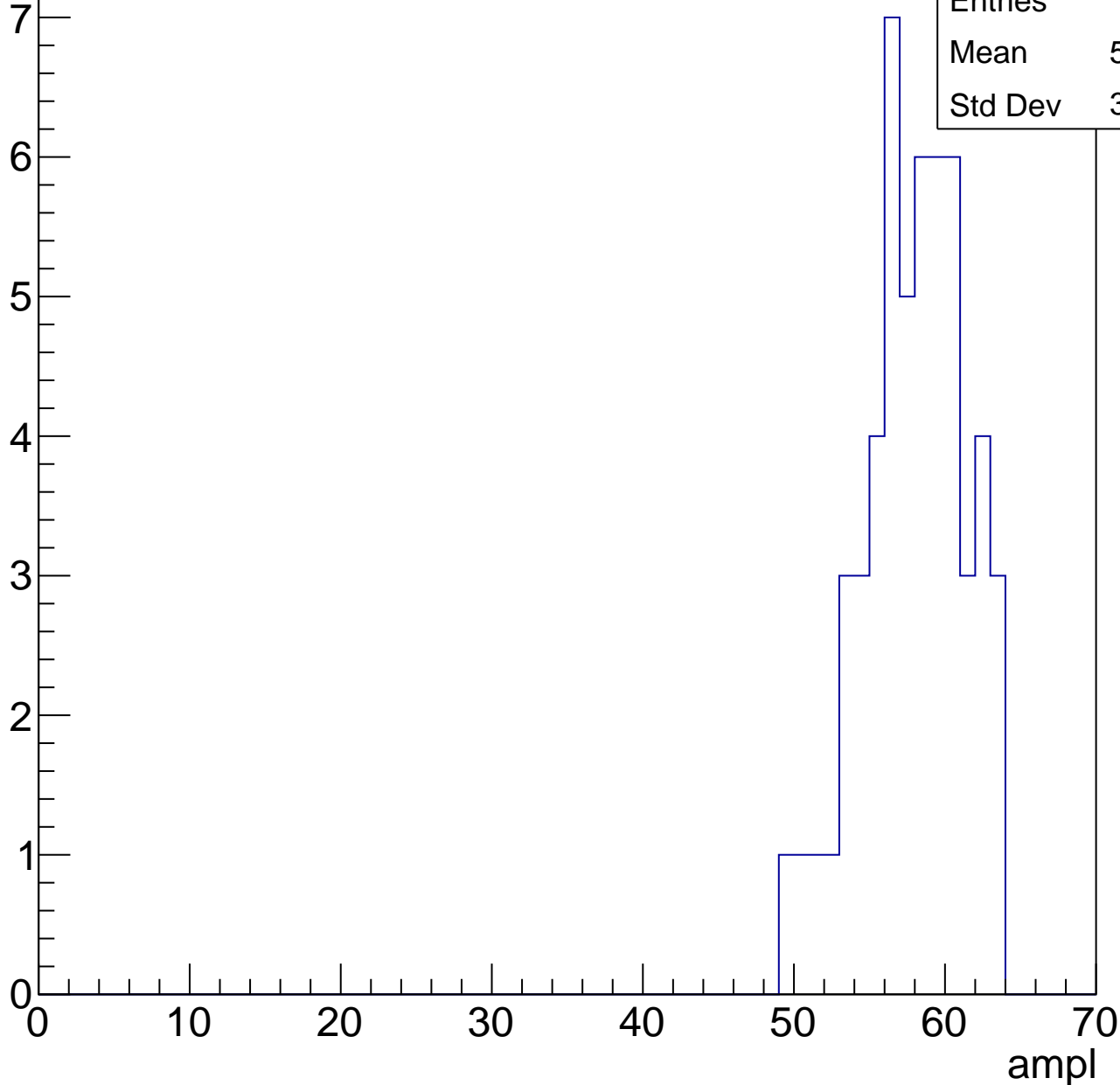
Entries	66
Mean	51.8
Std Dev	3.234



# B1L002S, U17-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



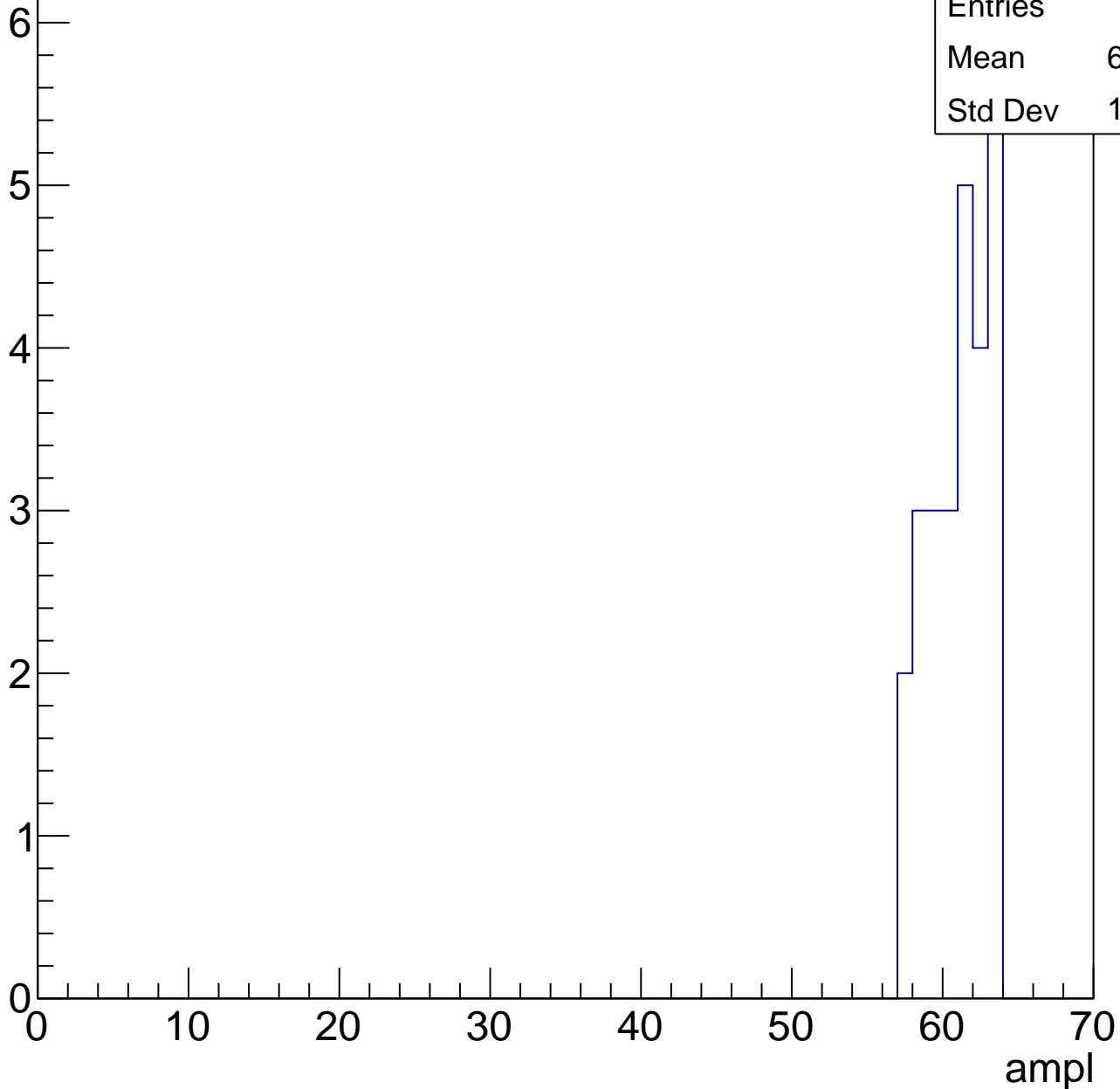
Entries	54
Mean	57.44
Std Dev	3.337

# B1L002S, U17-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	60.62
Std Dev	1.943

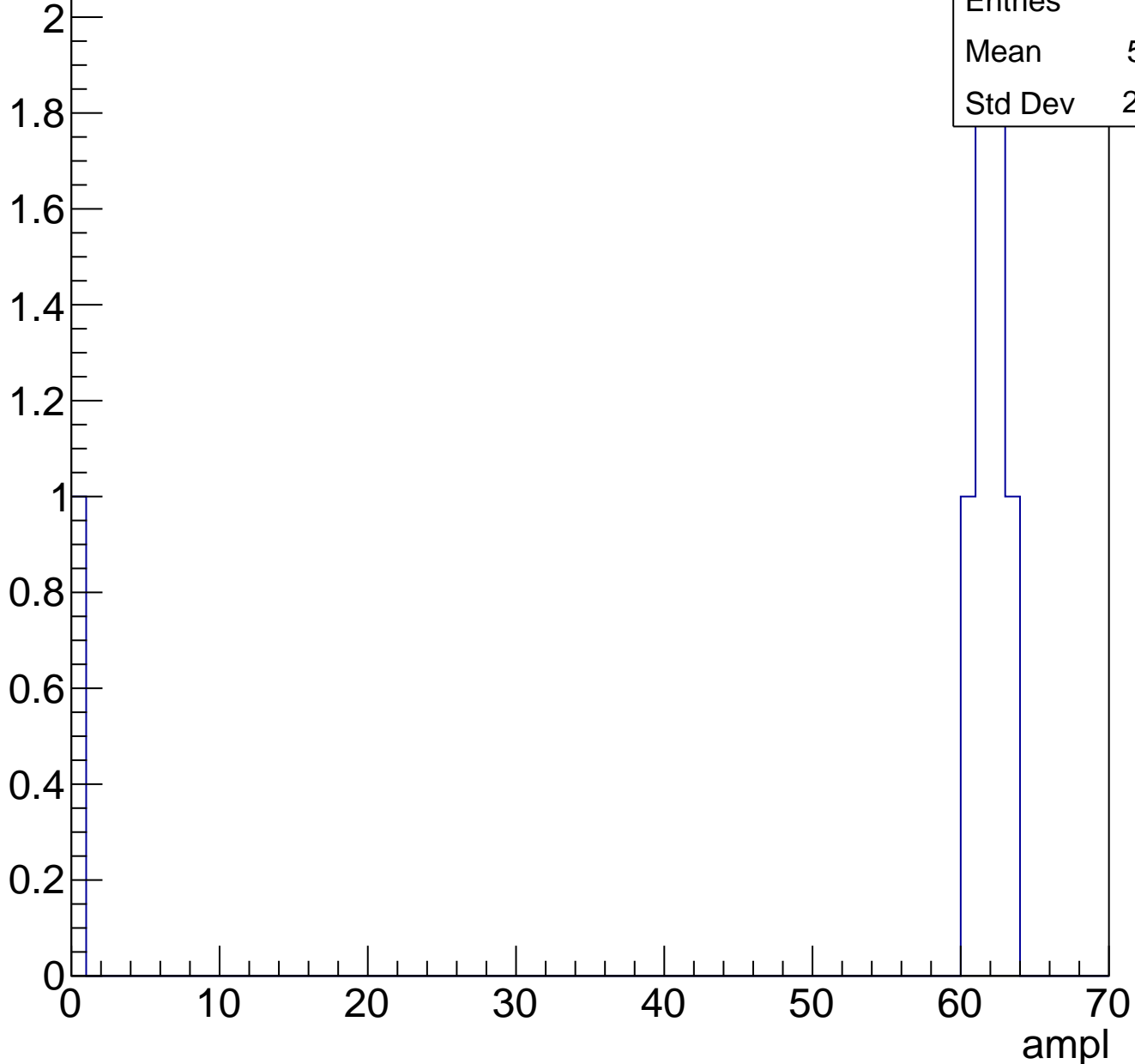




# B1L002S, U17-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	7
Mean	52.71
Std Dev	21.54

# B1L002S, U17-ch93, adc0

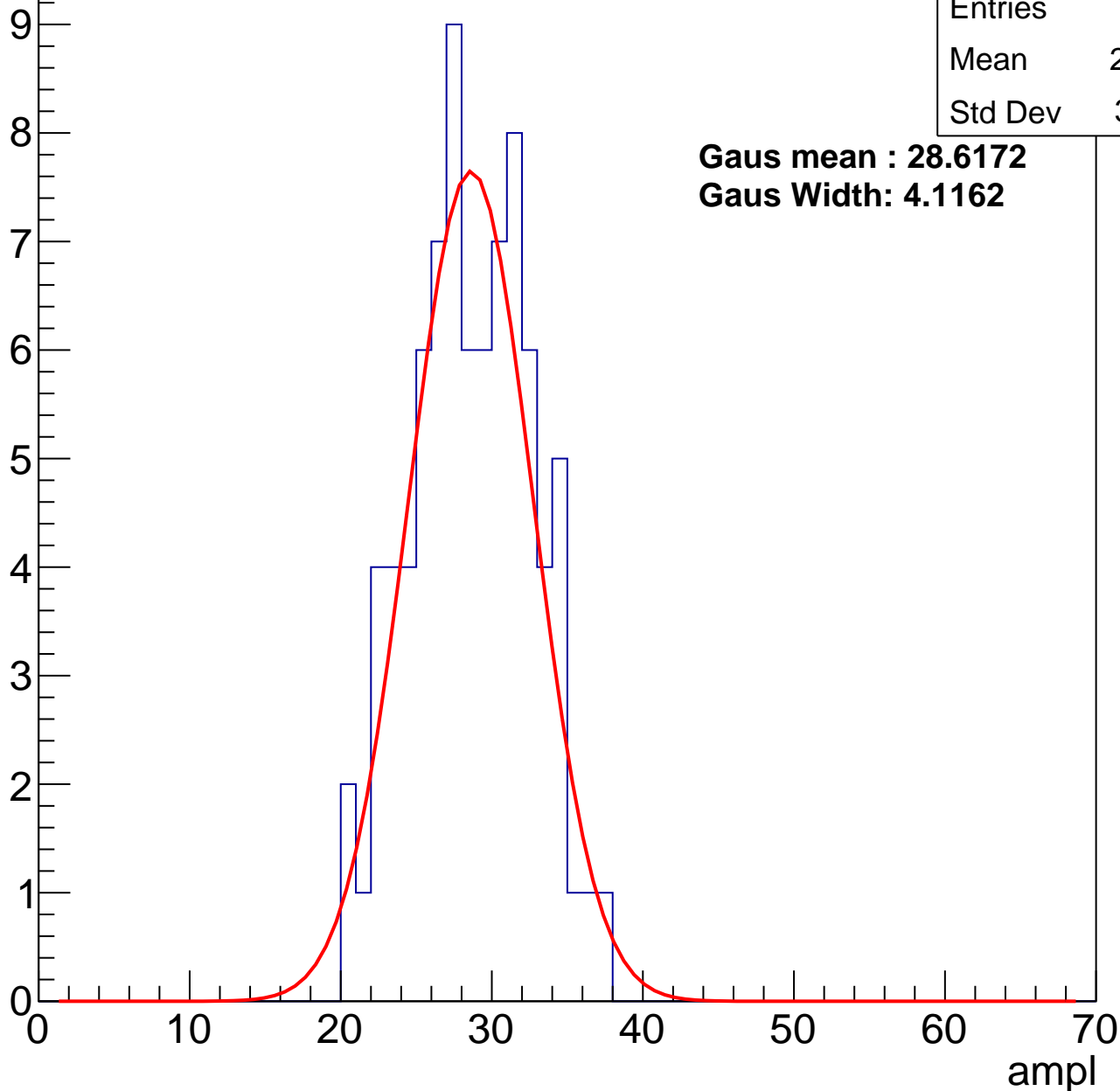
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	28.22
Std Dev	3.901

**Gaus mean : 28.6172**

**Gaus Width: 4.1162**



# B1L002S, U17-ch93, adc1

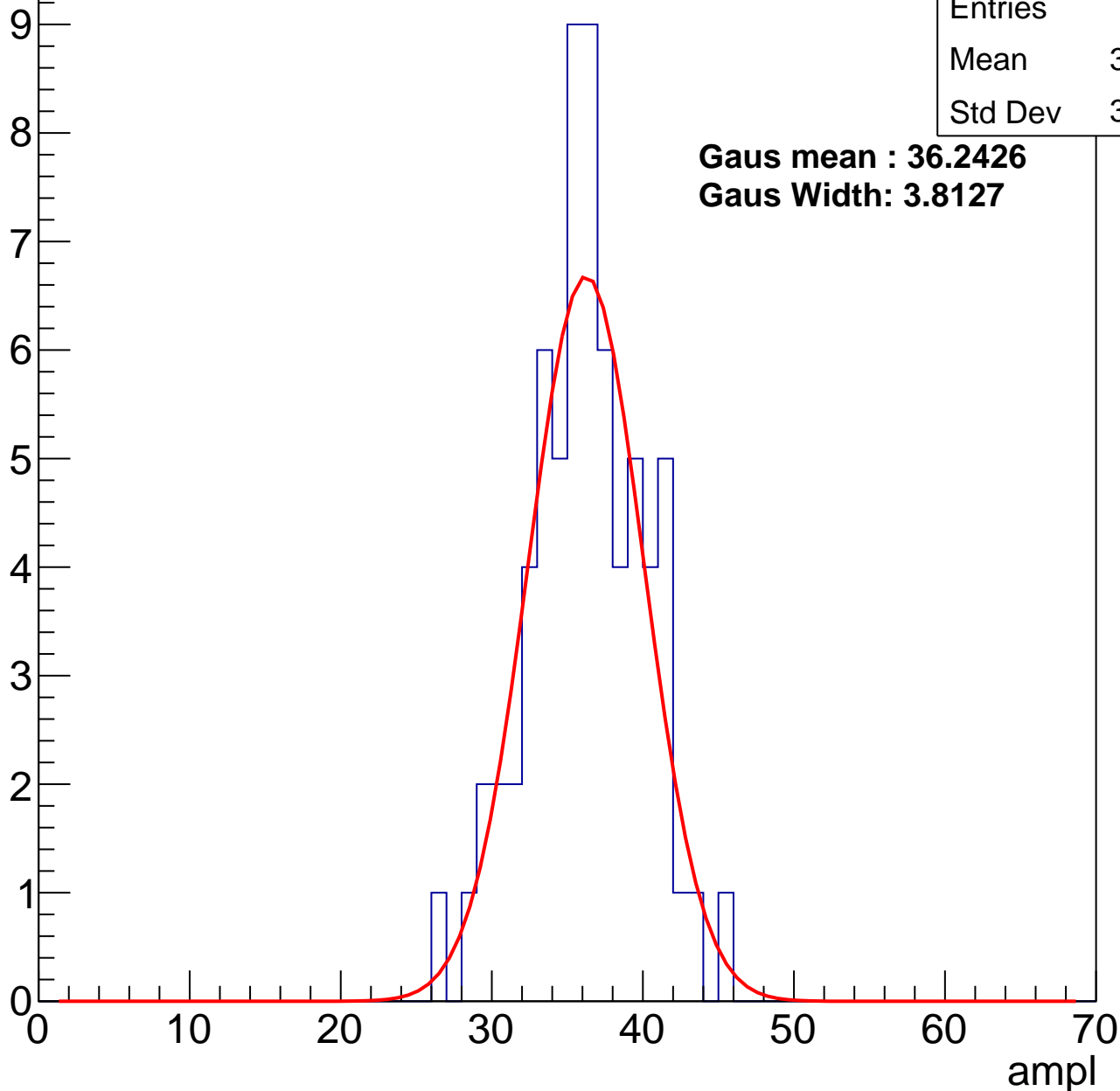
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.78
Std Dev	3.737

**Gaus mean : 36.2426**

**Gaus Width: 3.8127**



# B1L002S, U17-ch93, adc2

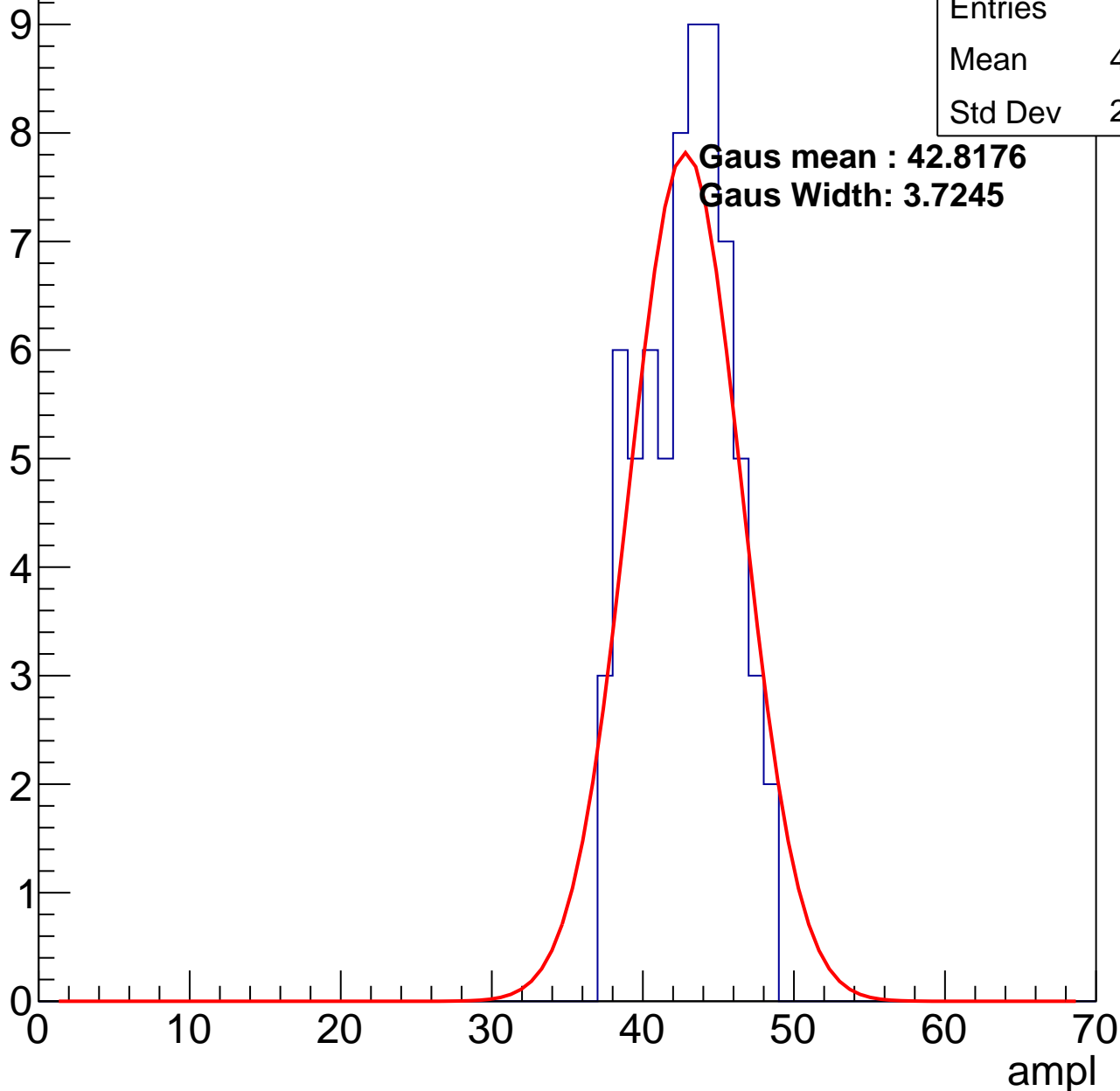
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	42.35
Std Dev	2.899

**Gaus mean : 42.8176**

**Gaus Width: 3.7245**



# B1L002S, U17-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	50
Mean	47.74
Std Dev	7.31

Entry

10

8

6

4

2

0

0

10

20

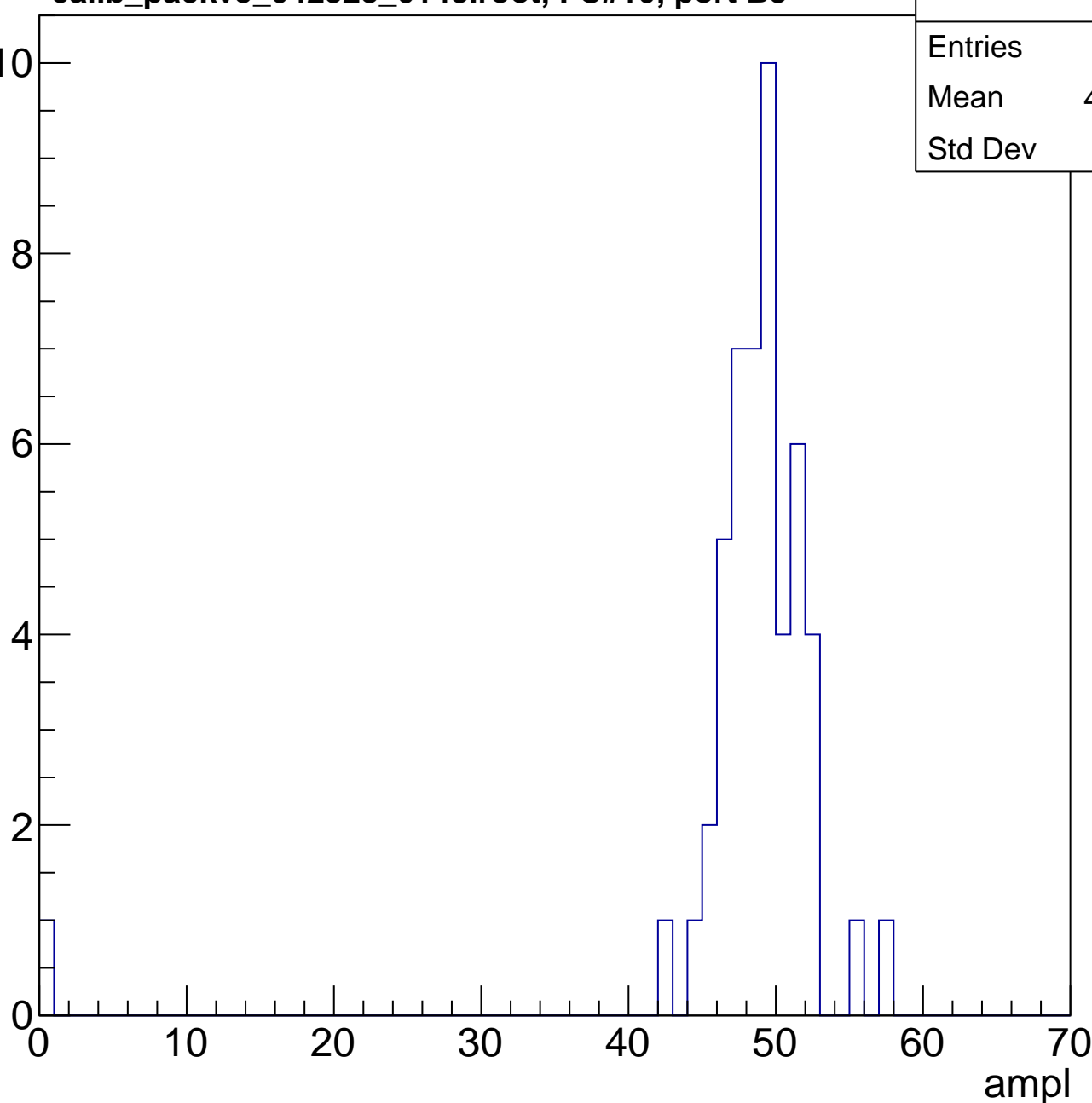
30

40

50

60

ampl

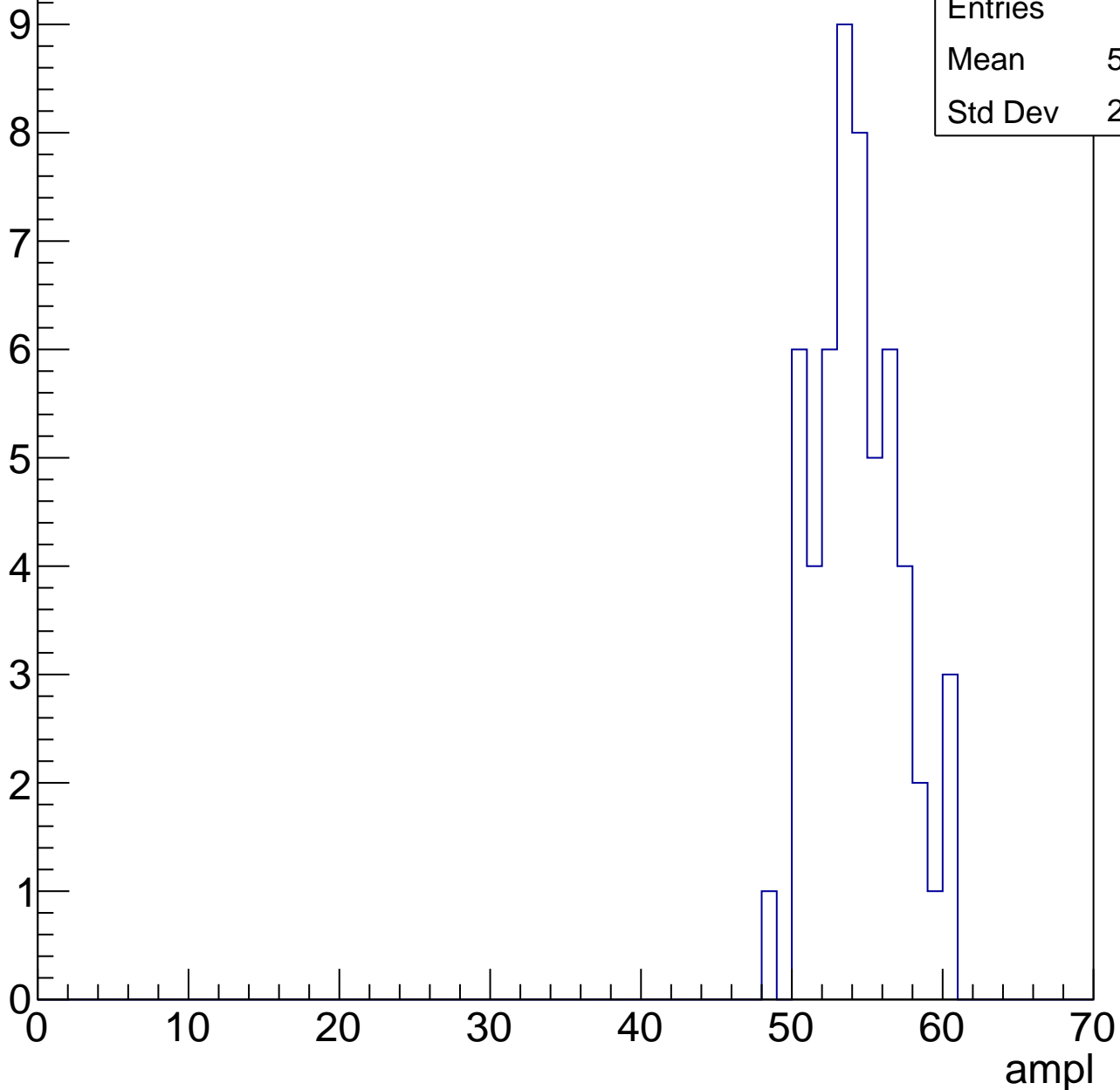


# B1L002S, U17-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.95
Std Dev	2.799

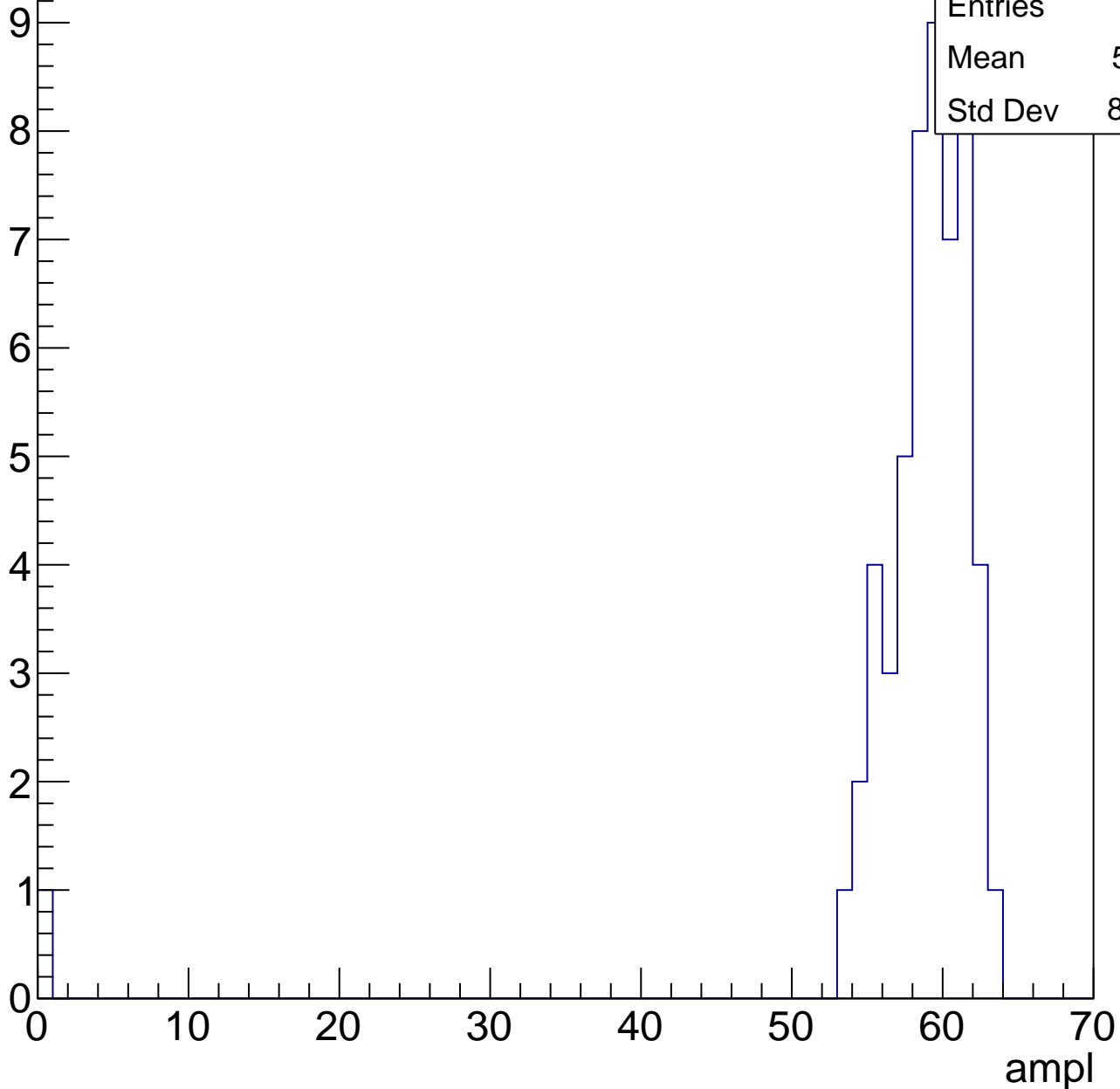


# B1L002S, U17-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.51
Std Dev	8.309

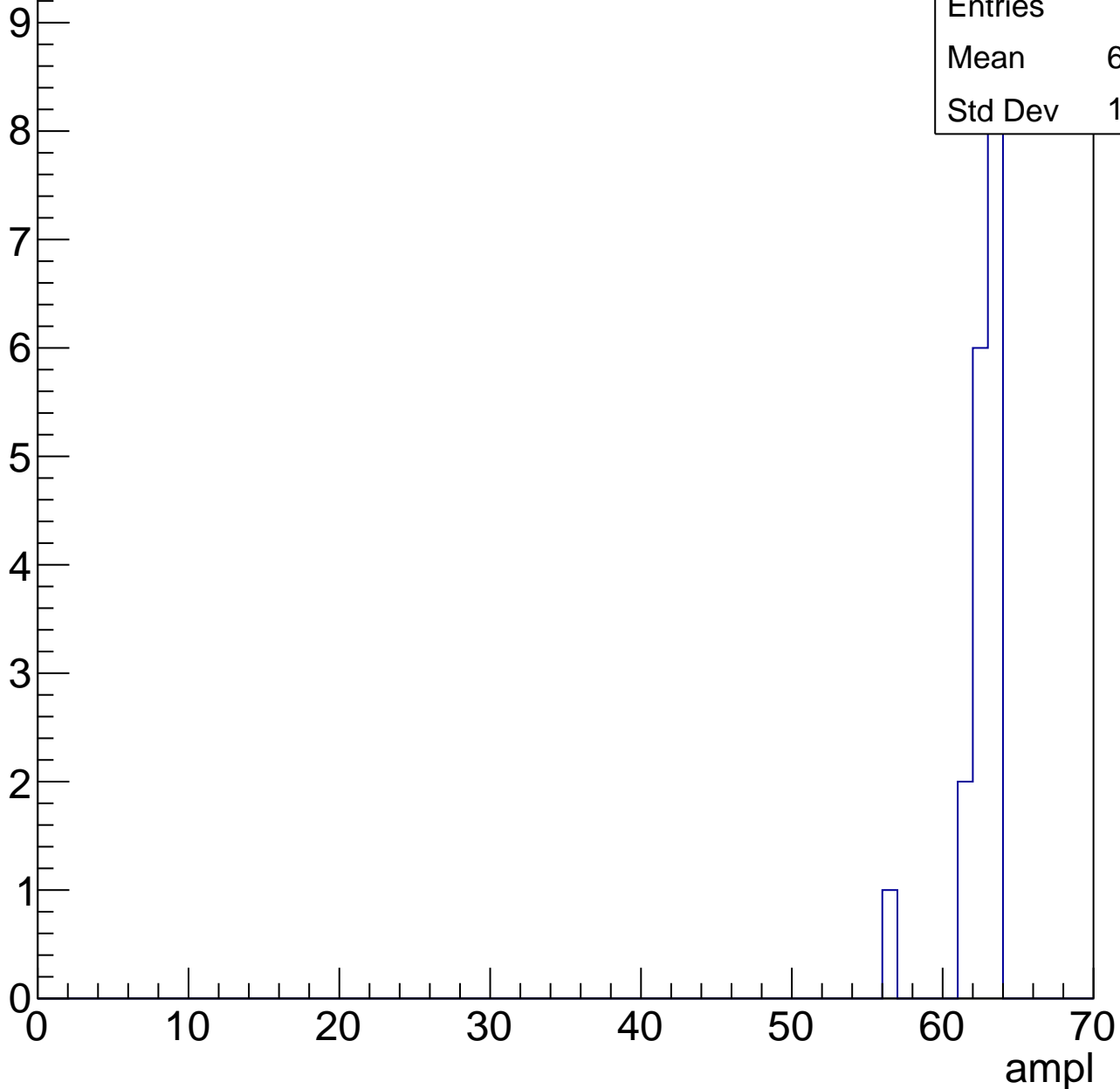


# B1L002S, U17-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	62.06
Std Dev	1.615





# B1L002S, U17-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch94, adc0

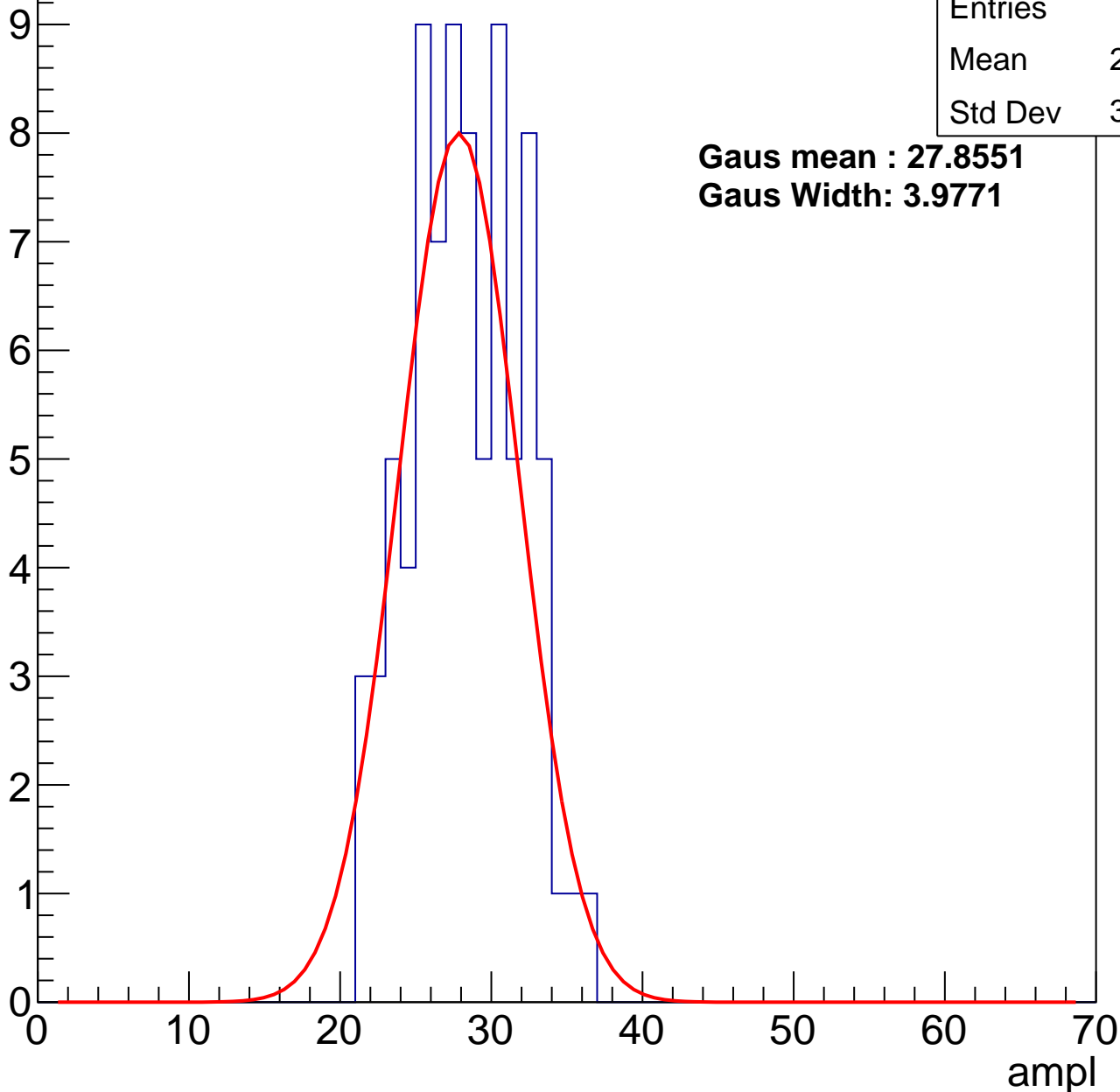
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	27.83
Std Dev	3.553

**Gaus mean : 27.8551**

**Gaus Width: 3.9771**



# B1L002S, U17-ch94, adc1

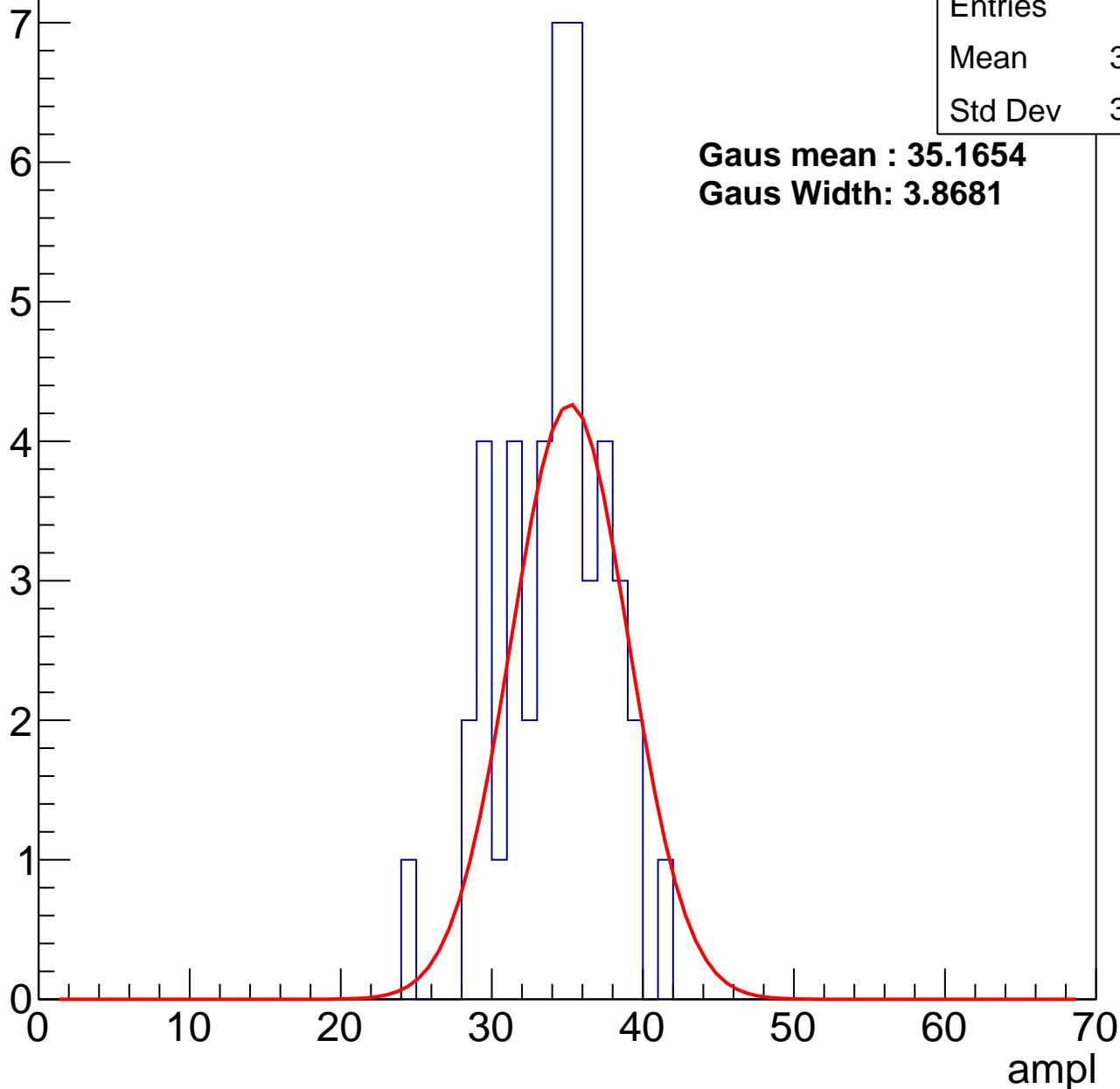
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	33.73
Std Dev	3.447

**Gaus mean : 35.1654**

**Gaus Width: 3.8681**



# B1L002S, U17-ch94, adc2

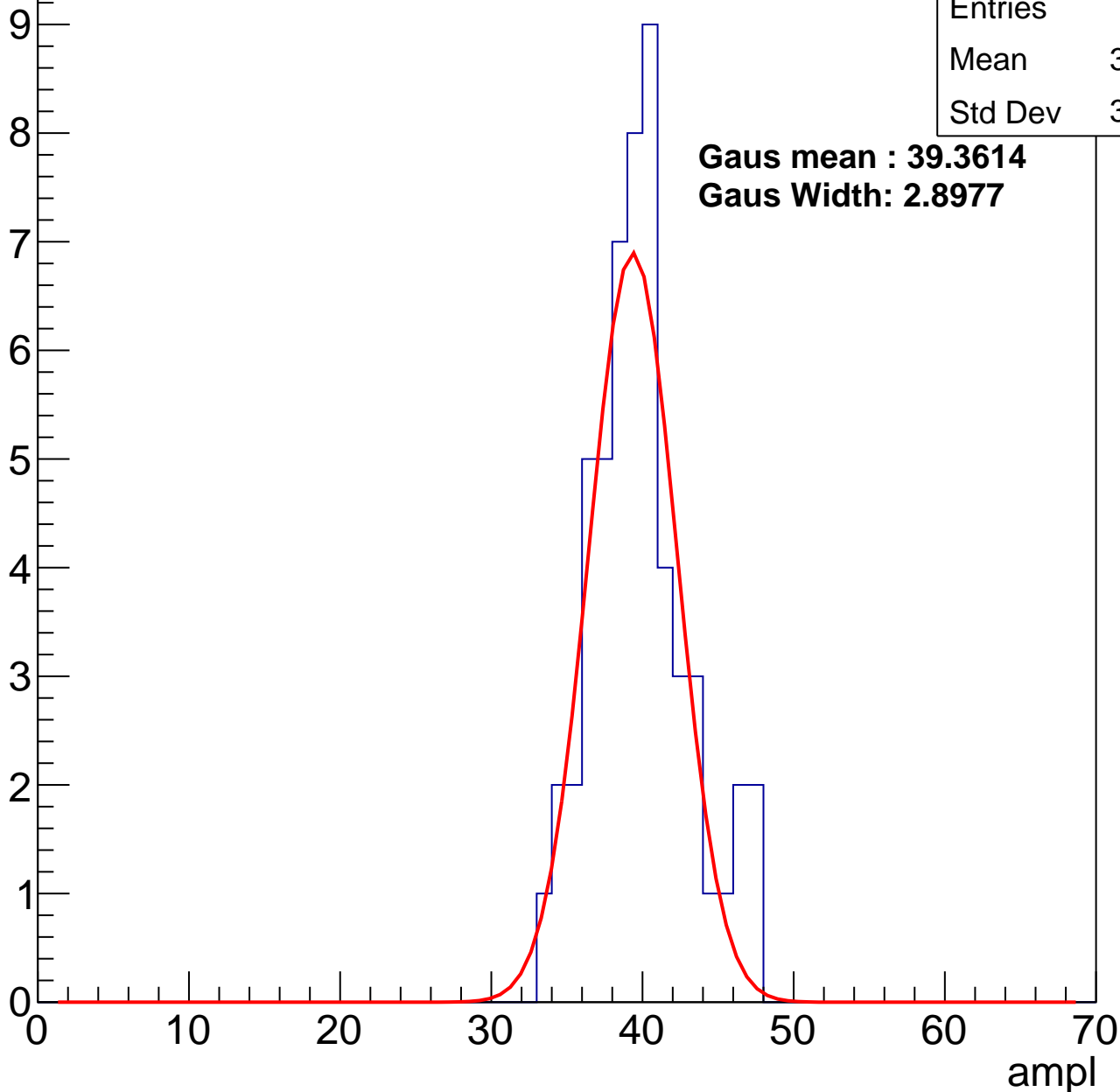
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	39.42
Std Dev	3.212

**Gaus mean : 39.3614**

**Gaus Width: 2.8977**

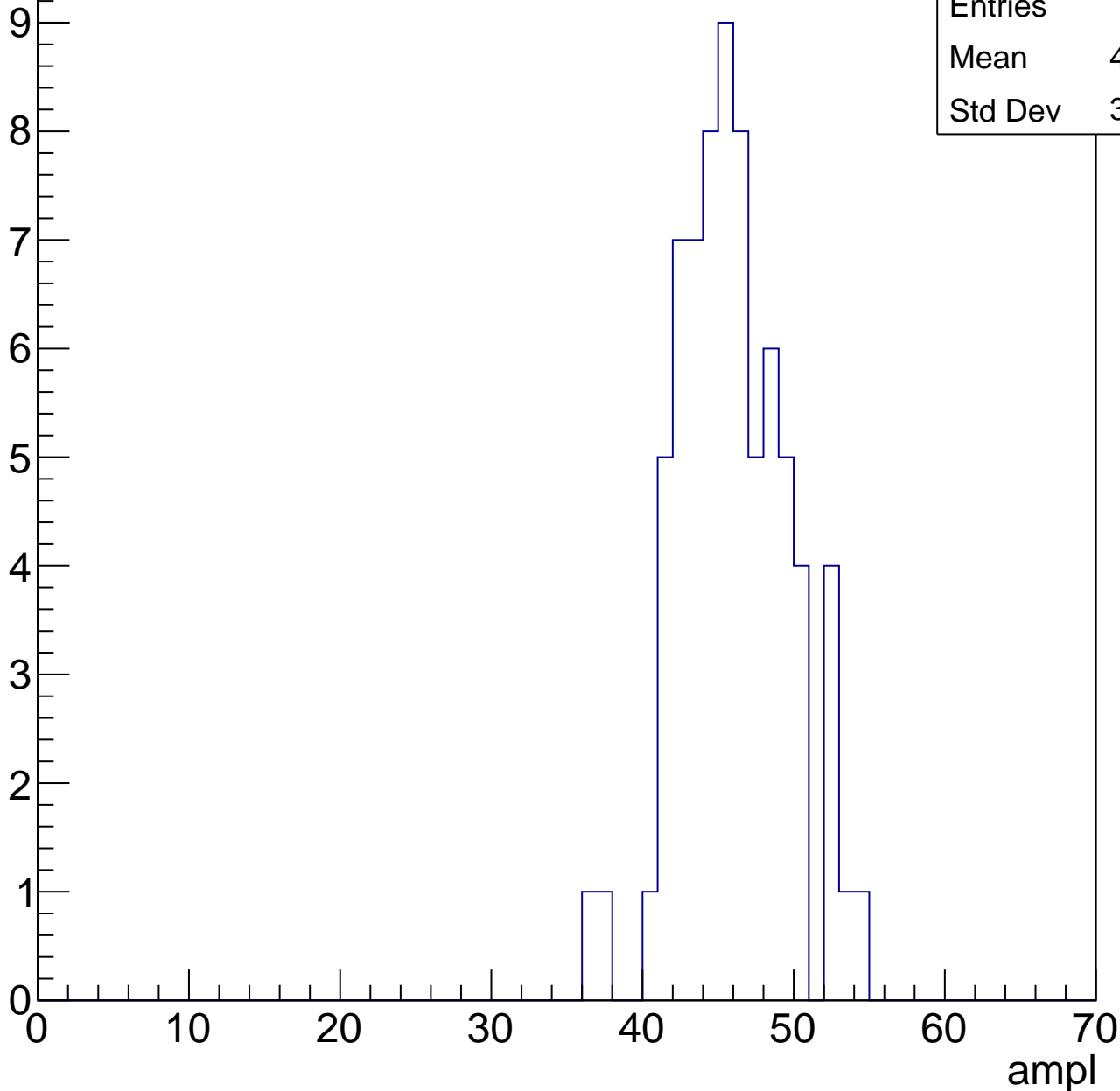


# B1L002S, U17-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	45.49
Std Dev	3.577

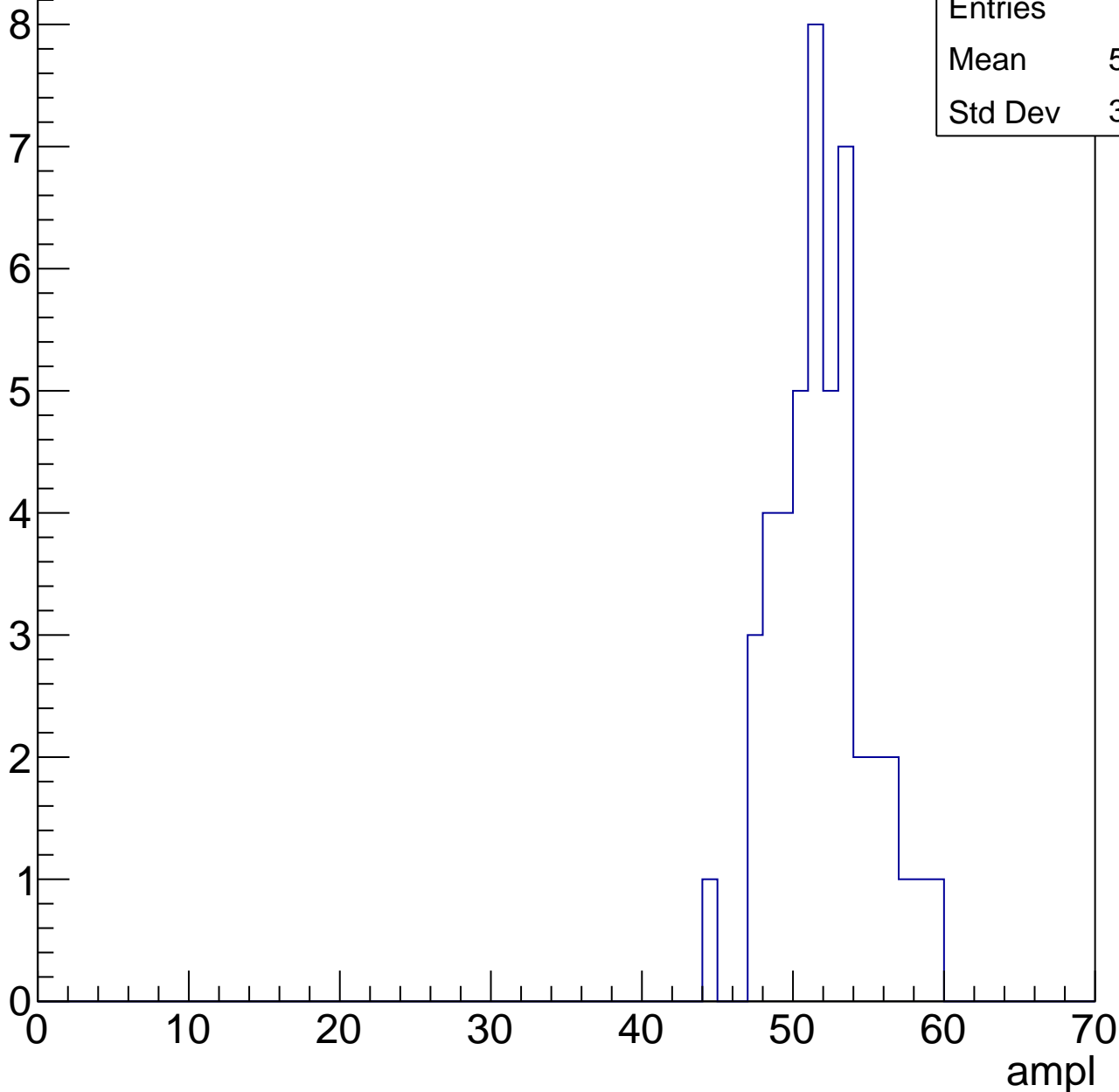


# B1L002S, U17-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	51.43
Std Dev	3.048

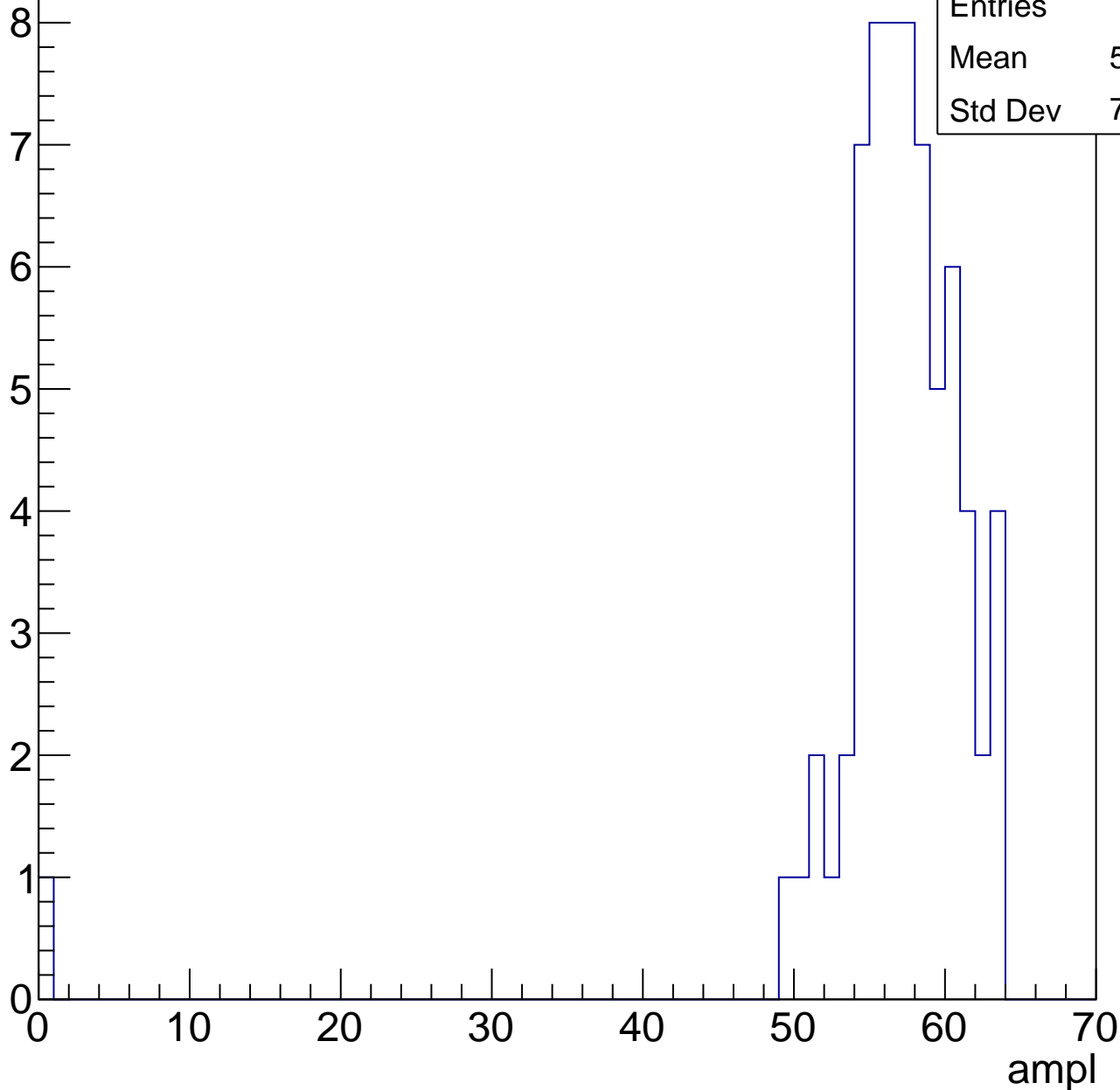


# B1L002S, U17-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	56.15
Std Dev	7.614

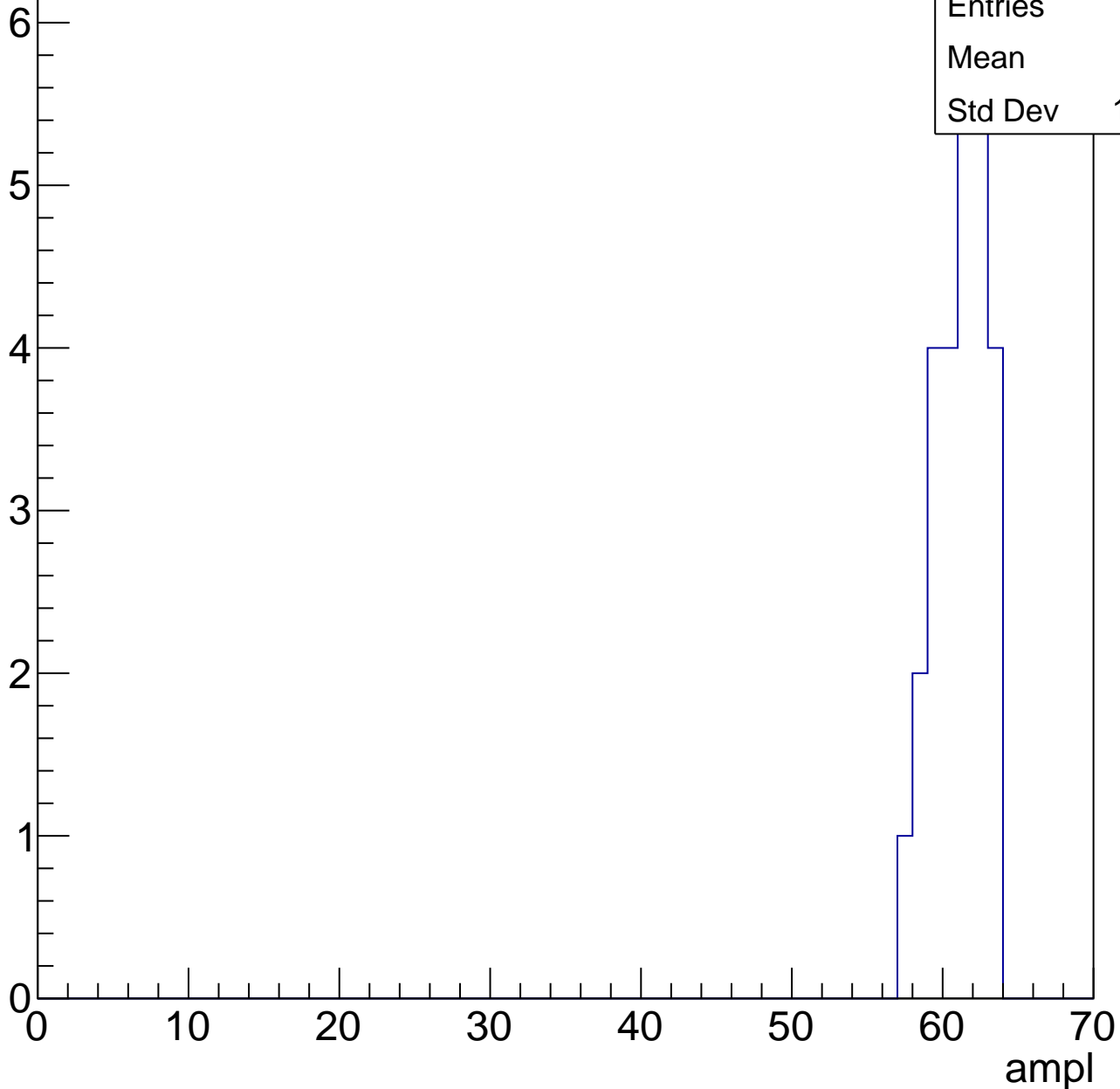


# B1L002S, U17-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.7
Std Dev	1.651

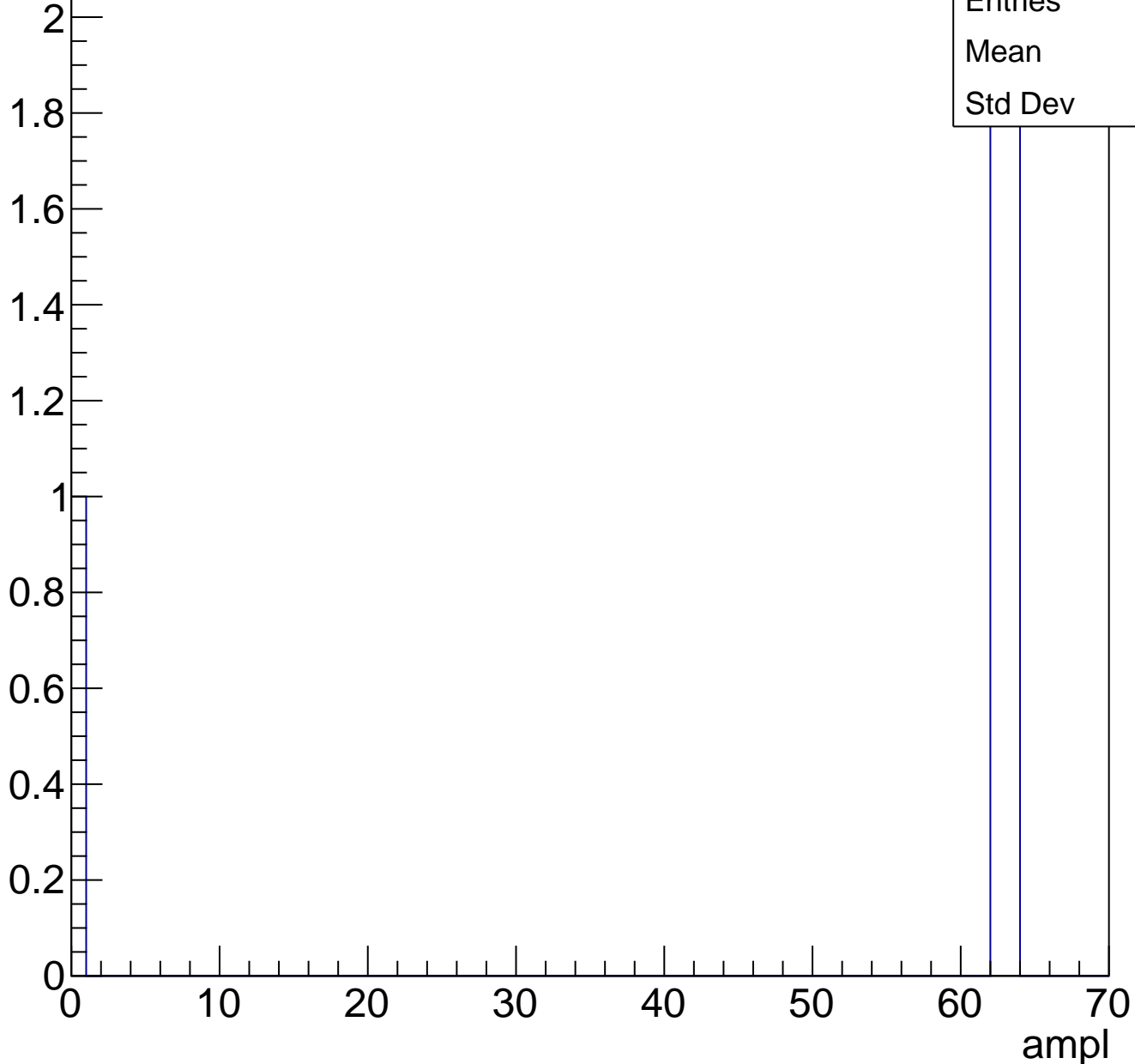




# B1L002S, U17-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	50
Std Dev	25

# B1L002S, U17-ch95, adc0

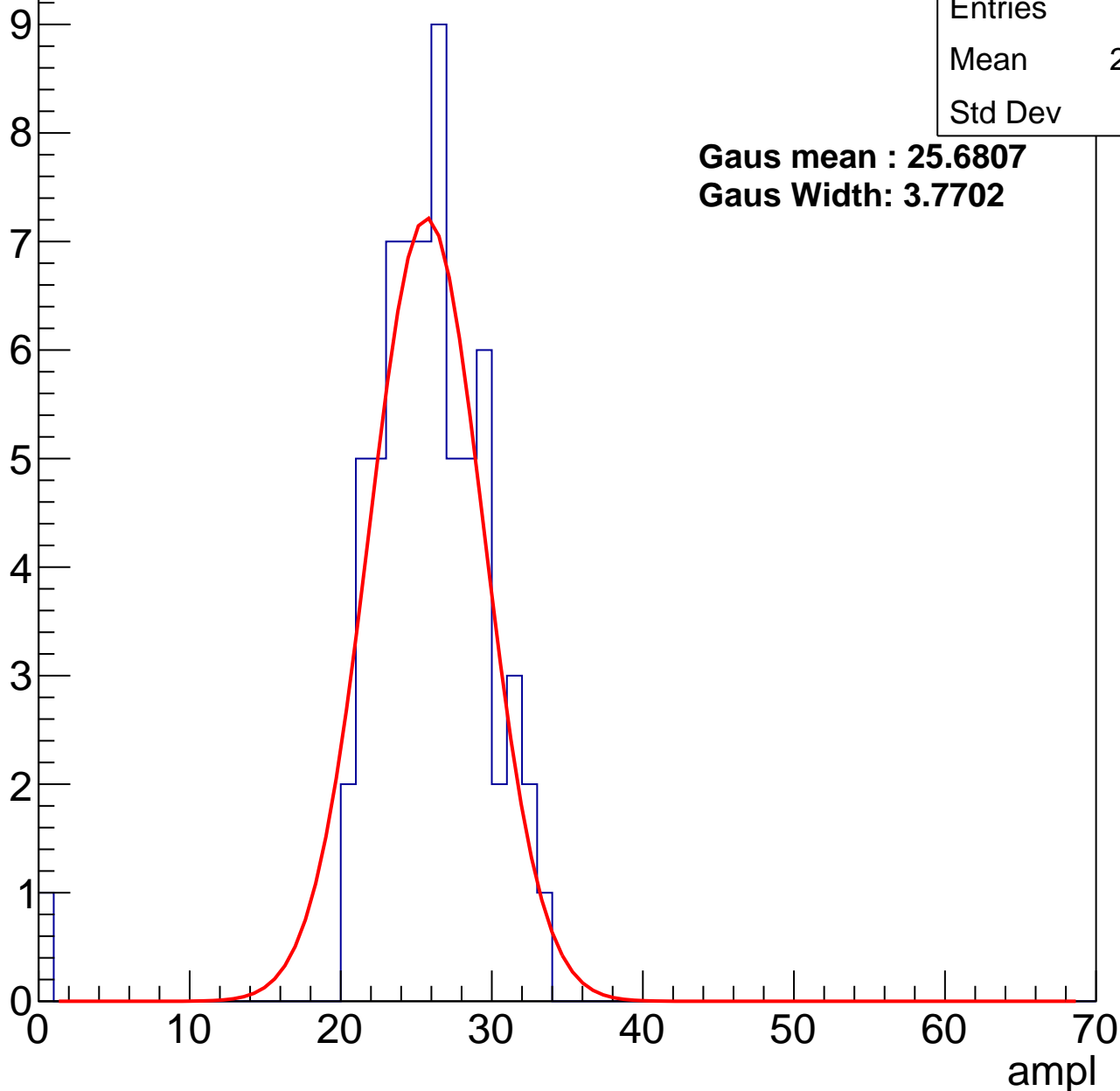
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	25.25
Std Dev	4.44

**Gaus mean : 25.6807**

**Gaus Width: 3.7702**



# B1L002S, U17-ch95, adc1

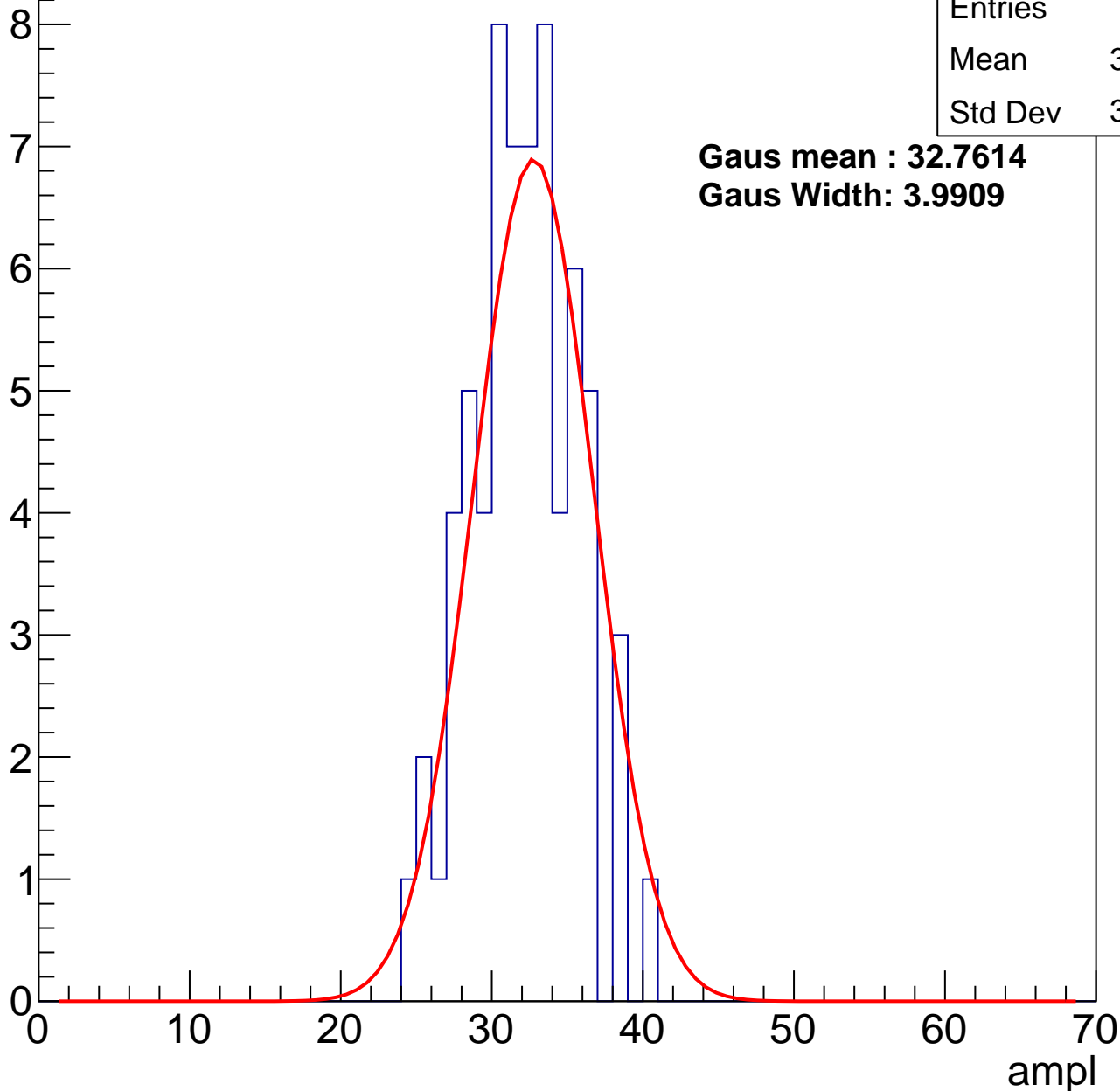
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	31.65
Std Dev	3.427

**Gaus mean : 32.7614**

**Gaus Width: 3.9909**



# B1L002S, U17-ch95, adc2

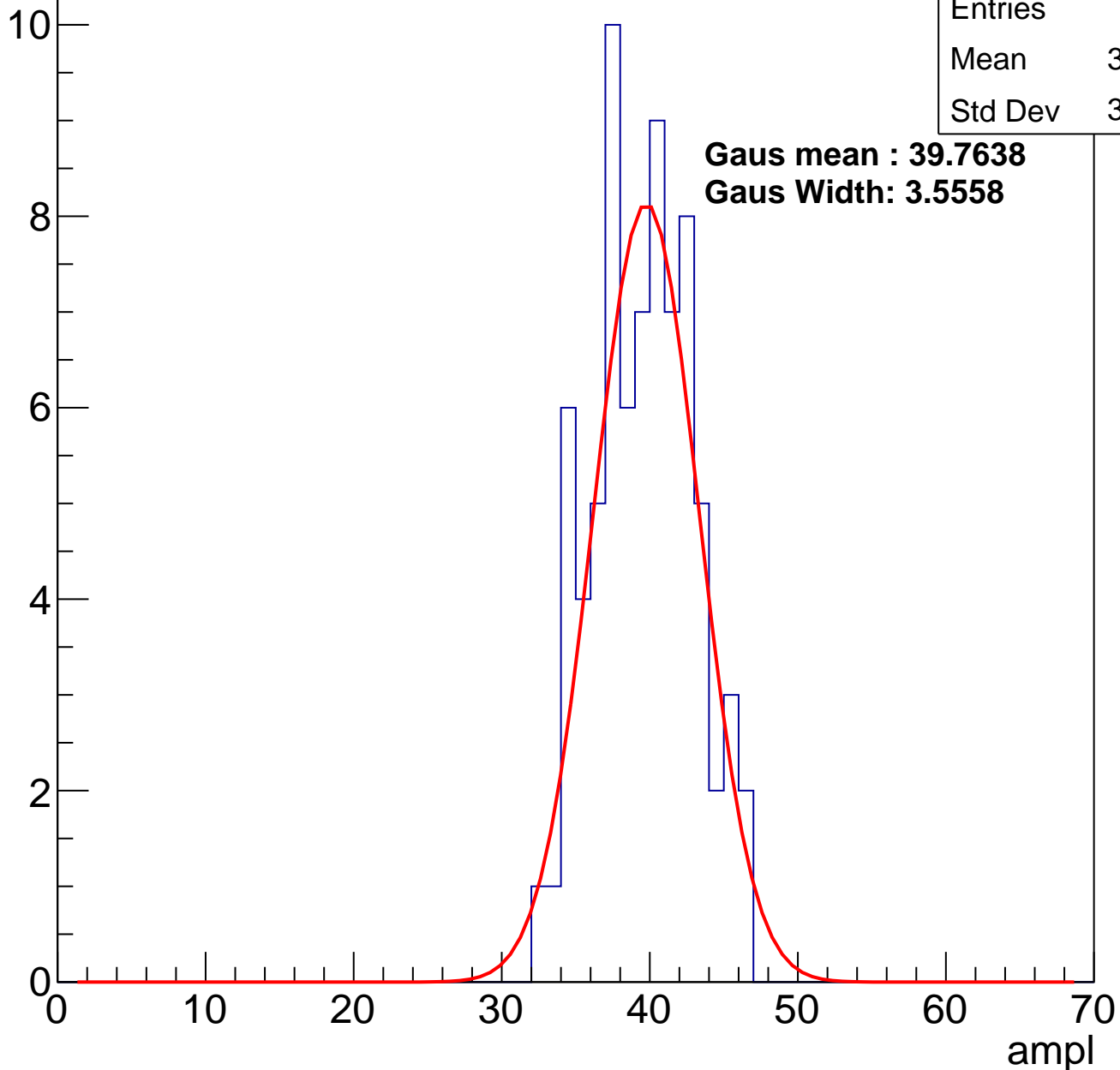
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	39.12
Std Dev	3.313

**Gaus mean : 39.7638**

**Gaus Width: 3.5558**

Entry

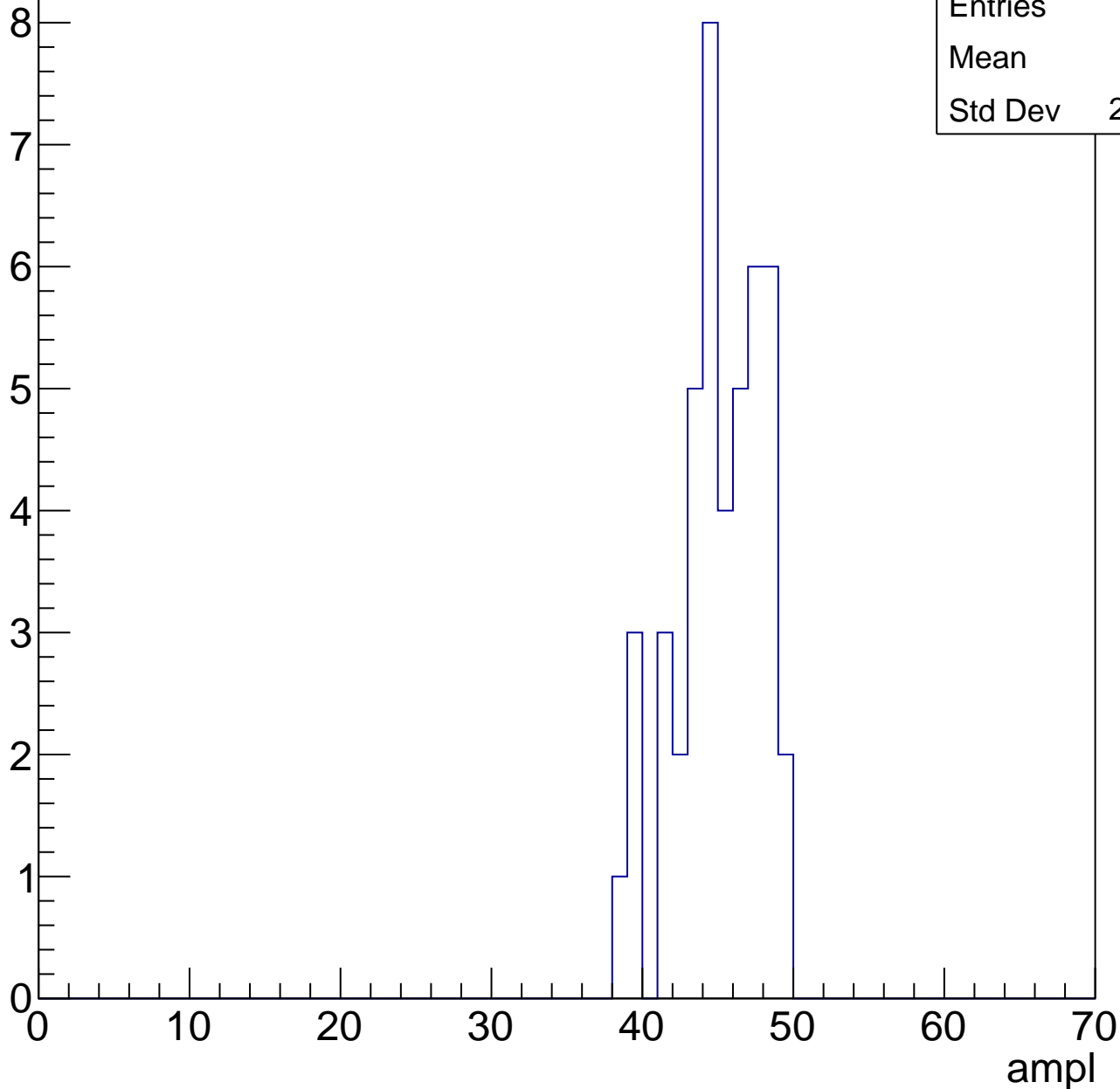


# B1L002S, U17-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

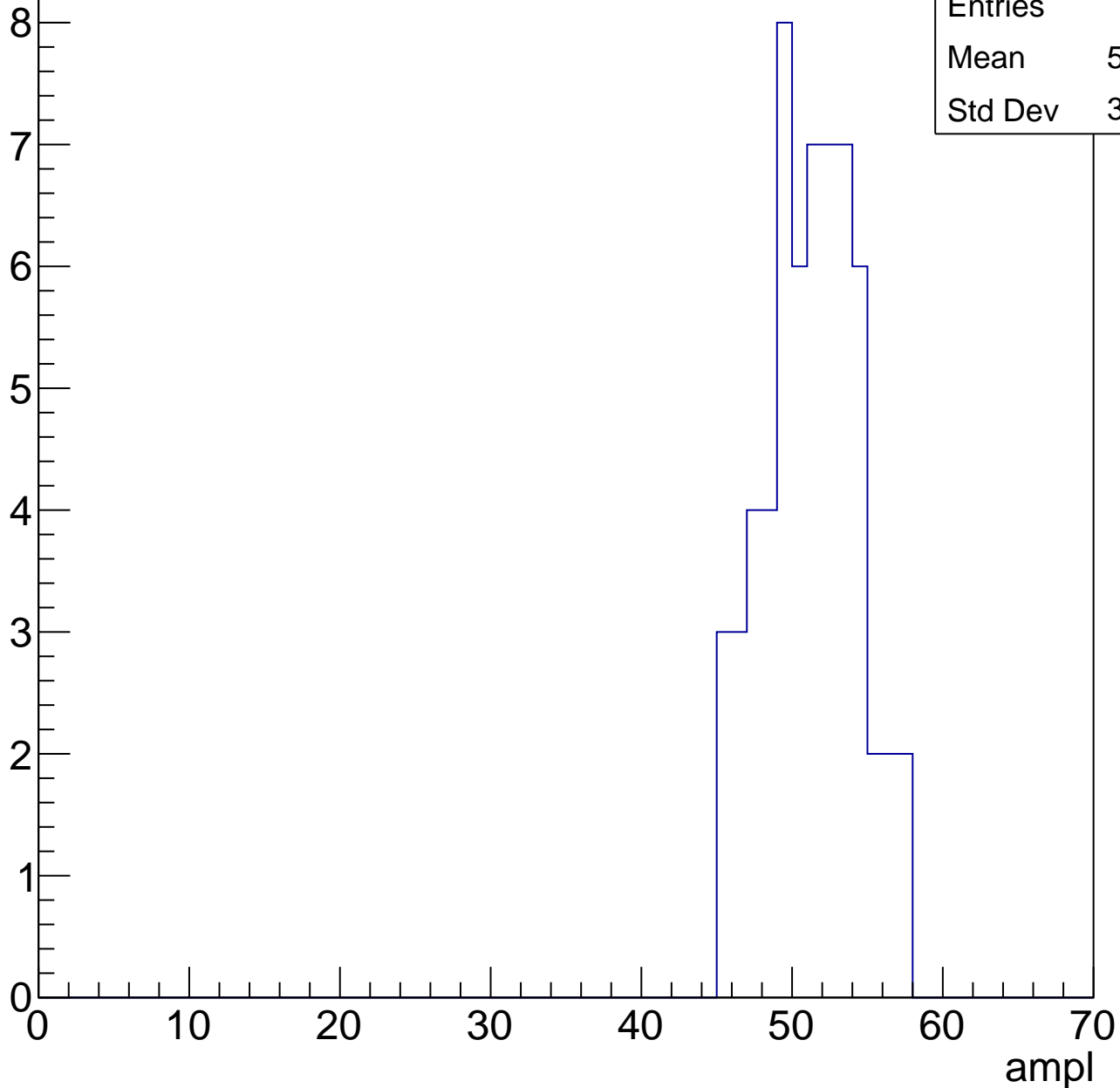
Entries	45
Mean	44.6
Std Dev	2.824



# B1L002S, U17-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



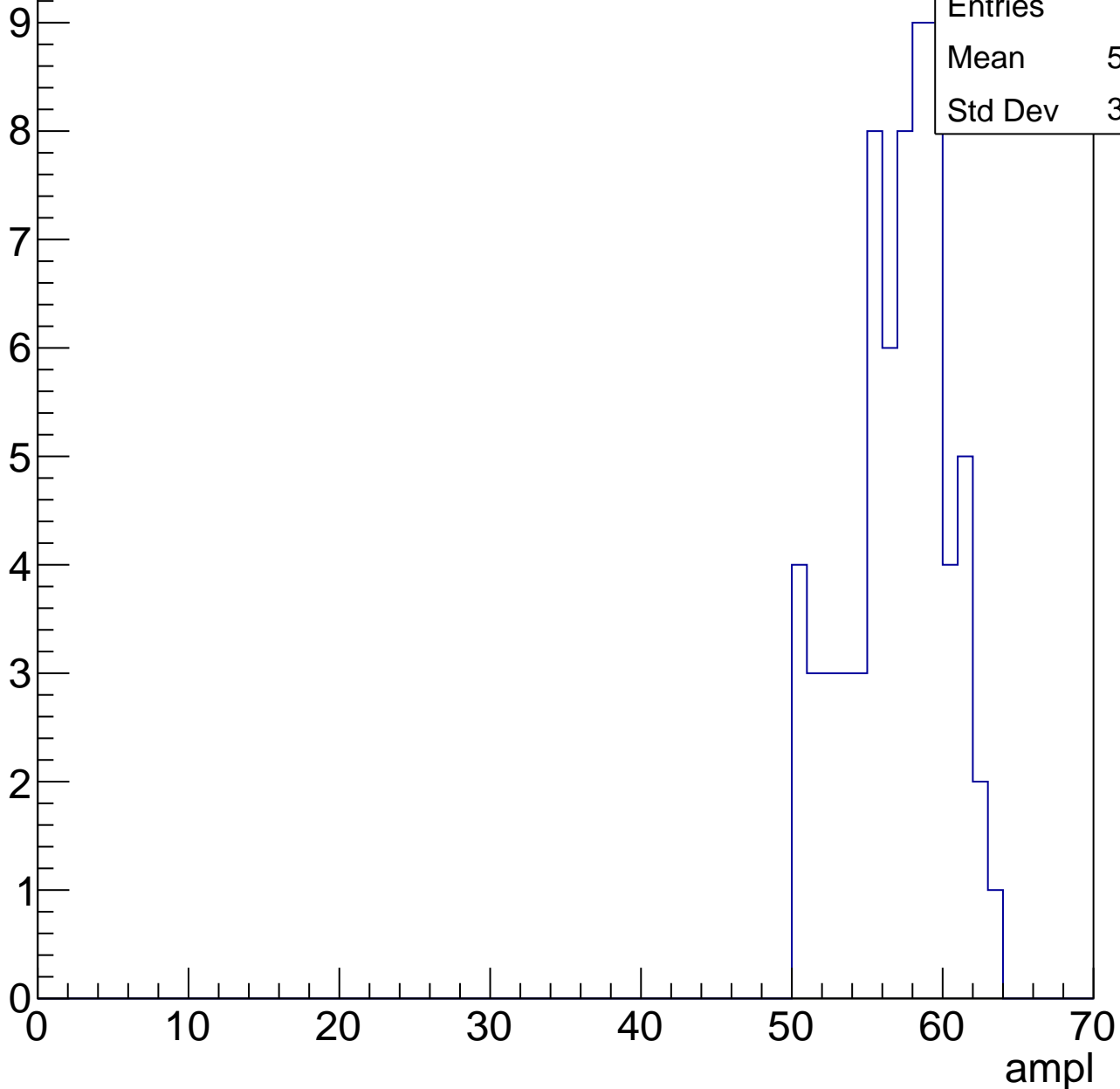
Entries	61
Mean	50.77
Std Dev	3.032

# B1L002S, U17-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	56.57
Std Dev	3.269

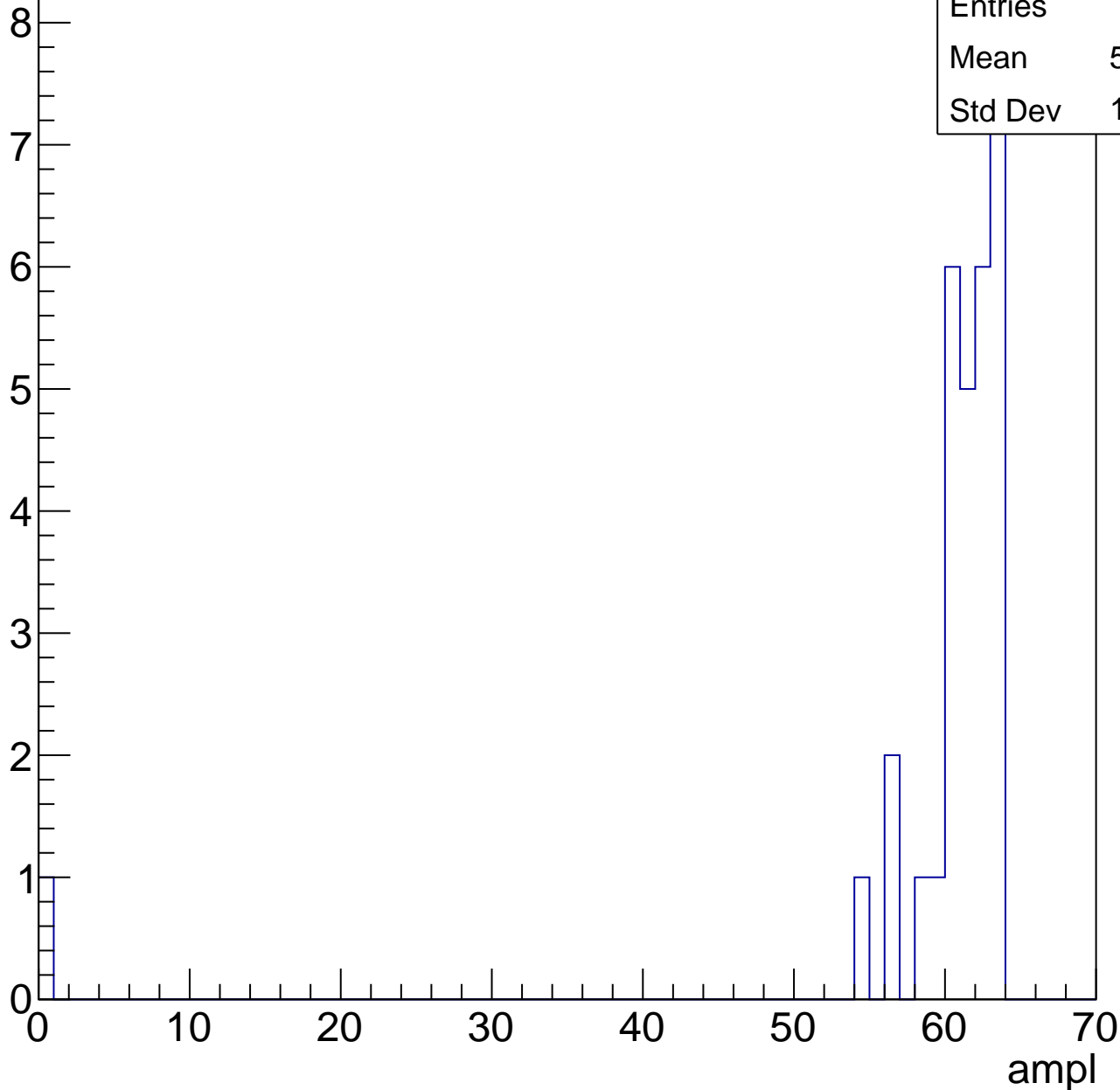


# B1L002S, U17-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.84
Std Dev	10.97

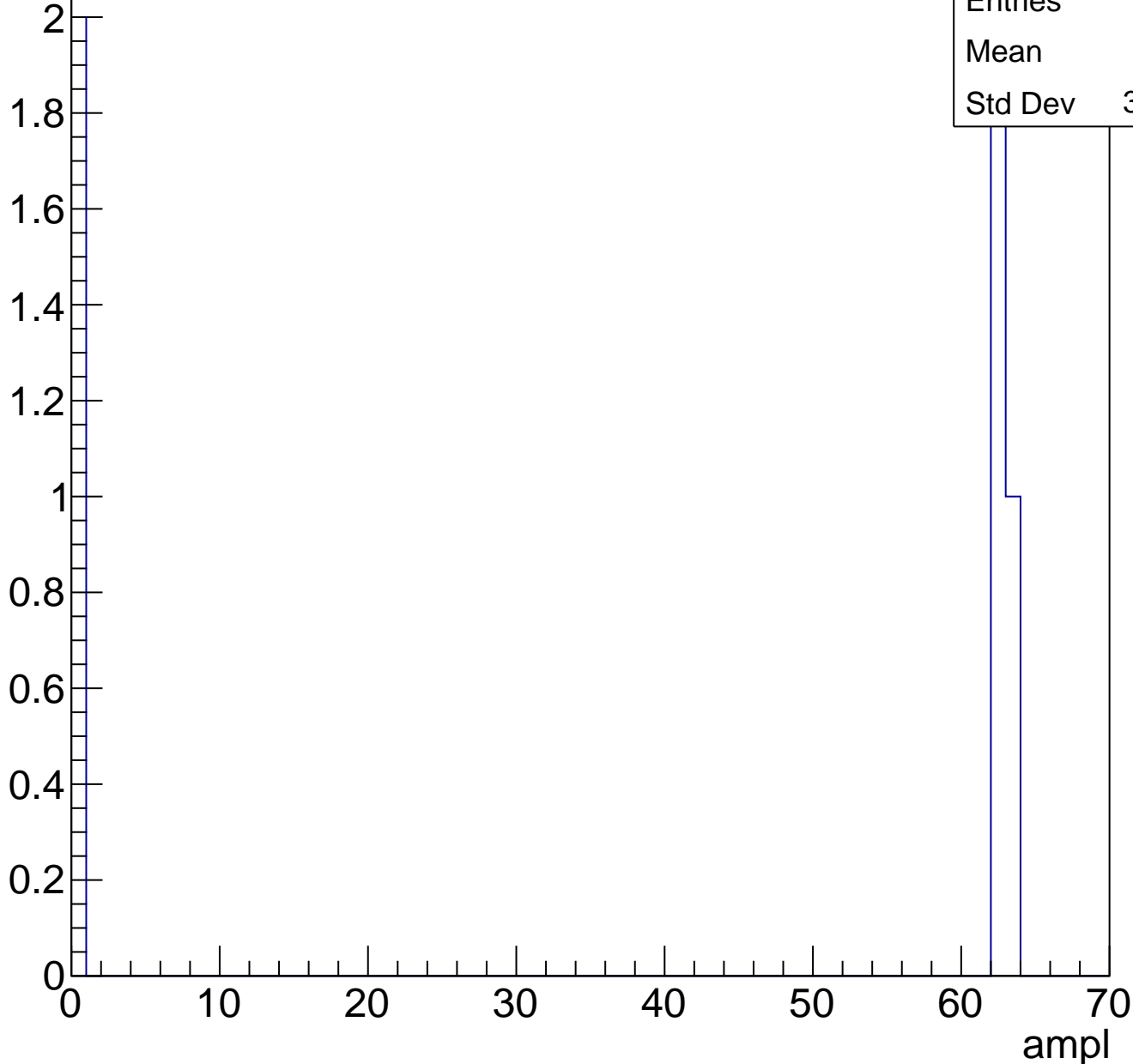




# B1L002S, U17-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch96, adc0

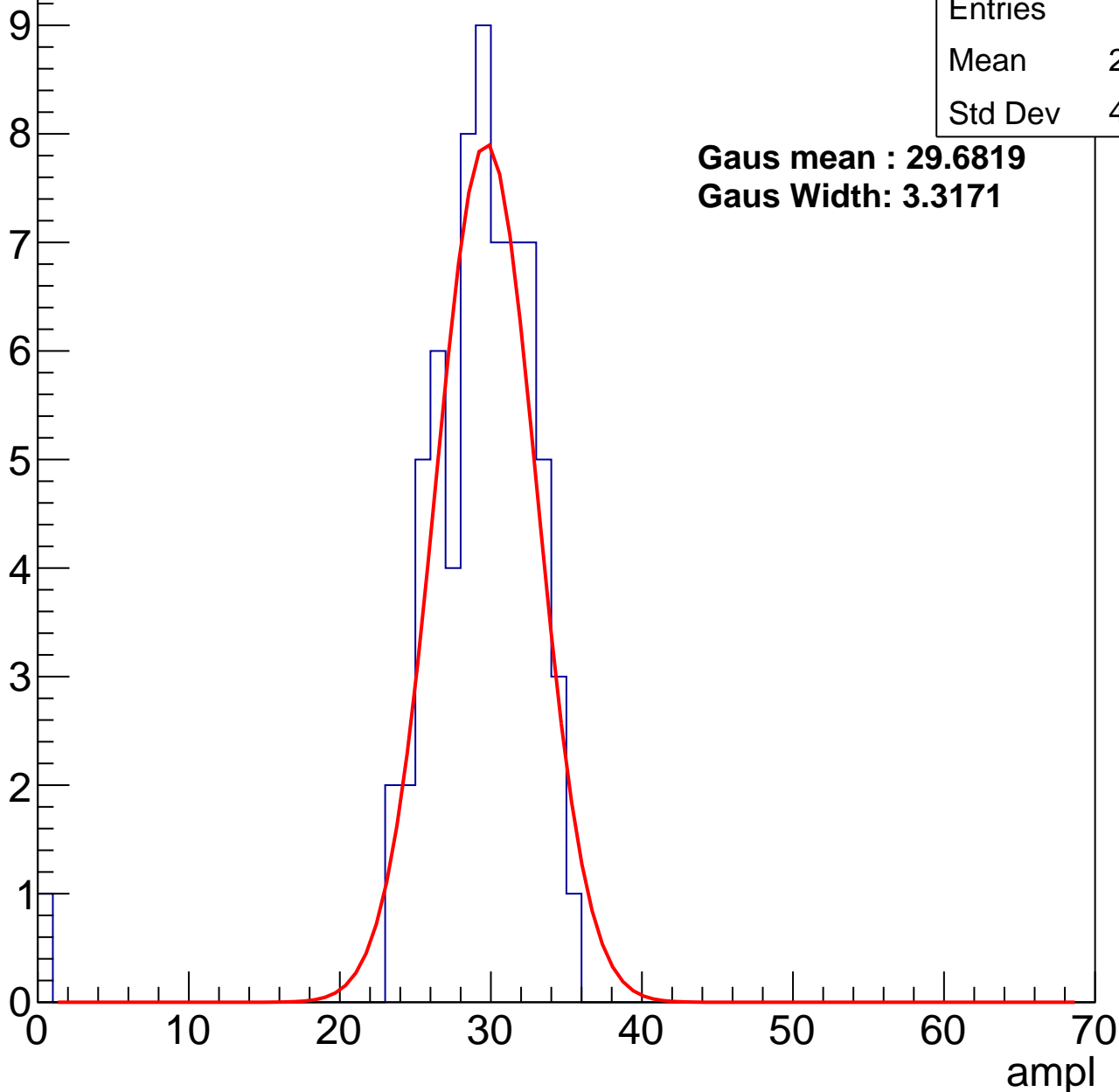
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.67
Std Dev	4.576

**Gaus mean : 29.6819**

**Gaus Width: 3.3171**



# B1L002S, U17-ch96, adc1

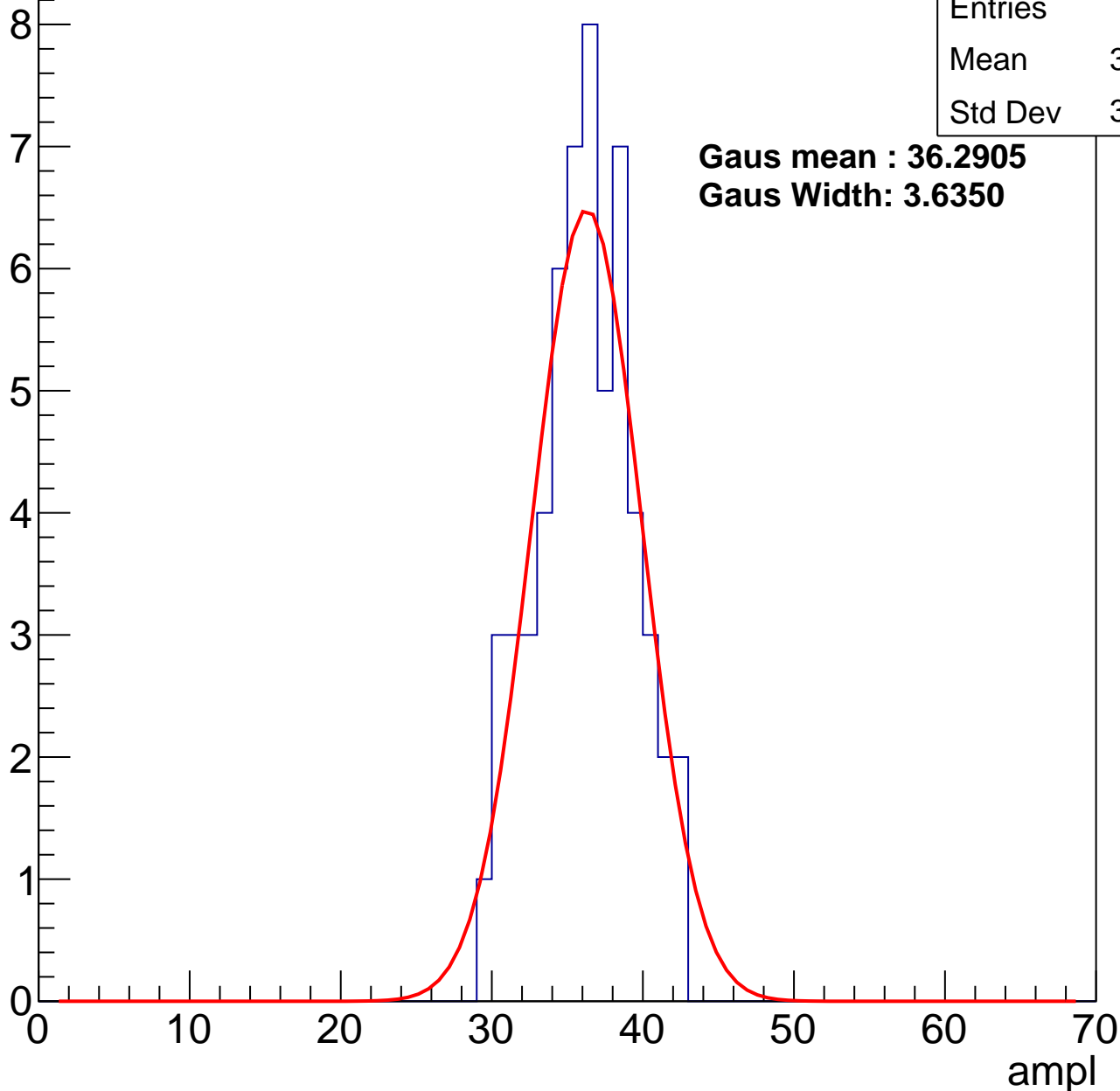
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.69
Std Dev	3.163

**Gaus mean : 36.2905**

**Gaus Width: 3.6350**



# B1L002S, U17-ch96, adc2

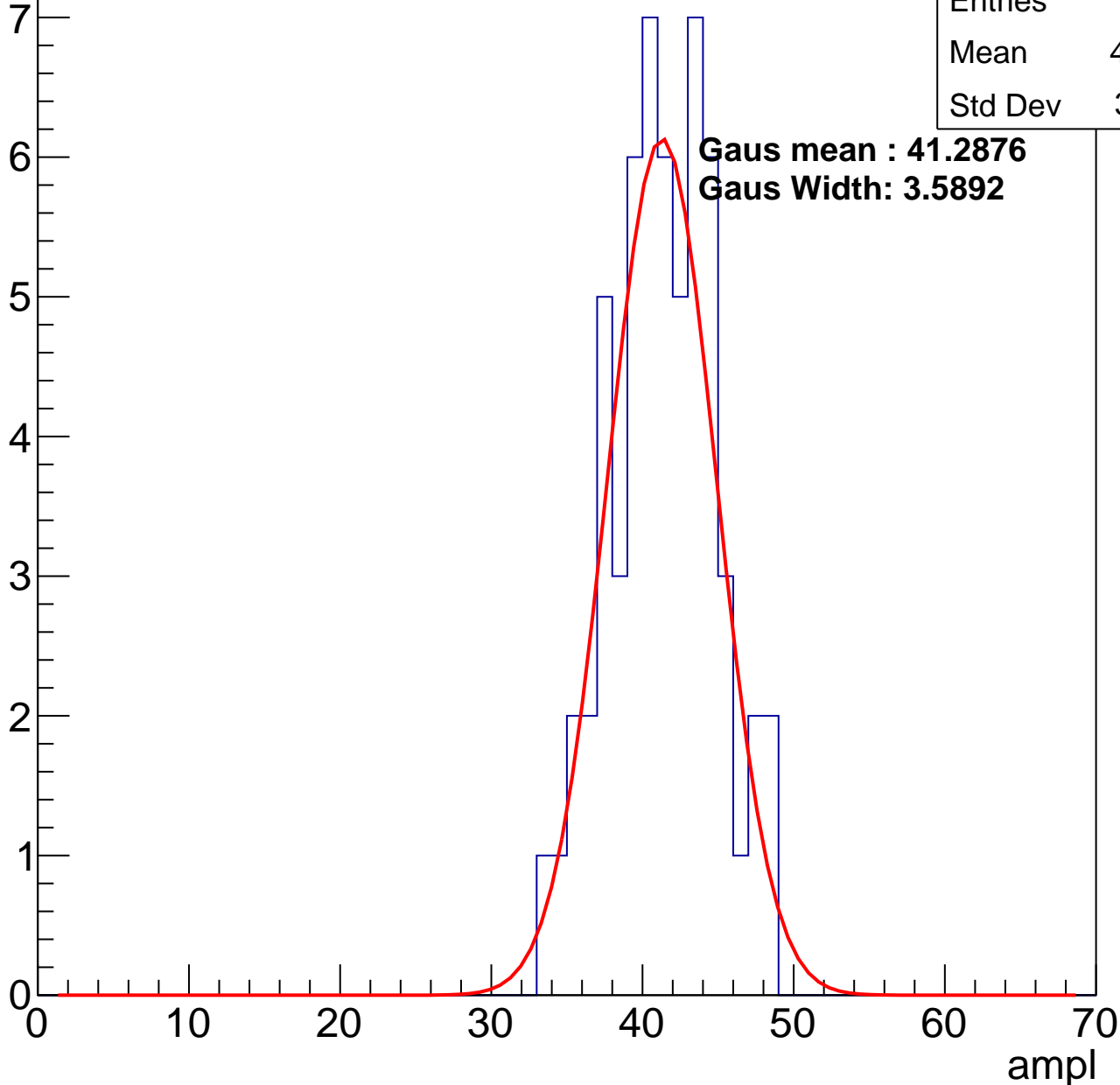
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.92
Std Dev	3.451

**Gaus mean : 41.2876**

**Gaus Width: 3.5892**

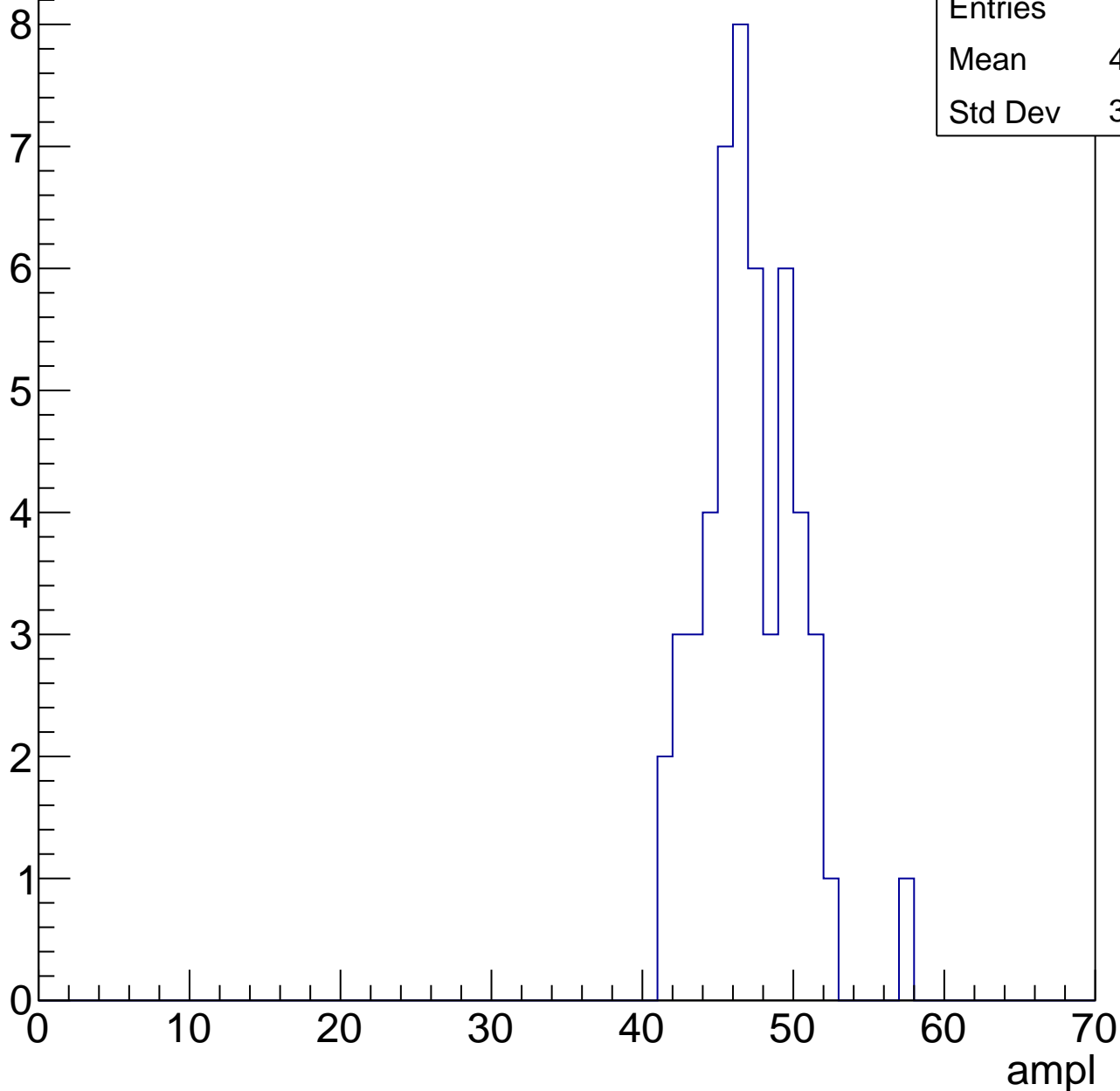


# B1L002S, U17-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	46.63
Std Dev	3.118

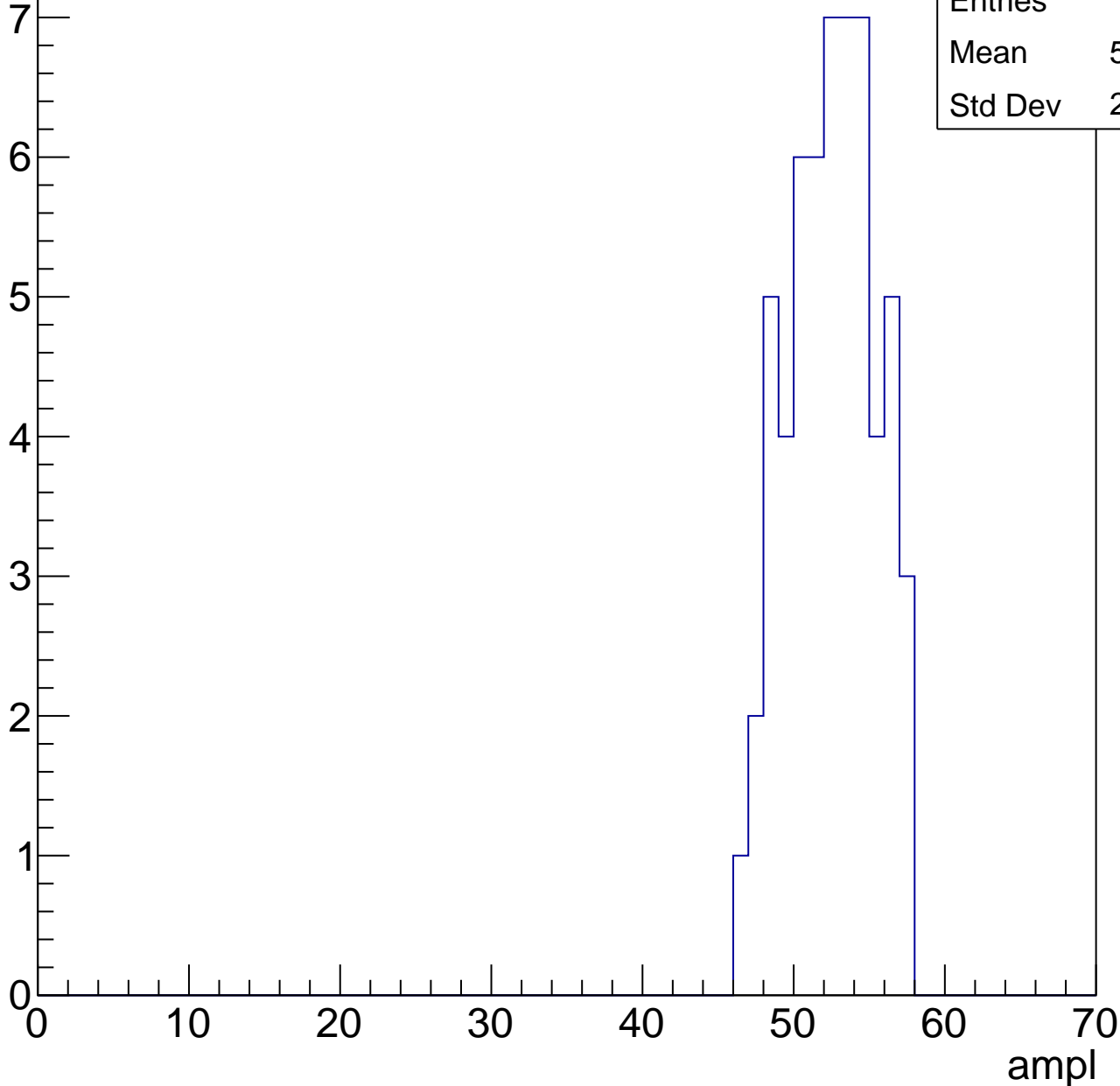


# B1L002S, U17-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.04
Std Dev	2.834



# B1L002S, U17-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	57.59
Std Dev	2.925

Entry

10

8

6

4

2

0

0

10

20

30

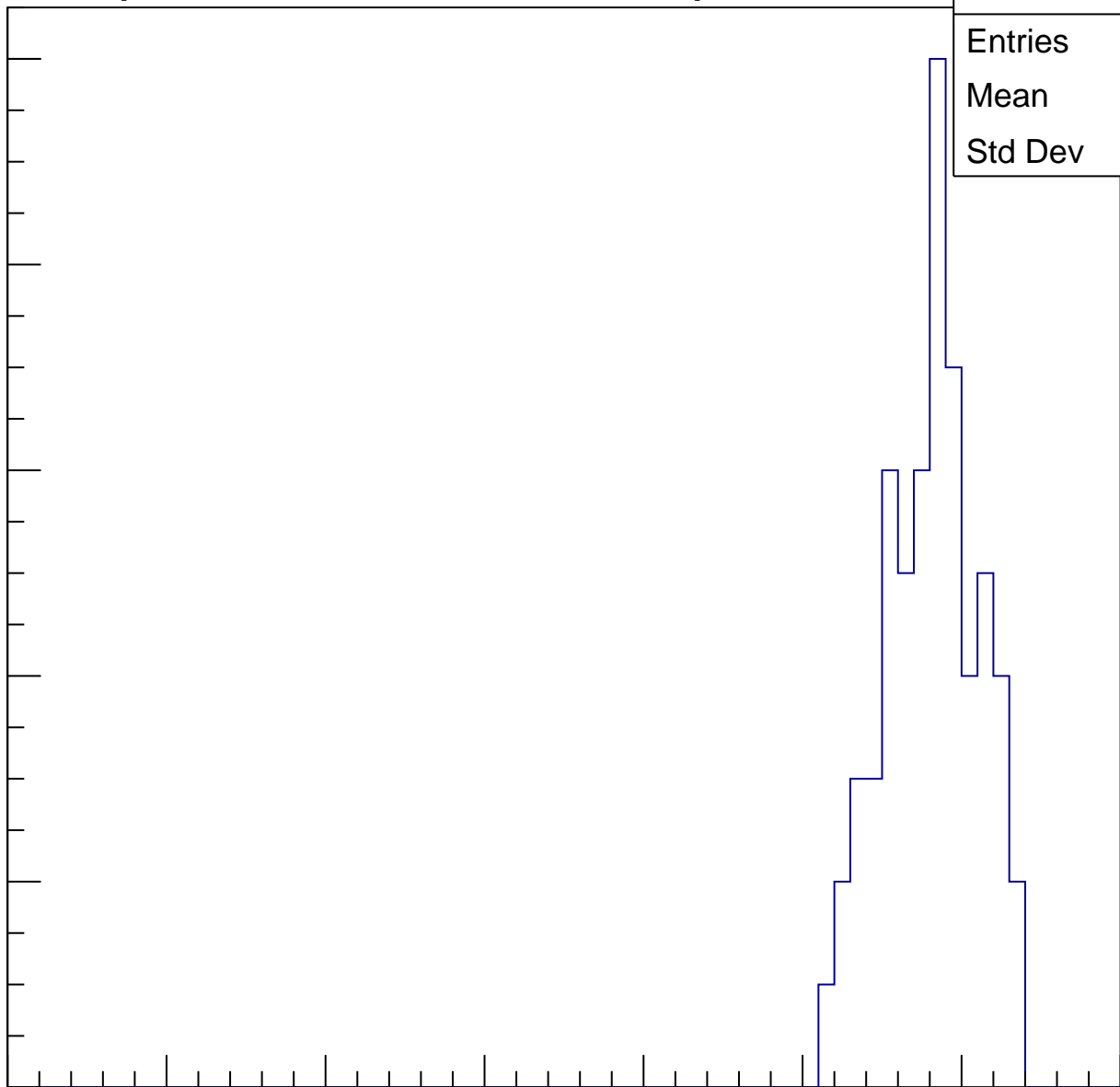
40

50

60

70

ampl

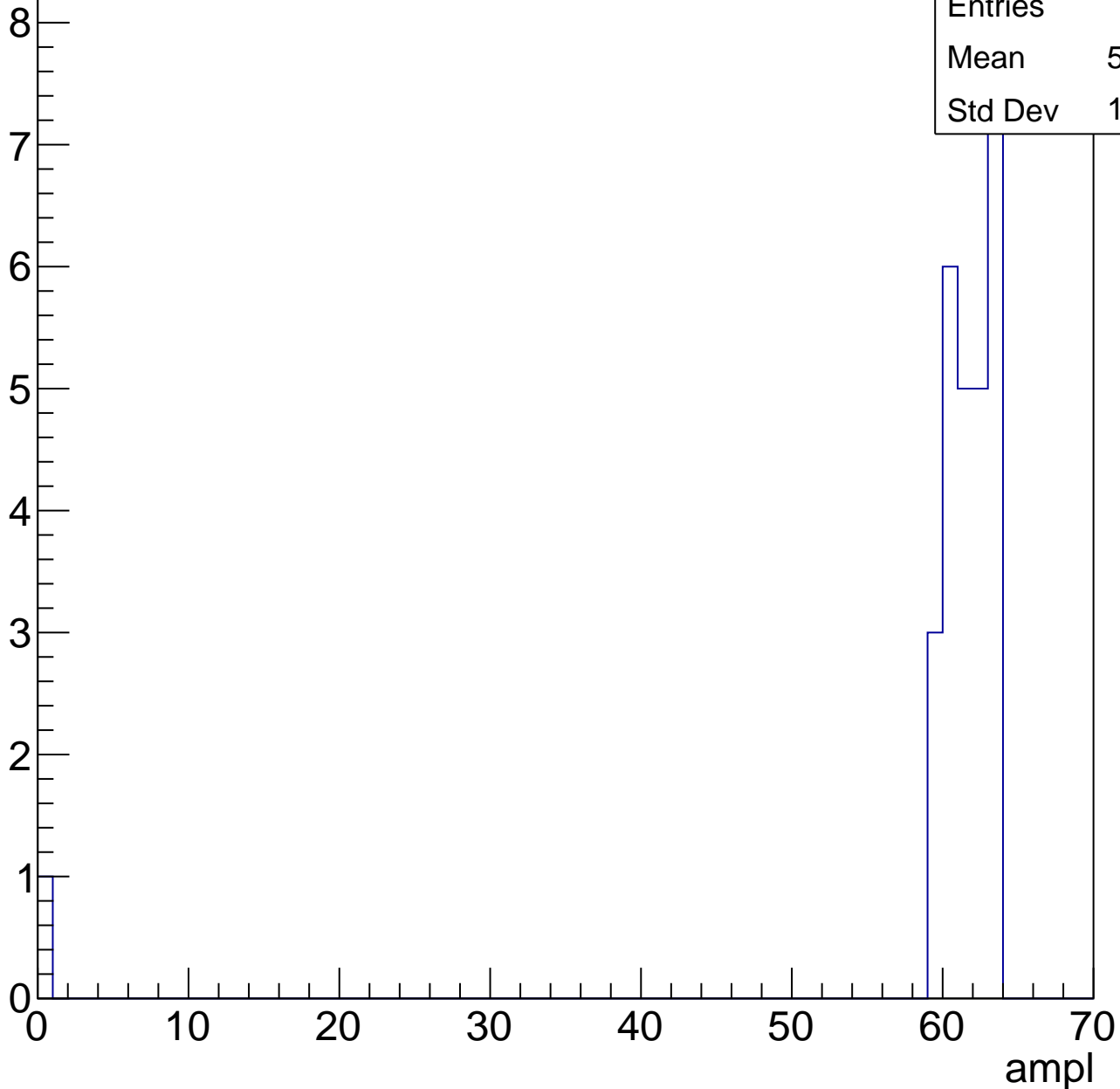


# B1L002S, U17-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59.14
Std Dev	11.46





# B1L002S, U17-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

# B1L002S, U17-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	56
Mean	27.8
Std Dev	2.741

**Gaus mean : 27.8016**

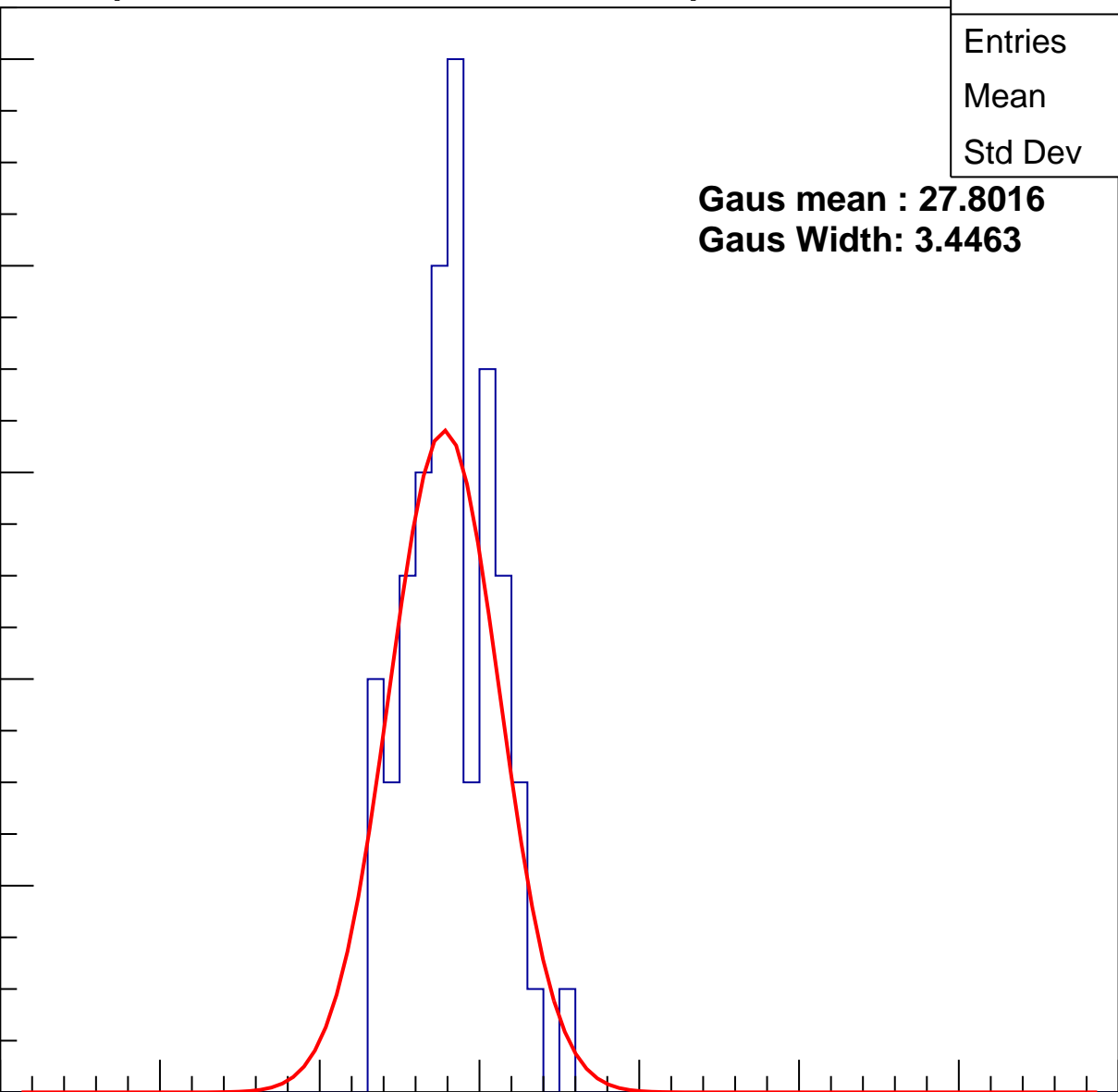
**Gaus Width: 3.4463**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch97, adc1

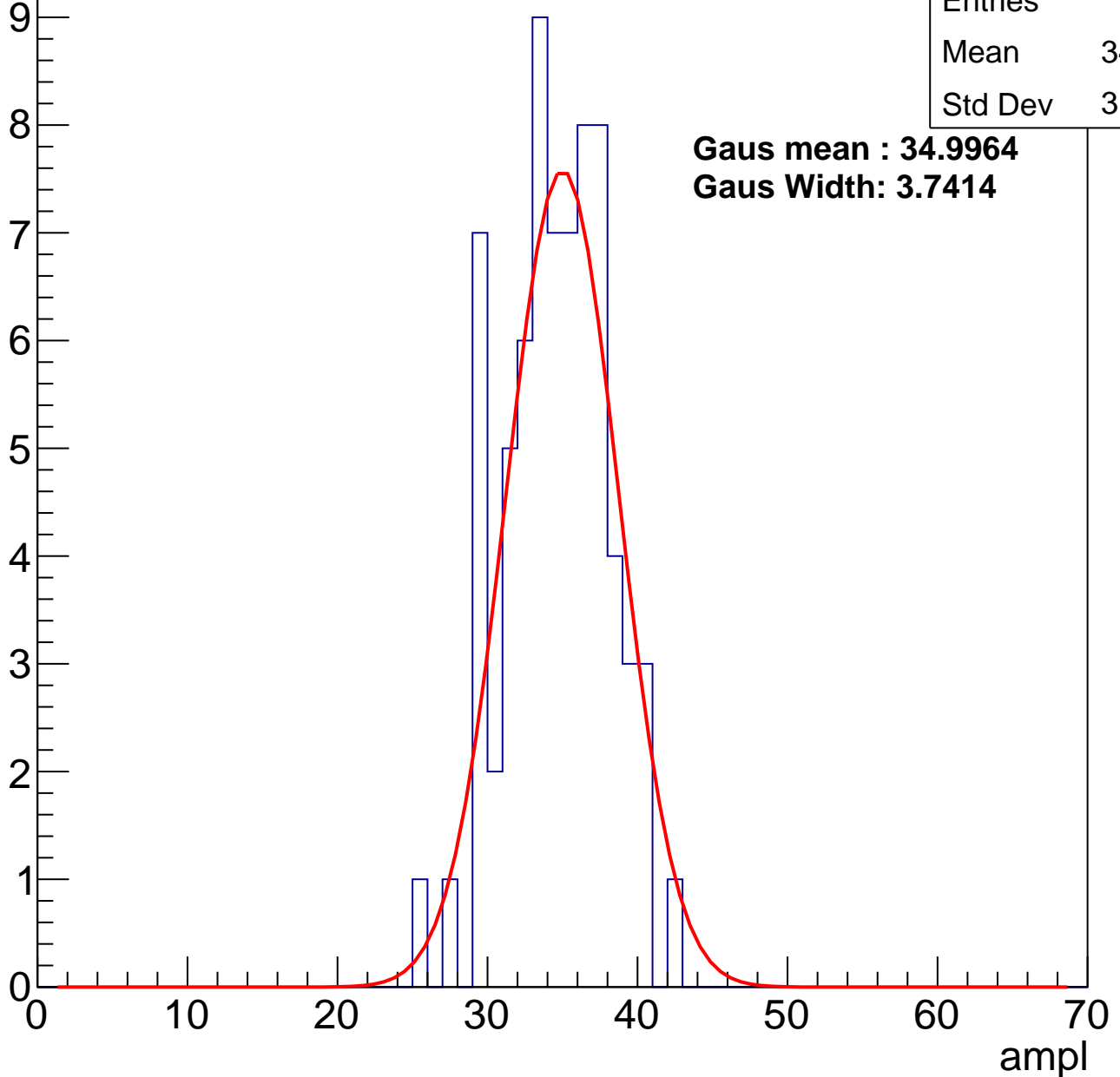
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.12
Std Dev	3.407

**Gaus mean : 34.9964**

**Gaus Width: 3.7414**



# B1L002S, U17-ch97, adc2

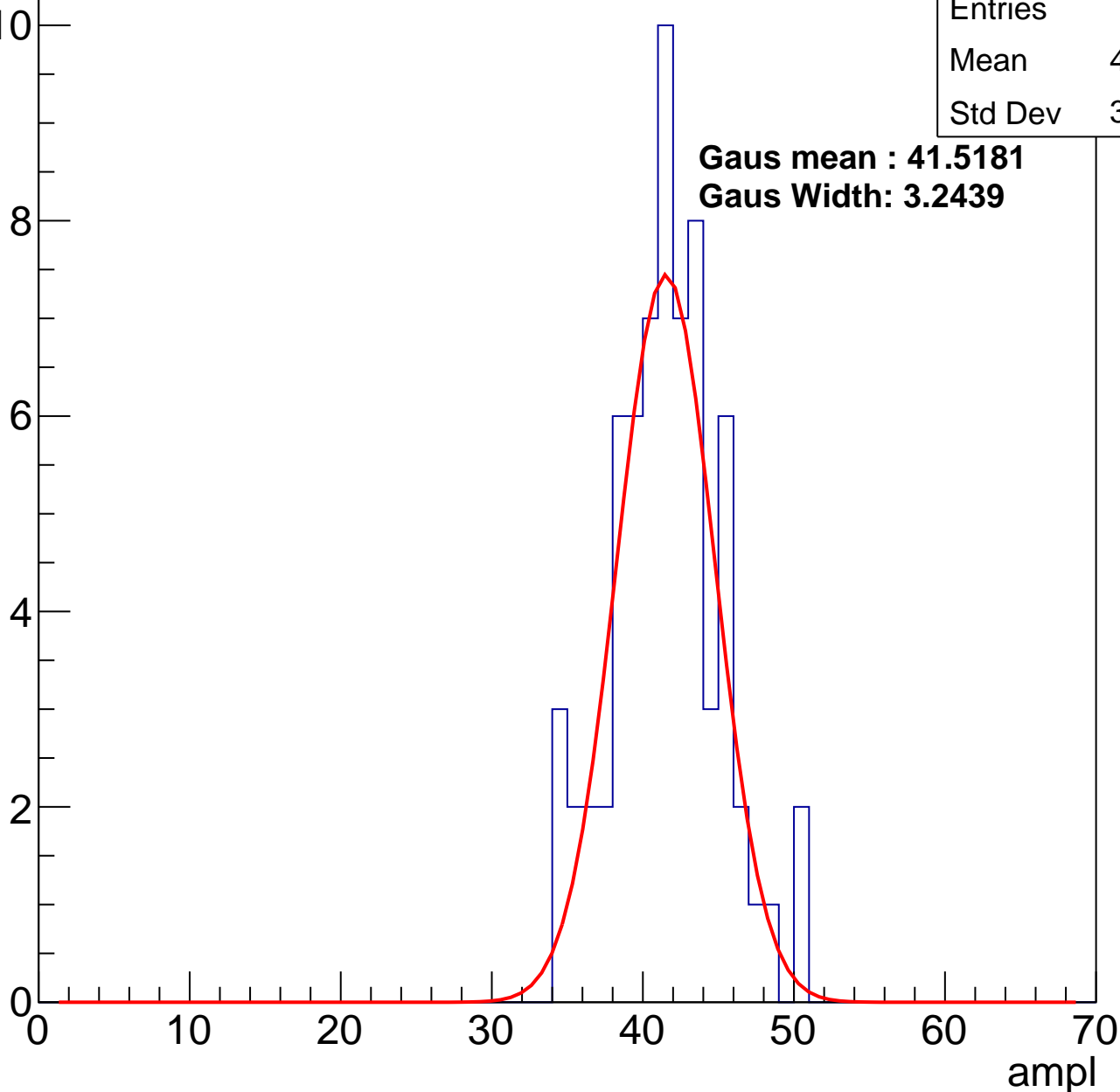
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.13
Std Dev	3.523

**Gaus mean : 41.5181**

**Gaus Width: 3.2439**



# B1L002S, U17-ch97, adc3

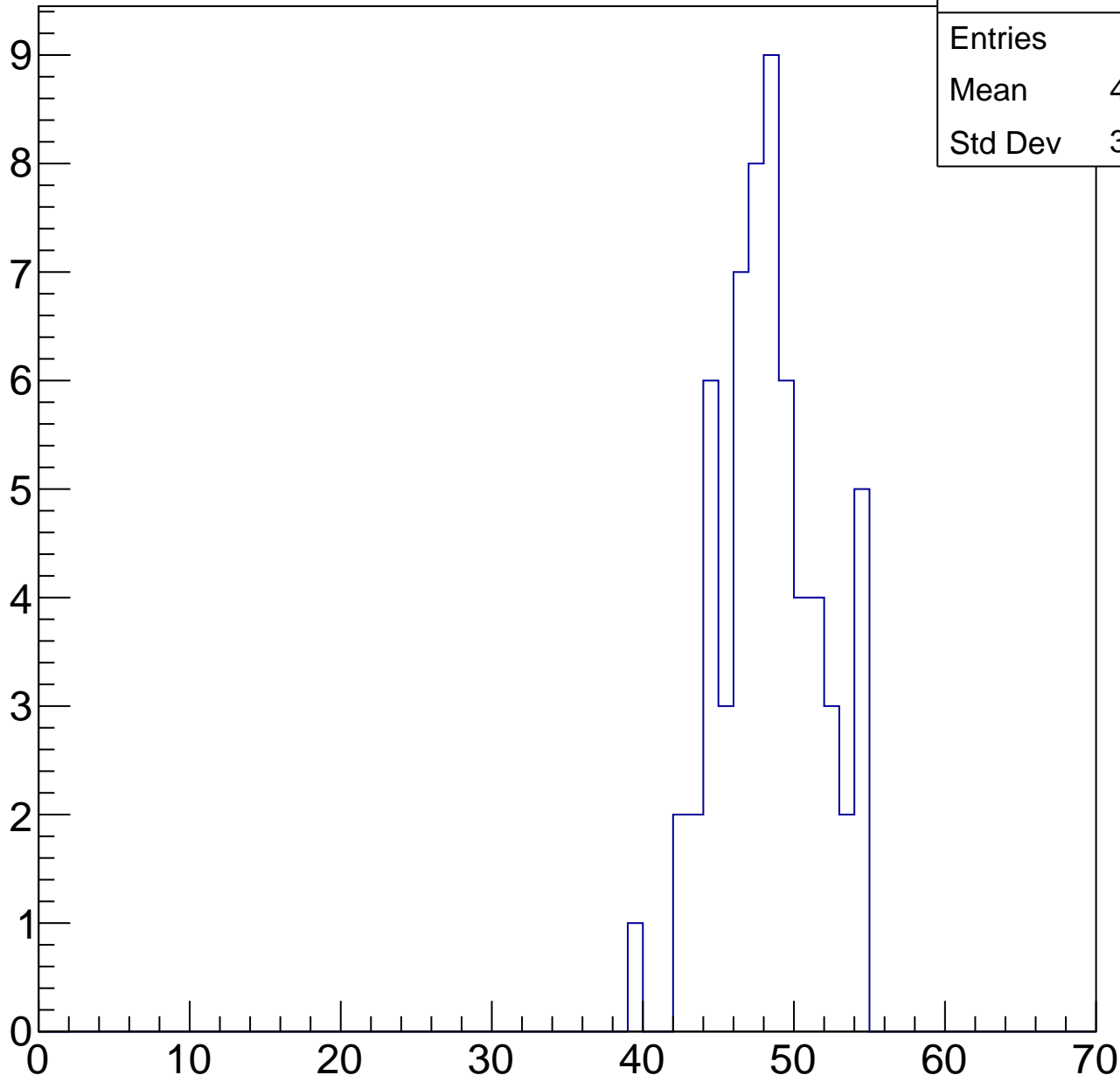
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	47.87
Std Dev	3.353

ampl

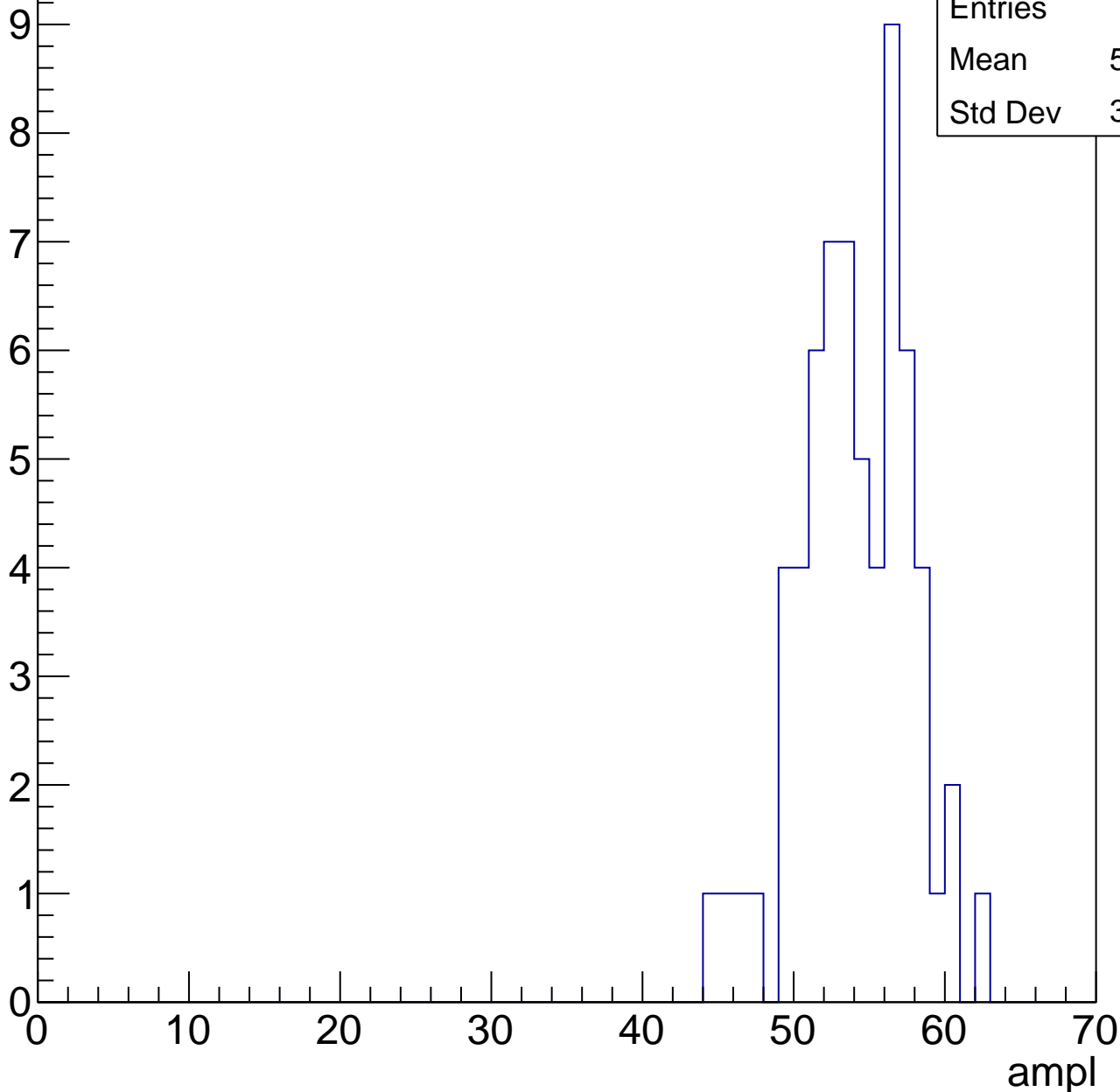


# B1L002S, U17-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	53.56
Std Dev	3.652

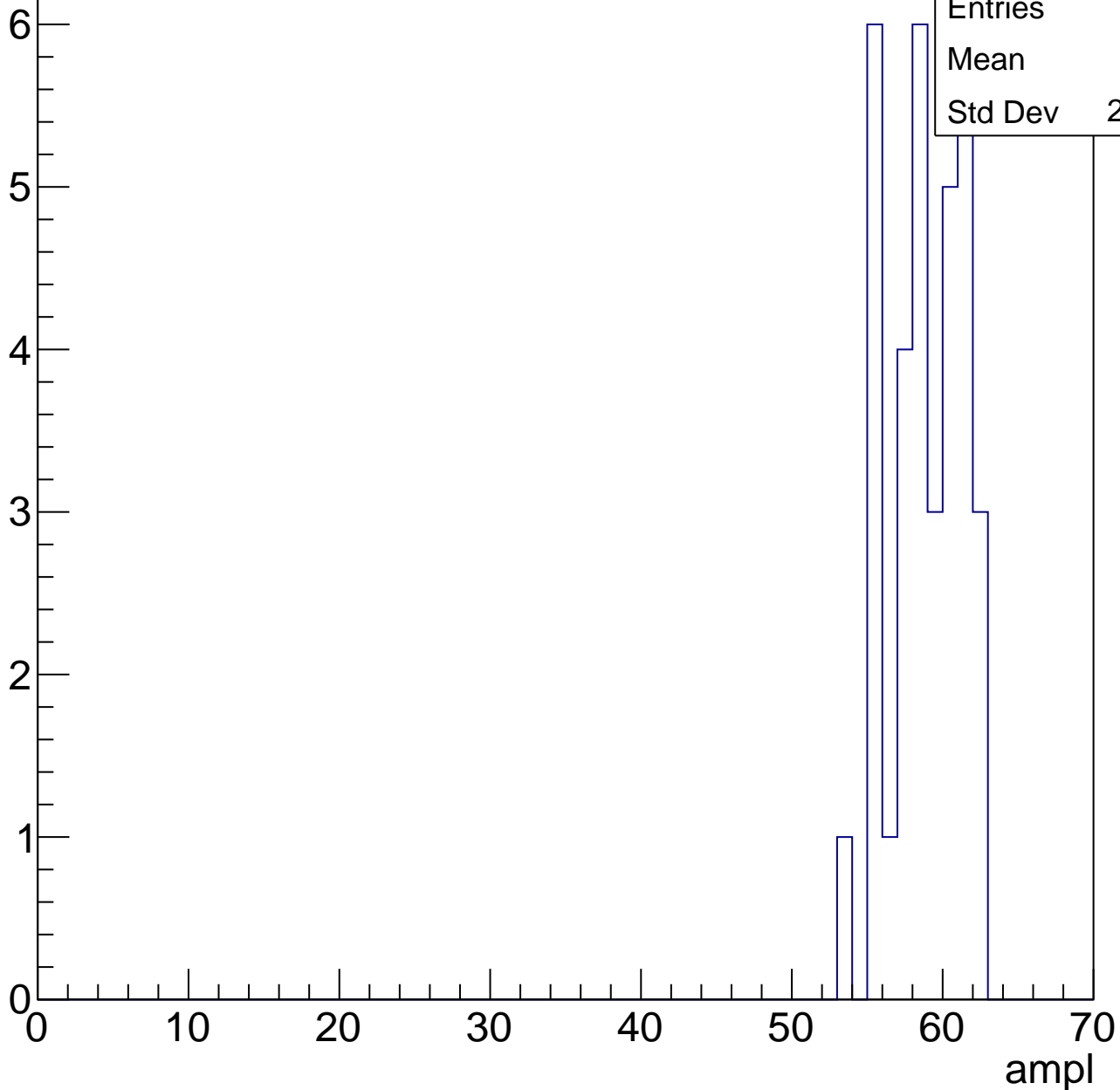


# B1L002S, U17-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.4
Std Dev	2.428

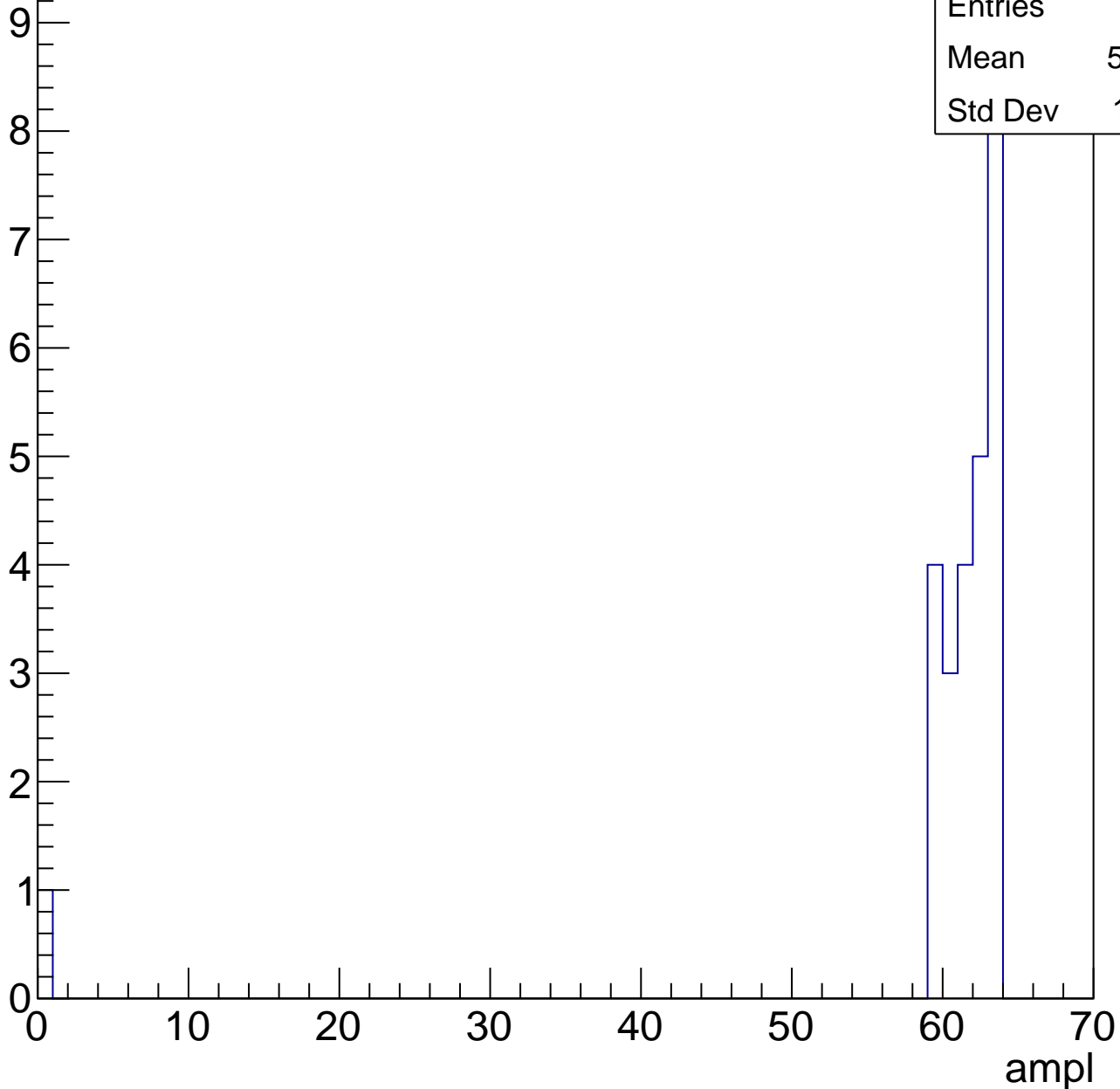


# B1L002S, U17-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59.12
Std Dev	11.91





# B1L002S, U17-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

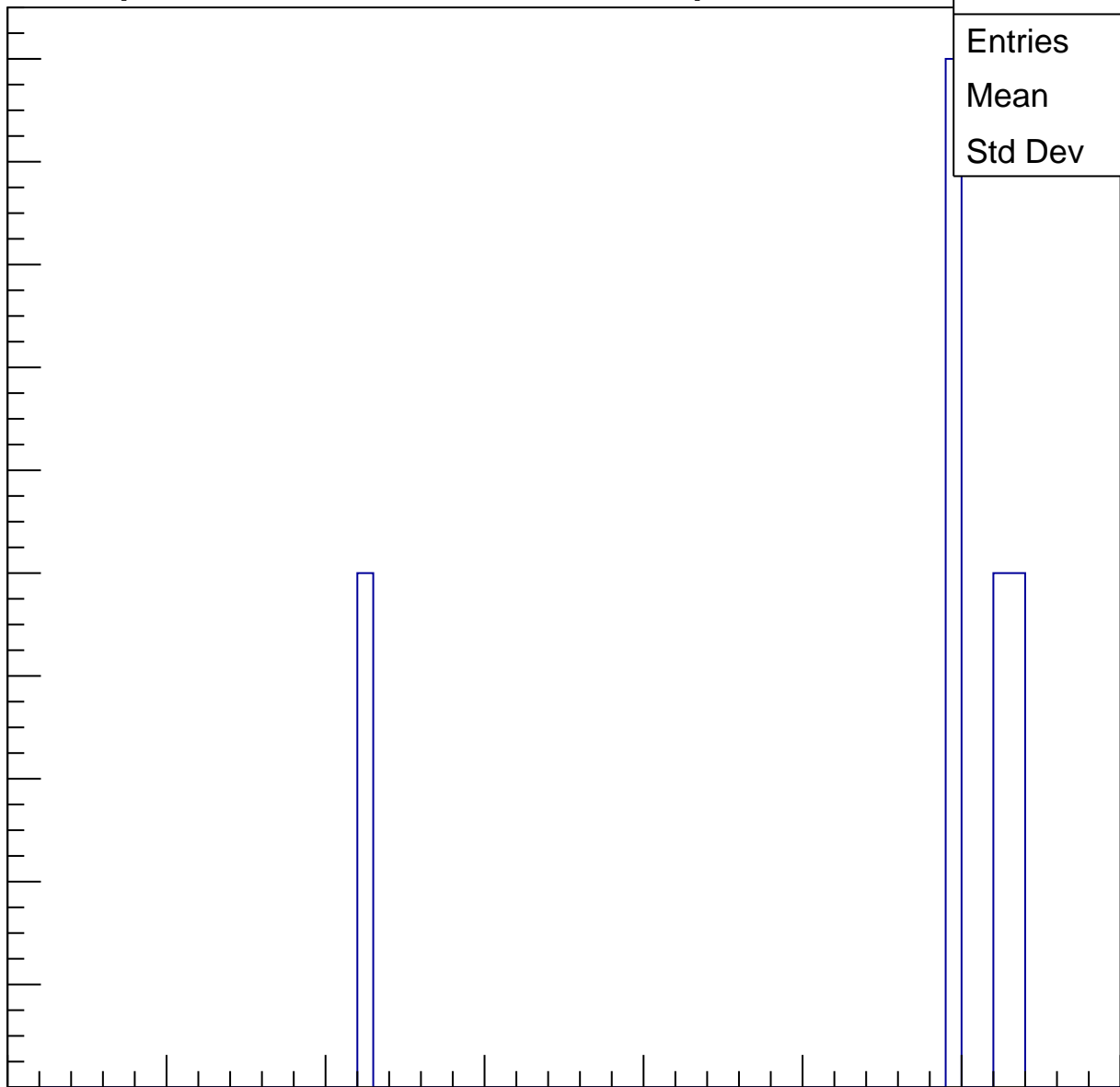
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	53
Std Dev	15.58

0 10 20 30 40 50 60 70

ampl



# B1L002S, U17-ch98, adc0

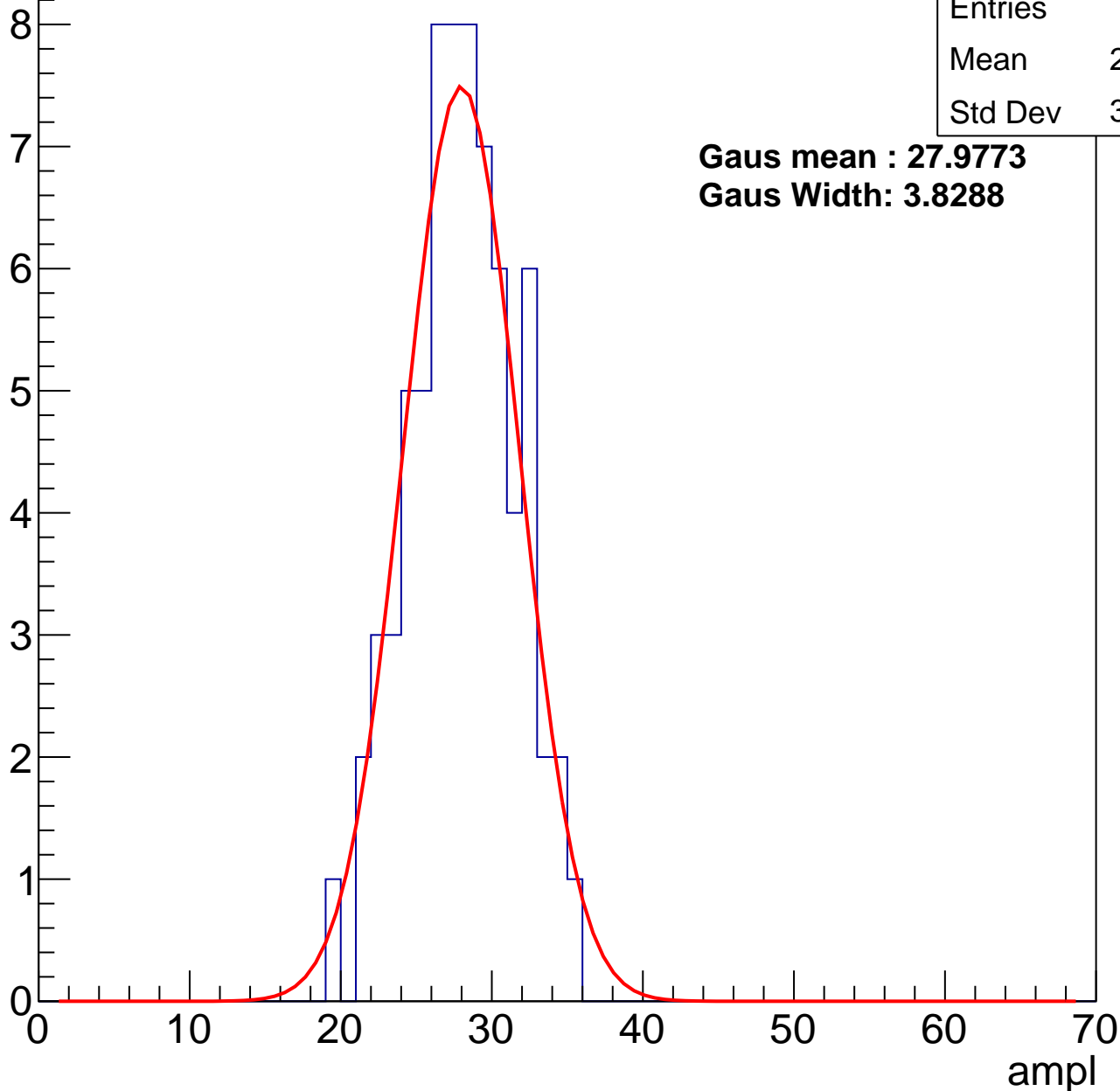
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	27.56
Std Dev	3.455

**Gaus mean : 27.9773**

**Gaus Width: 3.8288**



# B1L002S, U17-ch98, adc1

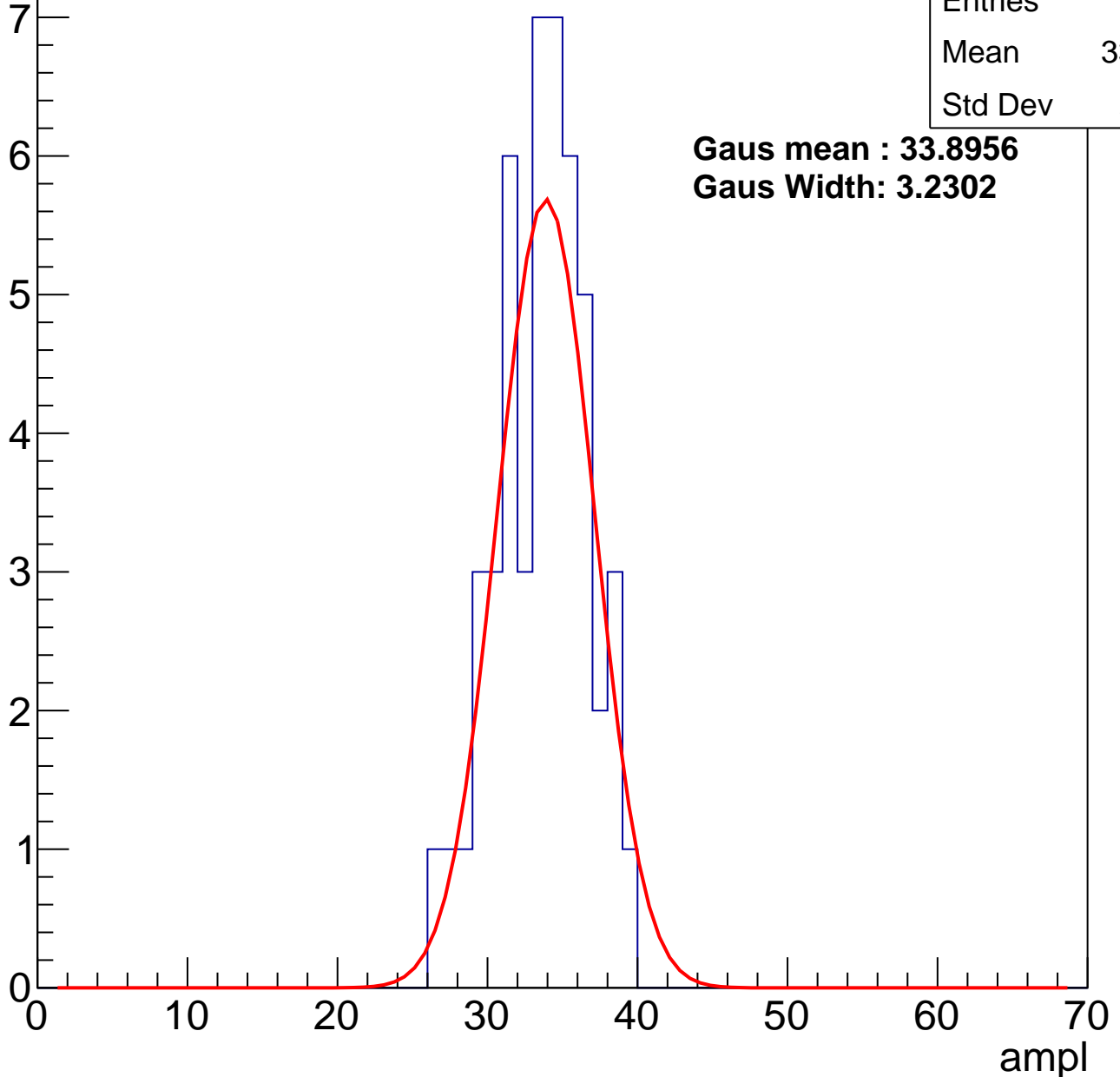
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	33.18
Std Dev	2.96

**Gaus mean : 33.8956**

**Gaus Width: 3.2302**



# B1L002S, U17-ch98, adc2

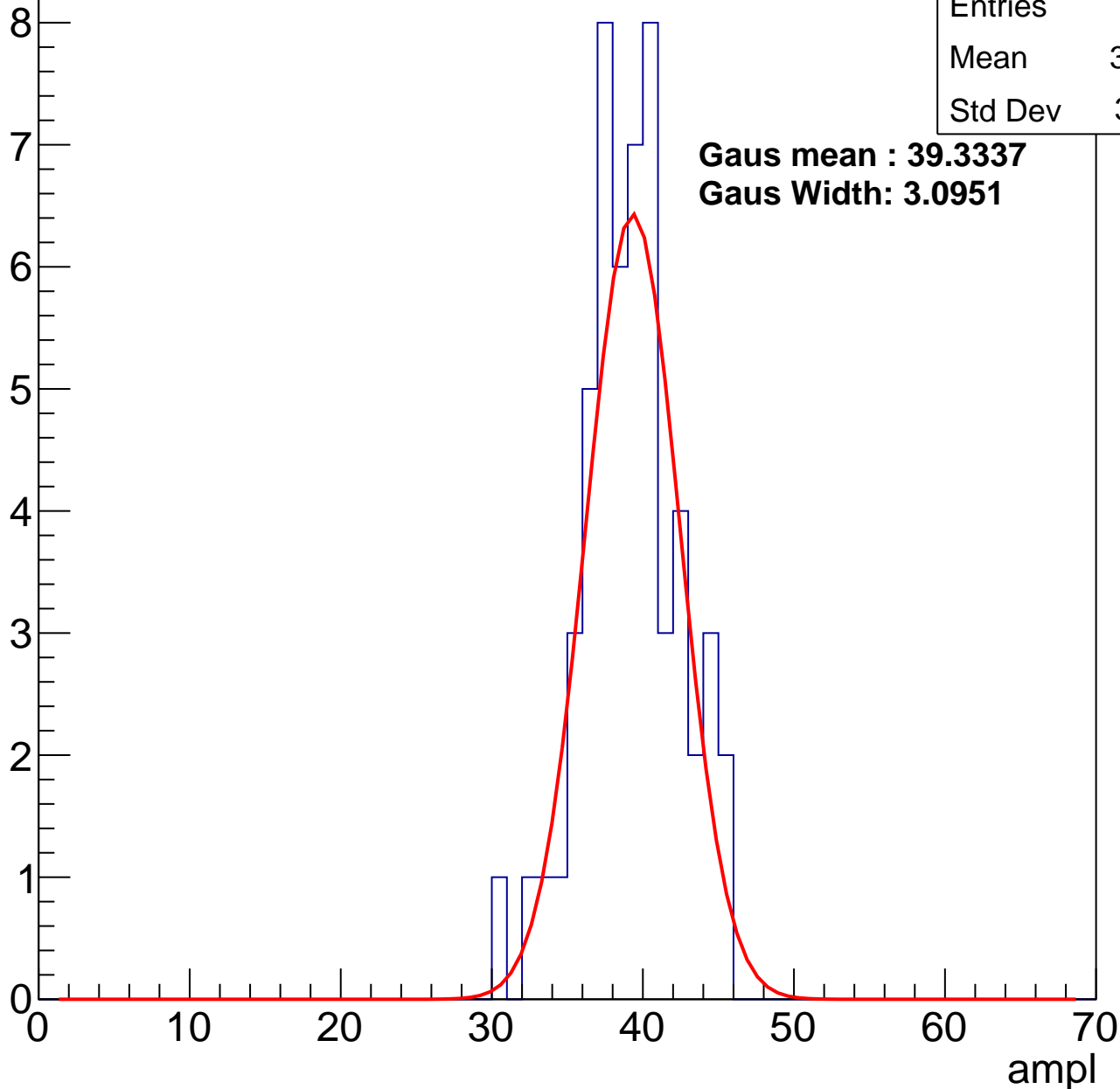
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	38.73
Std Dev	3.171

**Gaus mean : 39.3337**

**Gaus Width: 3.0951**

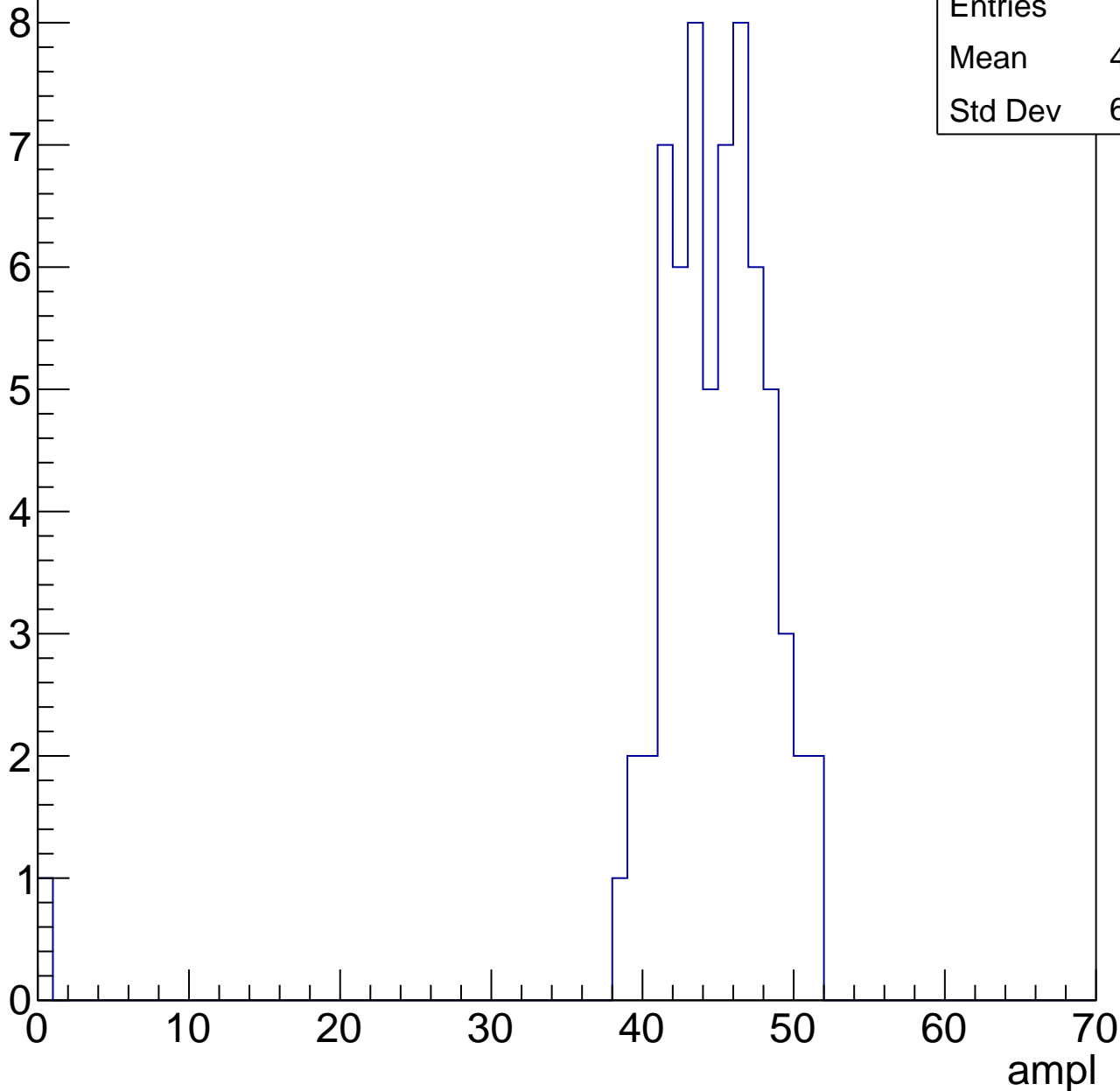


# B1L002S, U17-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	43.89
Std Dev	6.283

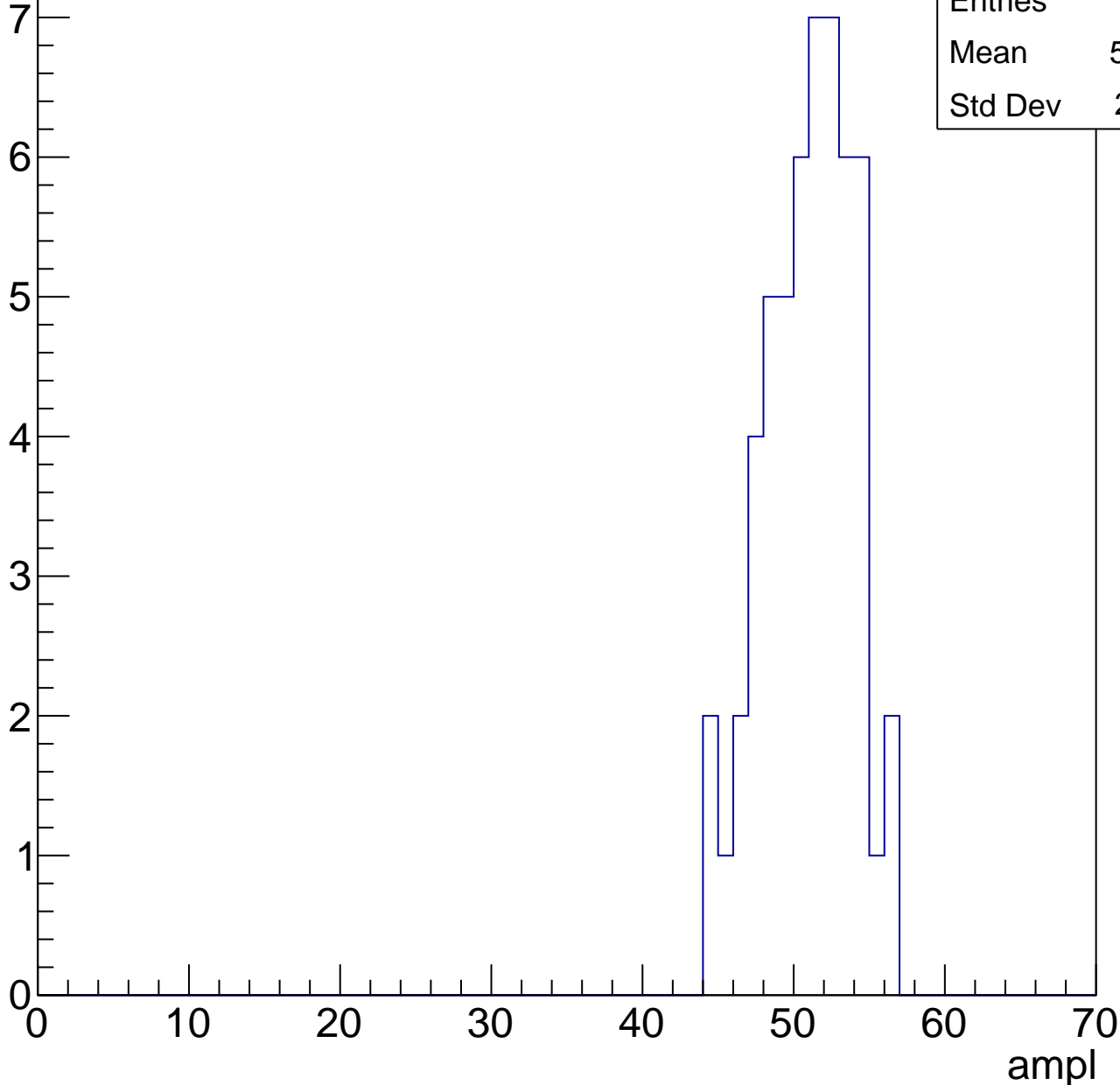


# B1L002S, U17-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

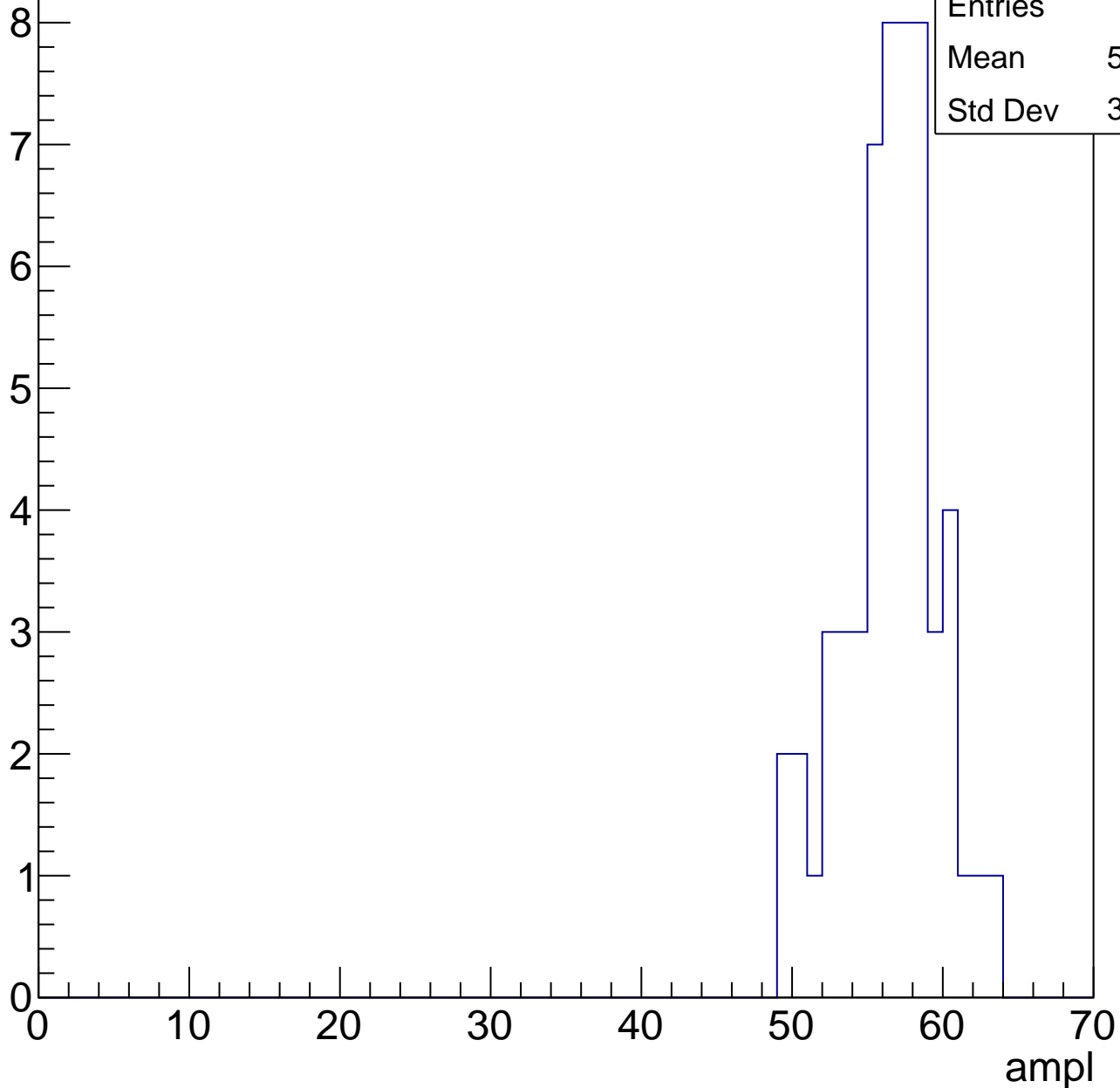
Entries	54
Mean	50.52
Std Dev	2.911



# B1L002S, U17-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	55
Mean	56.04
Std Dev	3.104

# B1L002S, U17-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries

35

Mean

59.71

Std Dev

2.536

0

10

20

30

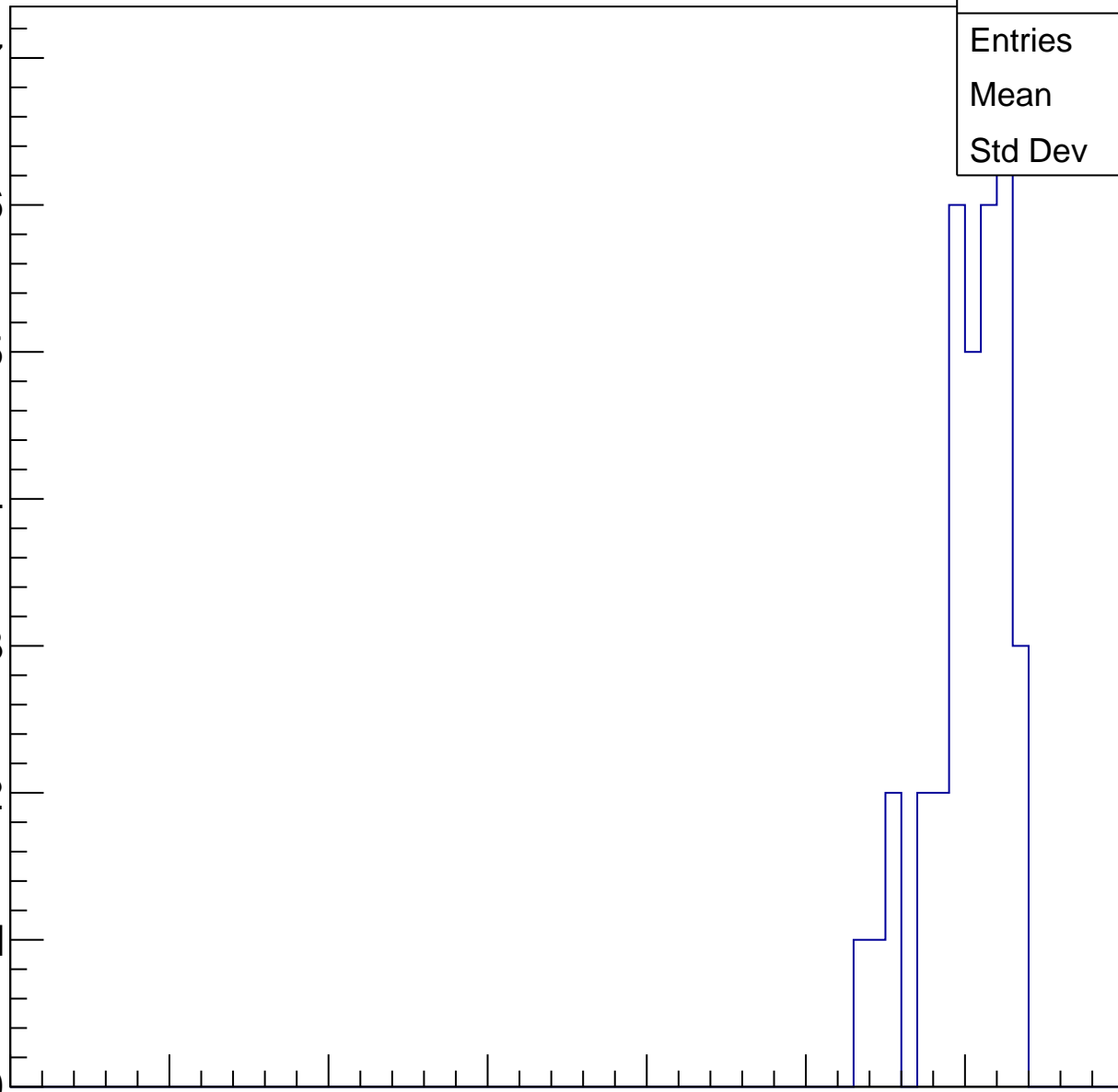
40

50

60

70

ampl



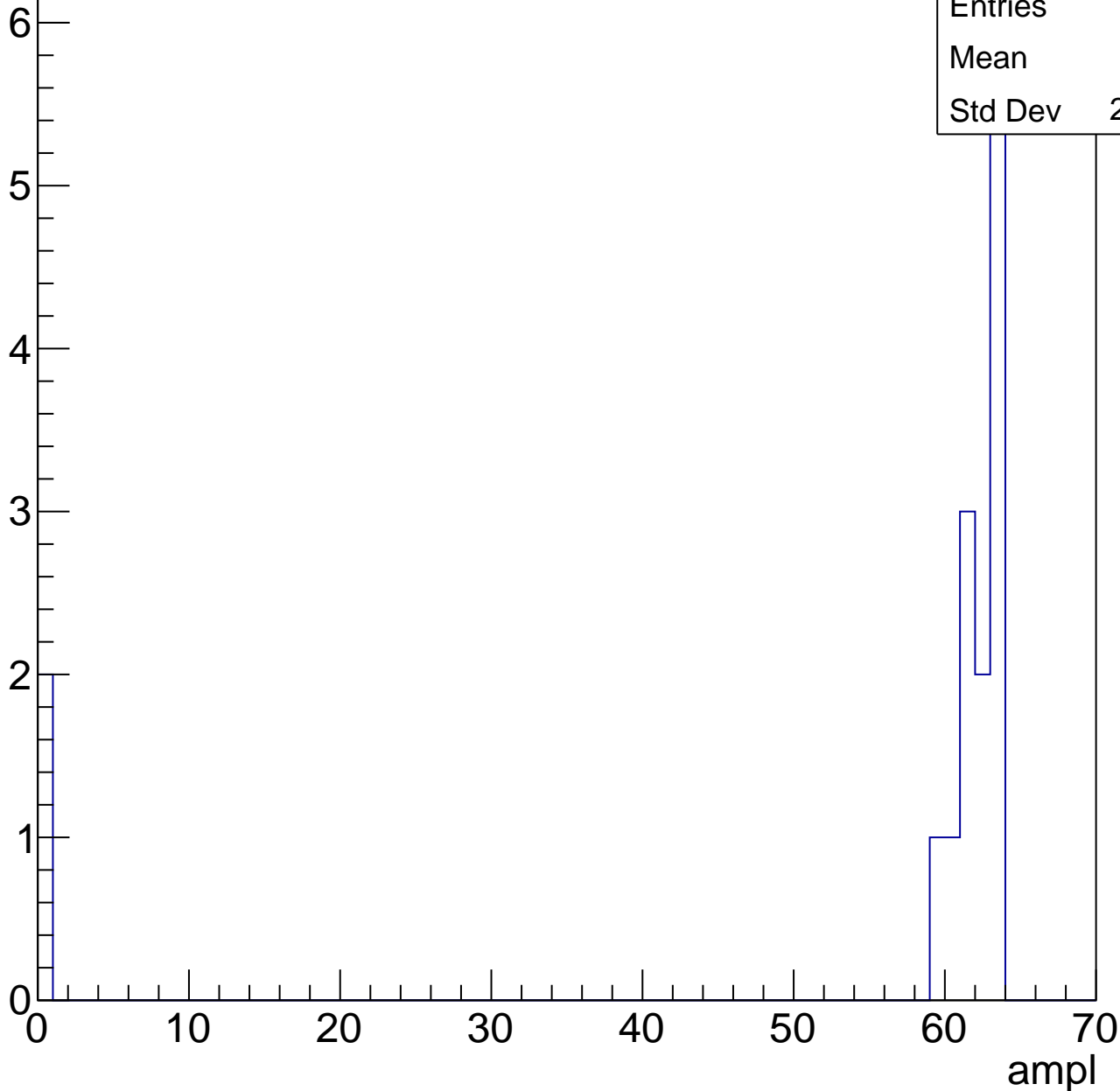


# B1L002S, U17-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	53.6
Std Dev	21.06



# B1L002S, U17-ch99, adc0

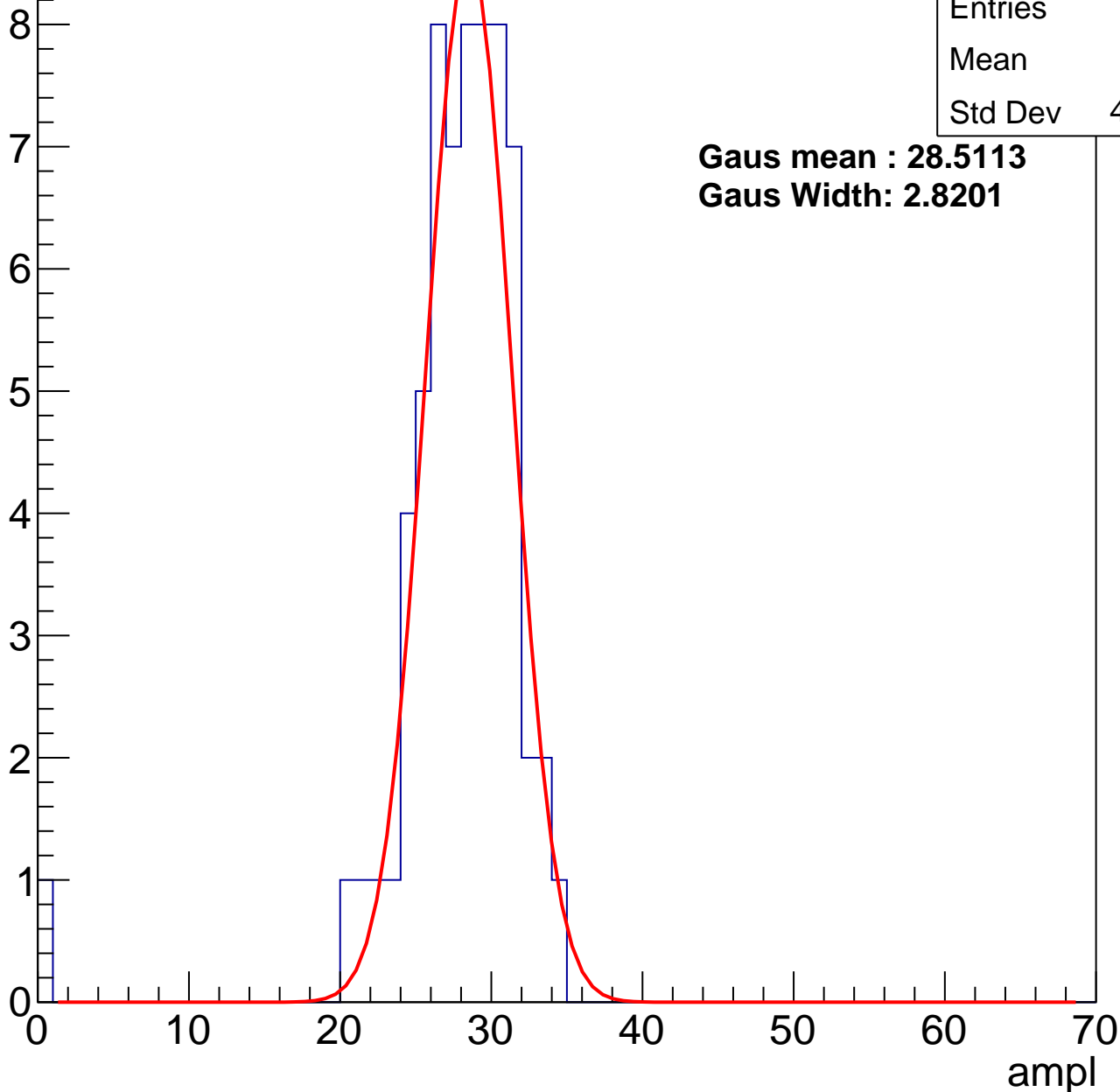
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.4
Std Dev	4.478

**Gaus mean : 28.5113**

**Gaus Width: 2.8201**



# B1L002S, U17-ch99, adc1

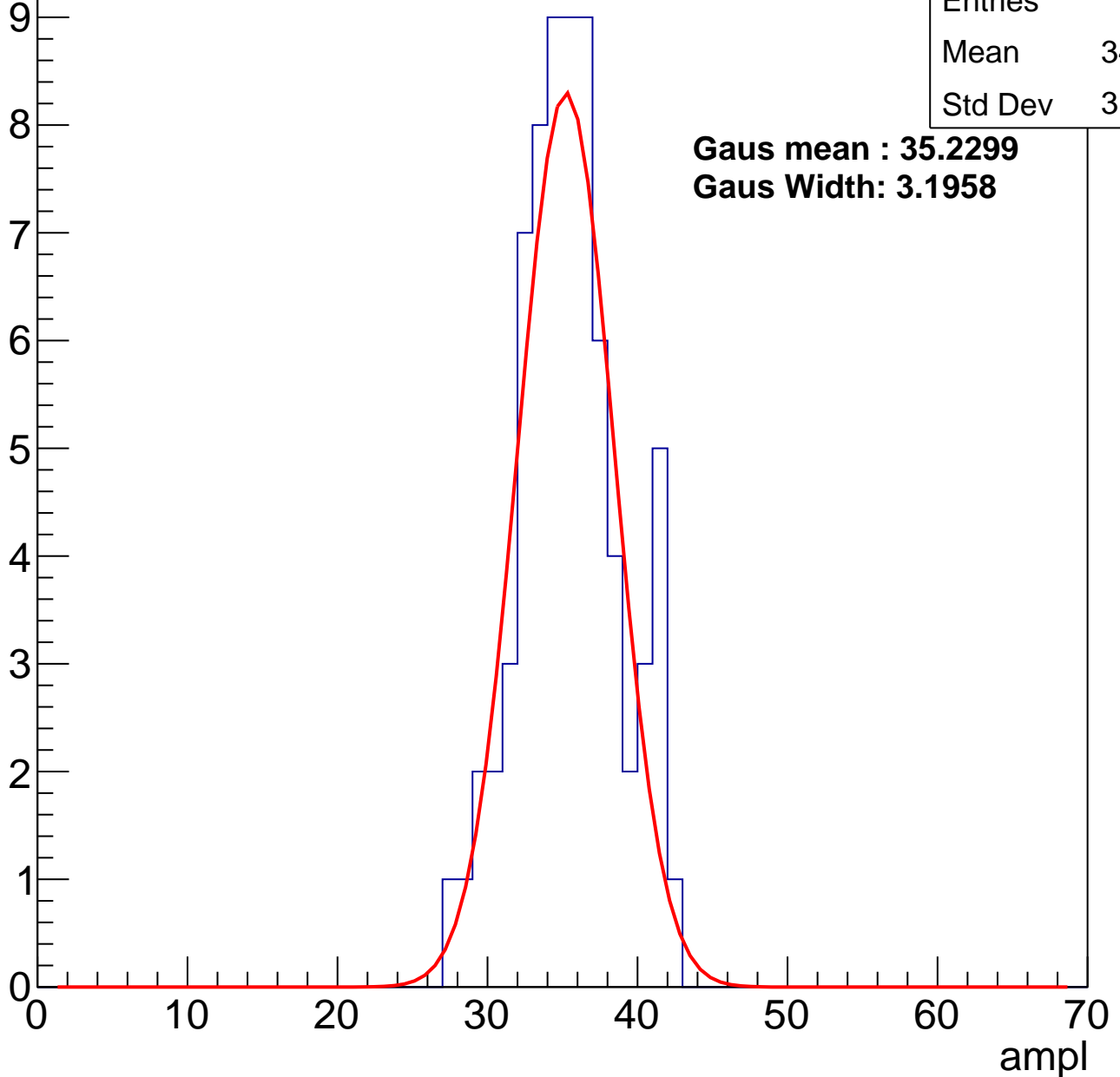
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.97
Std Dev	3.317

**Gaus mean : 35.2299**

**Gaus Width: 3.1958**



# B1L002S, U17-ch99, adc2

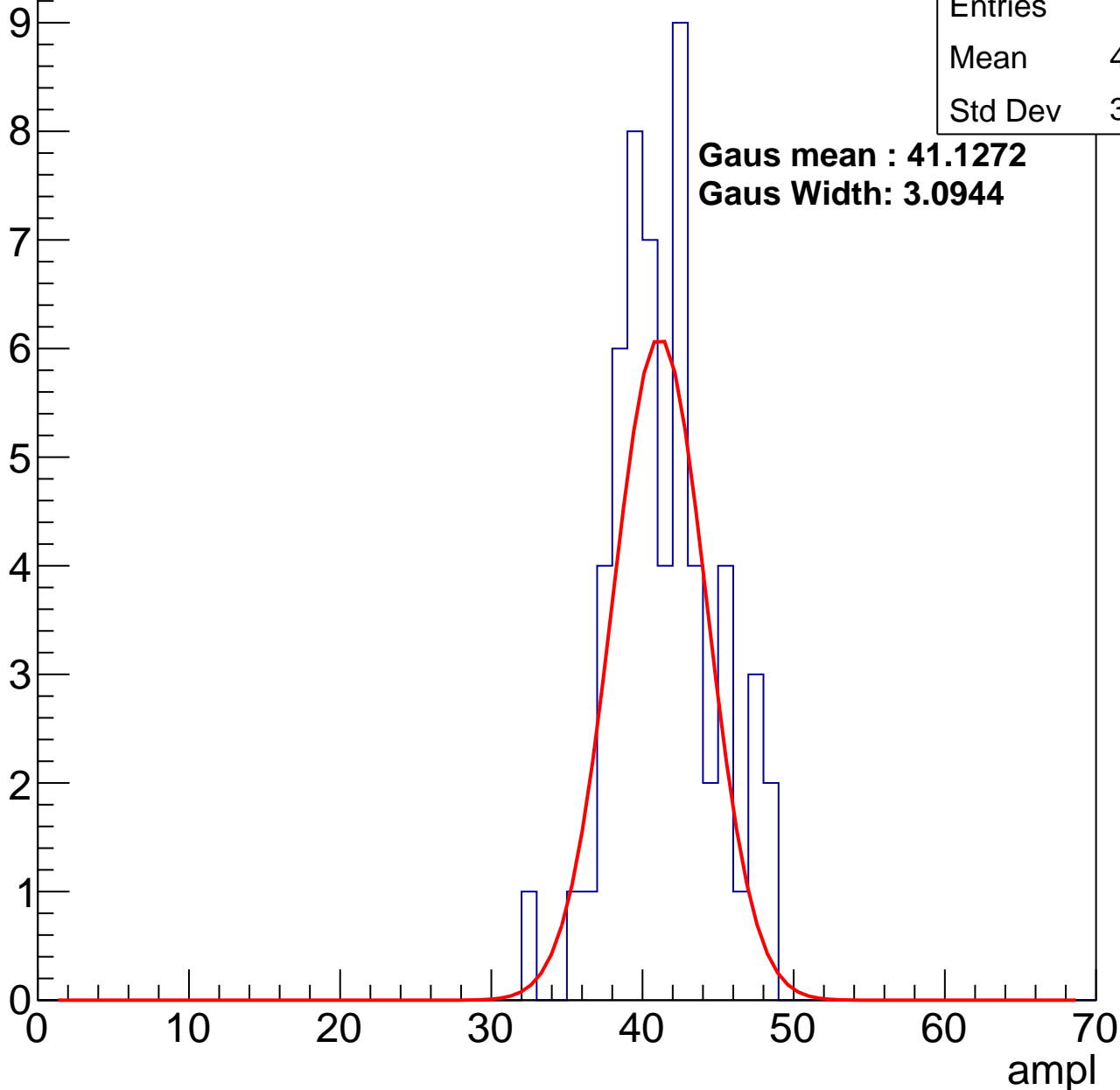
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.98
Std Dev	3.343

**Gaus mean : 41.1272**

**Gaus Width: 3.0944**

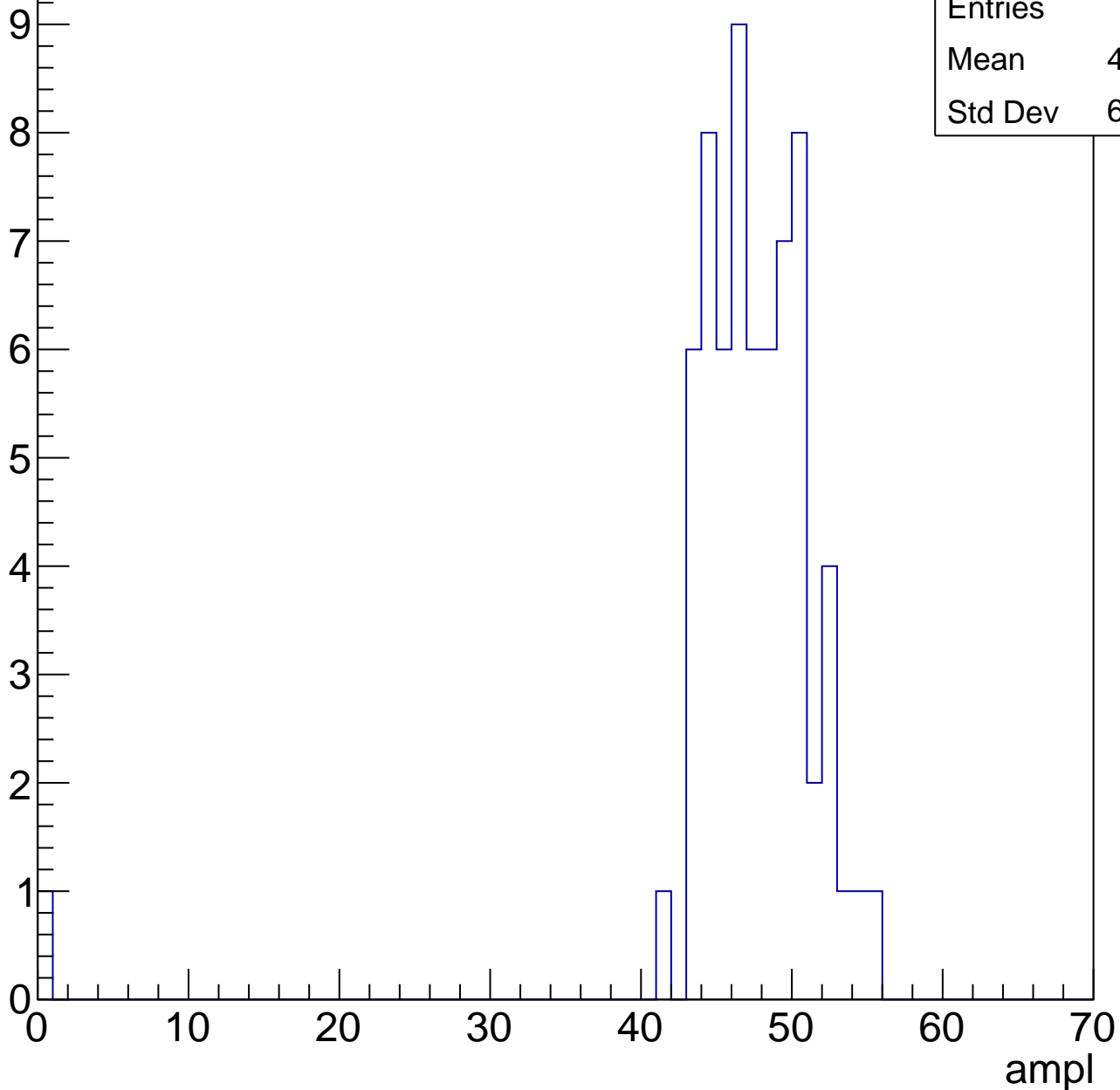


# B1L002S, U17-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	46.57
Std Dev	6.488

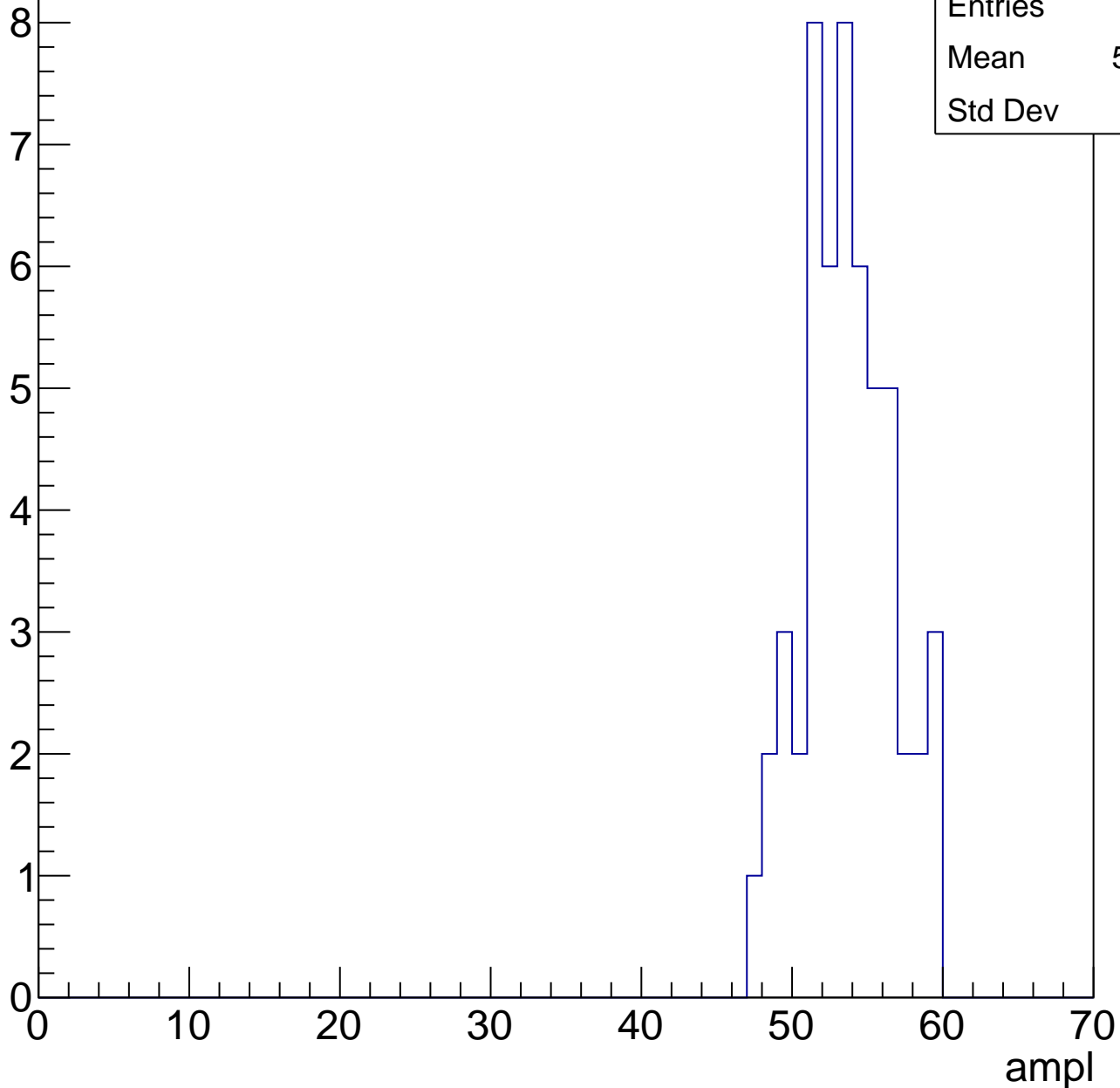


# B1L002S, U17-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

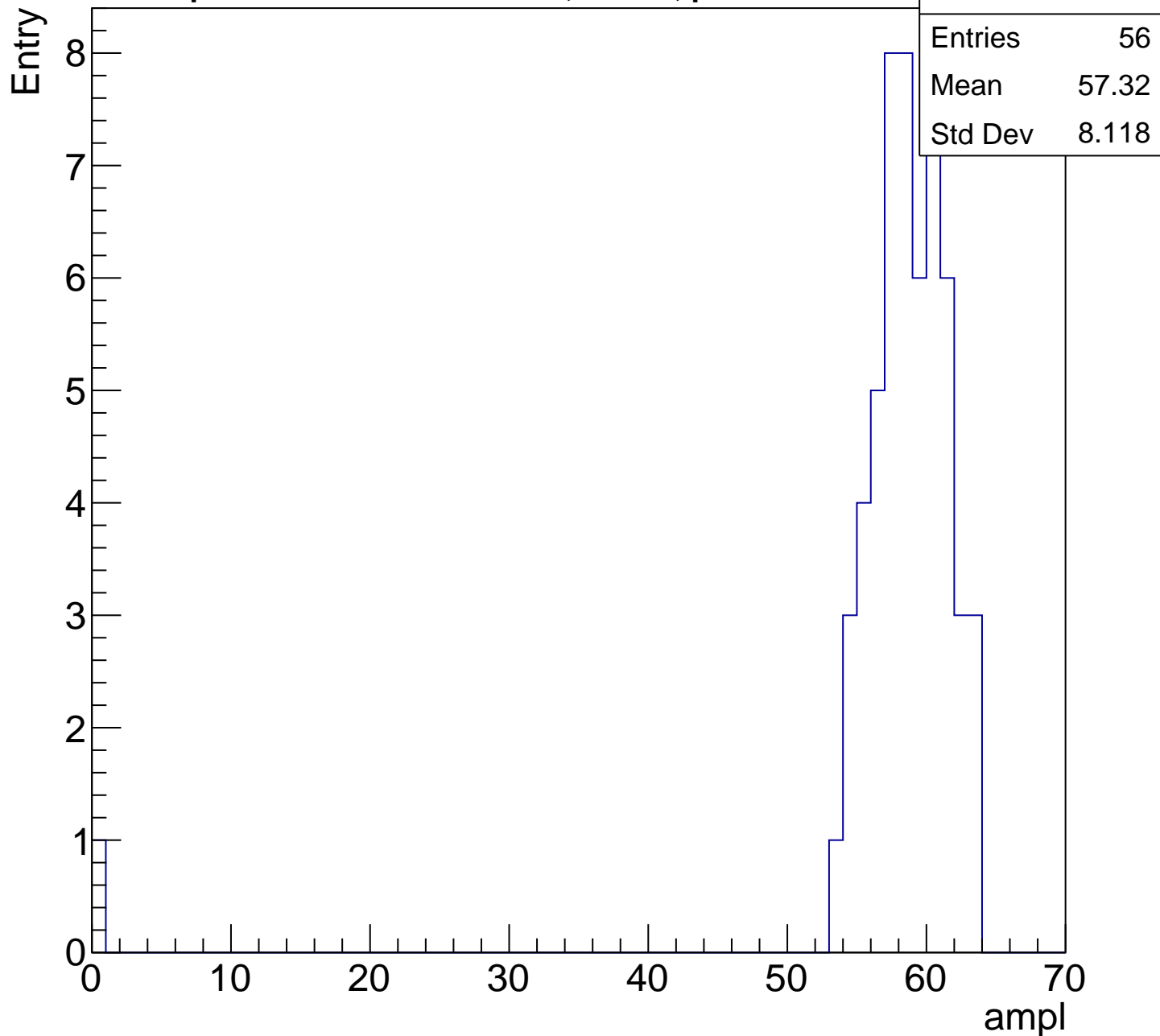
Entry

Entries	53
Mean	53.21
Std Dev	2.91



# B1L002S, U17-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

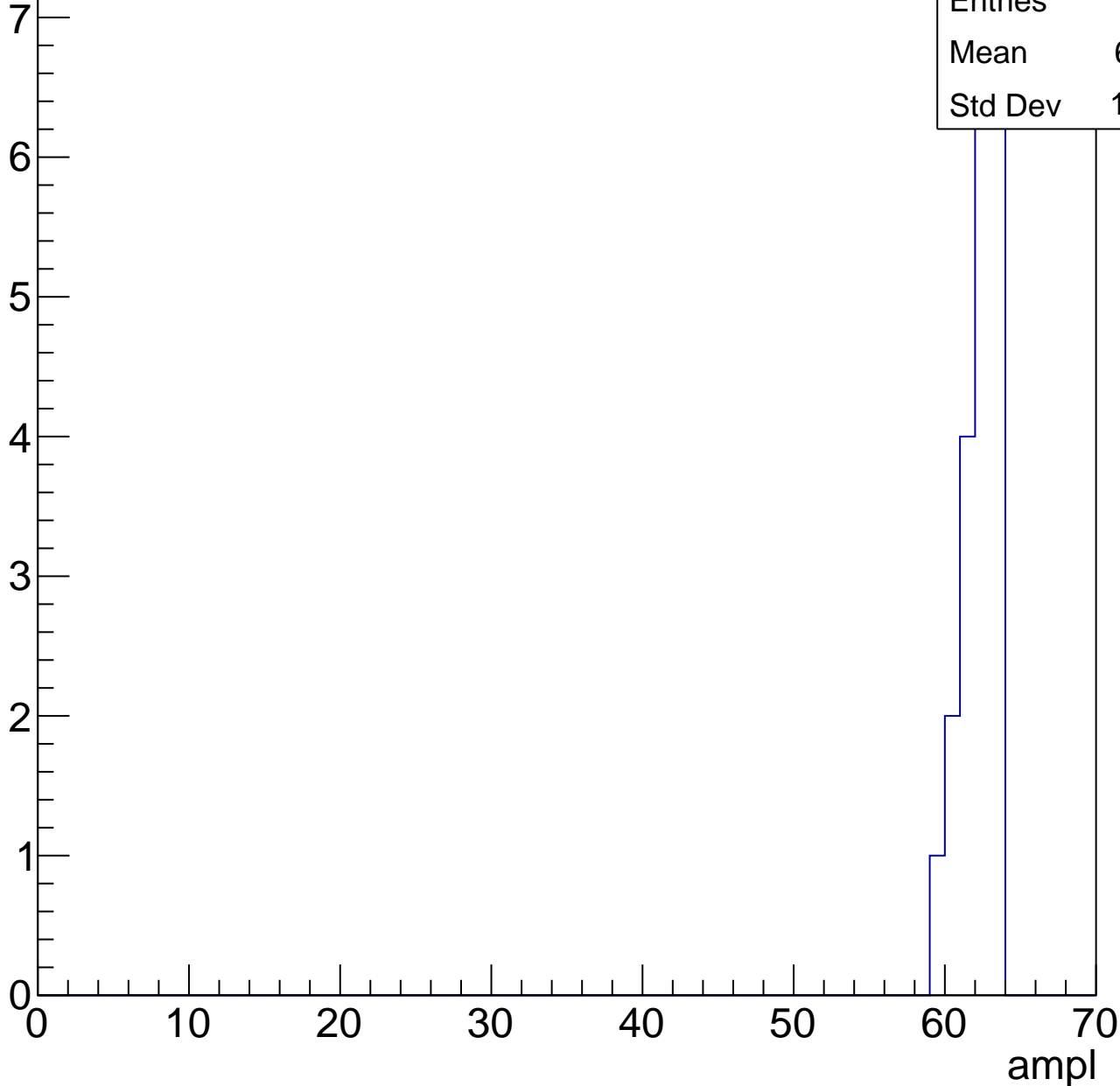


# B1L002S, U17-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.81
Std Dev	1.139





# B1L002S, U17-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U17-ch100, adc0

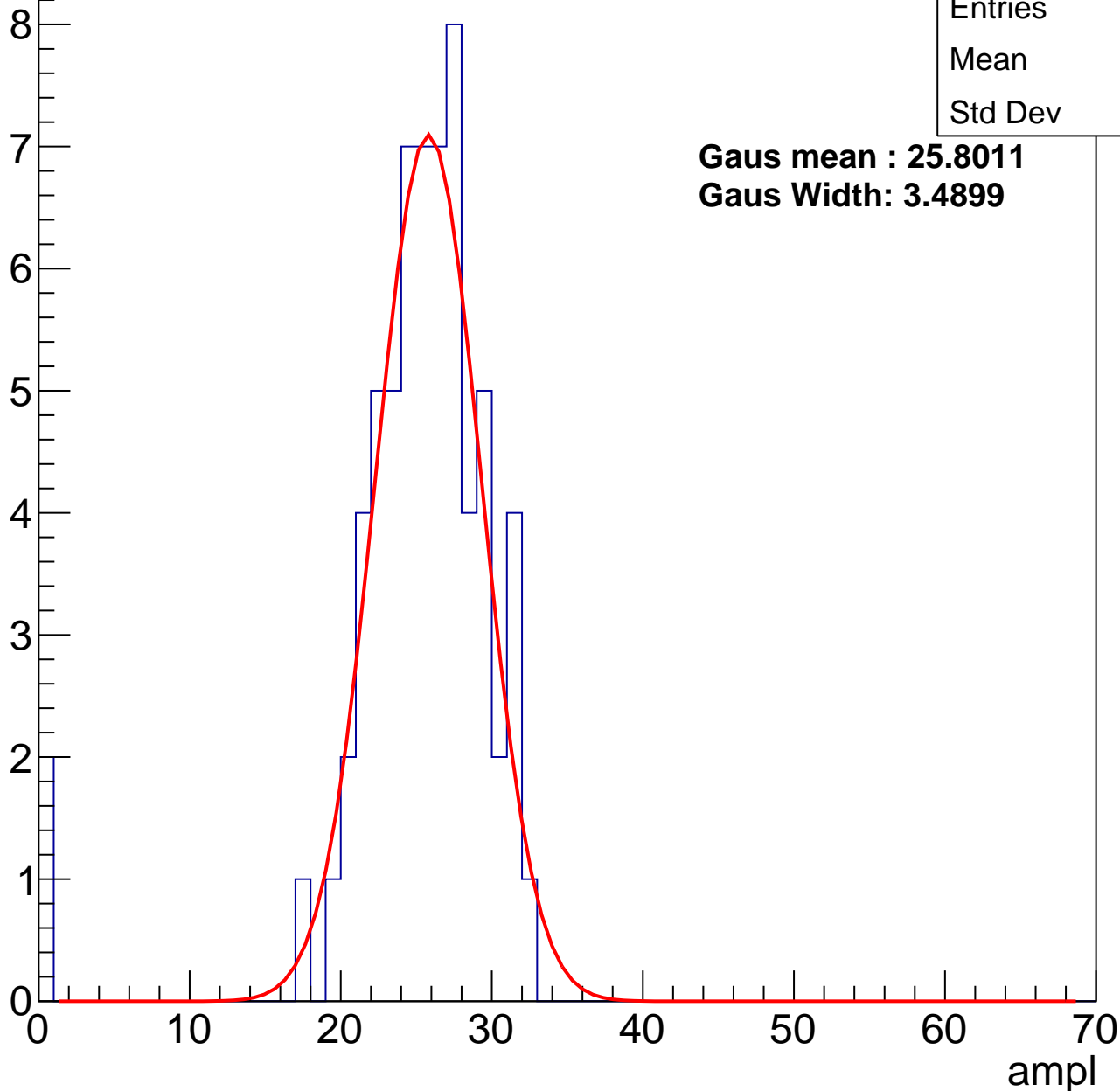
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	24.6
Std Dev	5.44

**Gaus mean : 25.8011**

**Gaus Width: 3.4899**



# B1L002S, U17-ch100, adc1

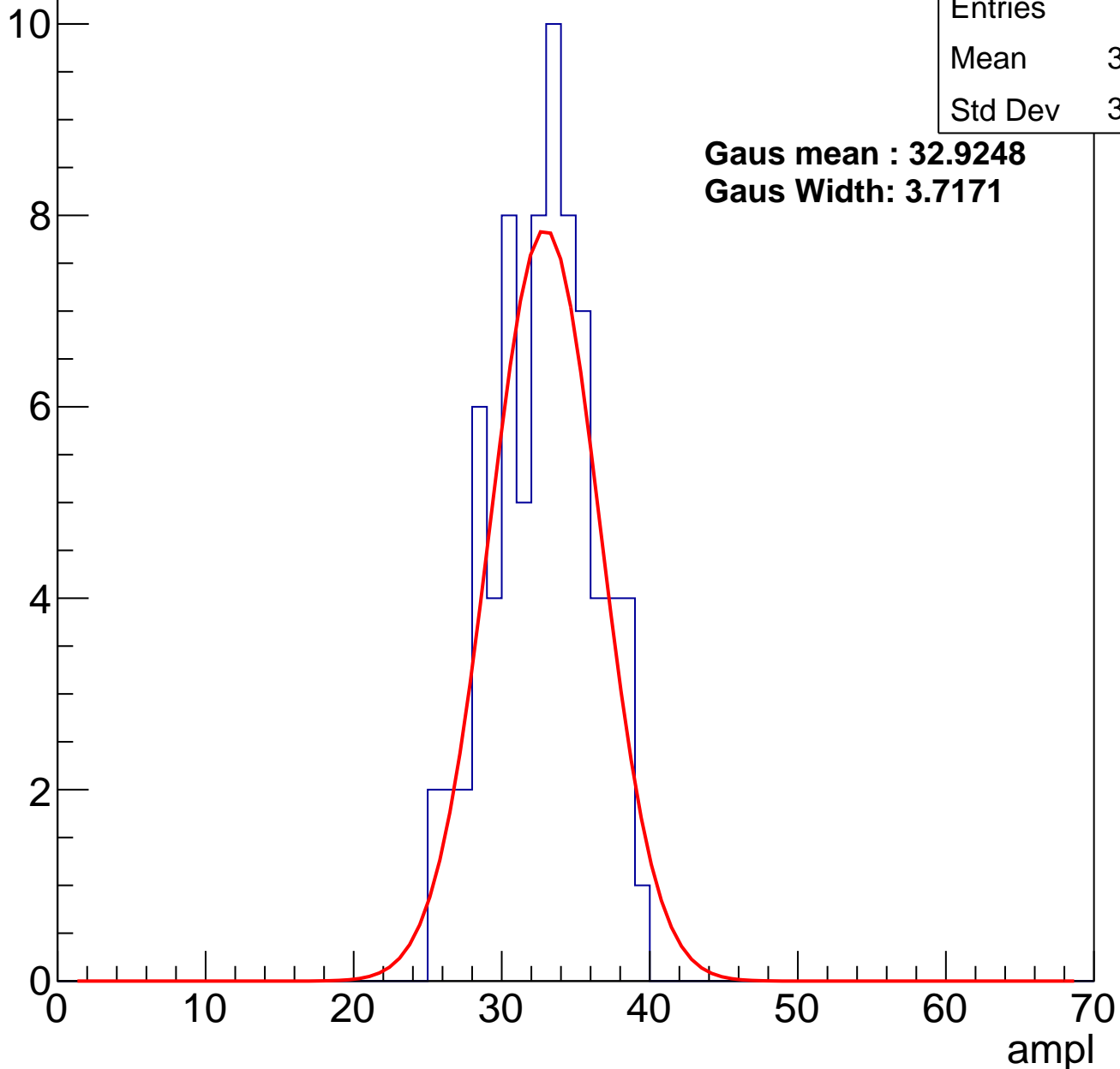
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	32.28
Std Dev	3.357

**Gaus mean : 32.9248**

**Gaus Width: 3.7171**

Entry



# B1L002S, U17-ch100, adc2

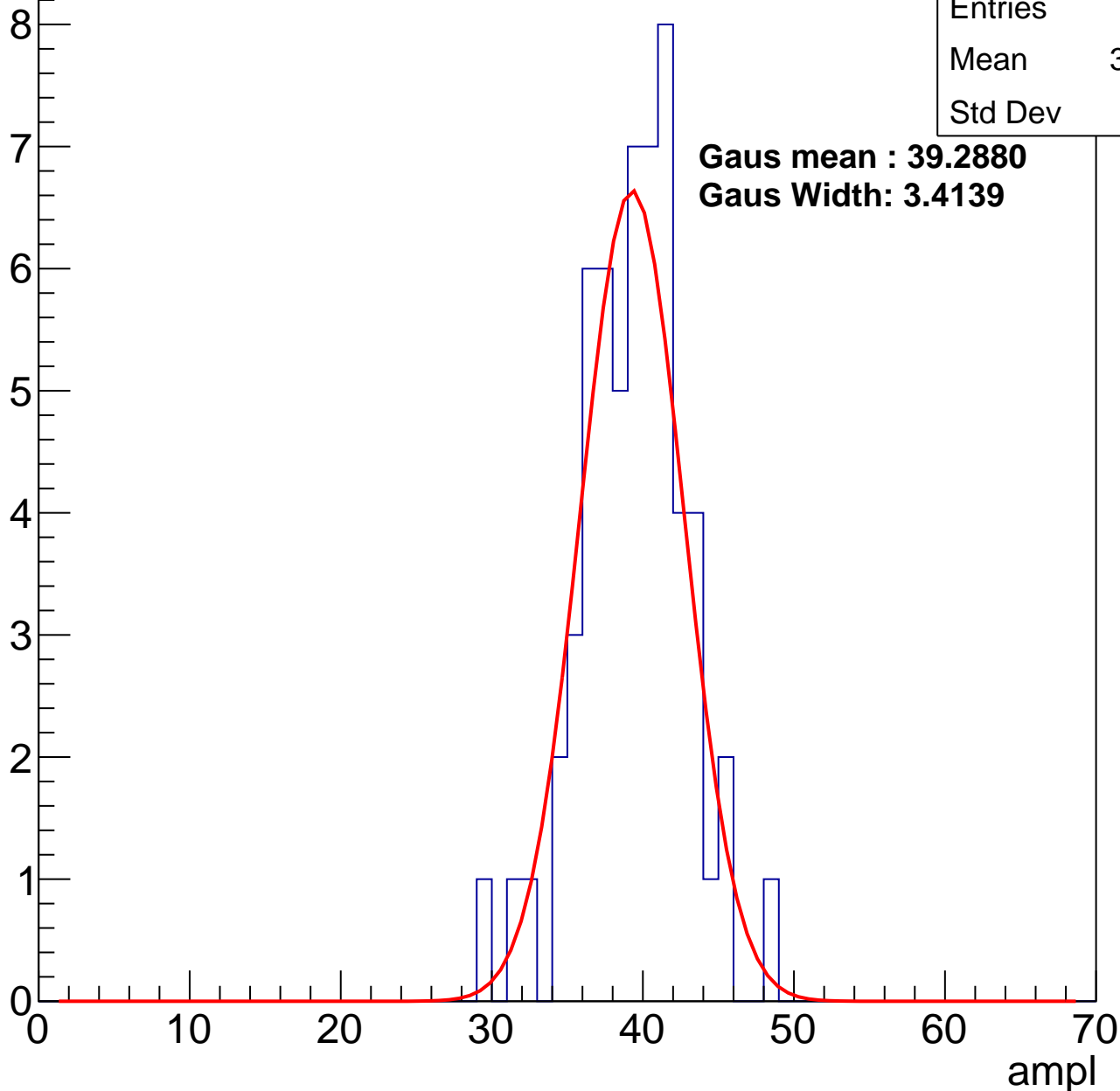
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	38.92
Std Dev	3.47

**Gaus mean : 39.2880**

**Gaus Width: 3.4139**

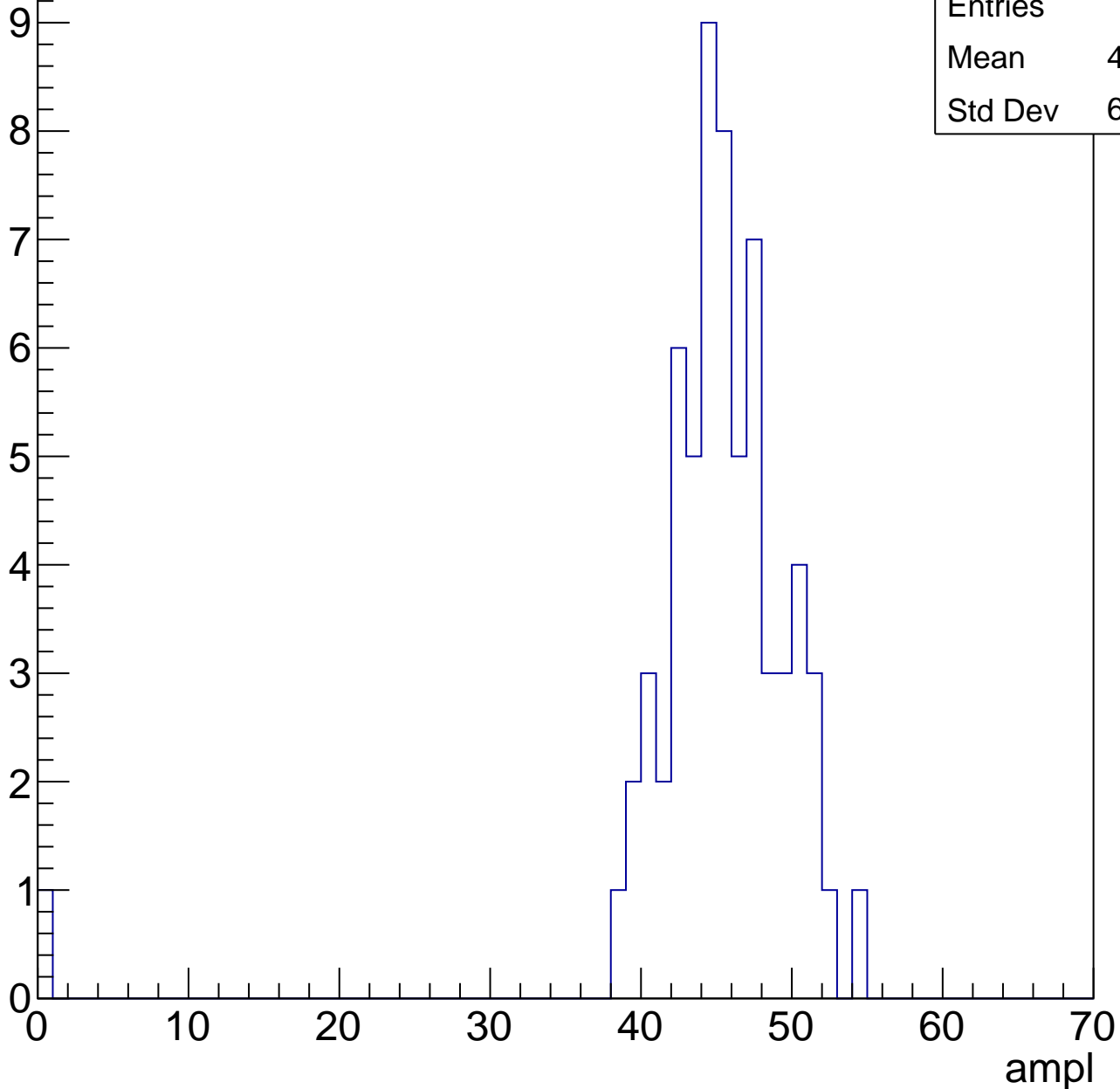


# B1L002S, U17-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	44.53
Std Dev	6.576

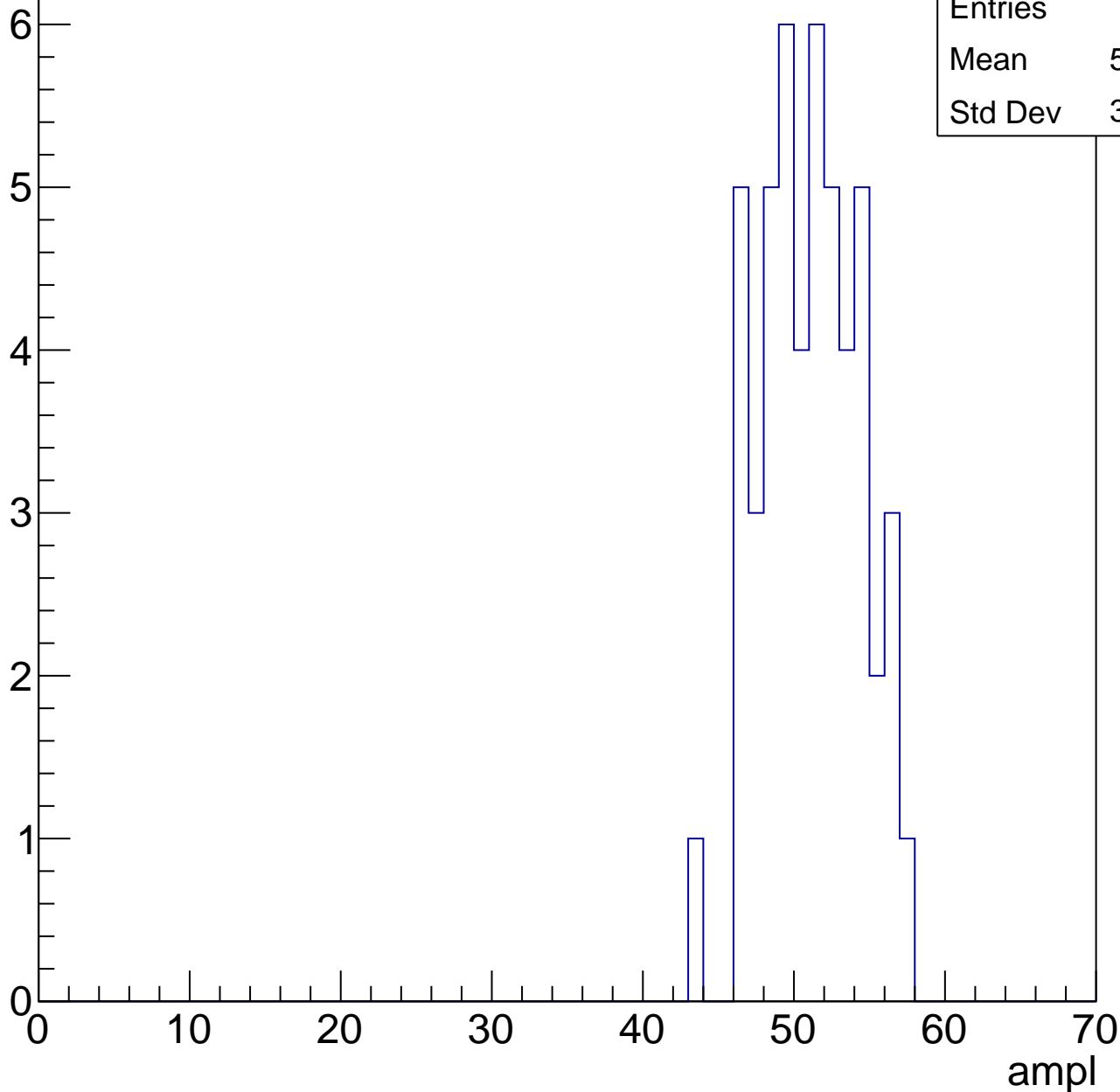


# B1L002S, U17-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	50.62
Std Dev	3.199

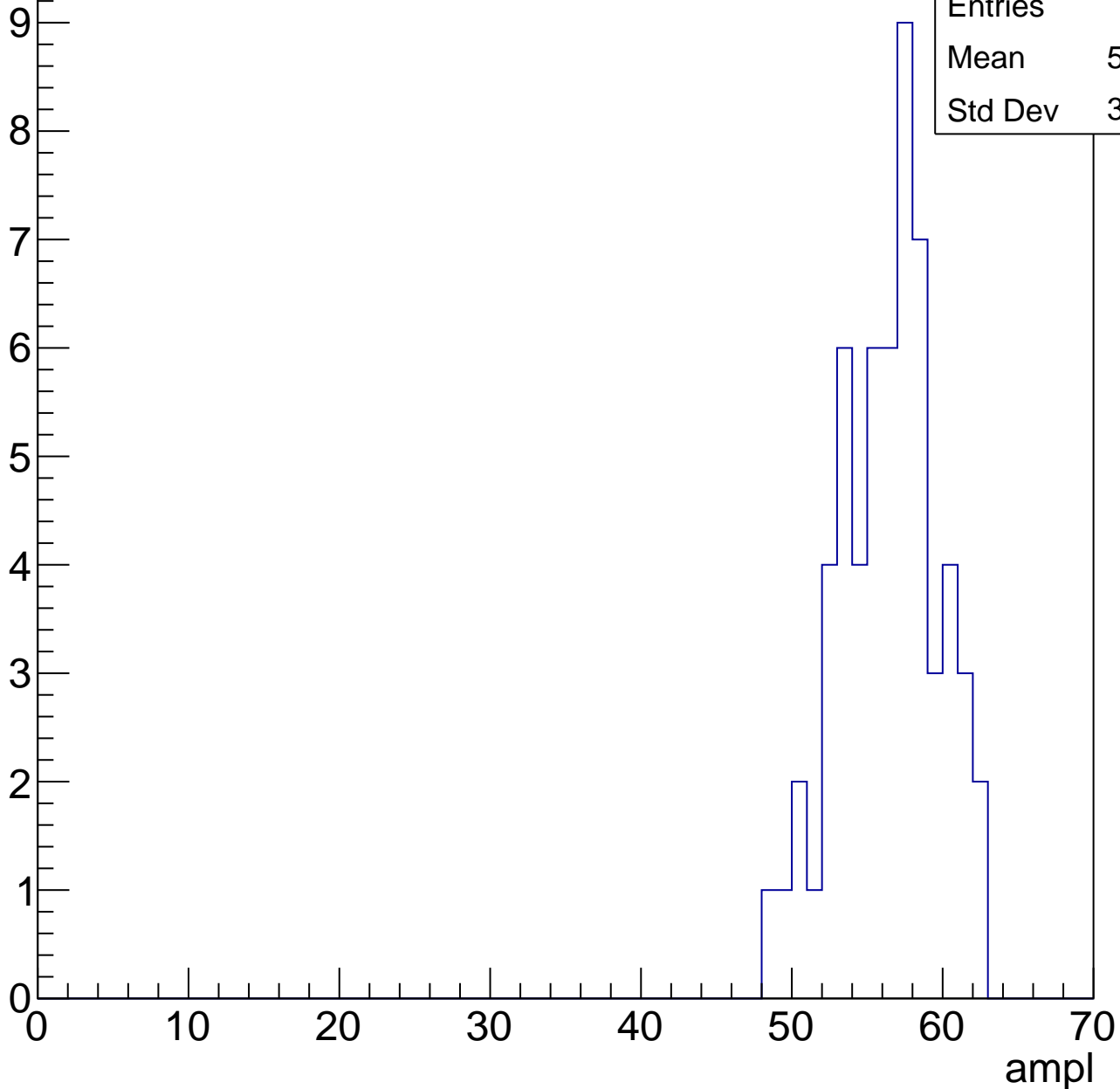


# B1L002S, U17-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	55.92
Std Dev	3.254

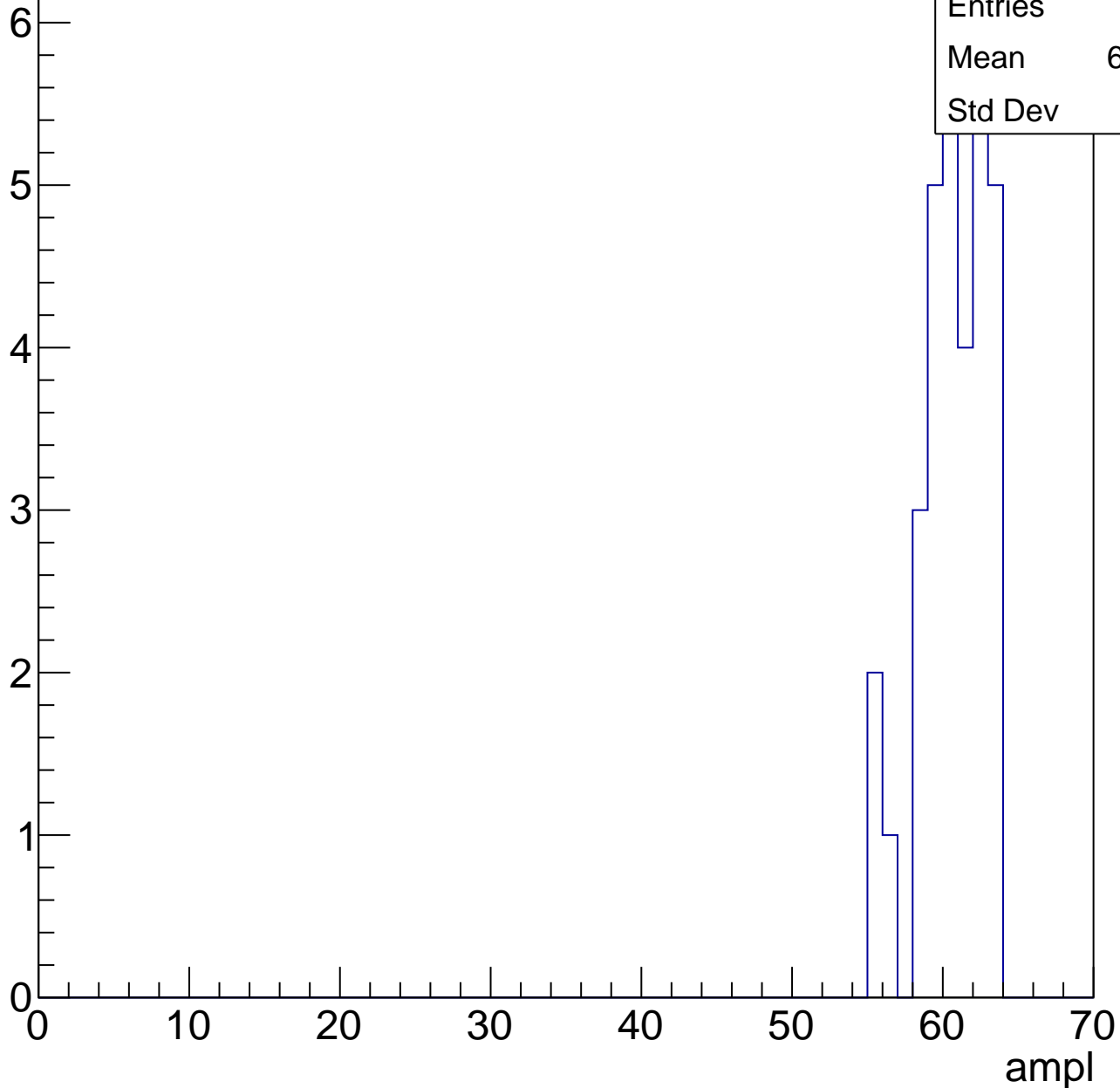


# B1L002S, U17-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	60.19
Std Dev	2.2



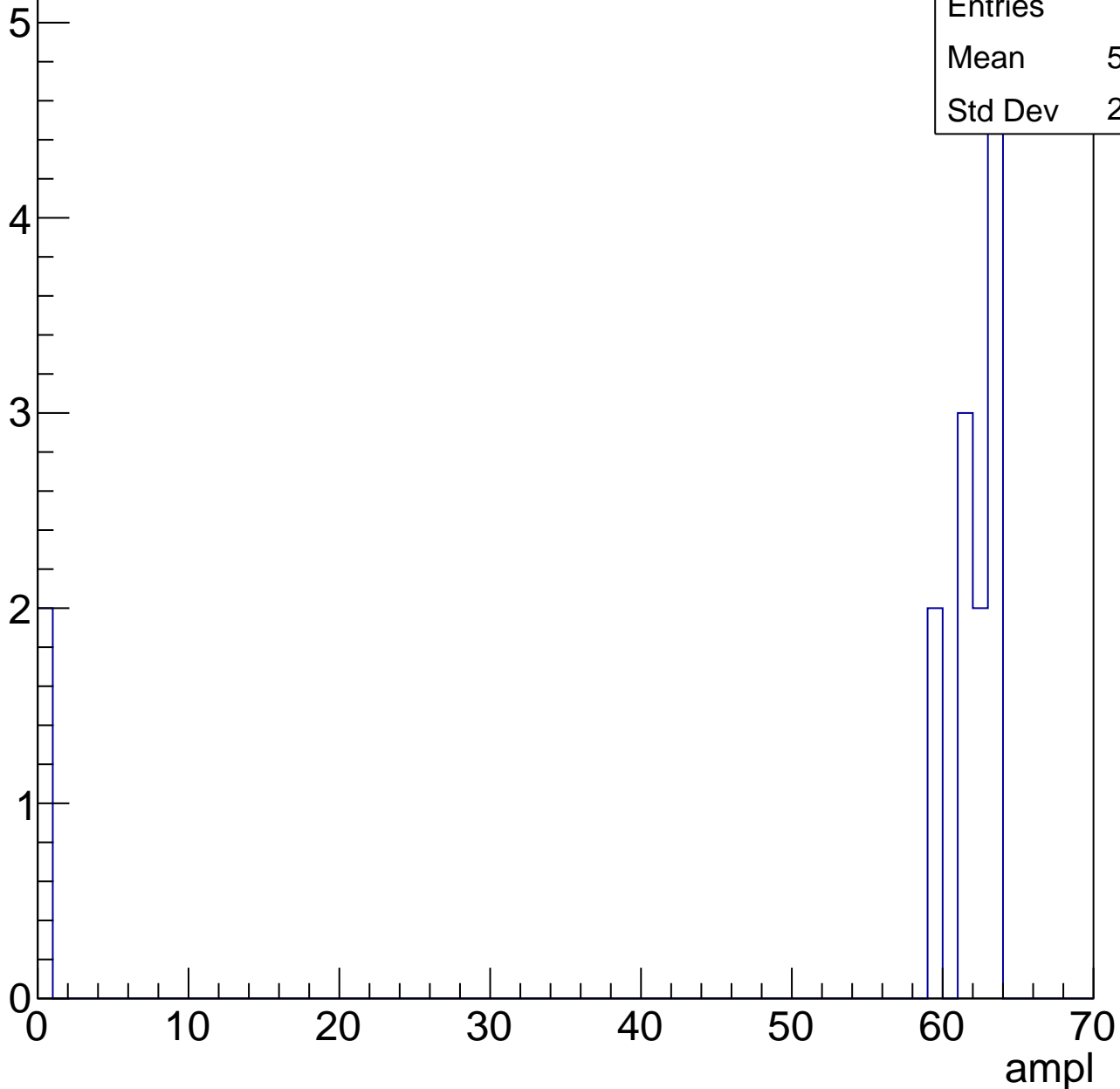


# B1L002S, U17-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	52.86
Std Dev	21.62



# B1L002S, U17-ch101, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	27.04
Std Dev	3.431

**Gaus mean : 28.4104**

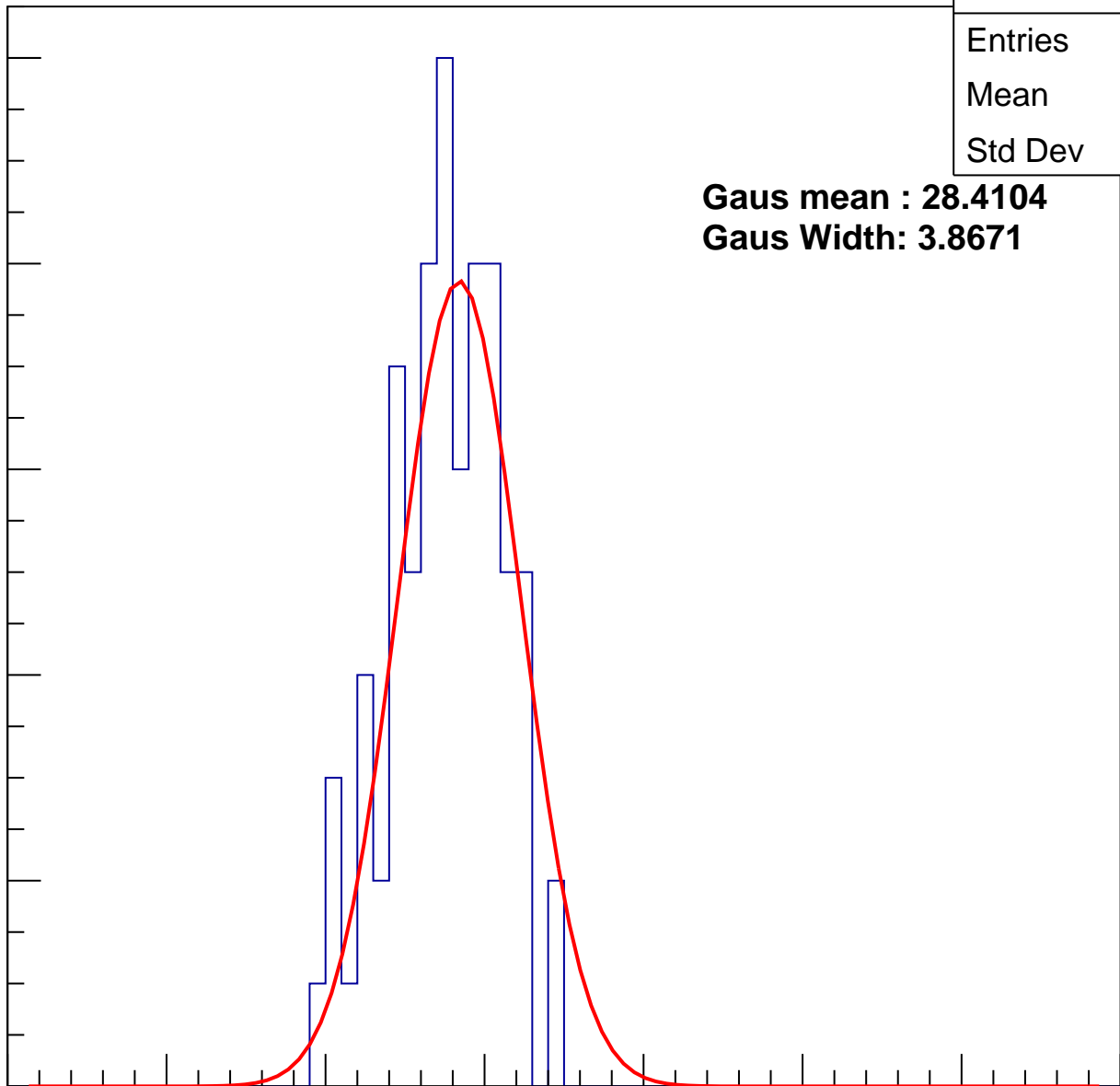
**Gaus Width: 3.8671**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch101, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	34.72
Std Dev	2.993

**Gaus mean : 35.4368**

**Gaus Width: 3.2933**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B1L002S, U17-ch101, adc2

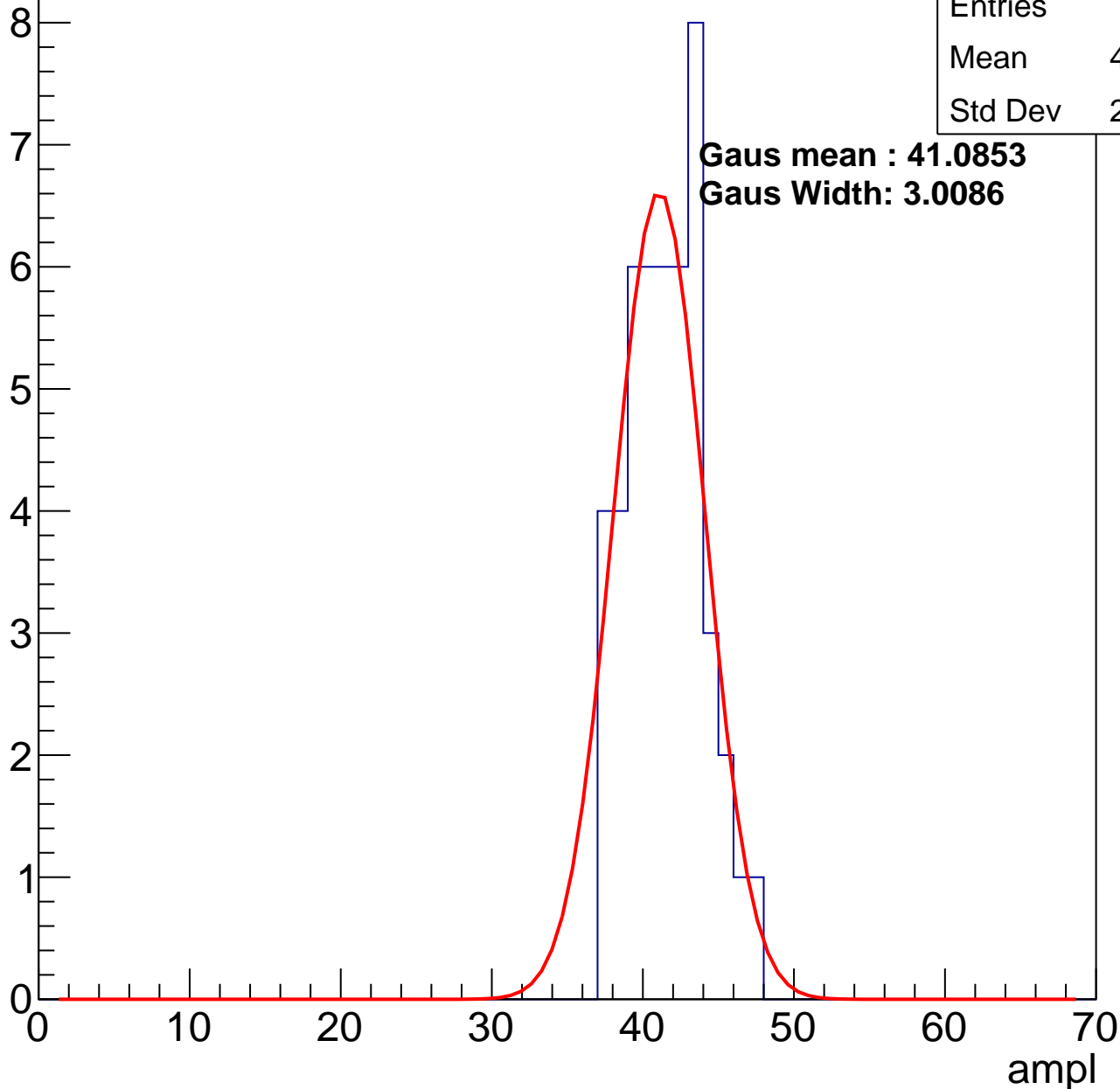
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	41.09
Std Dev	2.474

**Gaus mean : 41.0853**

**Gaus Width: 3.0086**

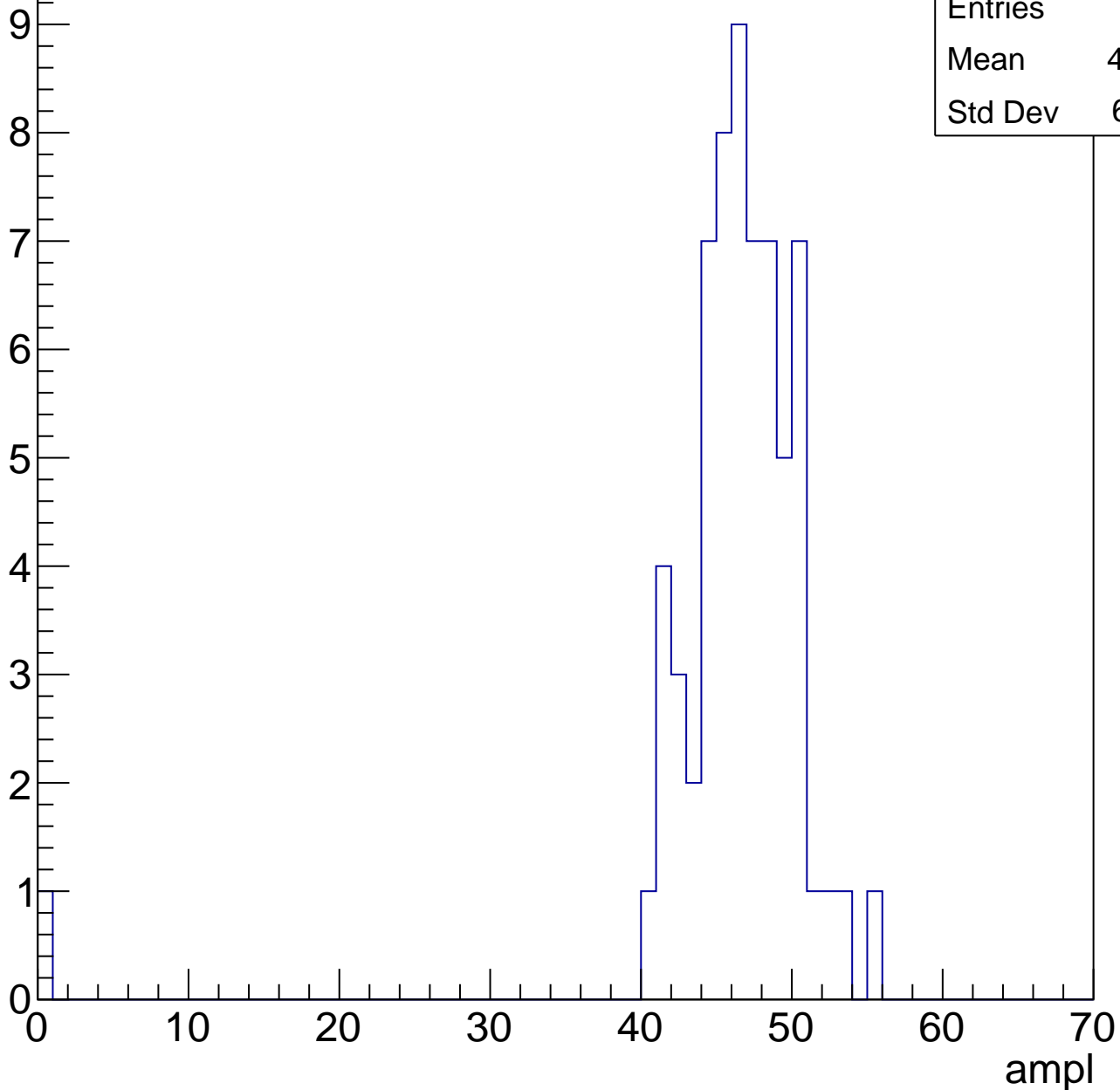


# B1L002S, U17-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	45.68
Std Dev	6.481

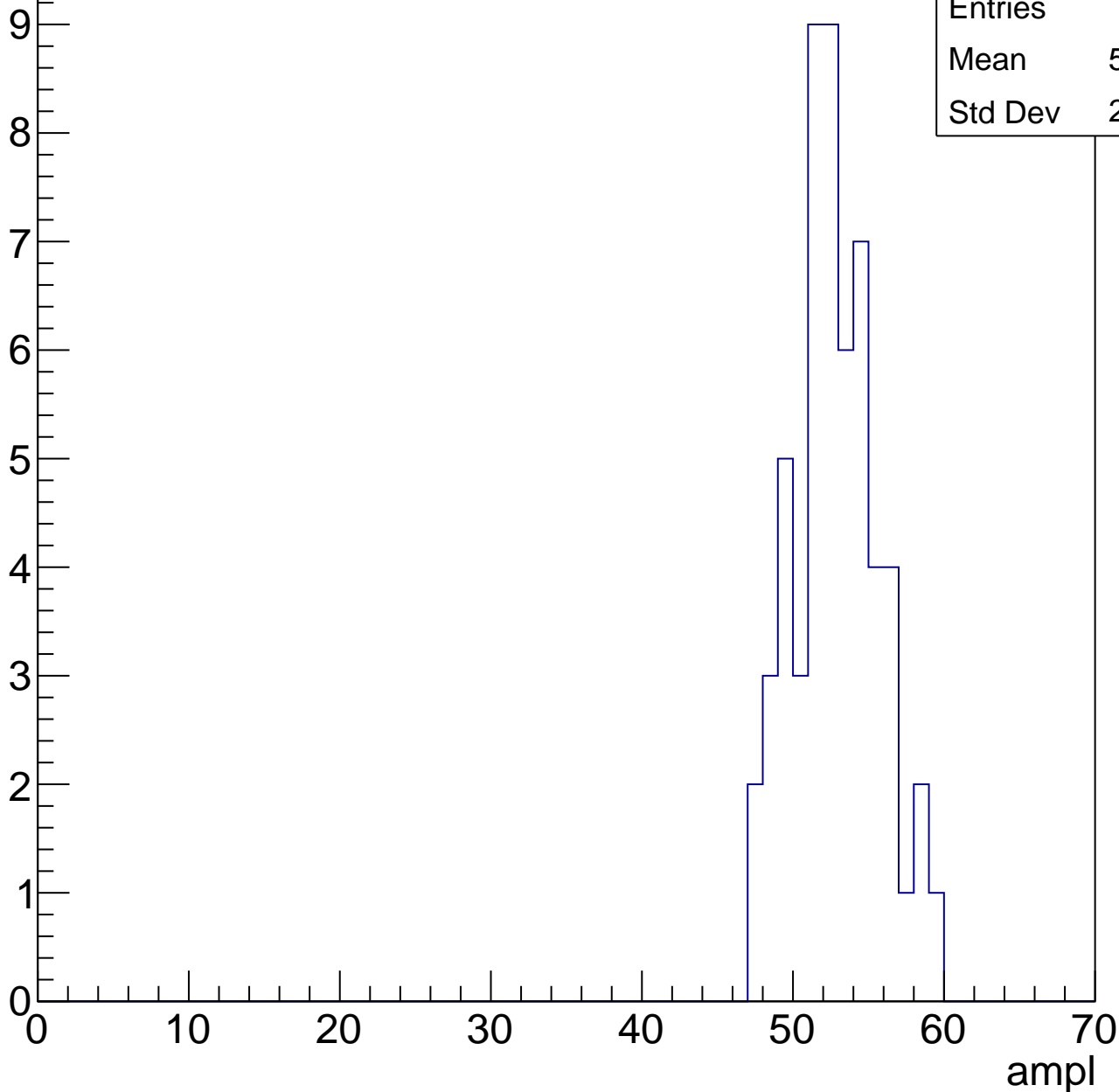


# B1L002S, U17-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

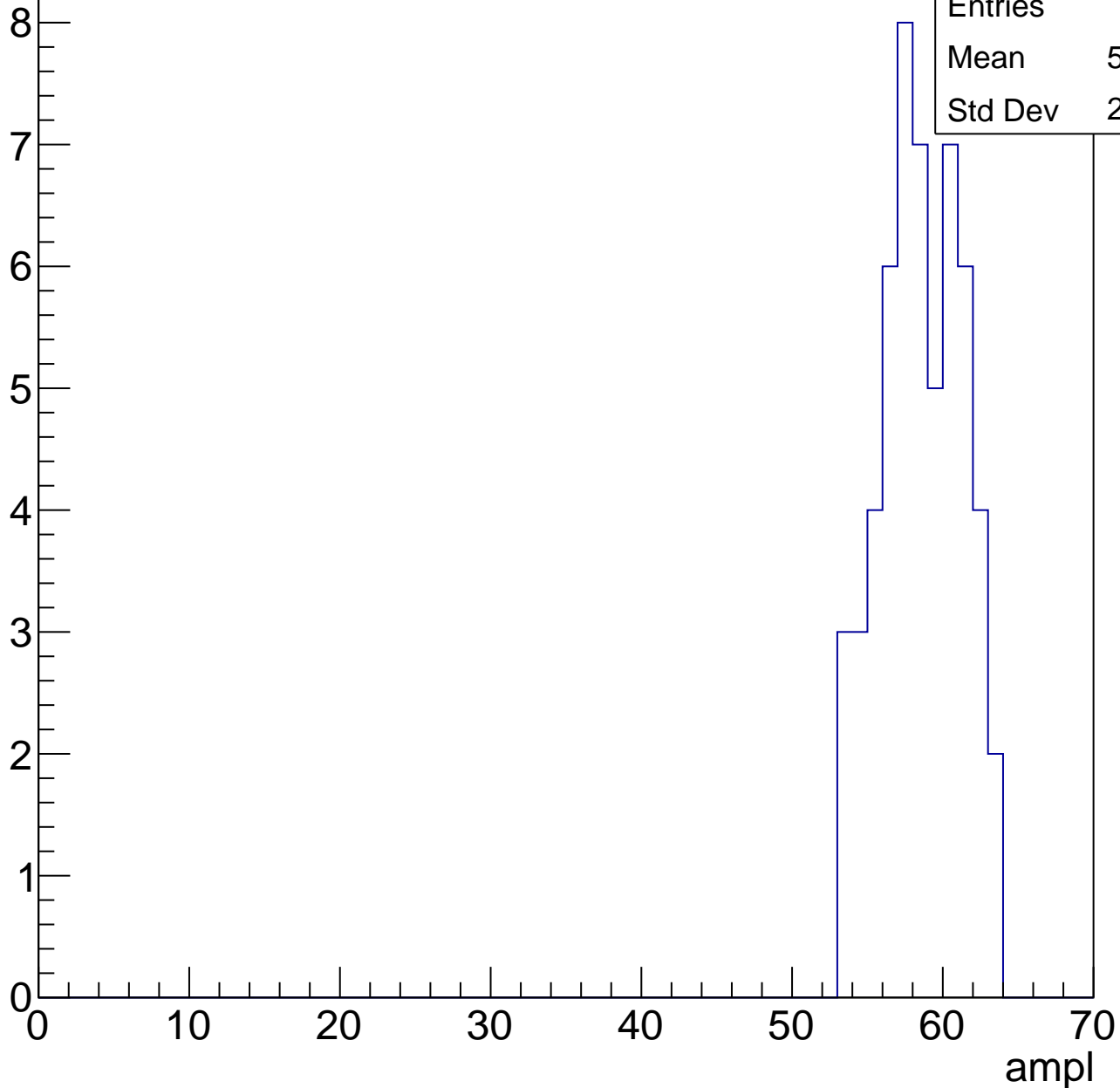
Entries	56
Mean	52.36
Std Dev	2.793



# B1L002S, U17-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



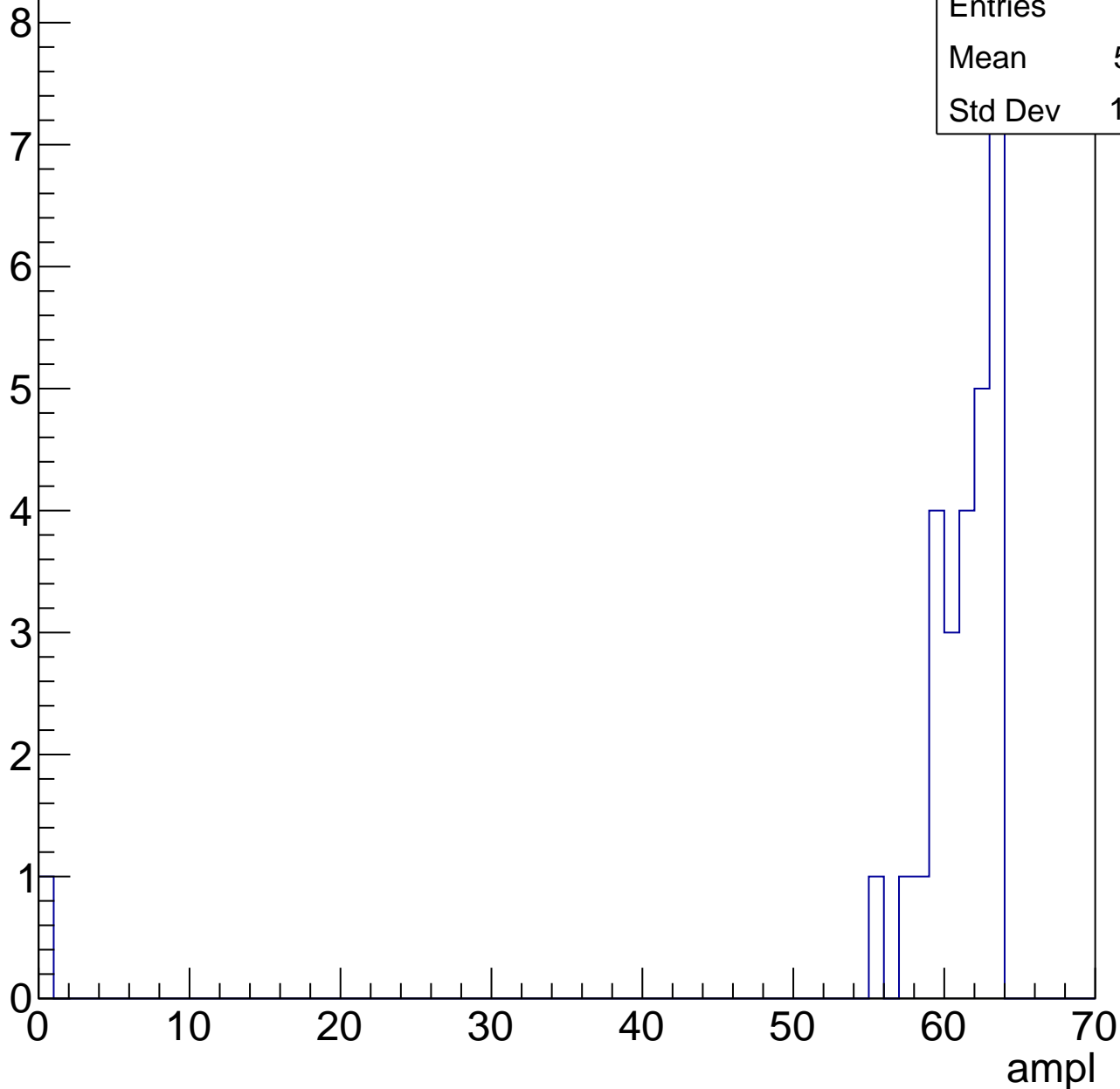
Entries	55
Mean	58.07
Std Dev	2.669

# B1L002S, U17-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.71
Std Dev	11.48





# B1L002S, U17-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch102, adc0

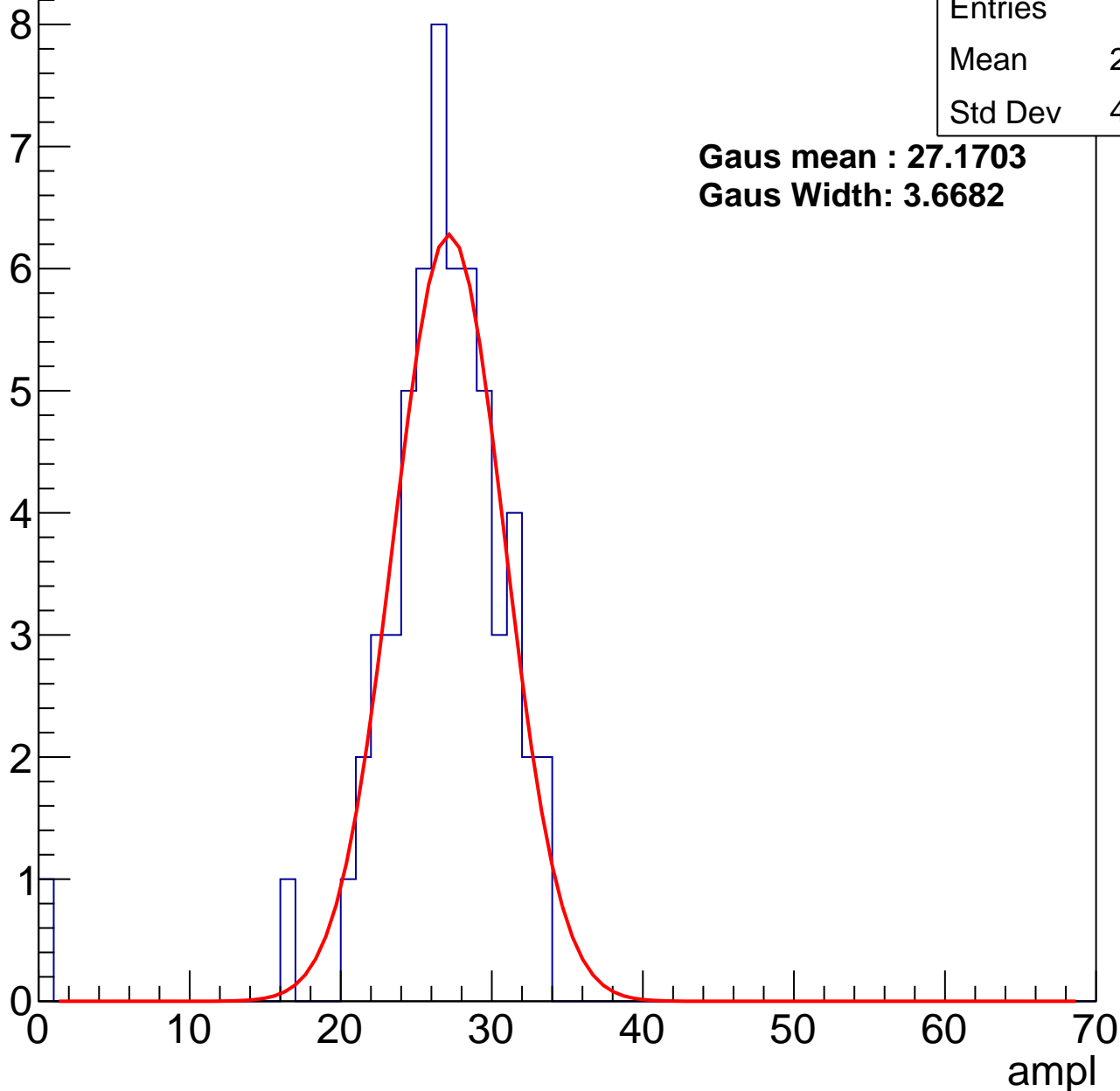
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	26.03
Std Dev	4.835

**Gaus mean : 27.1703**

**Gaus Width: 3.6682**



# B1L002S, U17-ch102, adc1

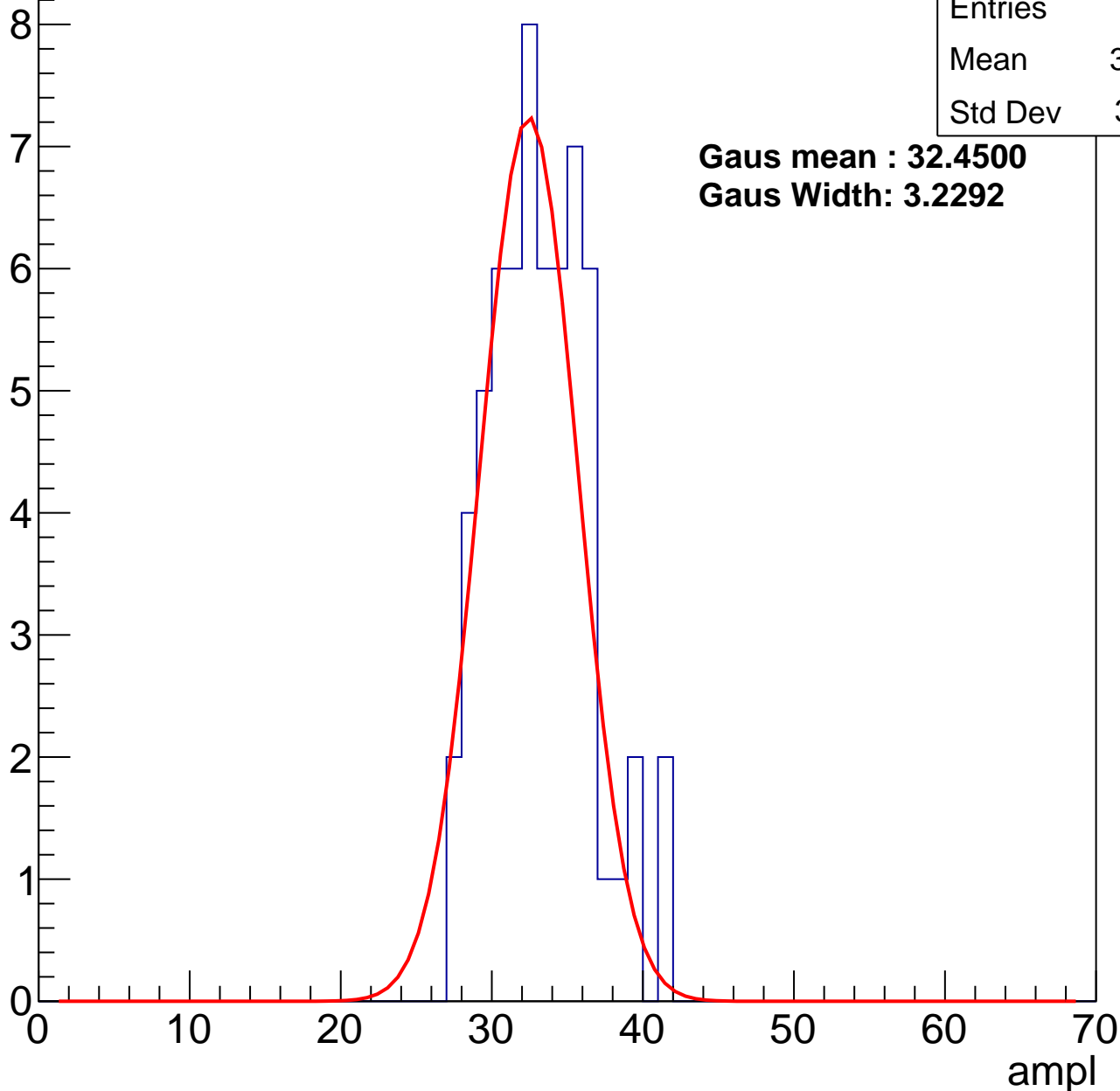
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	32.76
Std Dev	3.271

**Gaus mean : 32.4500**

**Gaus Width: 3.2292**



# B1L002S, U17-ch102, adc2

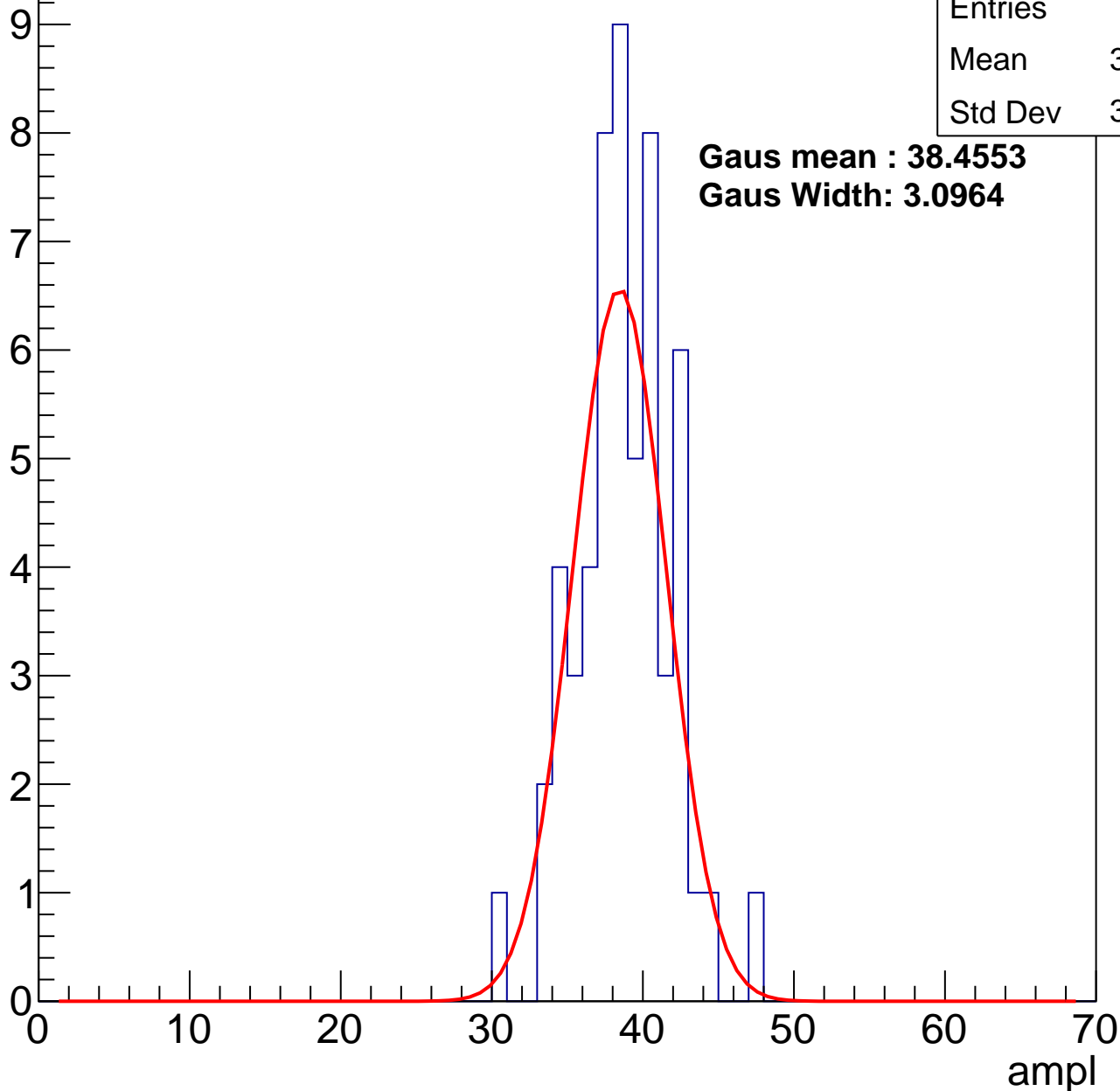
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	38.27
Std Dev	3.068

**Gaus mean : 38.4553**

**Gaus Width: 3.0964**

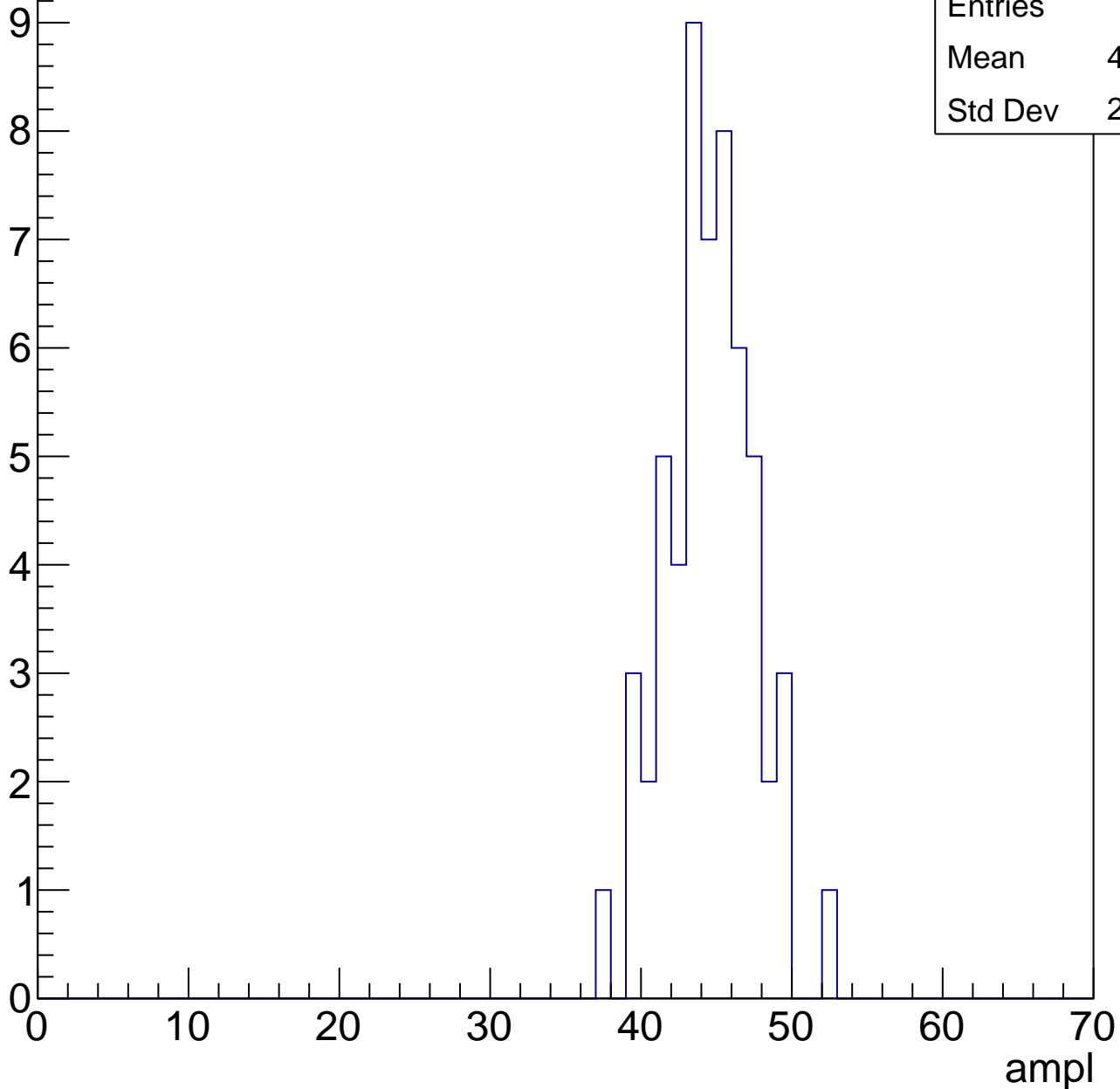


# B1L002S, U17-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	44.07
Std Dev	2.908

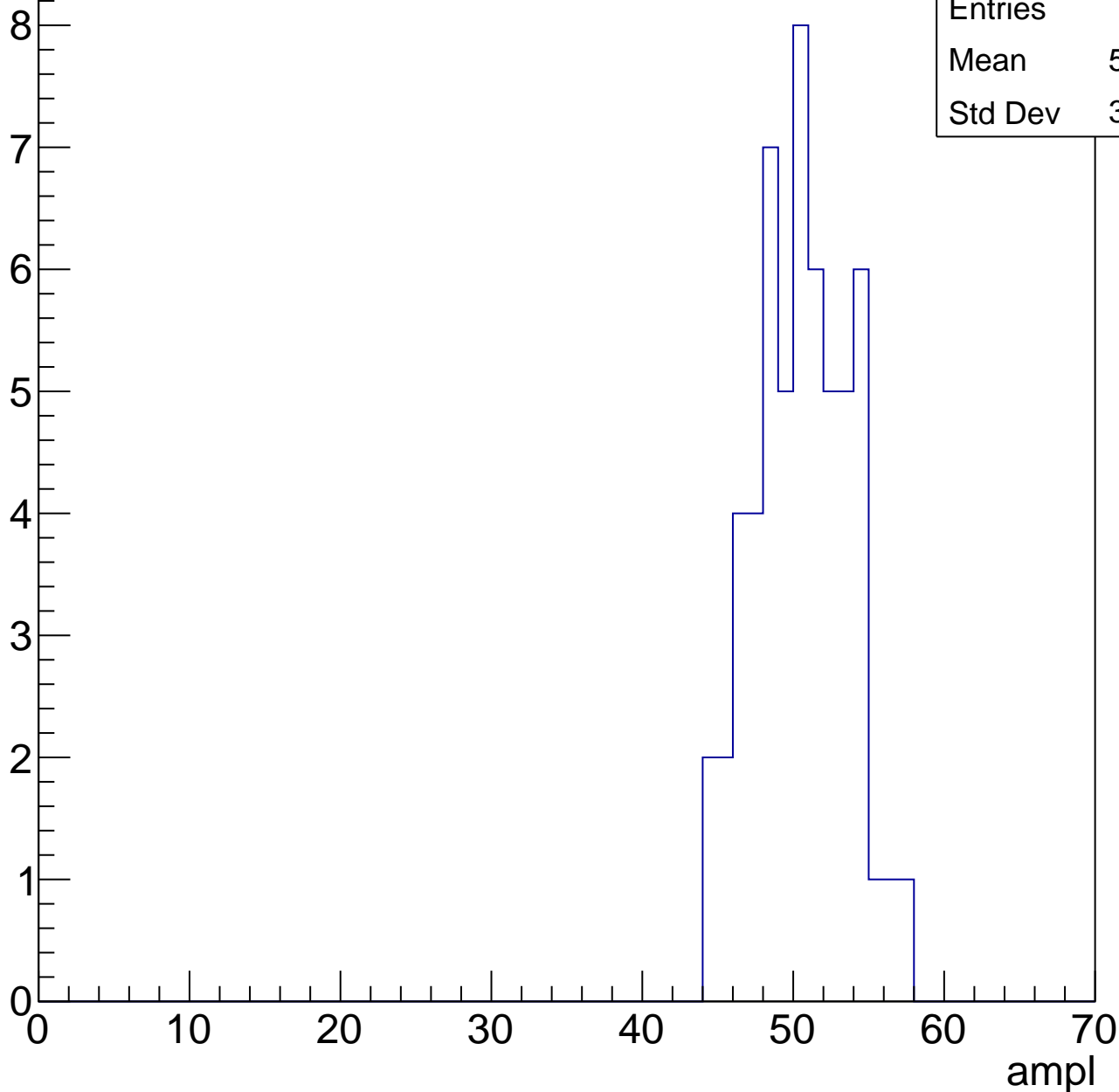


# B1L002S, U17-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	50.07
Std Dev	3.054

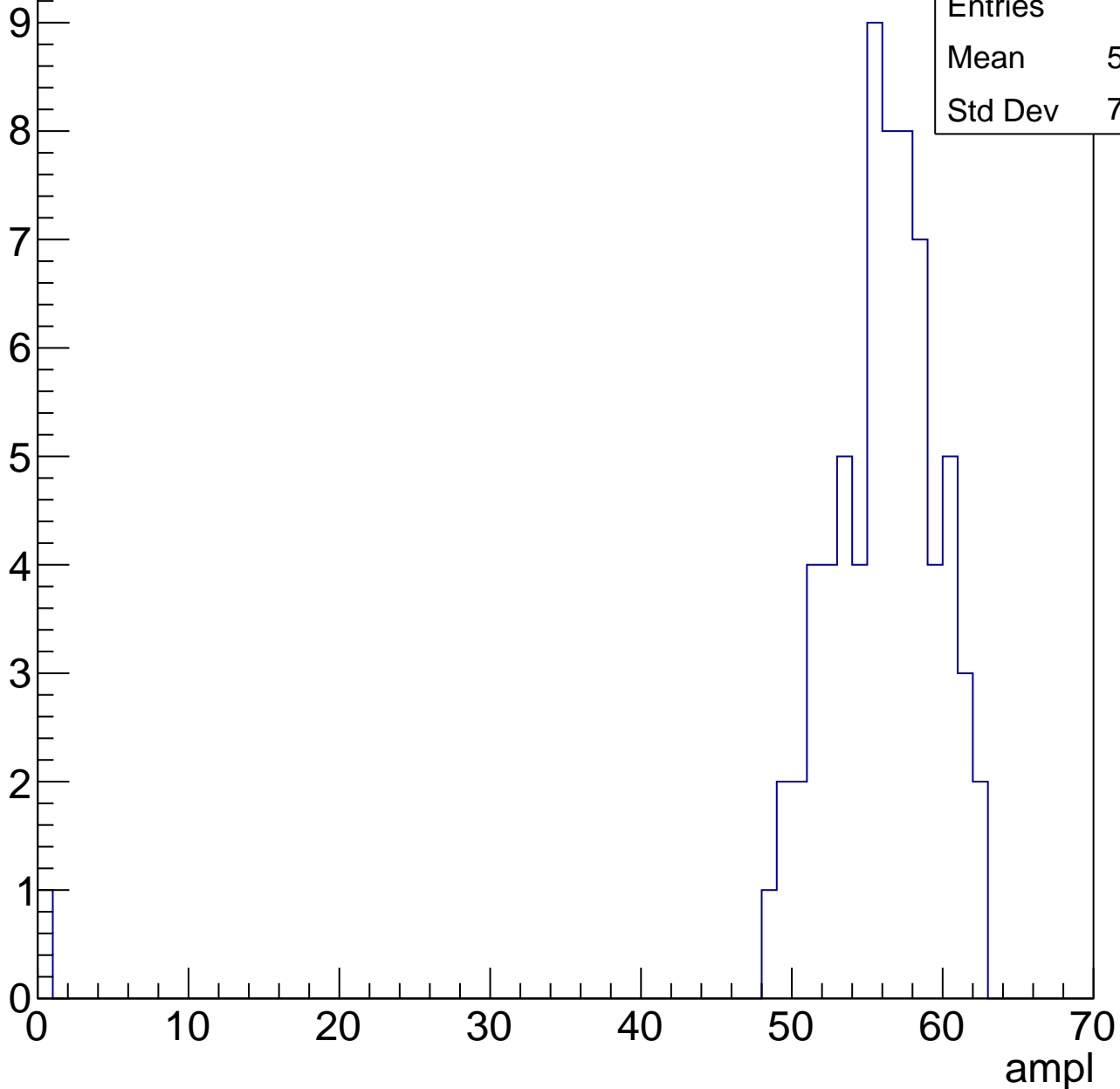


# B1L002S, U17-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	54.88
Std Dev	7.438

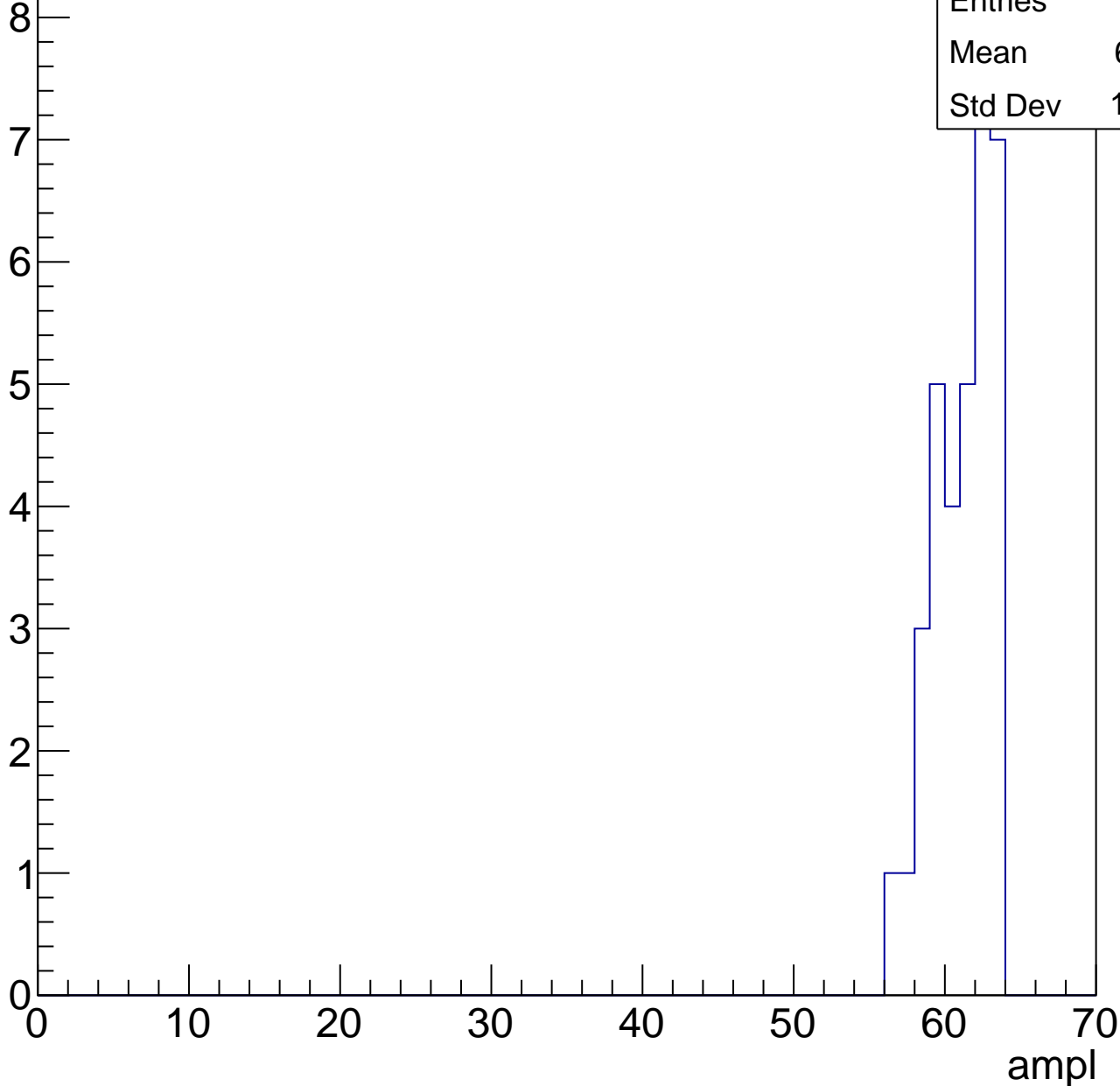


# B1L002S, U17-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	60.71
Std Dev	1.918





# B1L002S, U17-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	47.78
Std Dev	25.57

# B1L002S, U17-ch103, adc0

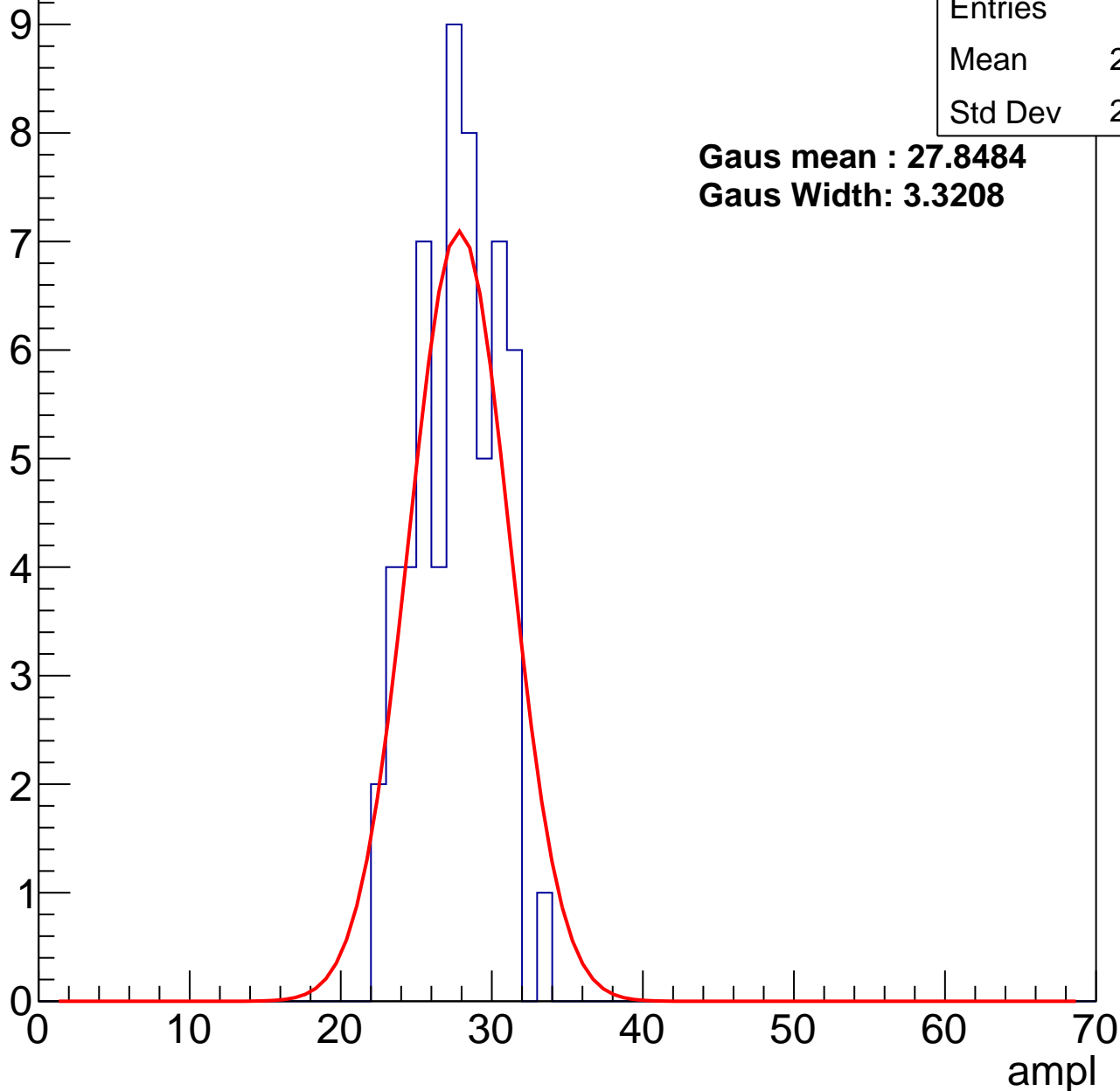
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	27.23
Std Dev	2.656

**Gaus mean : 27.8484**

**Gaus Width: 3.3208**



# B1L002S, U17-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	34.51
Std Dev	3.332

**Gaus mean : 35.4163**

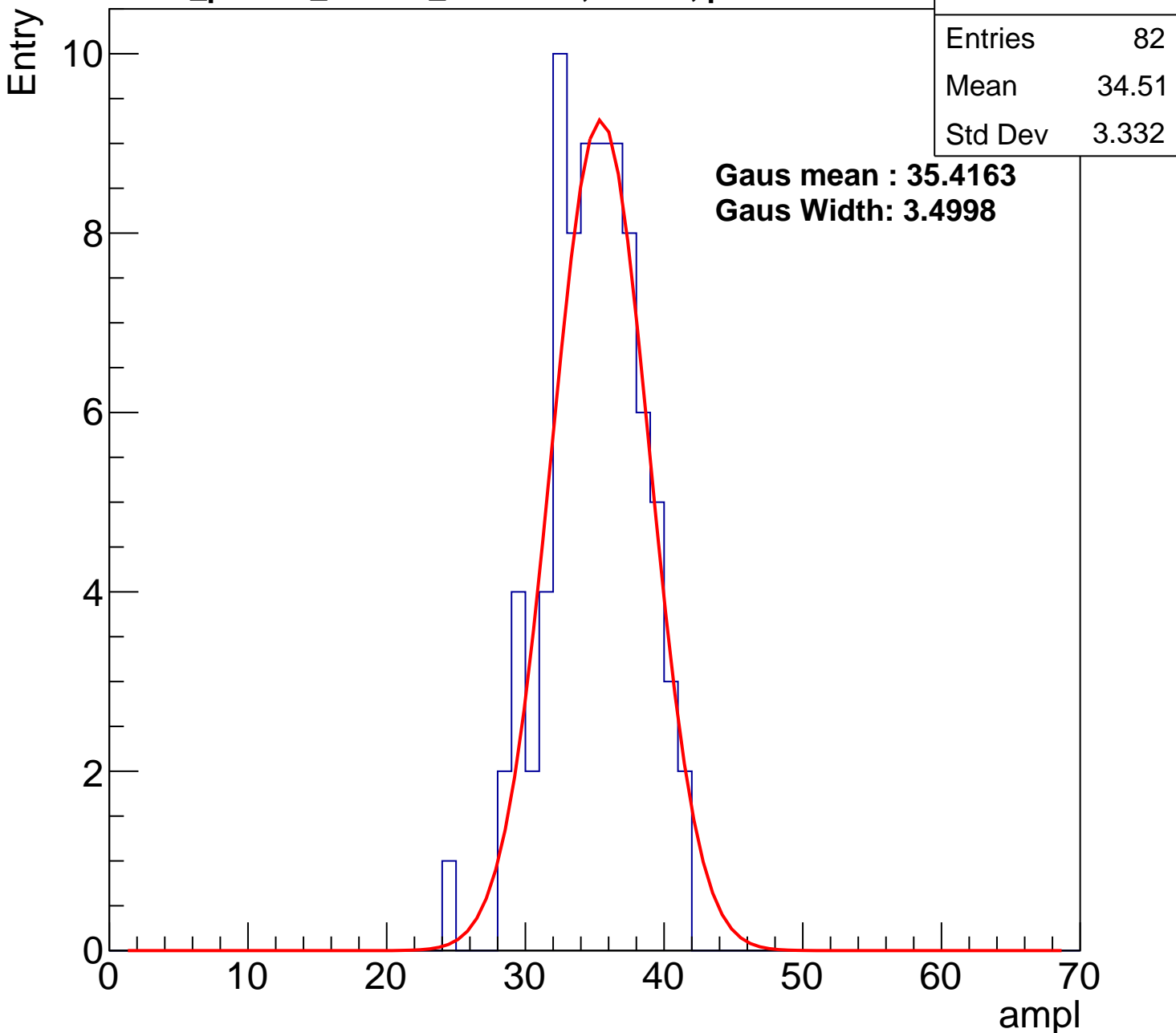
**Gaus Width: 3.4998**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch103, adc2

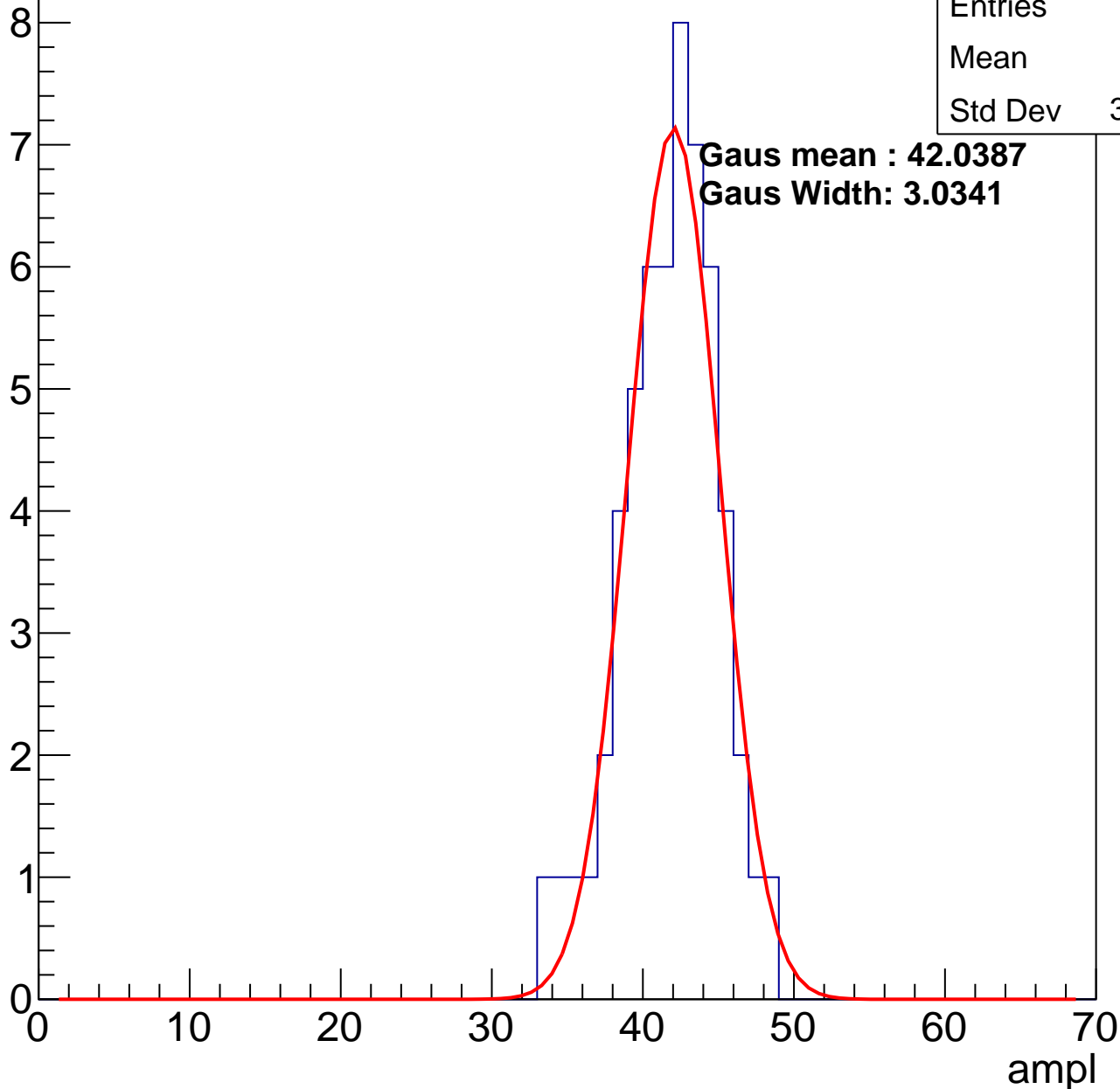
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.3
Std Dev	3.139

**Gaus mean : 42.0387**

**Gaus Width: 3.0341**

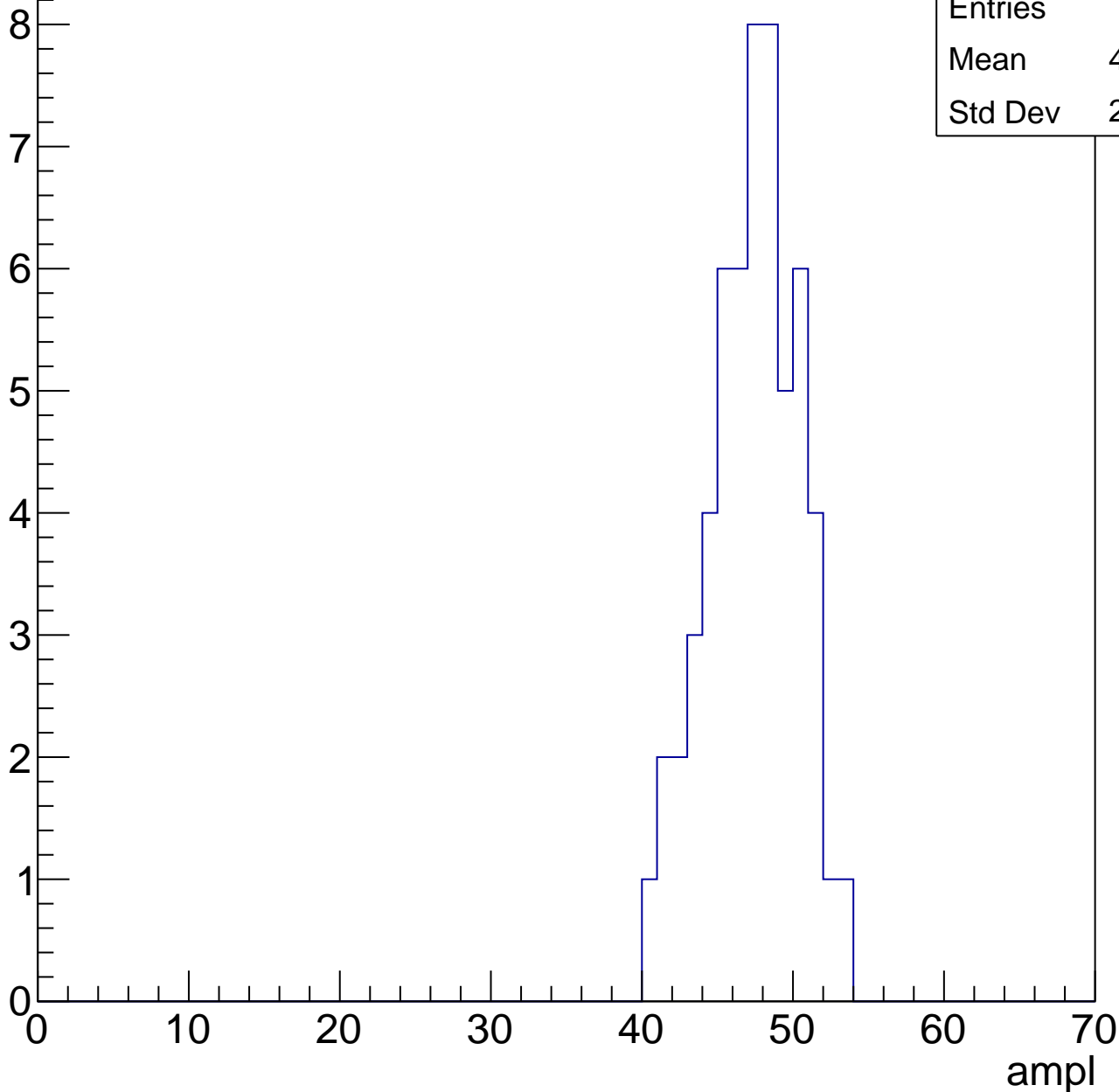


# B1L002S, U17-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.86
Std Dev	2.935

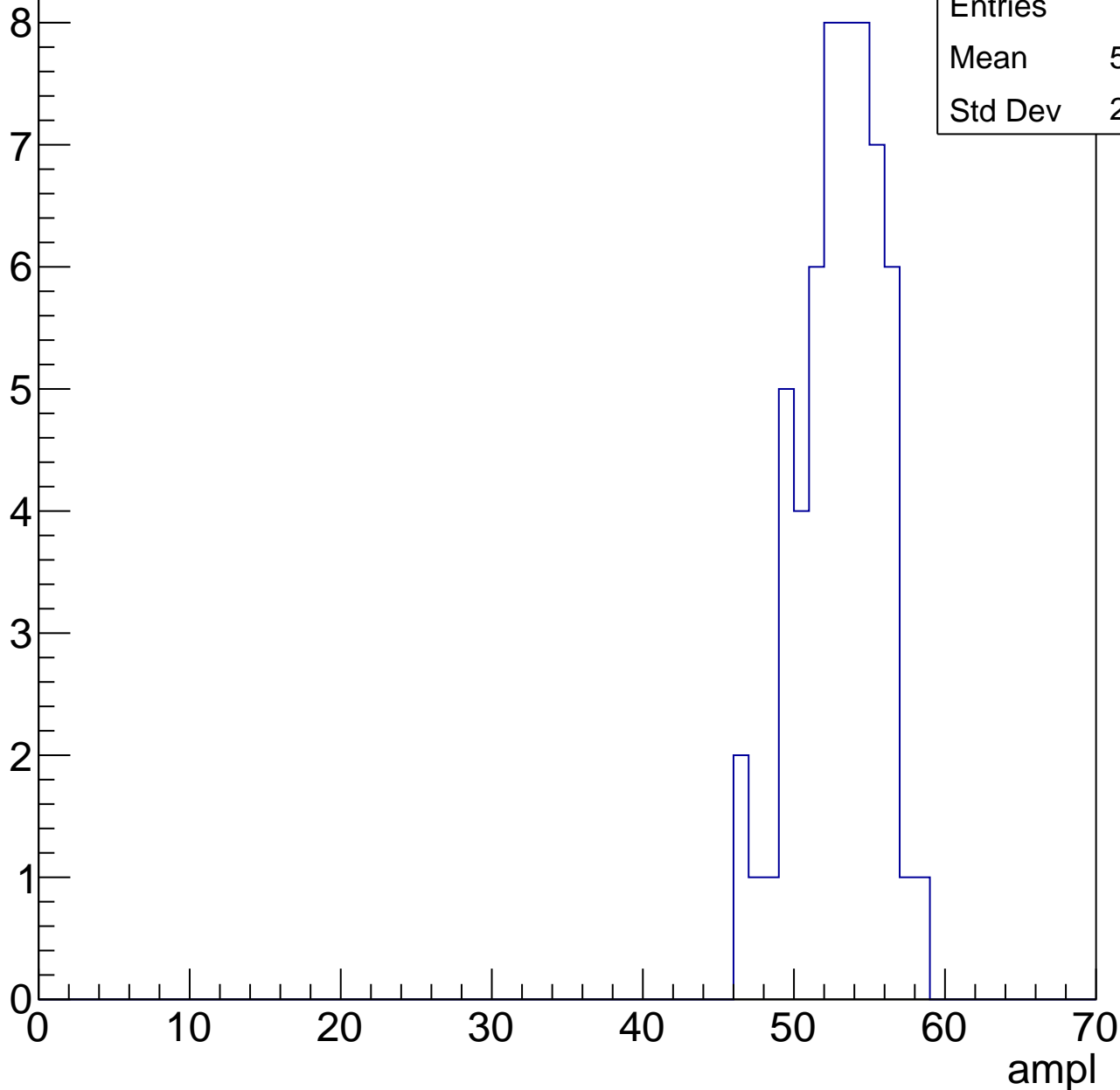


# B1L002S, U17-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.52
Std Dev	2.705

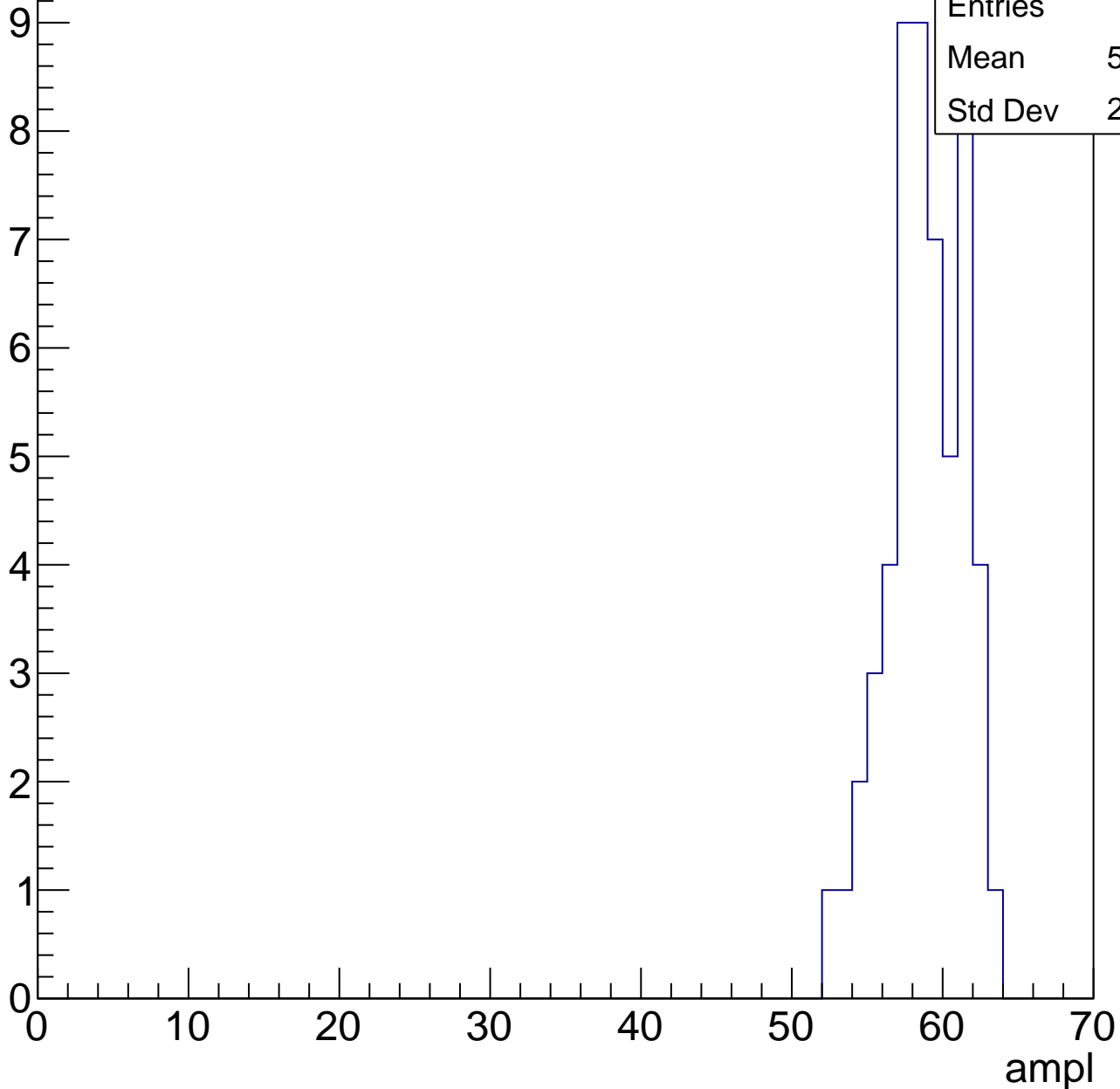


# B1L002S, U17-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	58.36
Std Dev	2.467

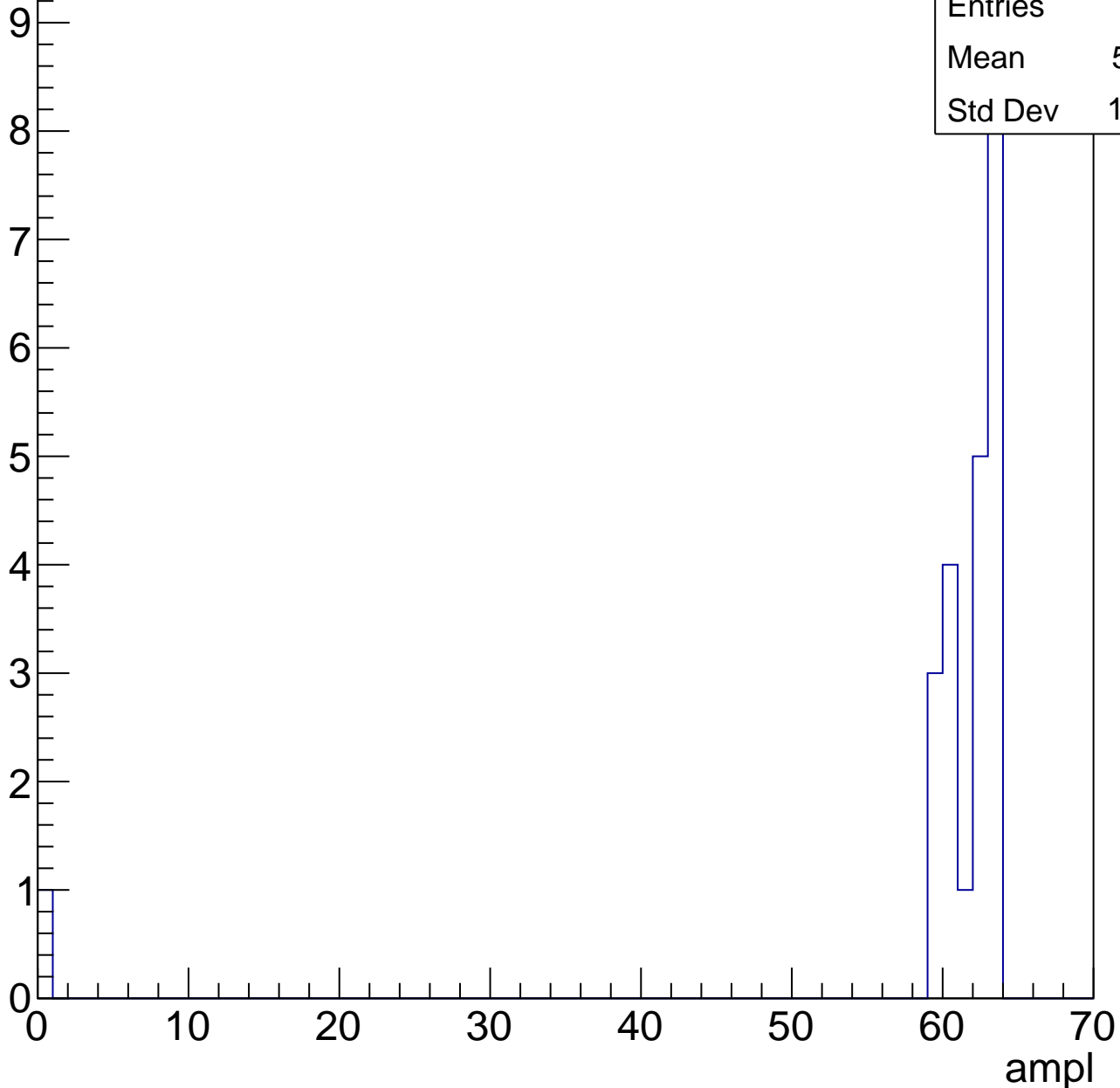


# B1L002S, U17-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.91
Std Dev	12.65





# B1L002S, U17-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries

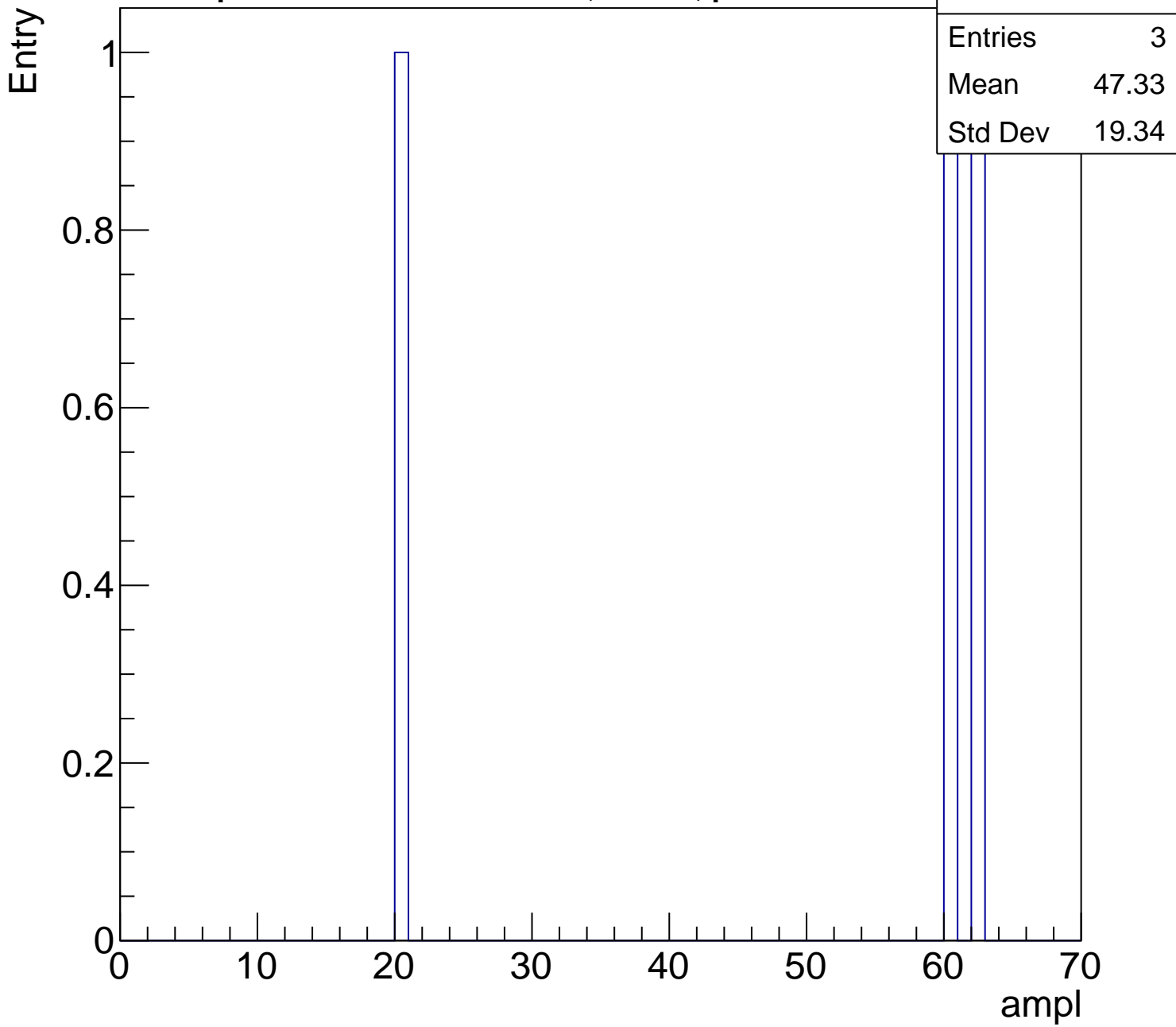
3

Mean

47.33

Std Dev

19.34



# B1L002S, U17-ch104, adc0

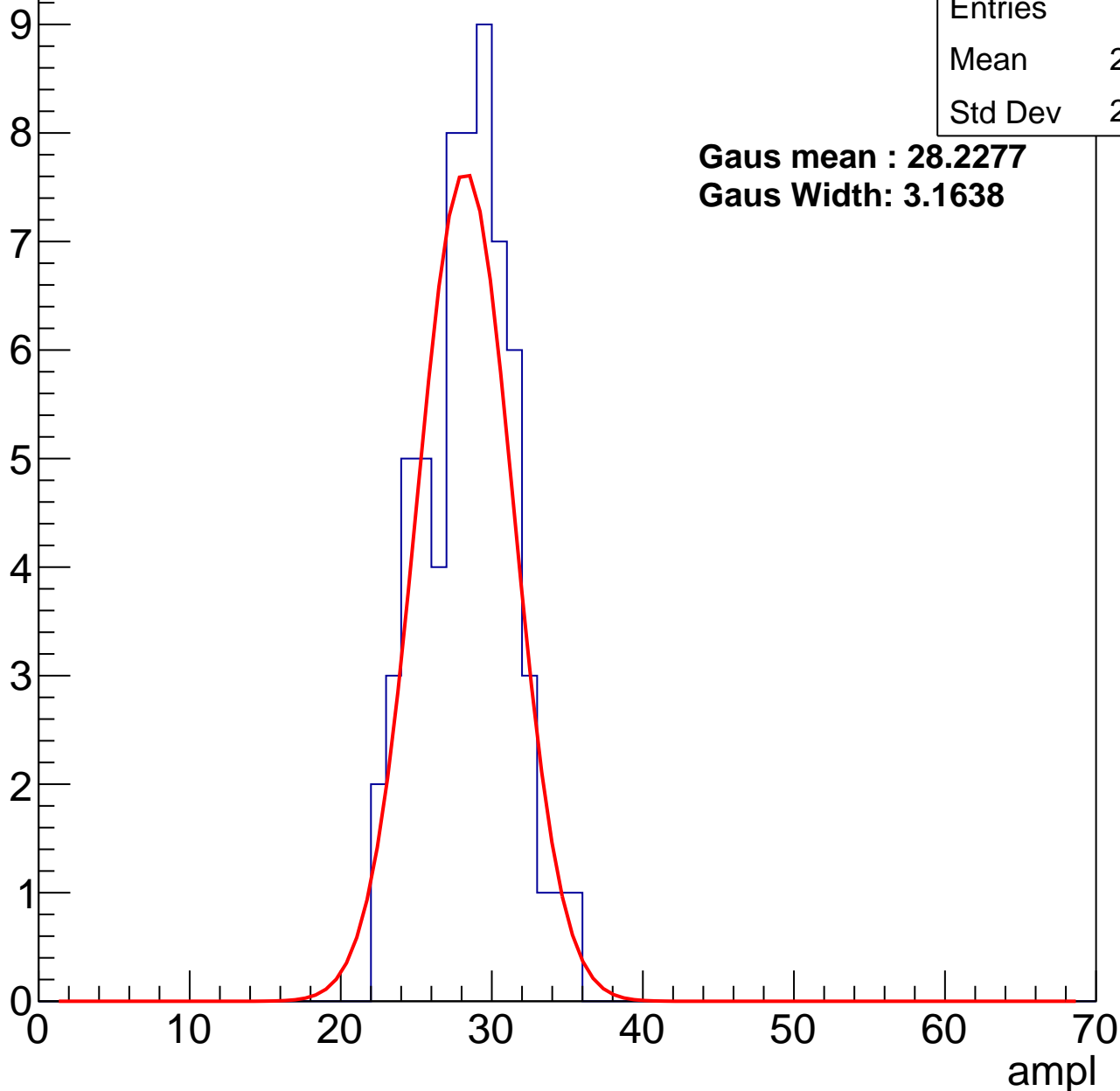
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.89
Std Dev	2.939

**Gaus mean : 28.2277**

**Gaus Width: 3.1638**



# B1L002S, U17-ch104, adc1

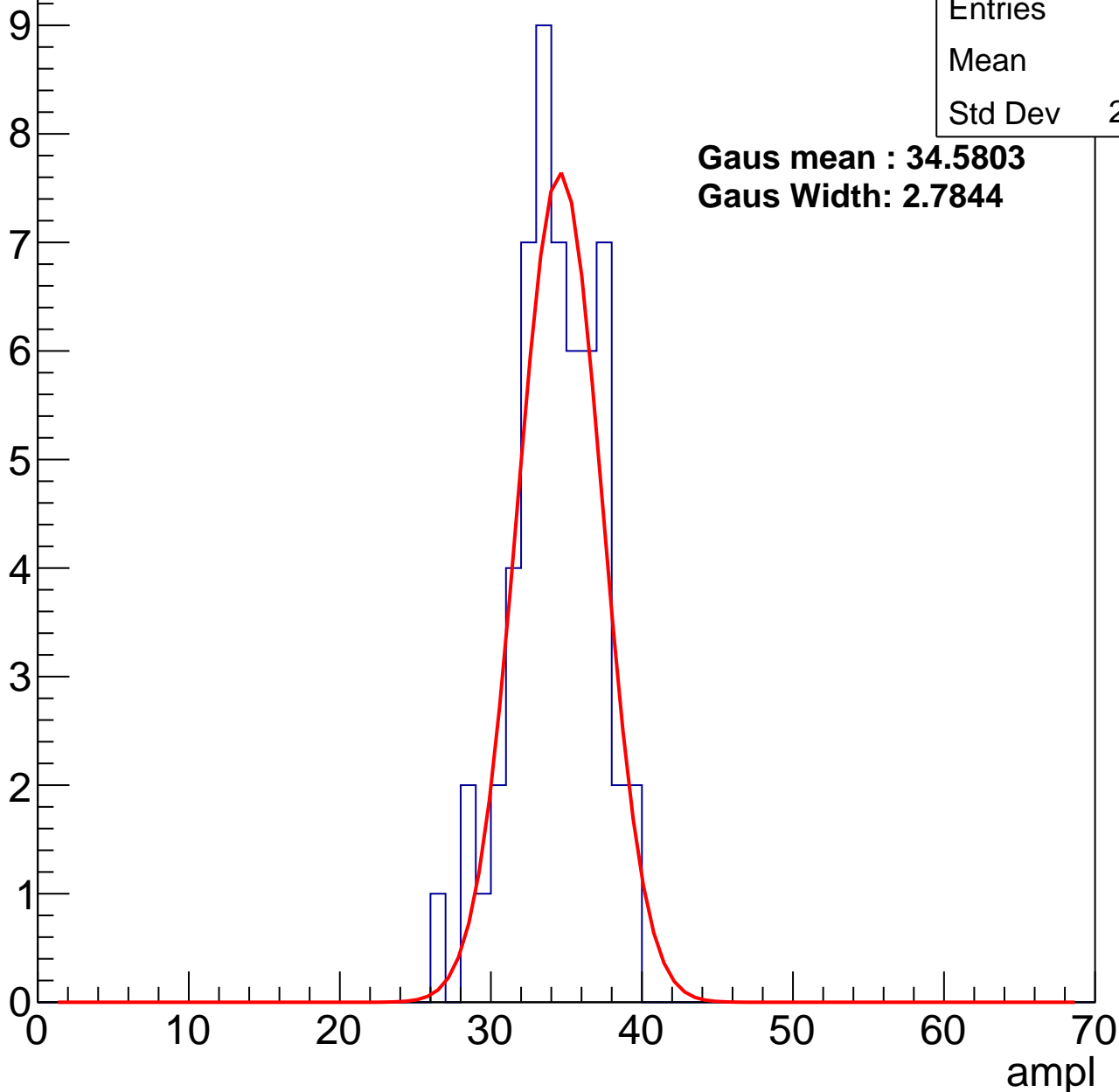
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.8
Std Dev	2.799

**Gaus mean : 34.5803**

**Gaus Width: 2.7844**



# B1L002S, U17-ch104, adc2

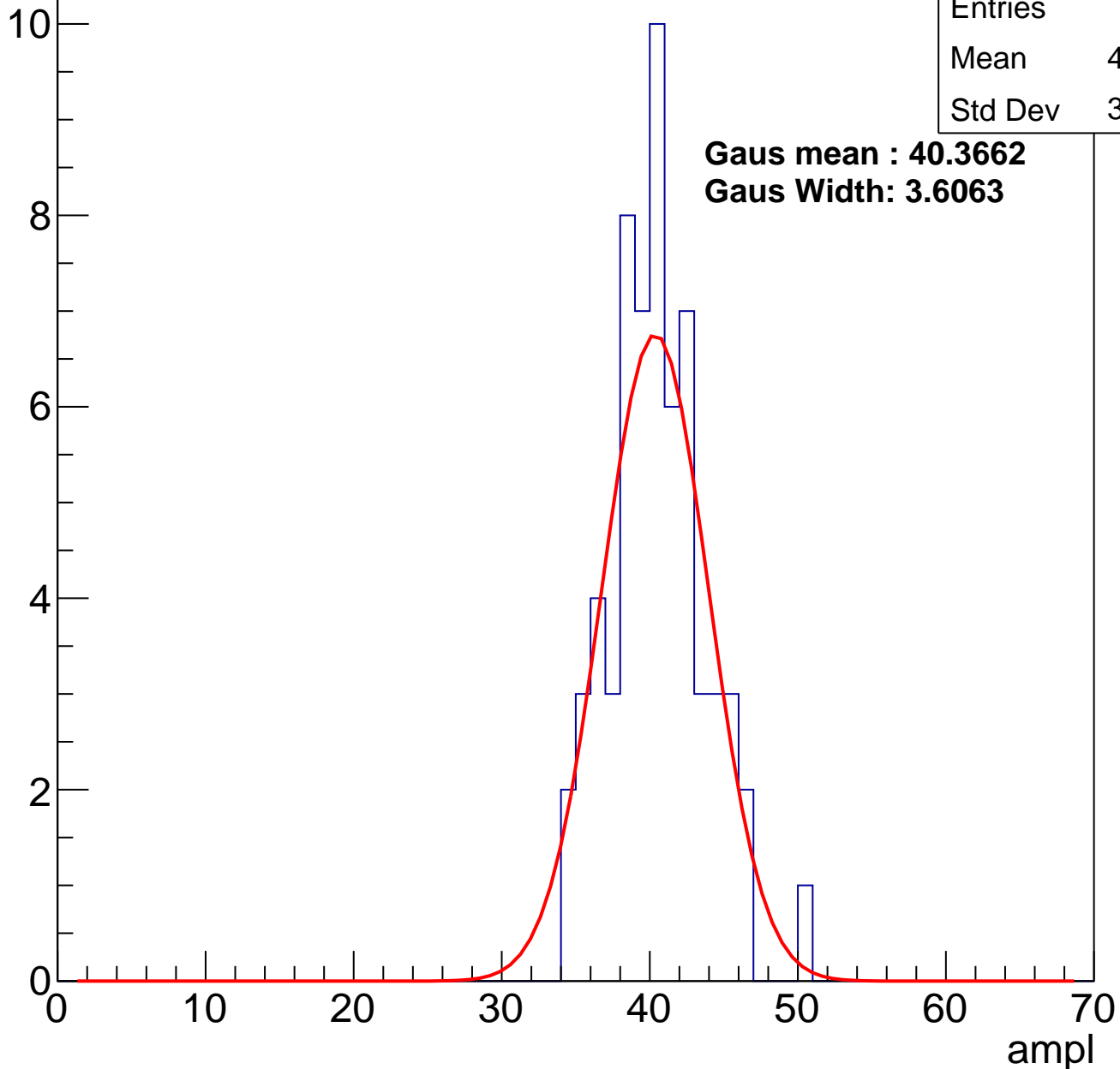
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	40.05
Std Dev	3.195

**Gaus mean : 40.3662**

**Gaus Width: 3.6063**

Entry

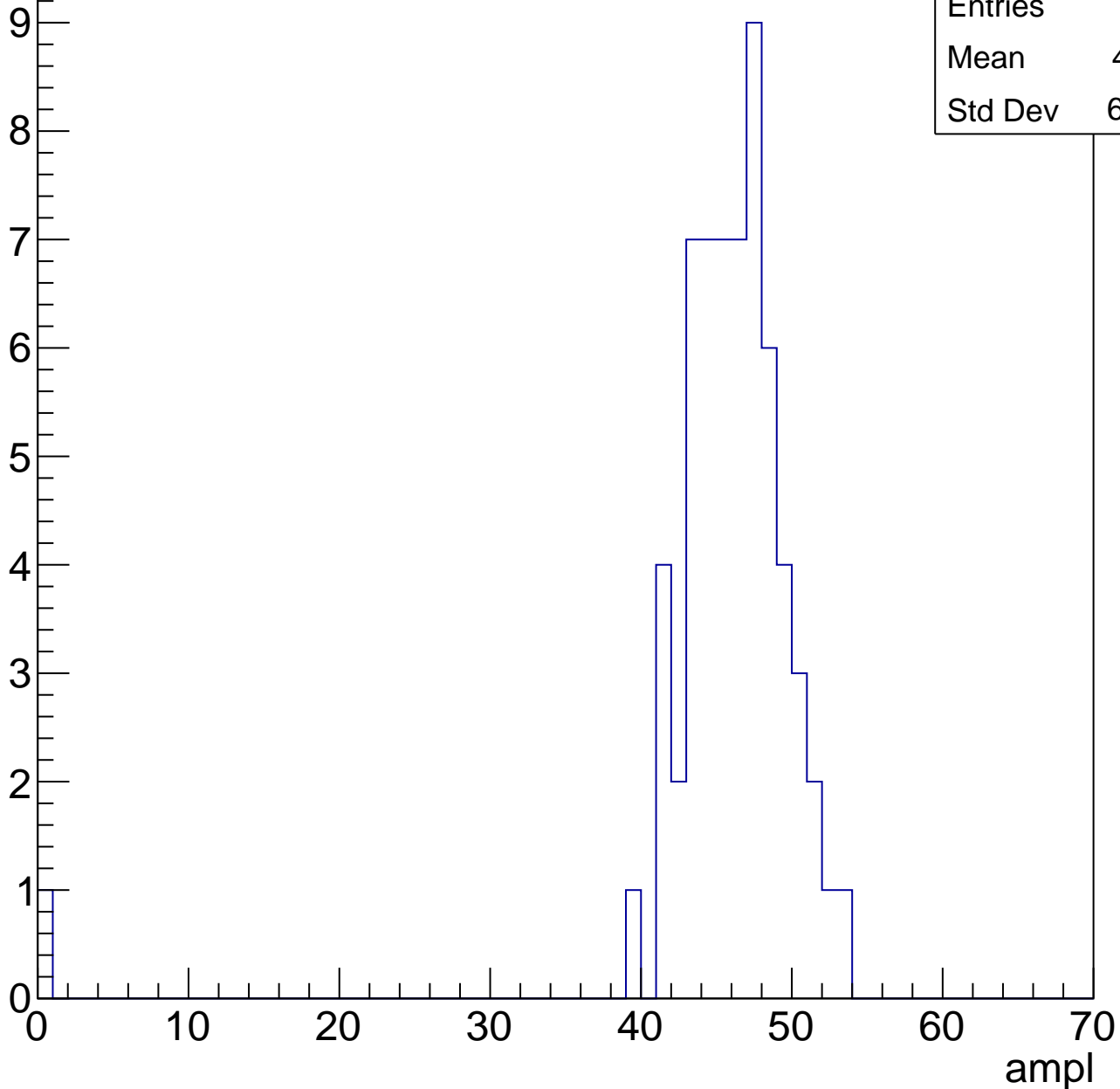


# B1L002S, U17-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	45.11
Std Dev	6.474

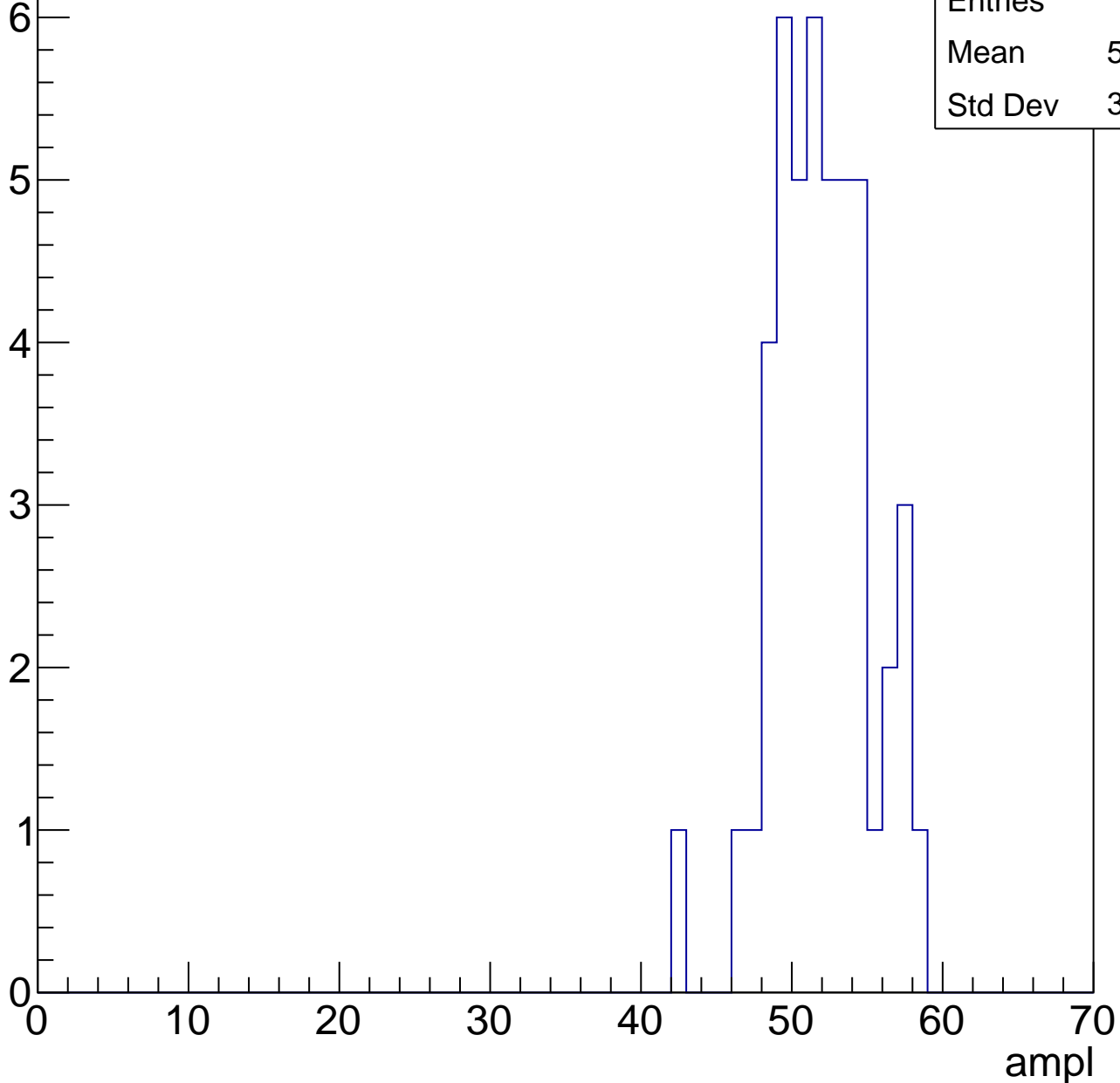


# B1L002S, U17-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	51.48
Std Dev	3.195

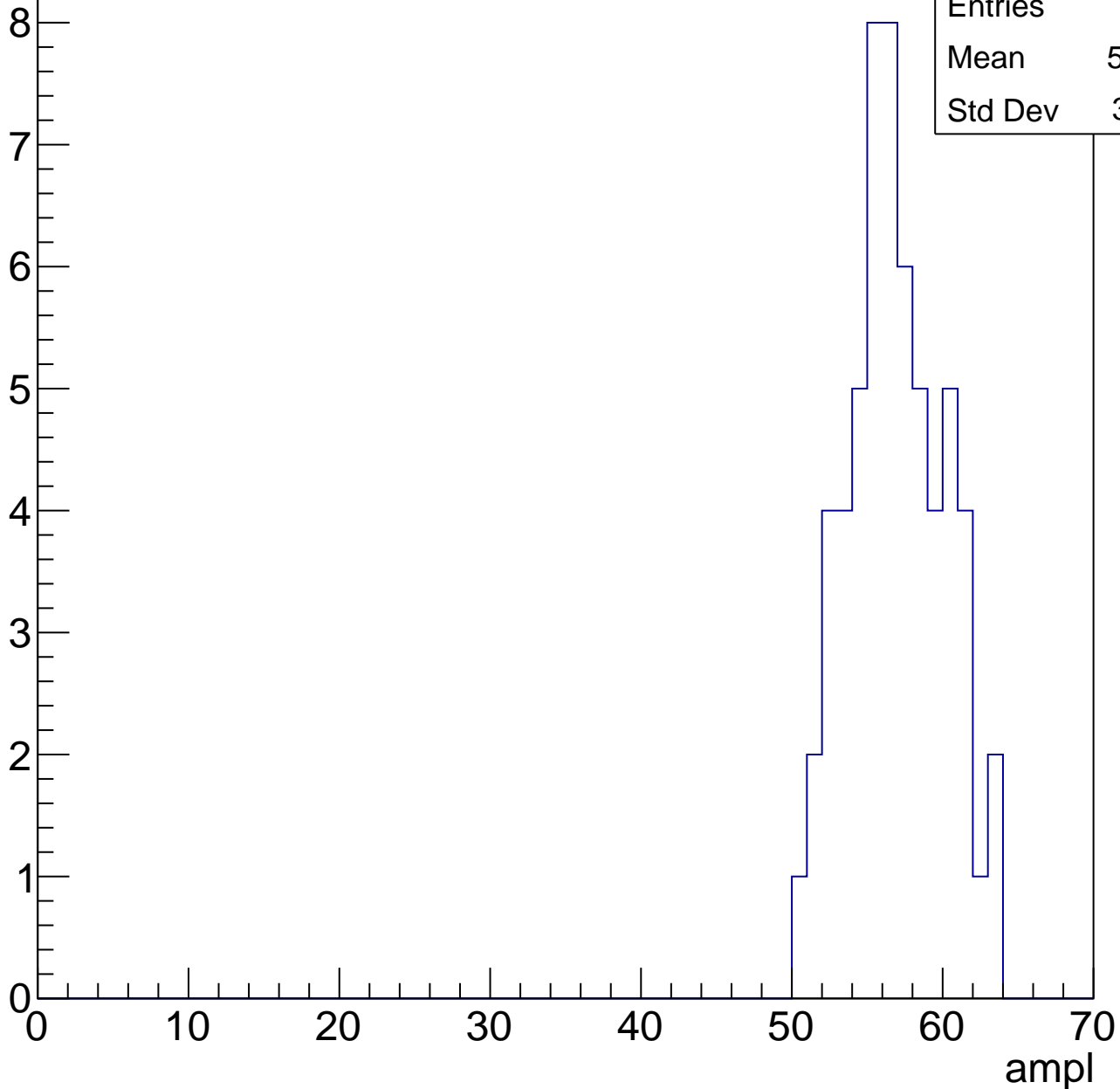


# B1L002S, U17-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	56.44
Std Dev	3.131

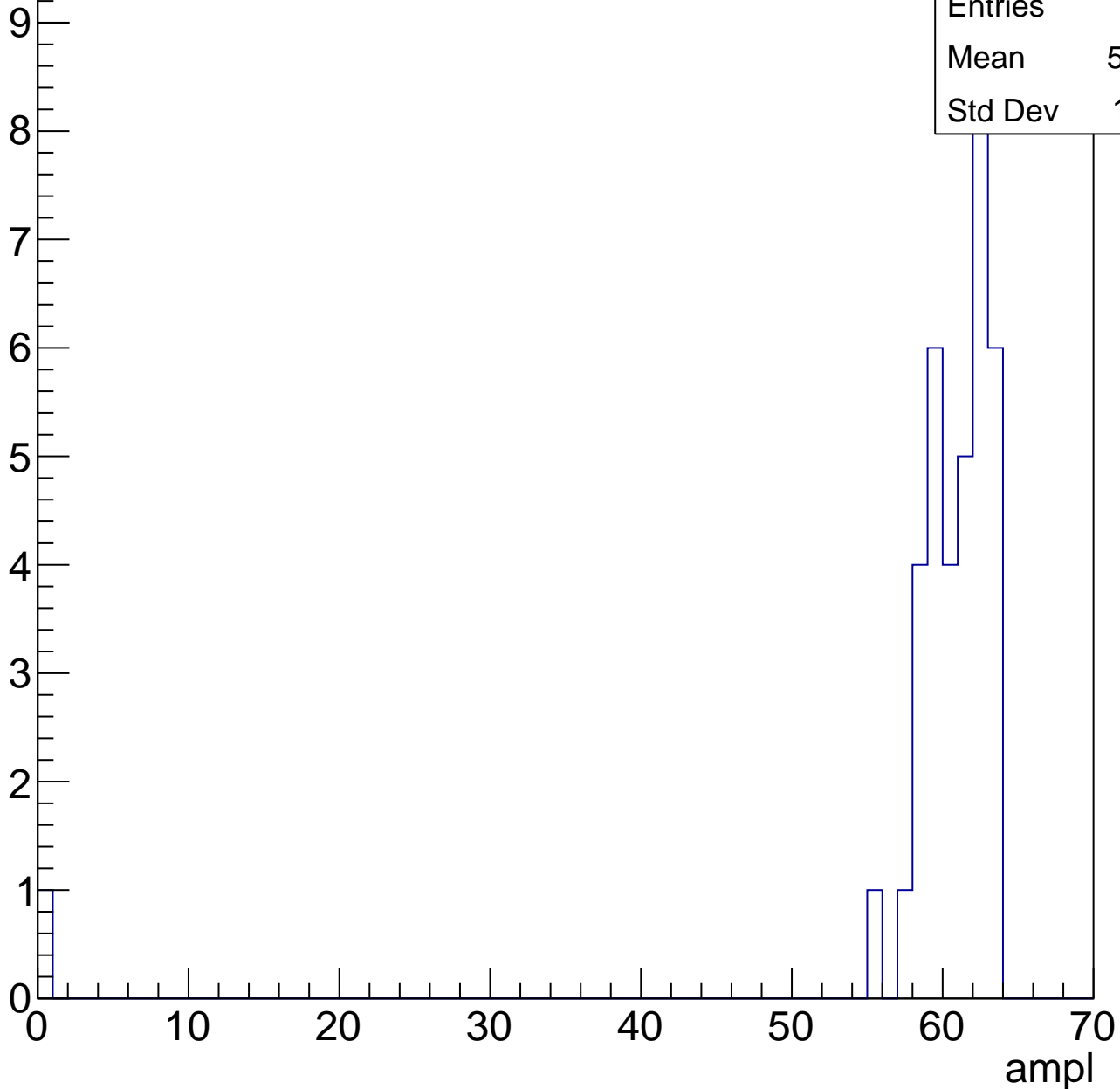


# B1L002S, U17-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	58.89
Std Dev	10.01

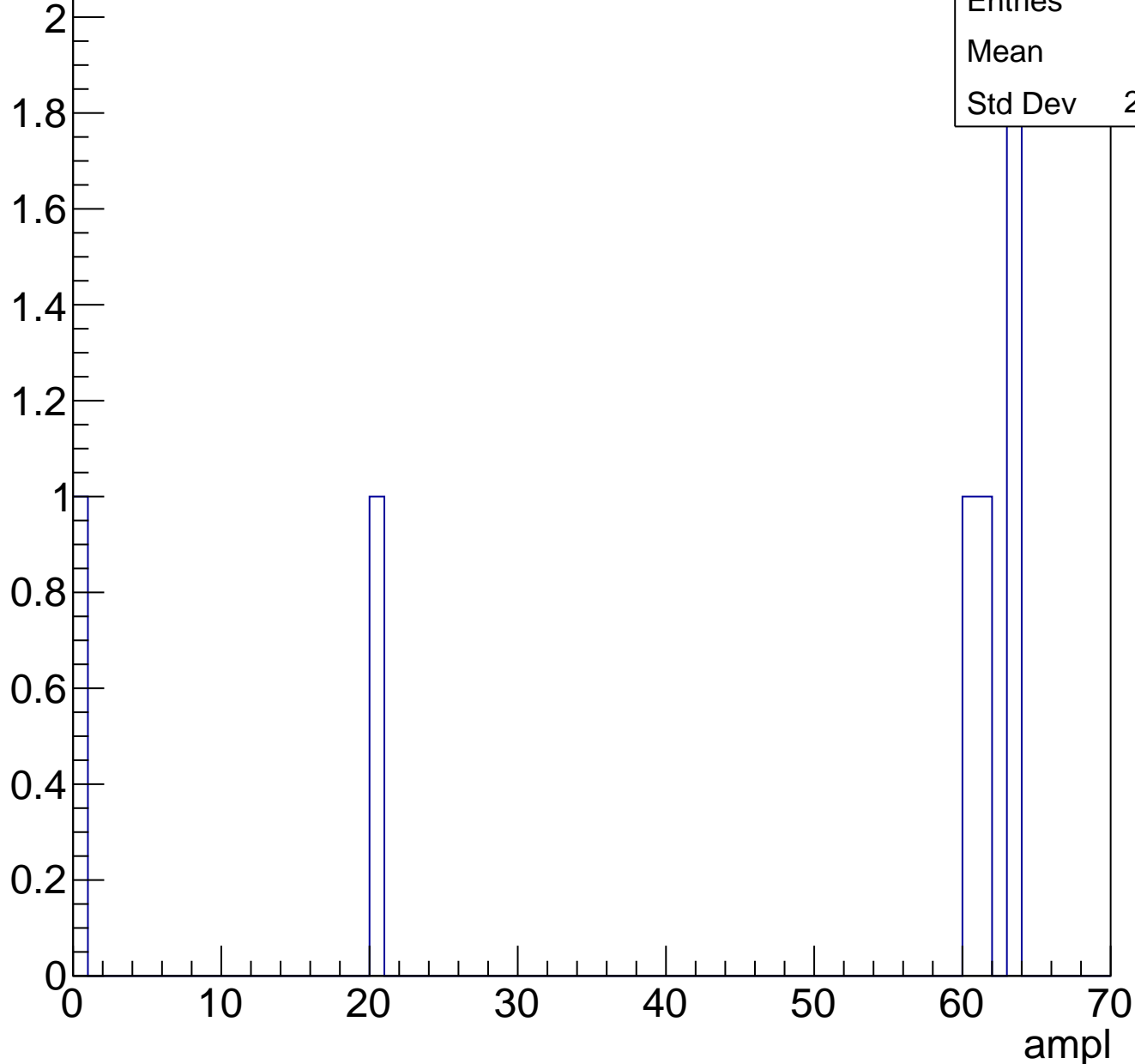




# B1L002S, U17-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch105, adc0

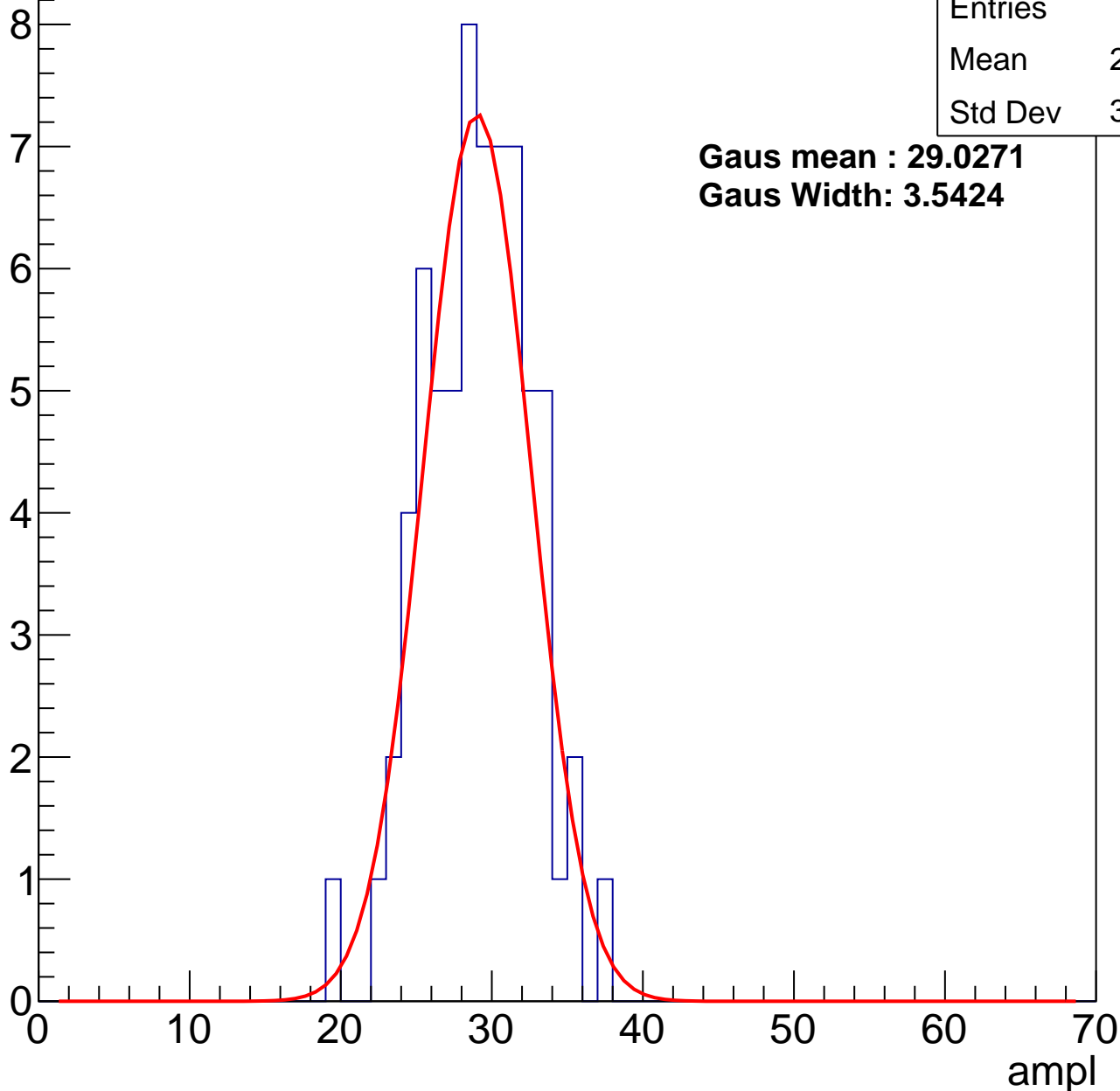
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.63
Std Dev	3.459

**Gaus mean : 29.0271**

**Gaus Width: 3.5424**



# B1L002S, U17-ch105, adc1

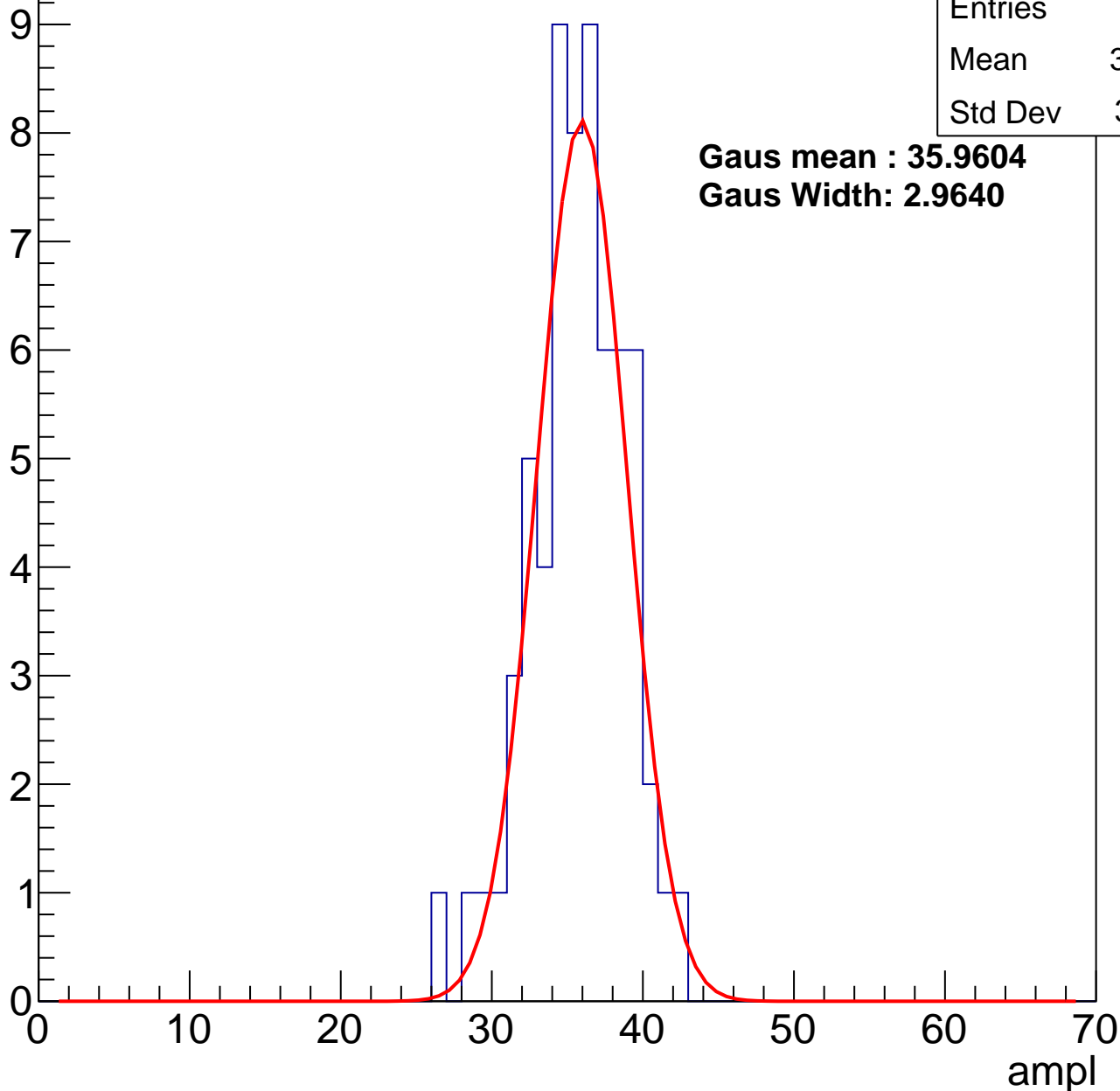
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.23
Std Dev	3.121

**Gaus mean : 35.9604**

**Gaus Width: 2.9640**



# B1L002S, U17-ch105, adc2

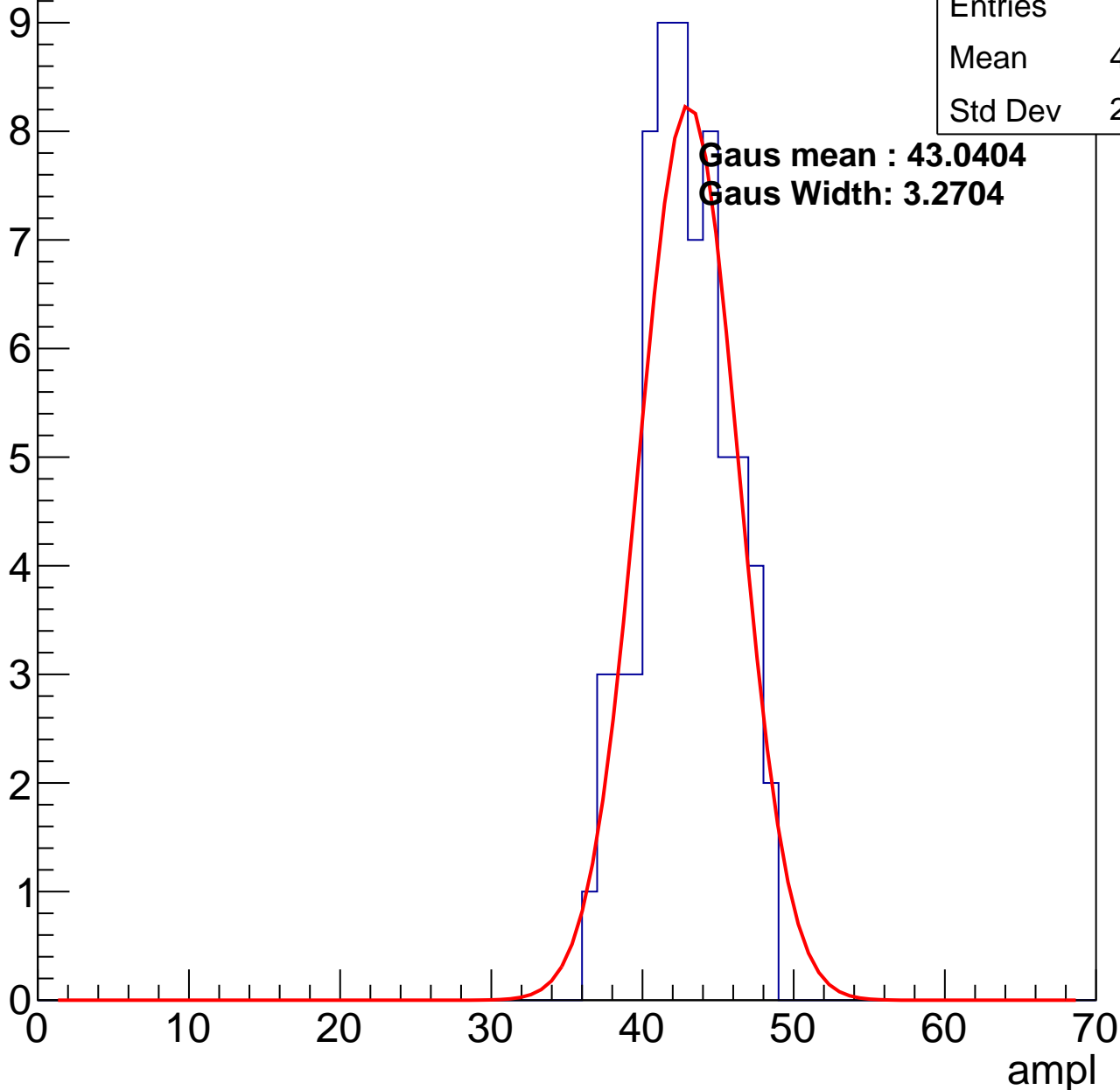
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	42.34
Std Dev	2.878

**Gaus mean : 43.0404**

**Gaus Width: 3.2704**

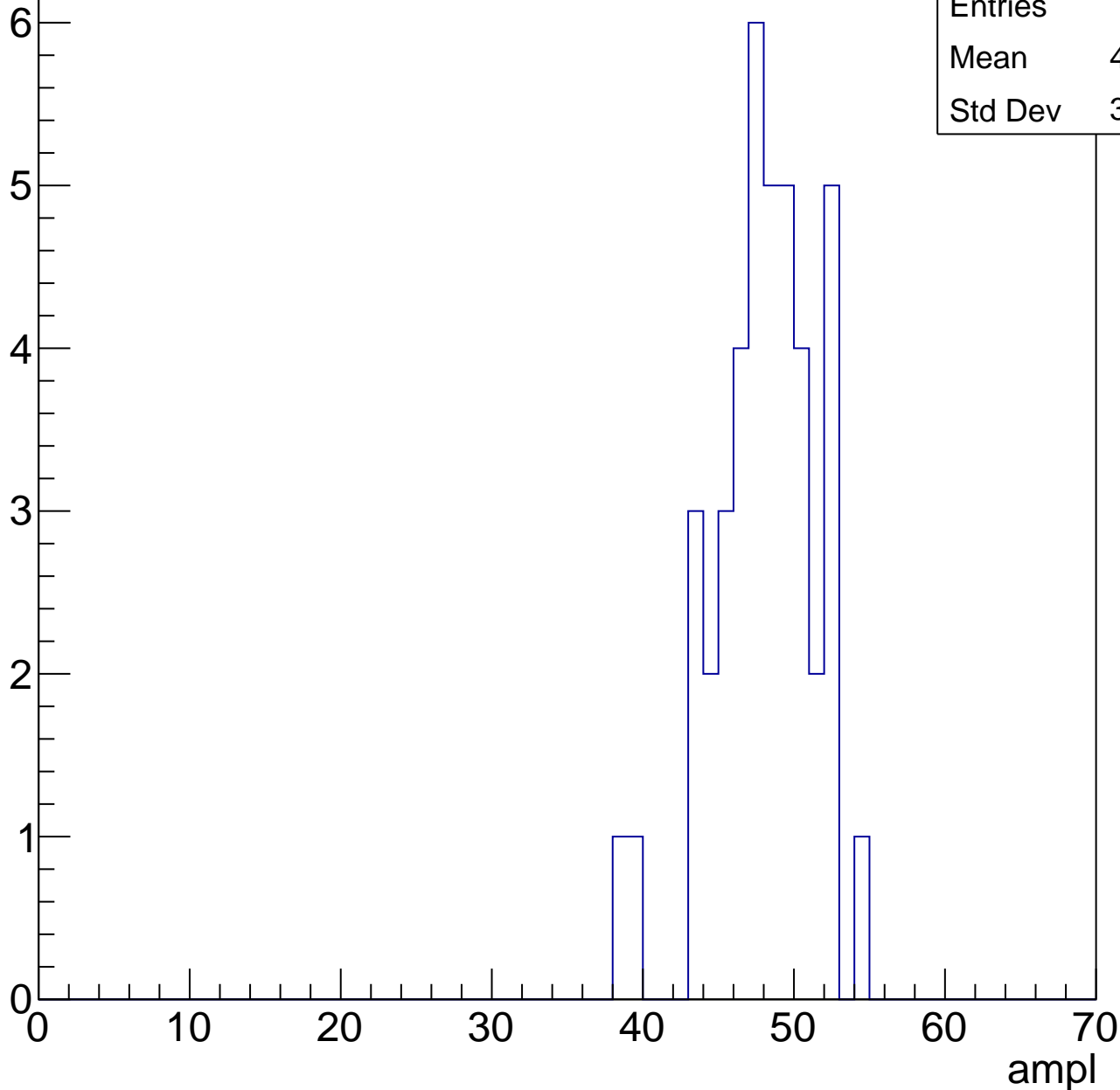


# B1L002S, U17-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	47.52
Std Dev	3.389

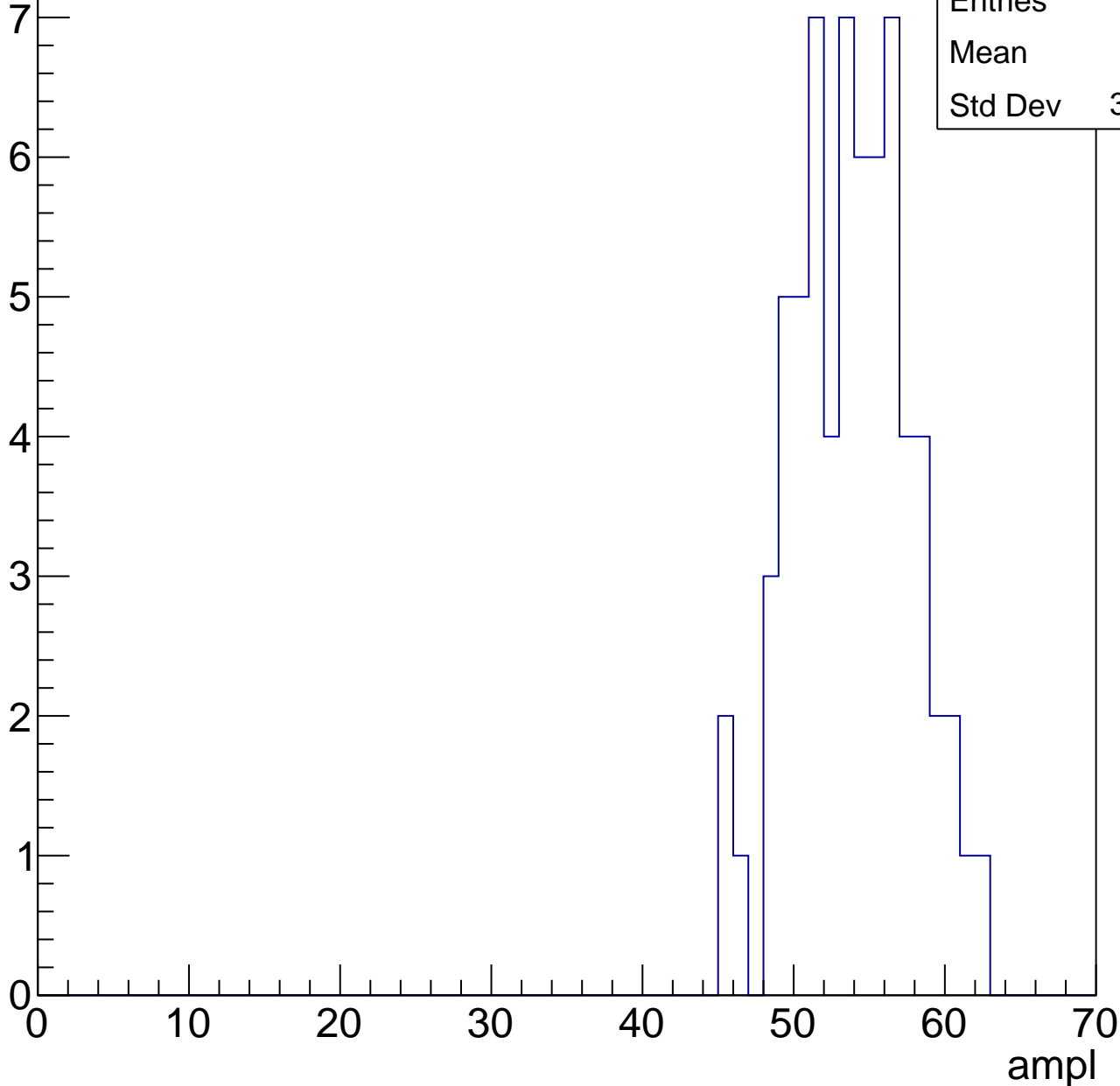


# B1L002S, U17-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	53.4
Std Dev	3.805

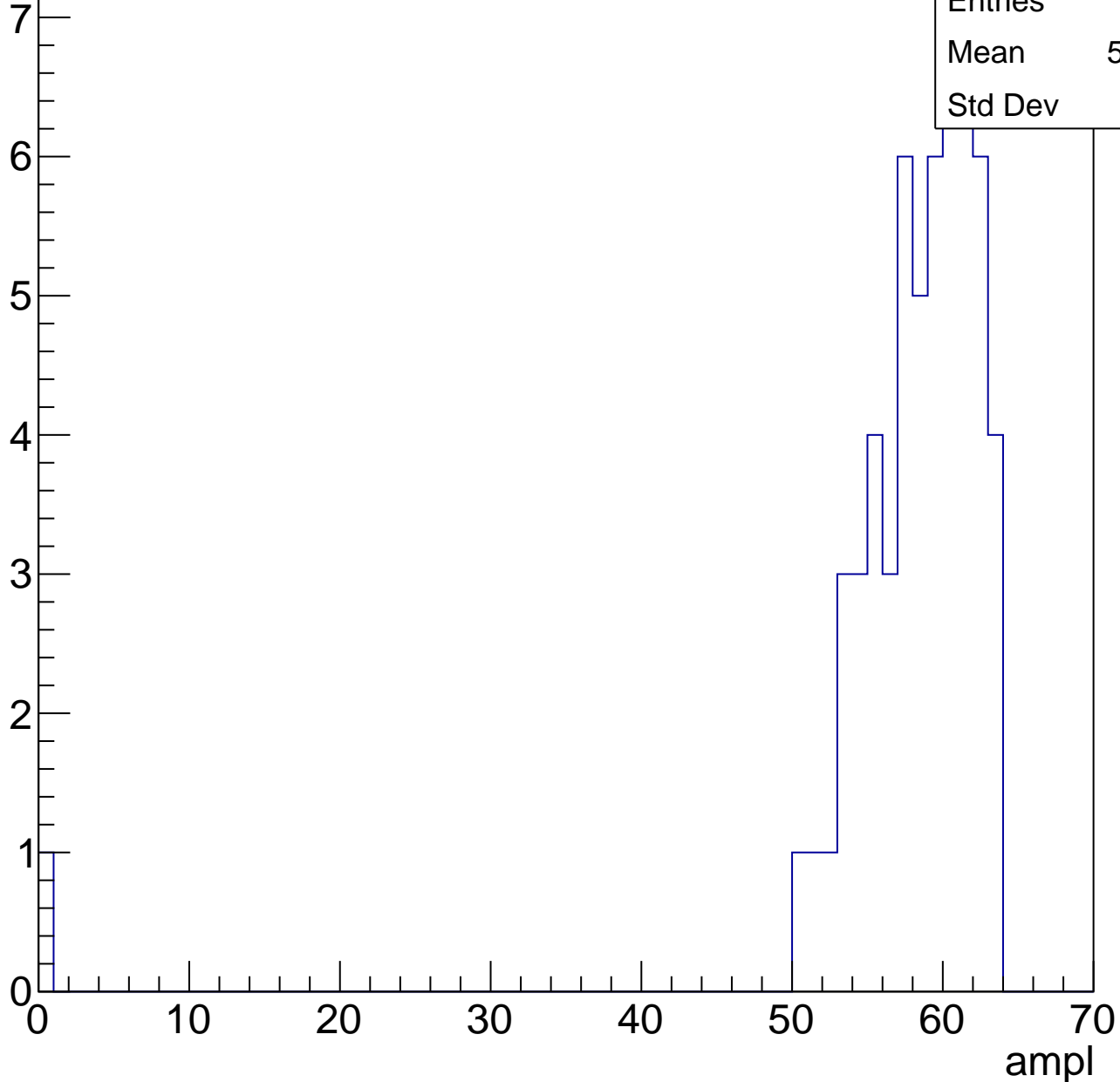


# B1L002S, U17-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.22
Std Dev	8.25

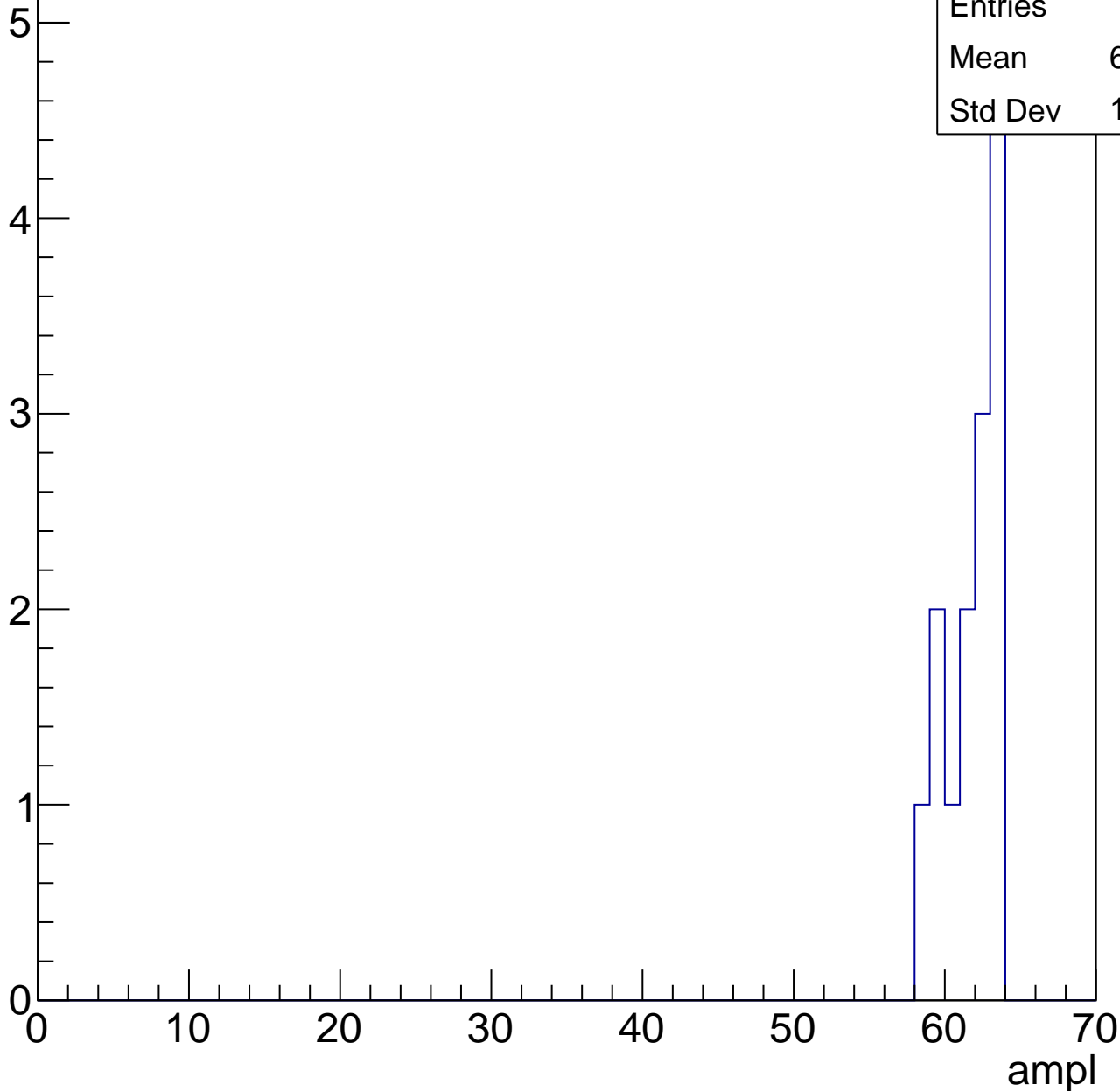


# B1L002S, U17-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.36
Std Dev	1.674





# B1L002S, U17-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch106, adc0

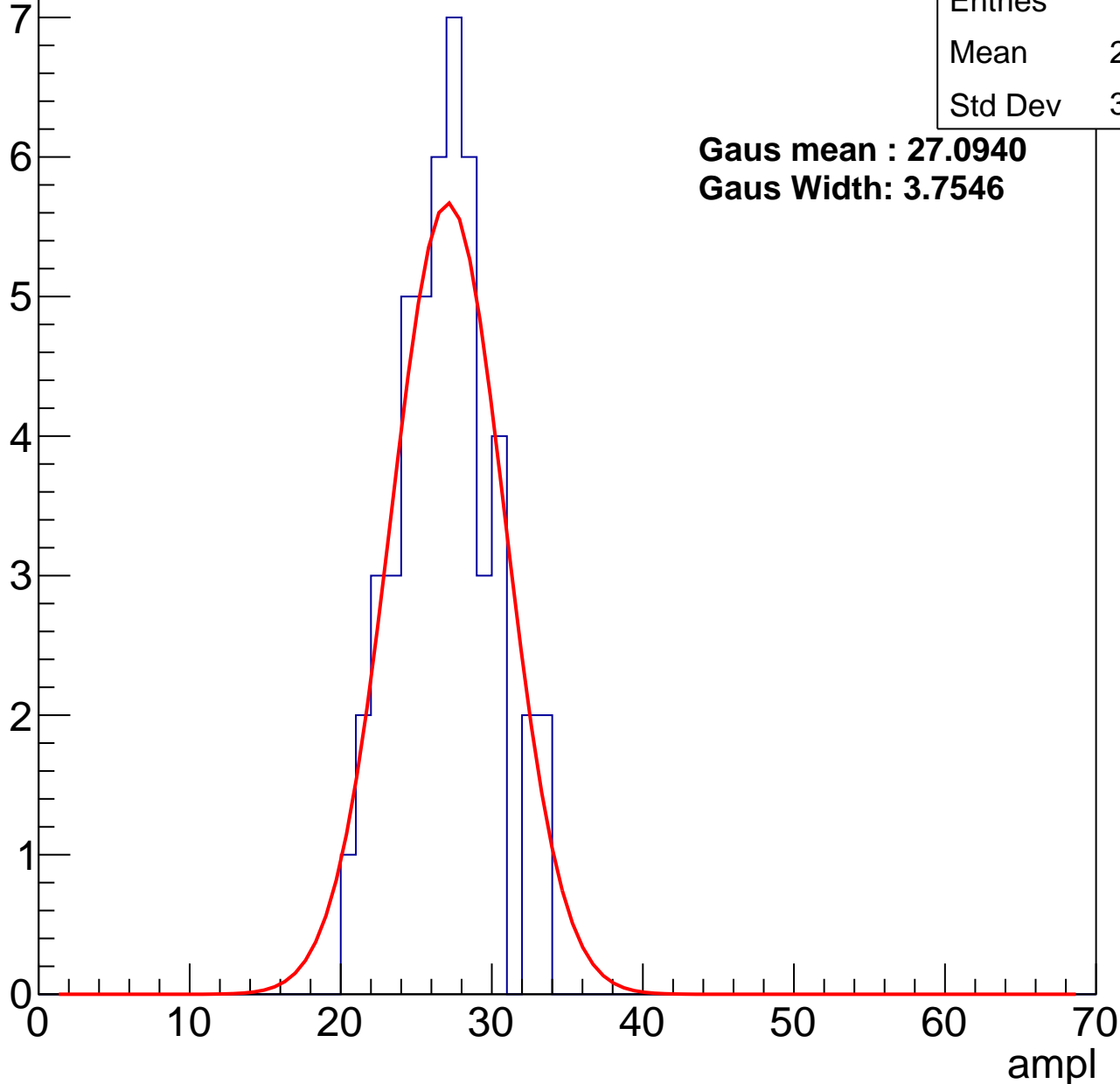
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	26.37
Std Dev	3.102

**Gaus mean : 27.0940**

**Gaus Width: 3.7546**



# B1L002S, U17-ch106, adc1

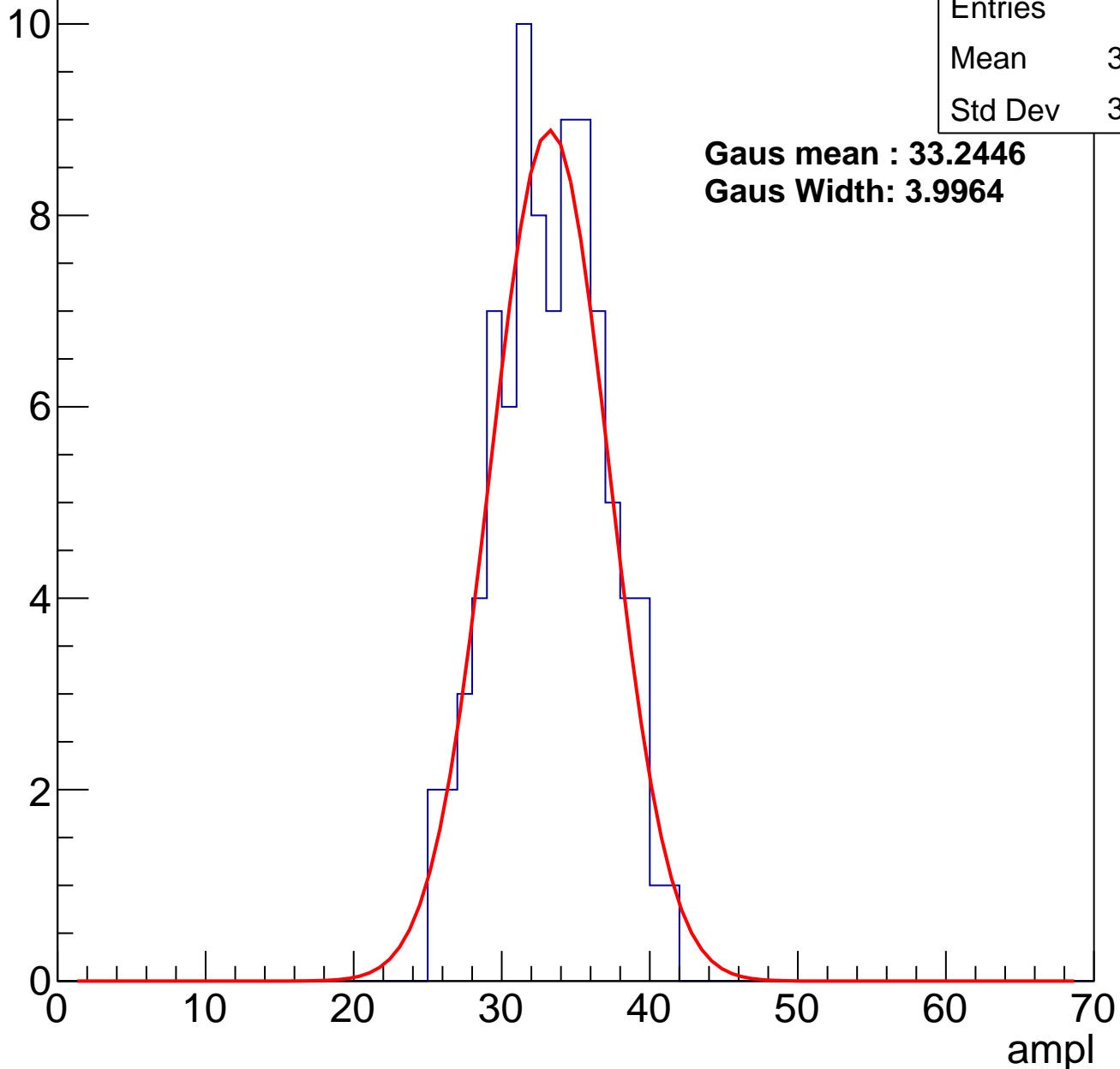
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	89
Mean	32.83
Std Dev	3.657

**Gaus mean : 33.2446**

**Gaus Width: 3.9964**

Entry



# B1L002S, U17-ch106, adc2

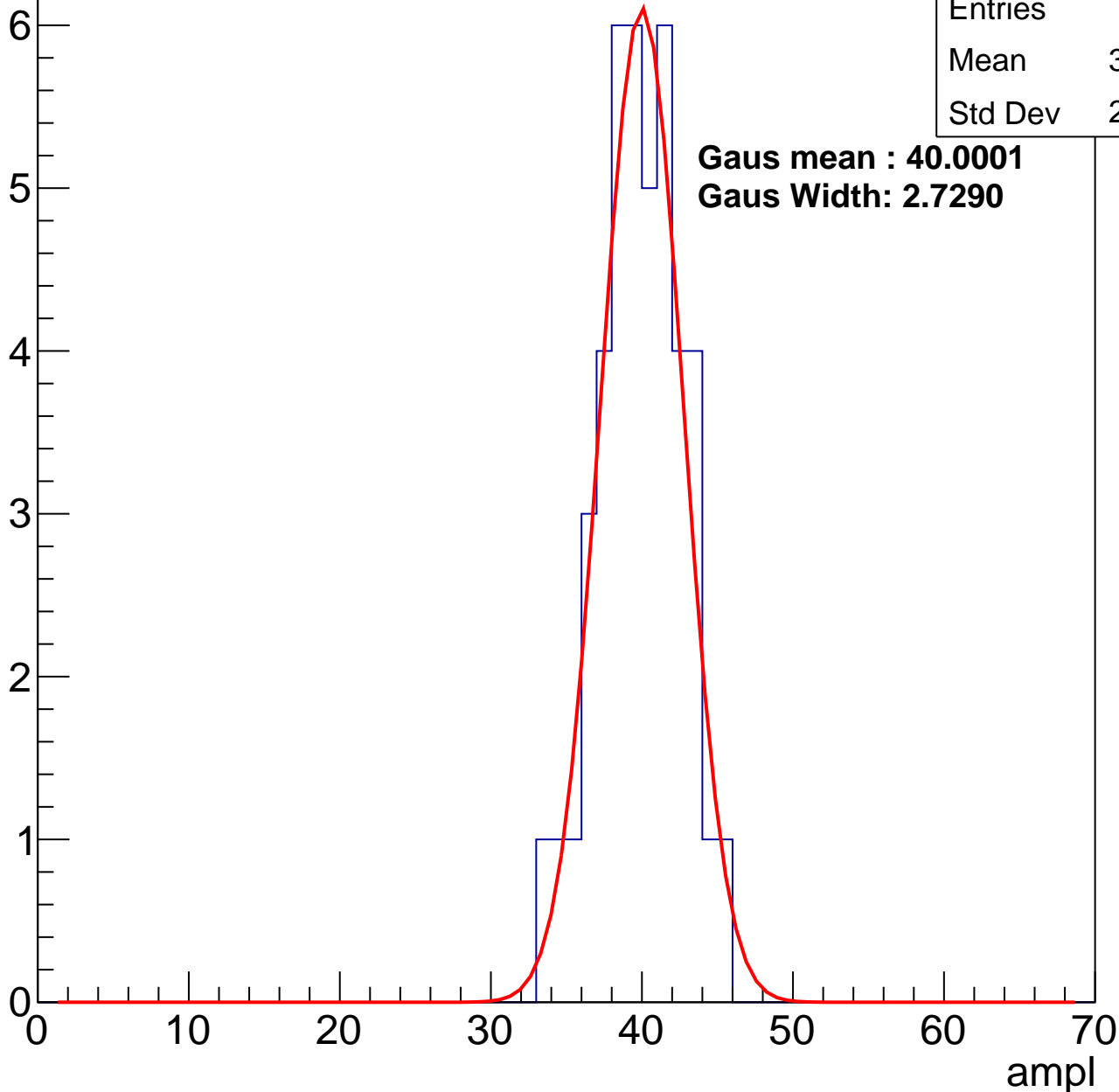
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	39.42
Std Dev	2.678

**Gaus mean : 40.0001**

**Gaus Width: 2.7290**

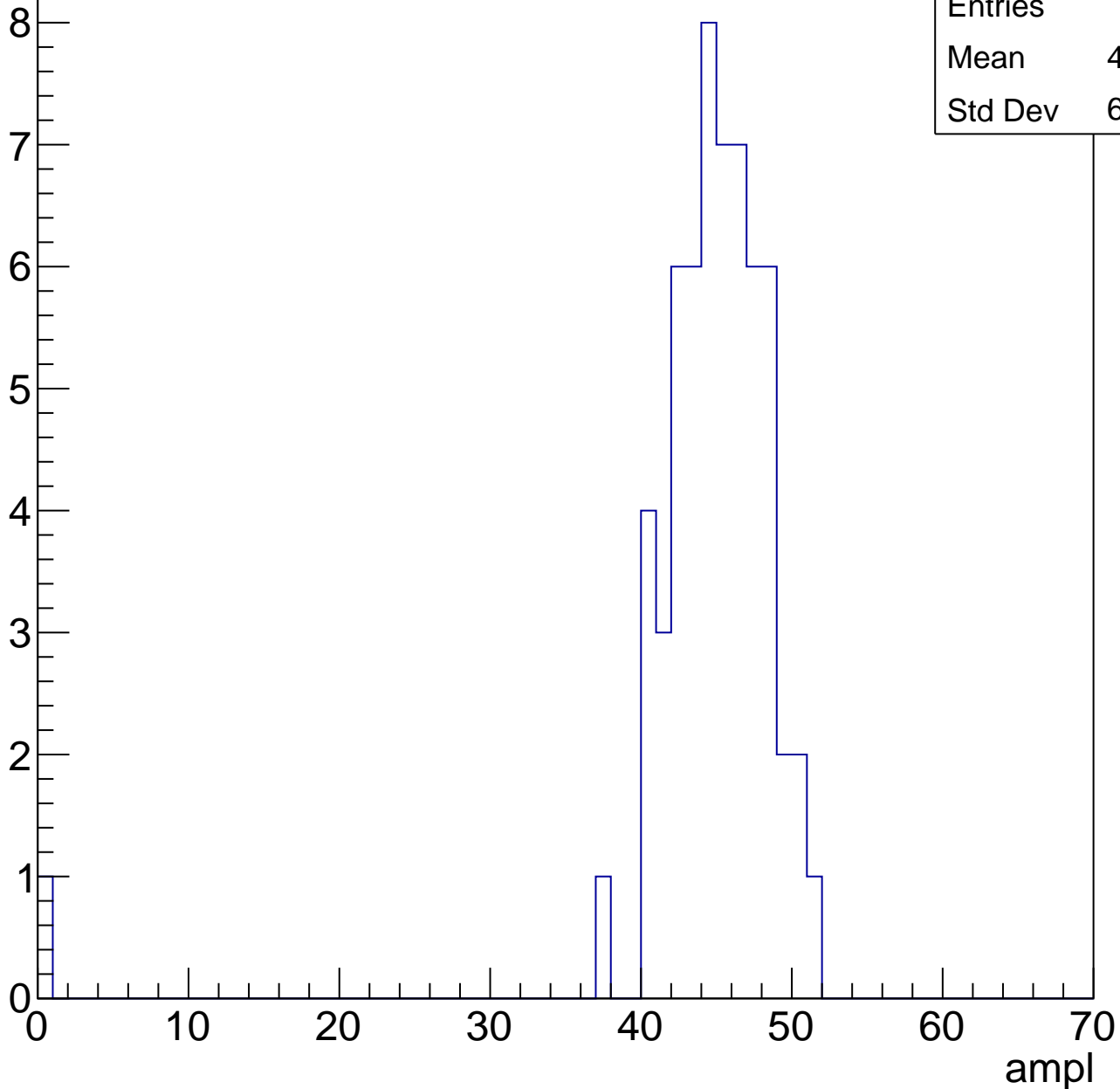


# B1L002S, U17-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	43.97
Std Dev	6.406

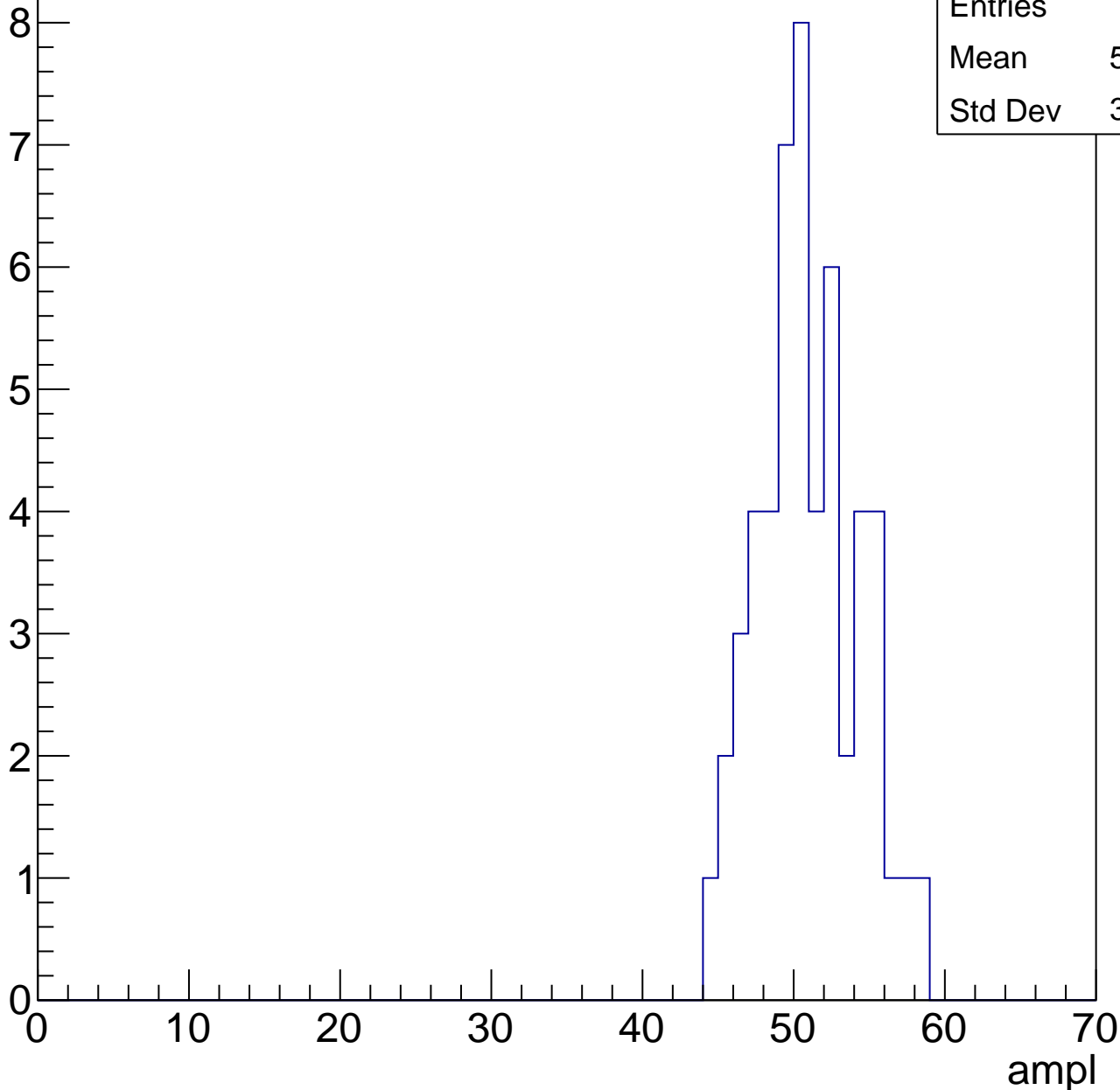


# B1L002S, U17-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	50.46
Std Dev	3.225

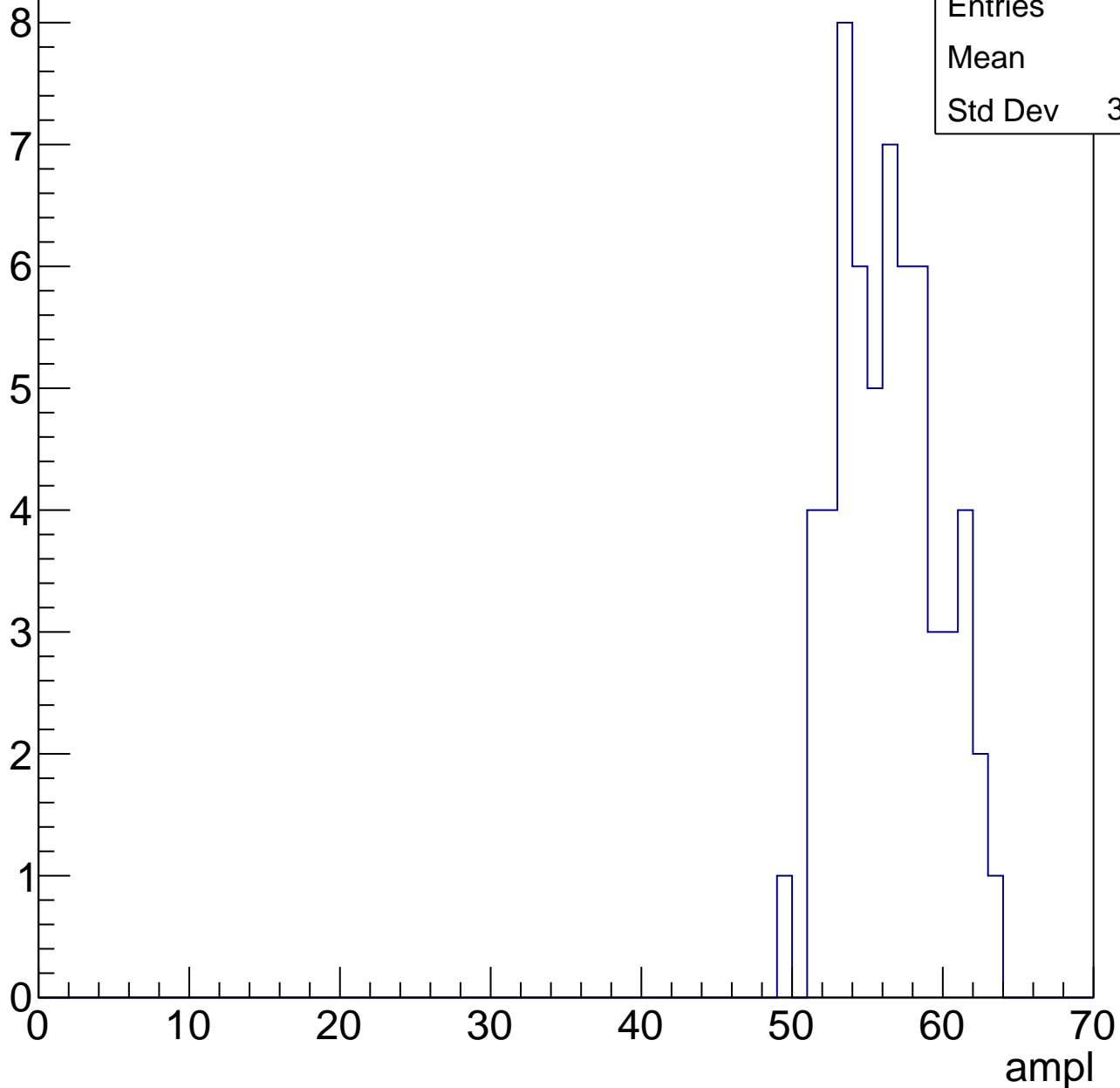


# B1L002S, U17-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	55.9
Std Dev	3.264

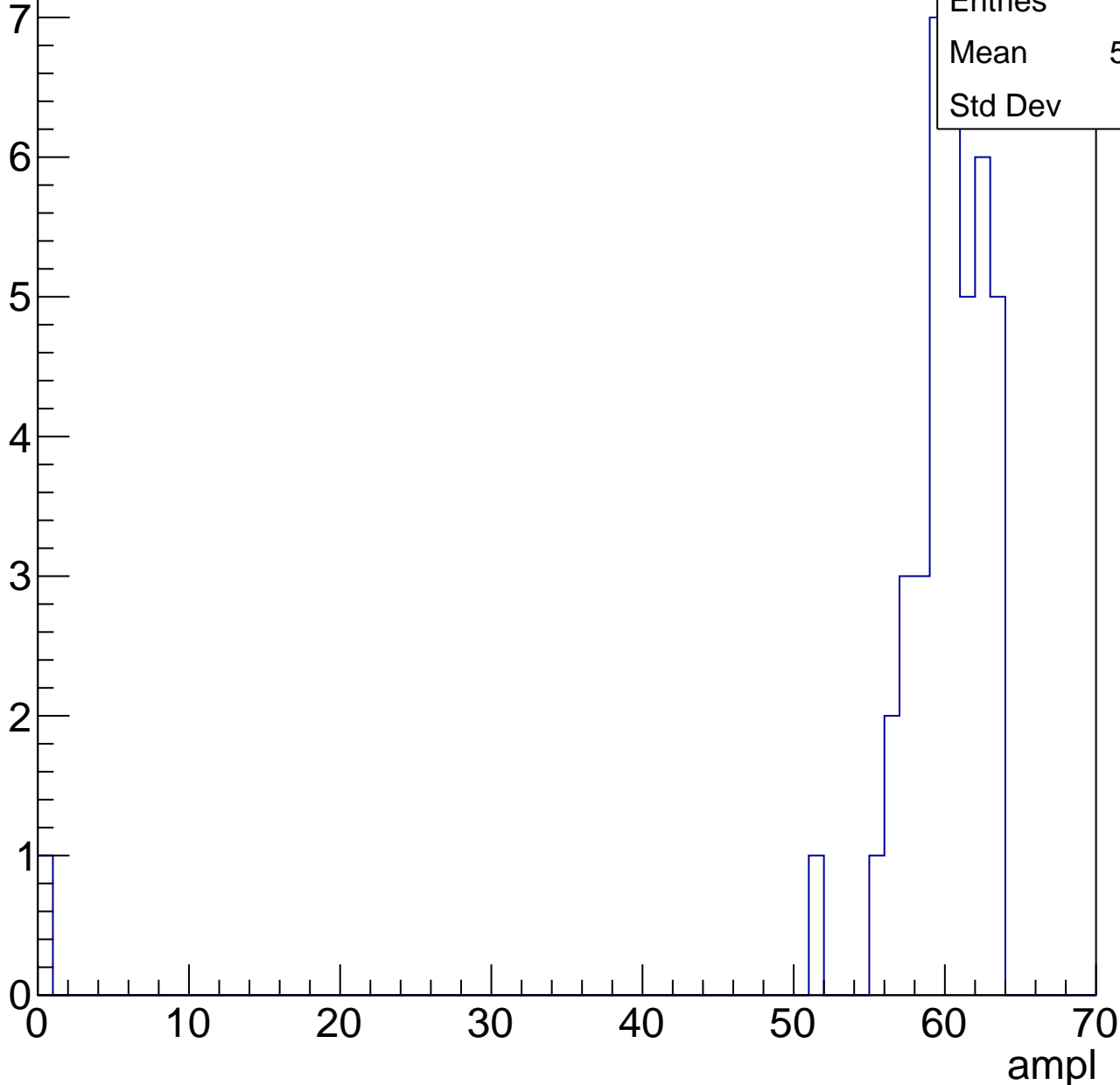


# B1L002S, U17-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	58.24
Std Dev	9.54

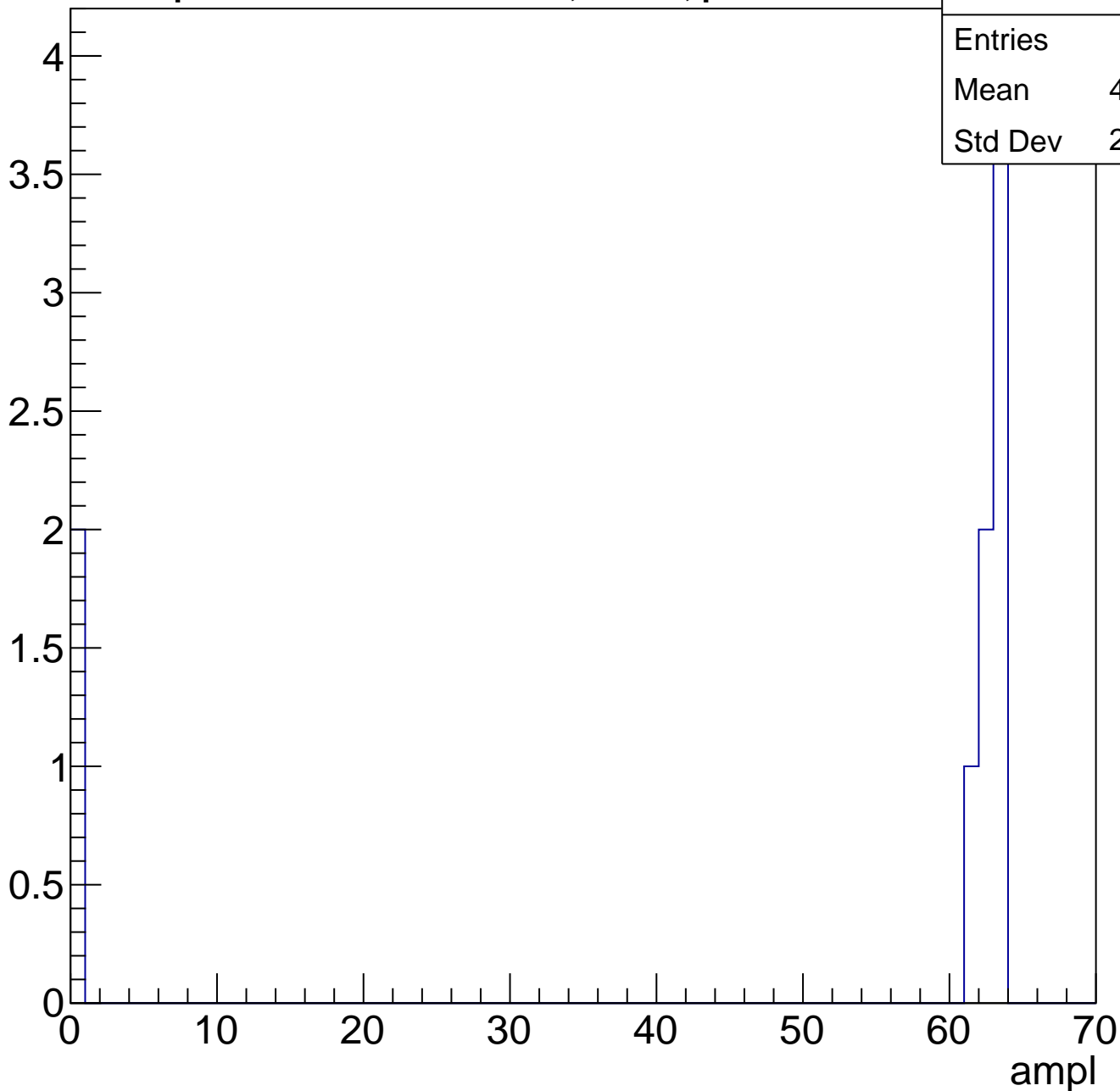




# B1L002S, U17-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch107, adc0

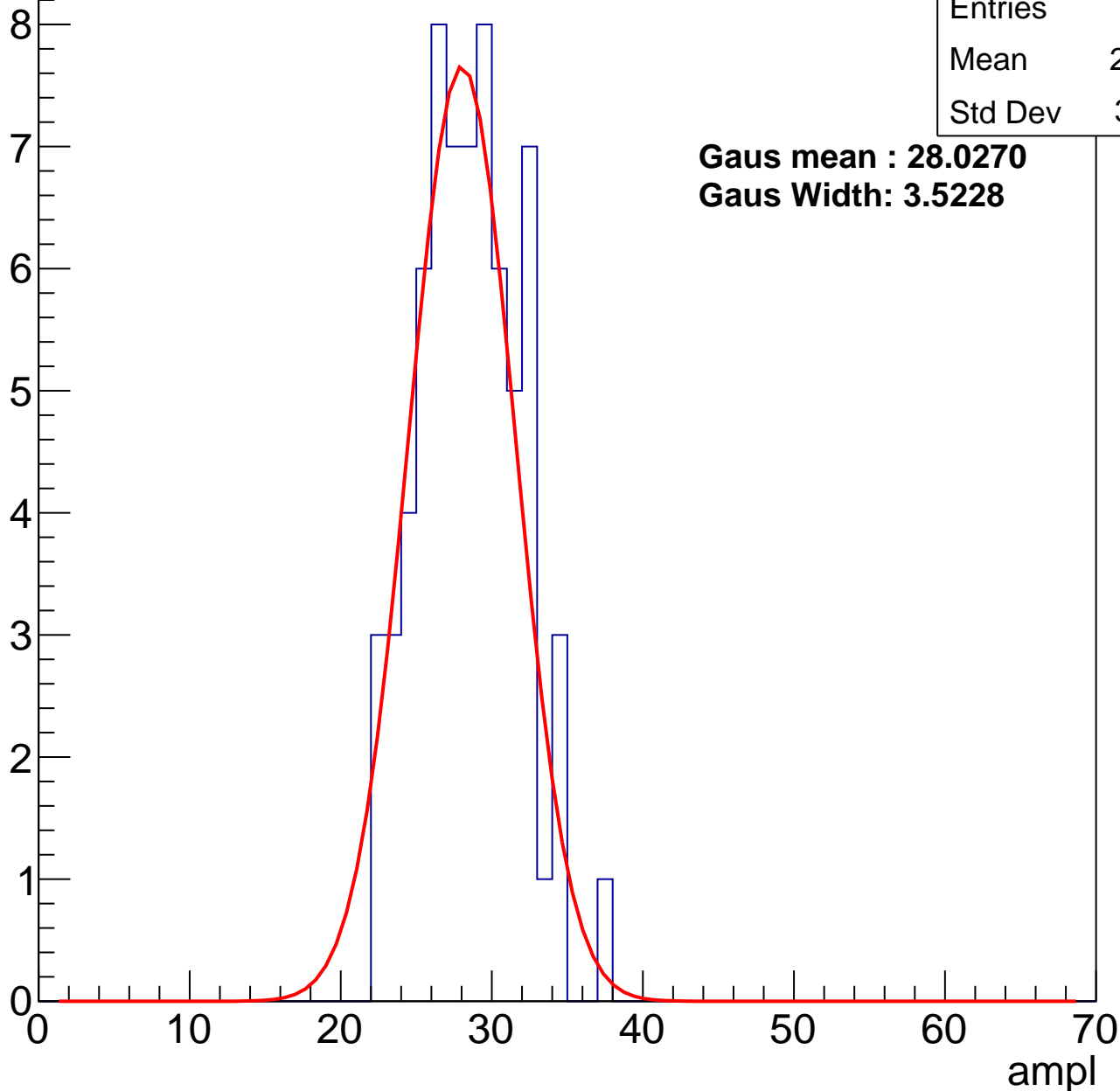
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	28.07
Std Dev	3.281

**Gaus mean : 28.0270**

**Gaus Width: 3.5228**



# B1L002S, U17-ch107, adc1

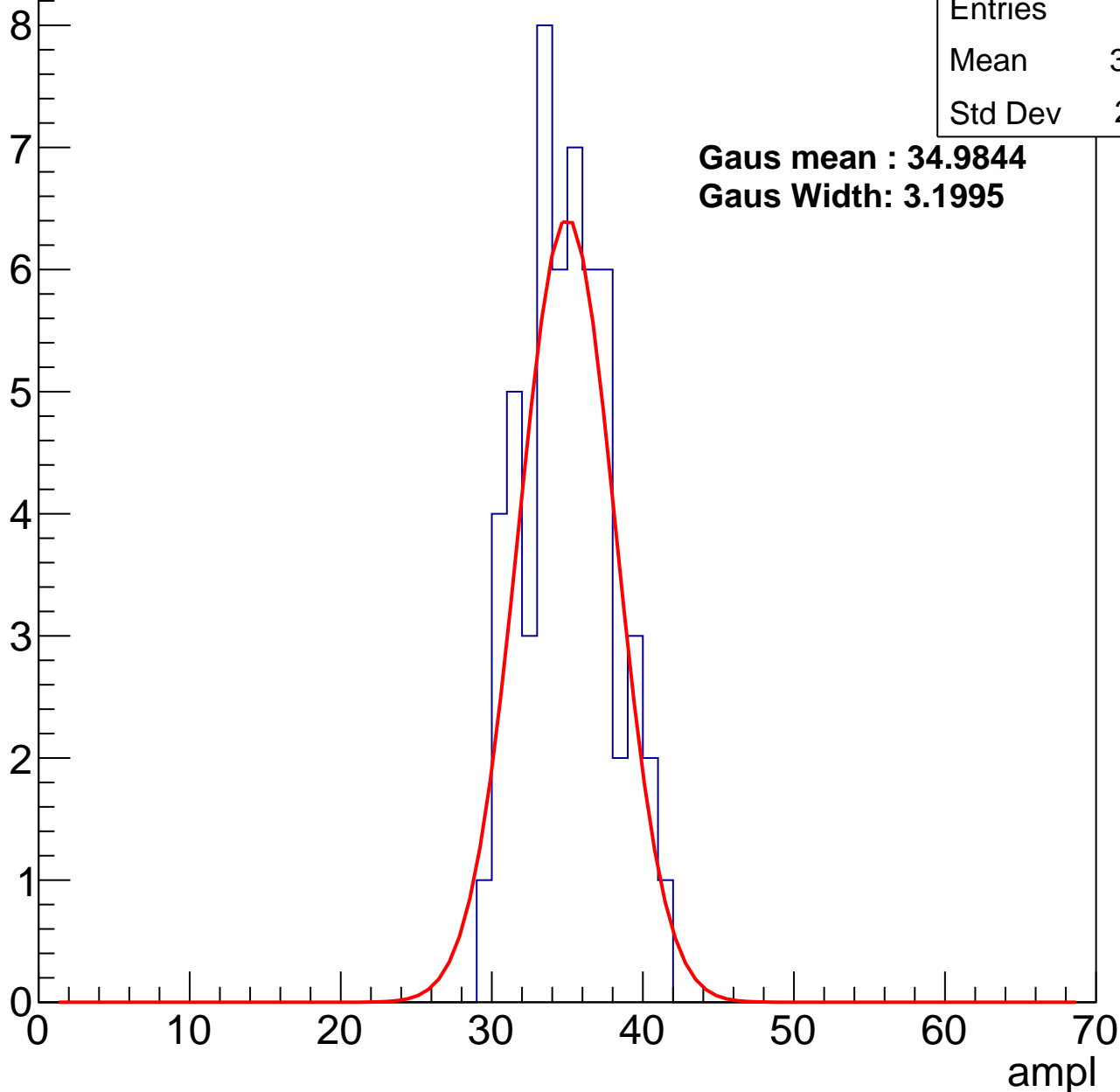
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.54
Std Dev	2.891

**Gaus mean : 34.9844**

**Gaus Width: 3.1995**



# B1L002S, U17-ch107, adc2

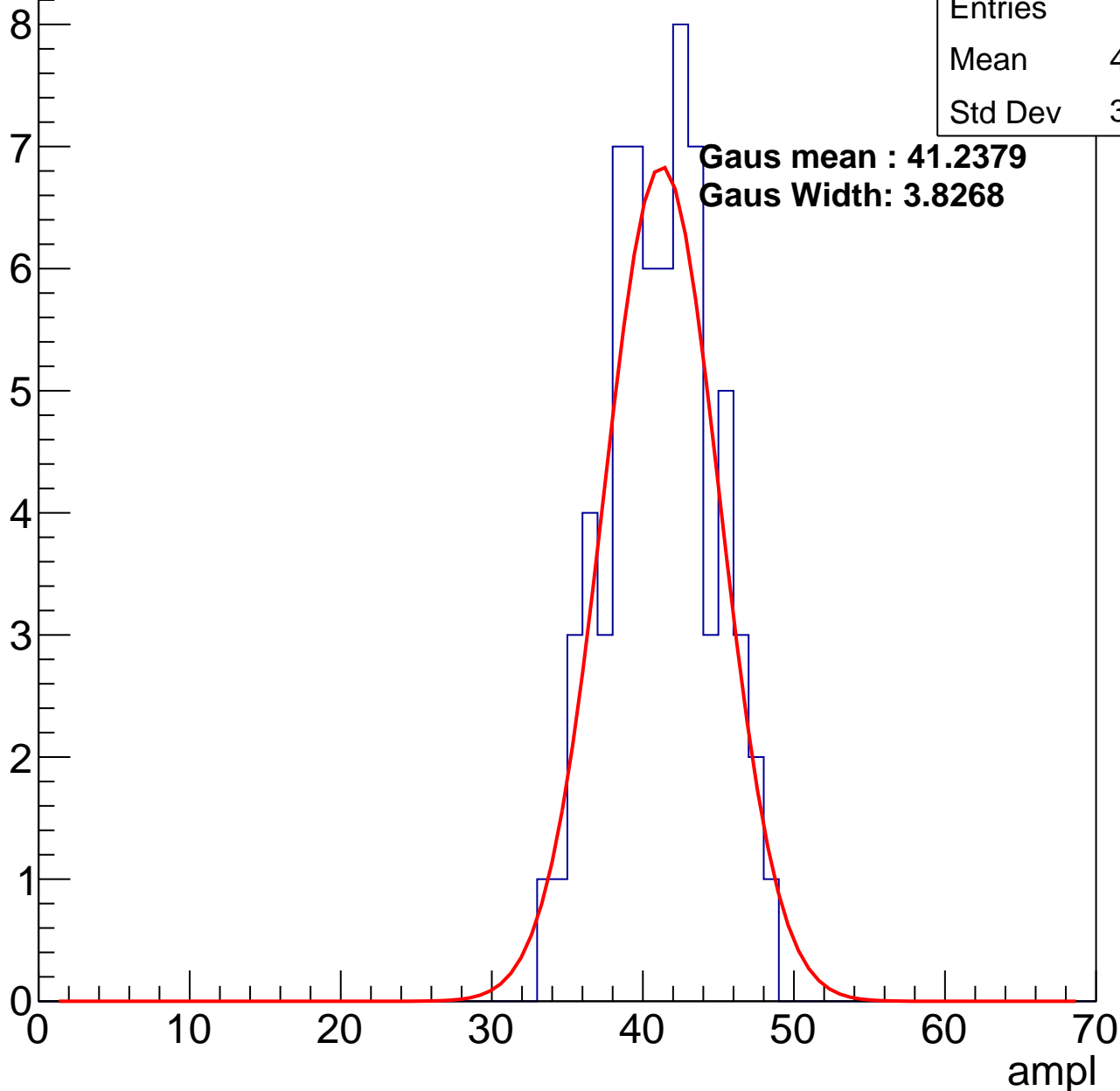
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.69
Std Dev	3.452

**Gaus mean : 41.2379**

**Gaus Width: 3.8268**

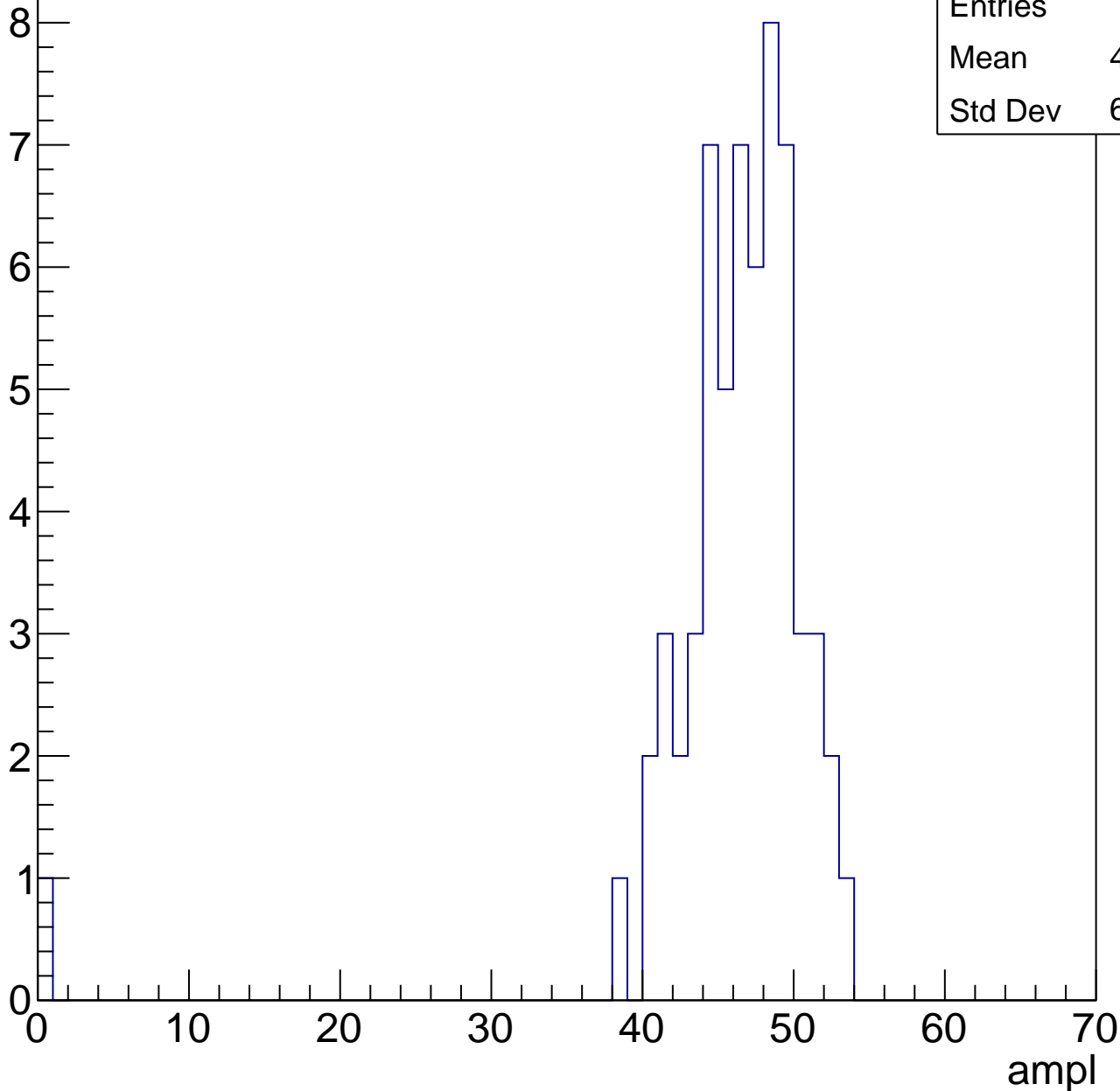


# B1L002S, U17-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.54
Std Dev	6.716

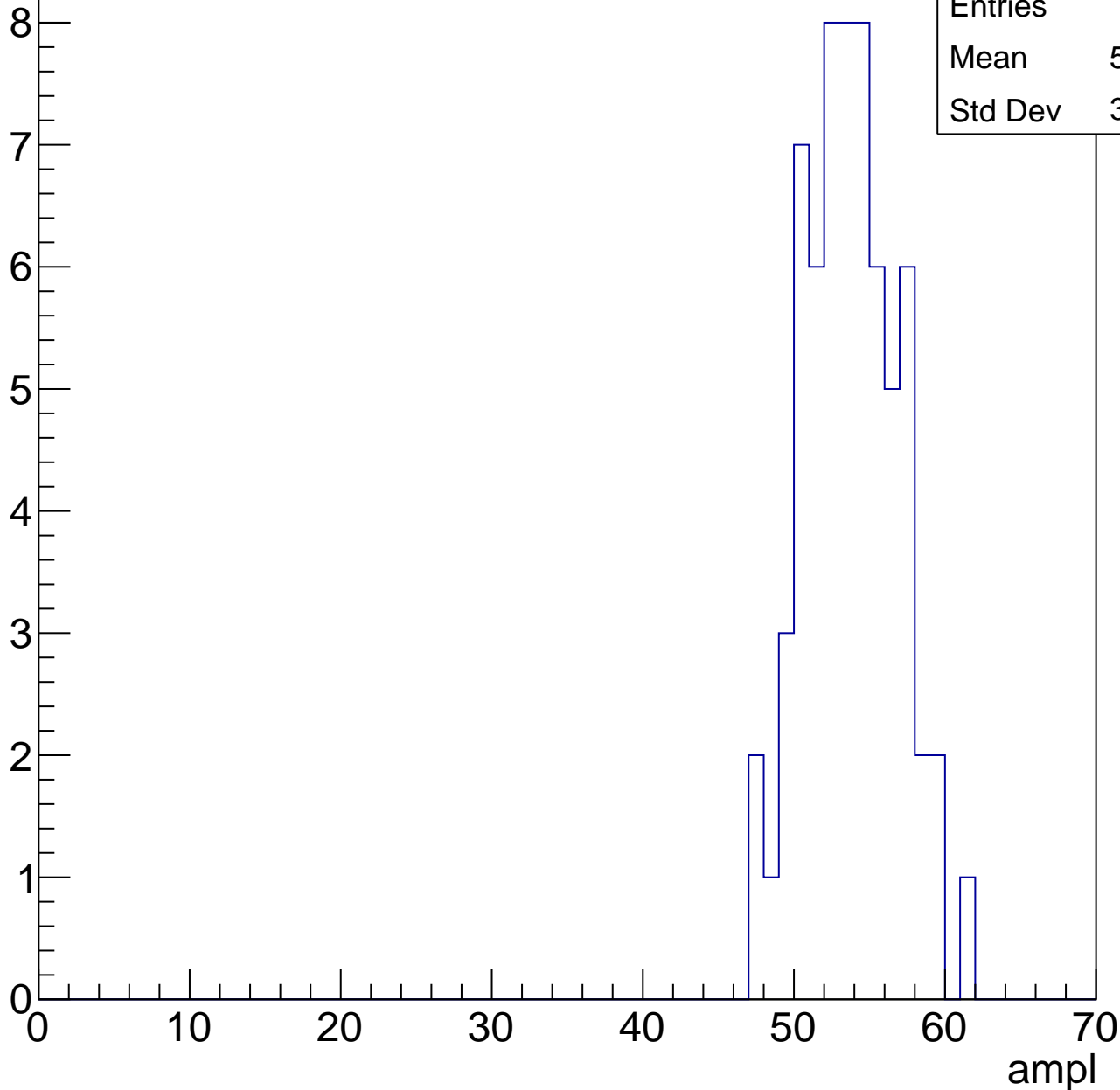


# B1L002S, U17-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	53.29
Std Dev	3.022

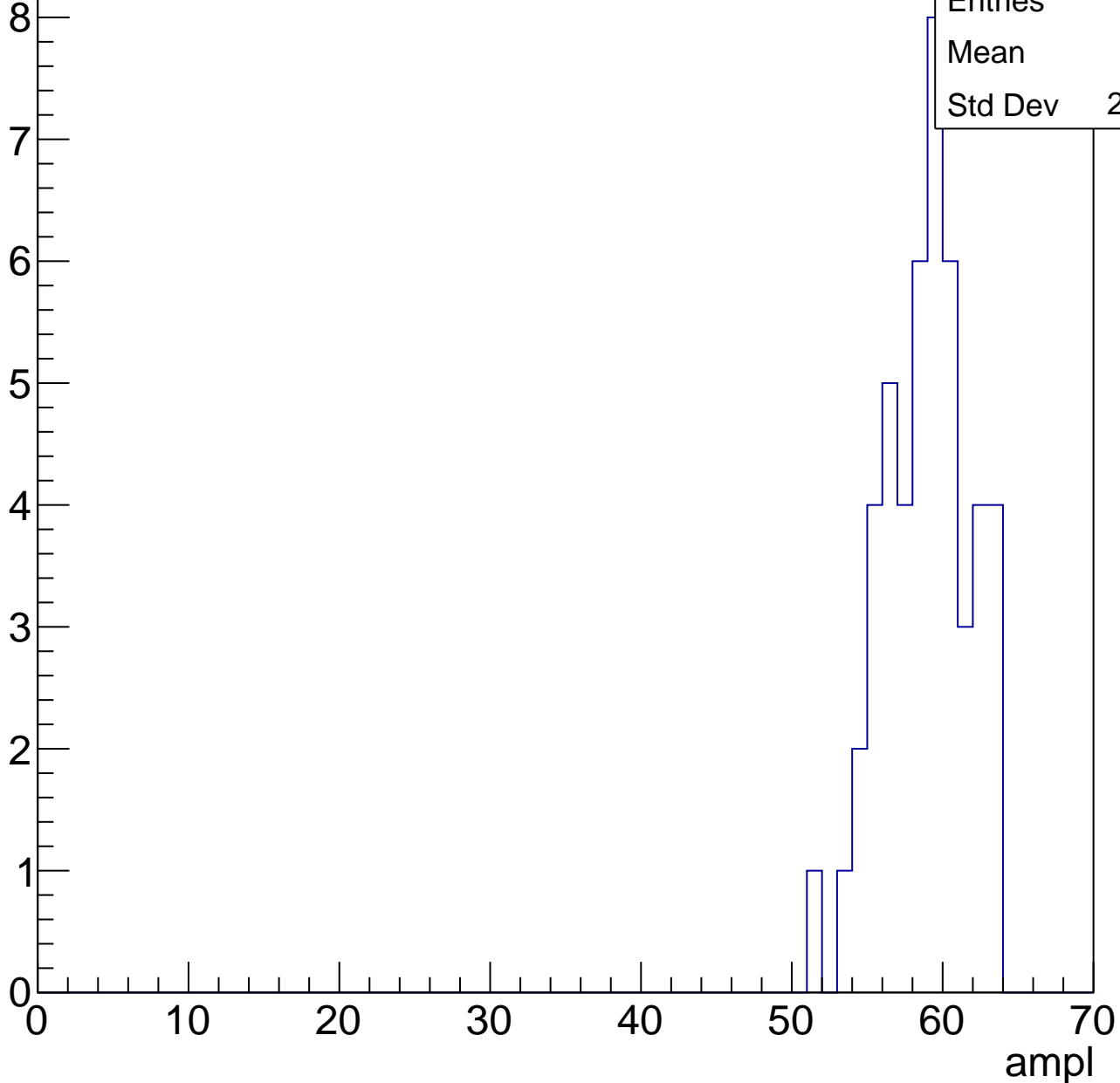


# B1L002S, U17-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.4
Std Dev	2.819

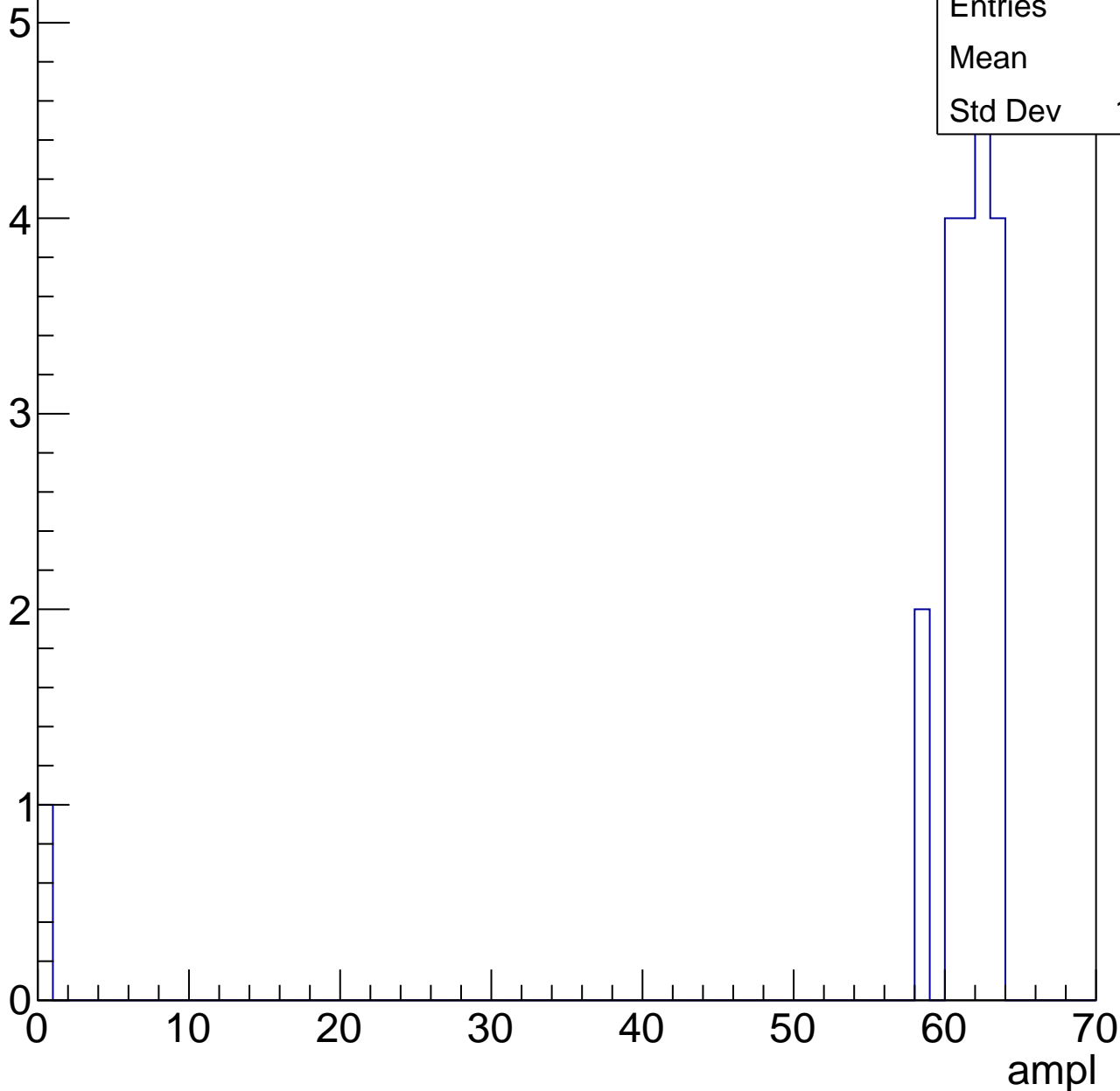


# B1L002S, U17-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.1
Std Dev	13.41

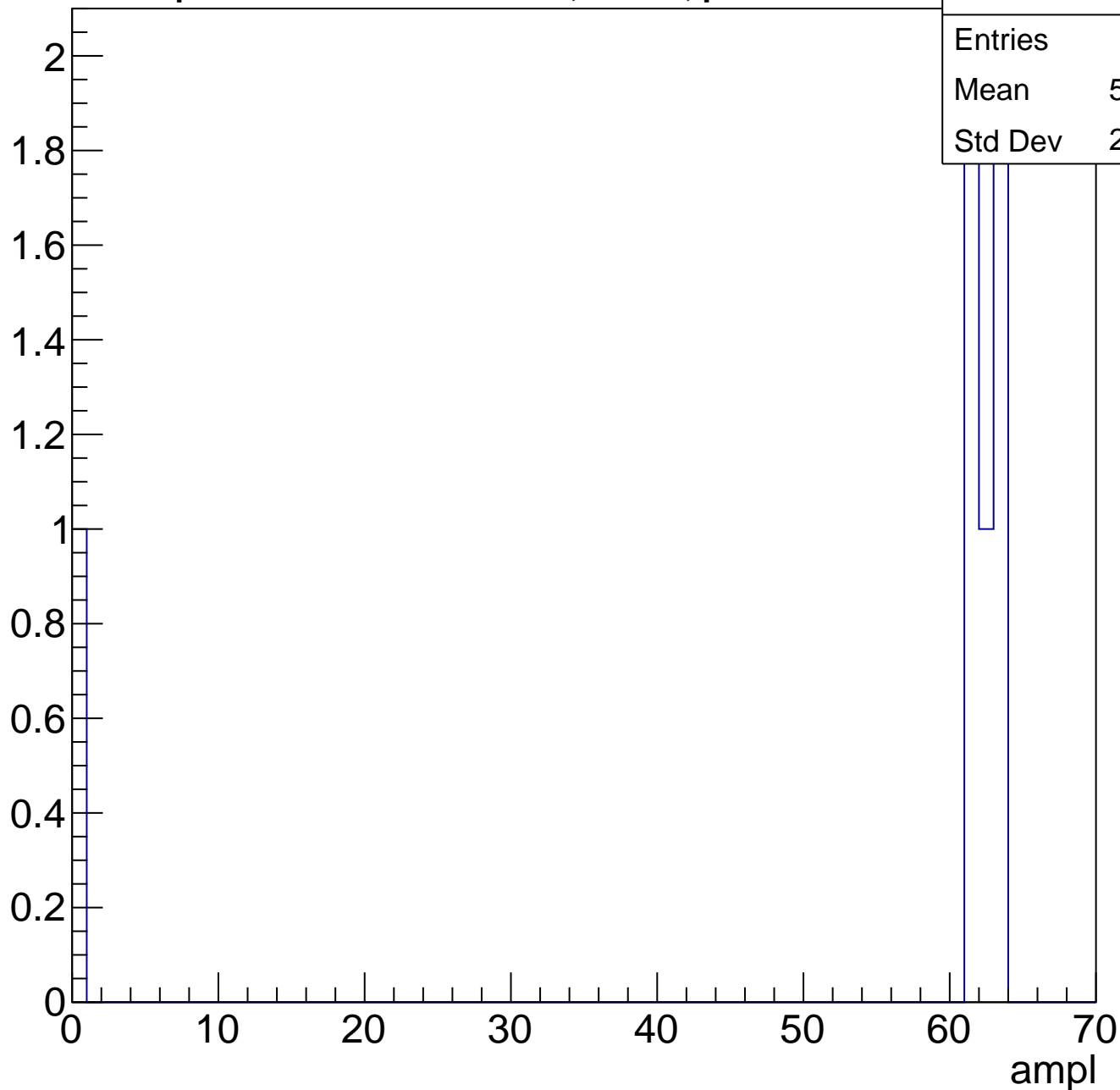




# B1L002S, U17-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch108, adc0

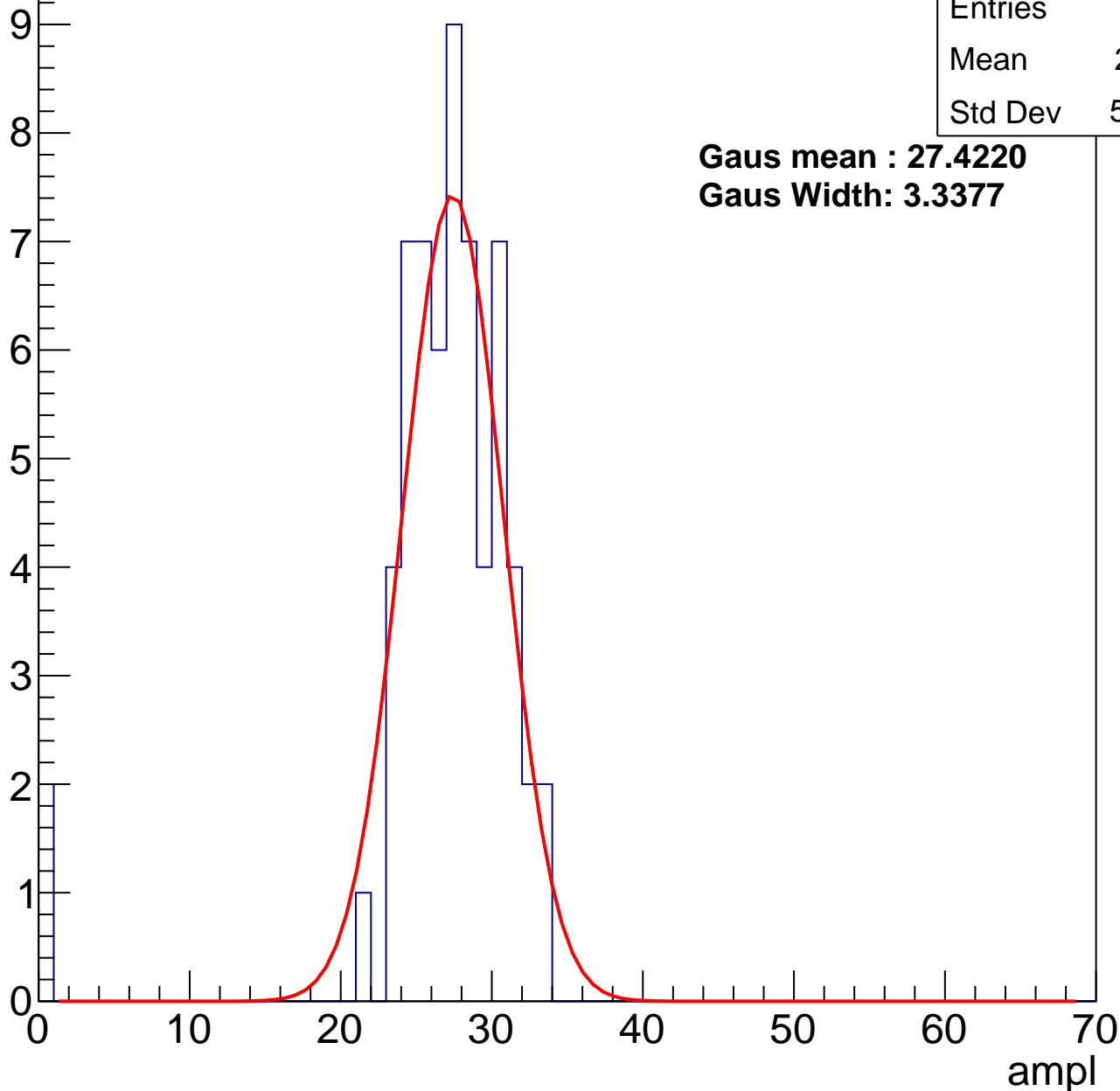
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26.31
Std Dev	5.532

**Gaus mean : 27.4220**

**Gaus Width: 3.3377**



# B1L002S, U17-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	33.87
Std Dev	3.539

**Gaus mean : 34.8862**

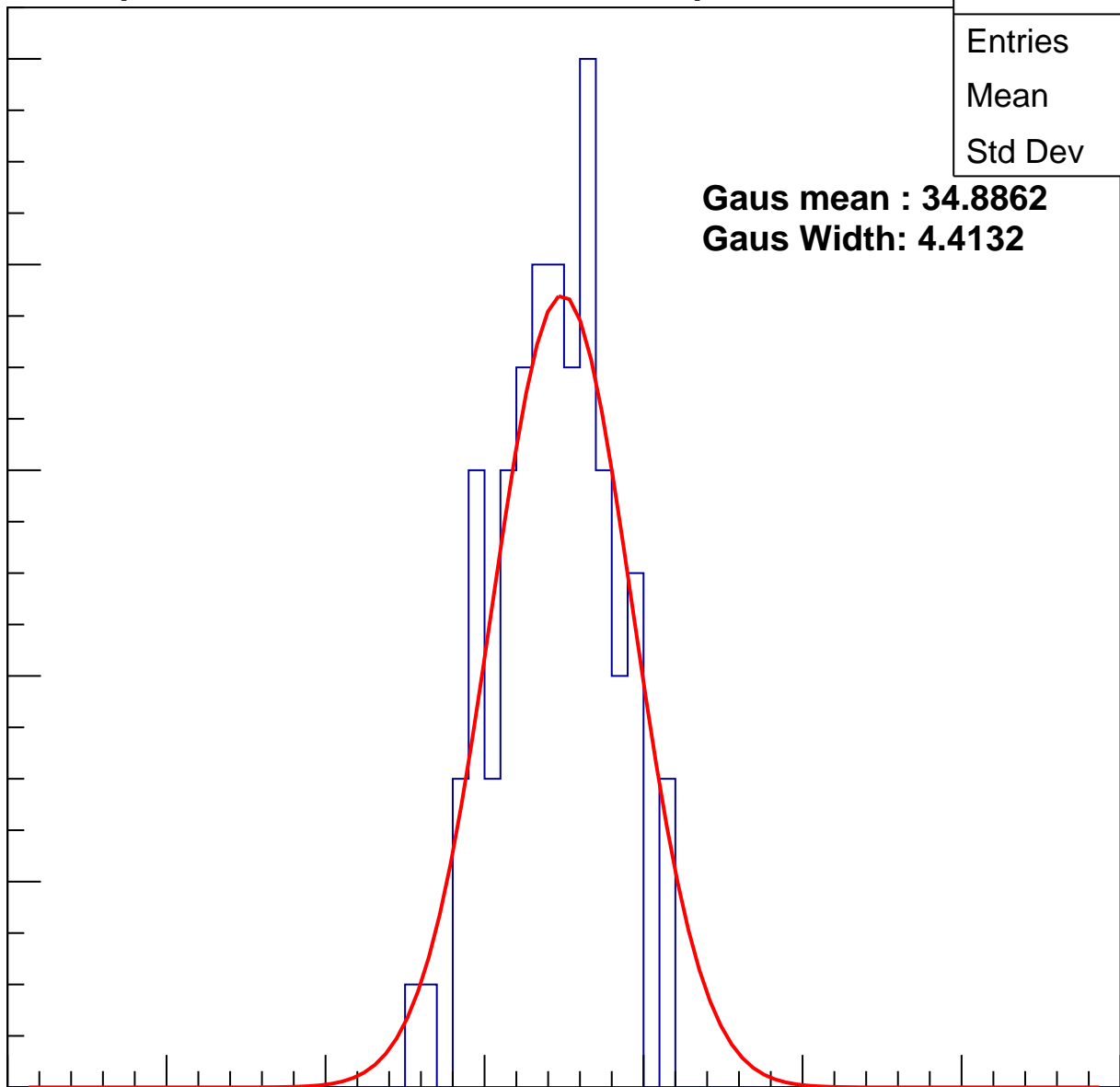
**Gaus Width: 4.4132**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch108, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	40.38
Std Dev	3.362

**Gaus mean : 40.0992**

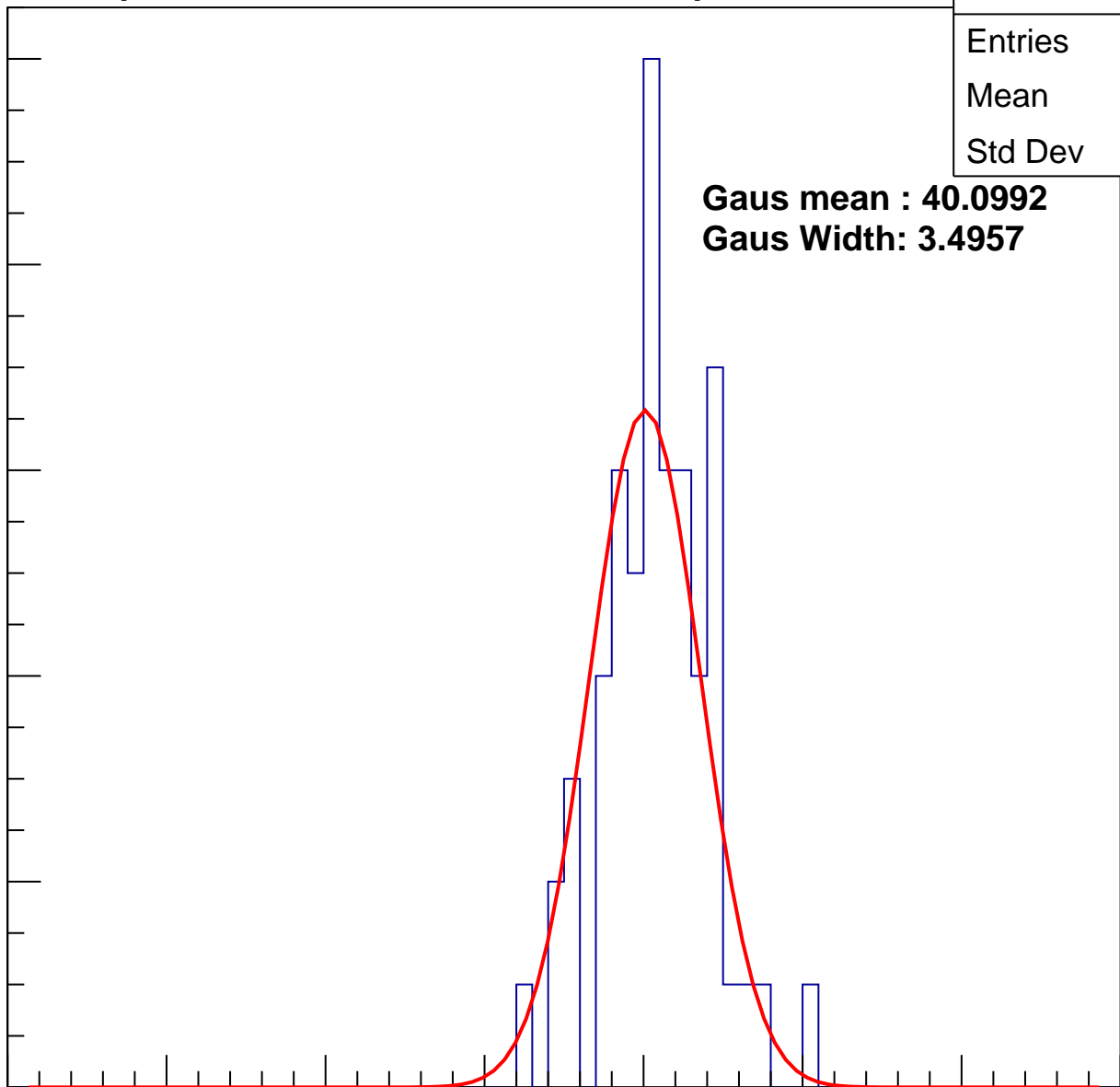
**Gaus Width: 3.4957**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

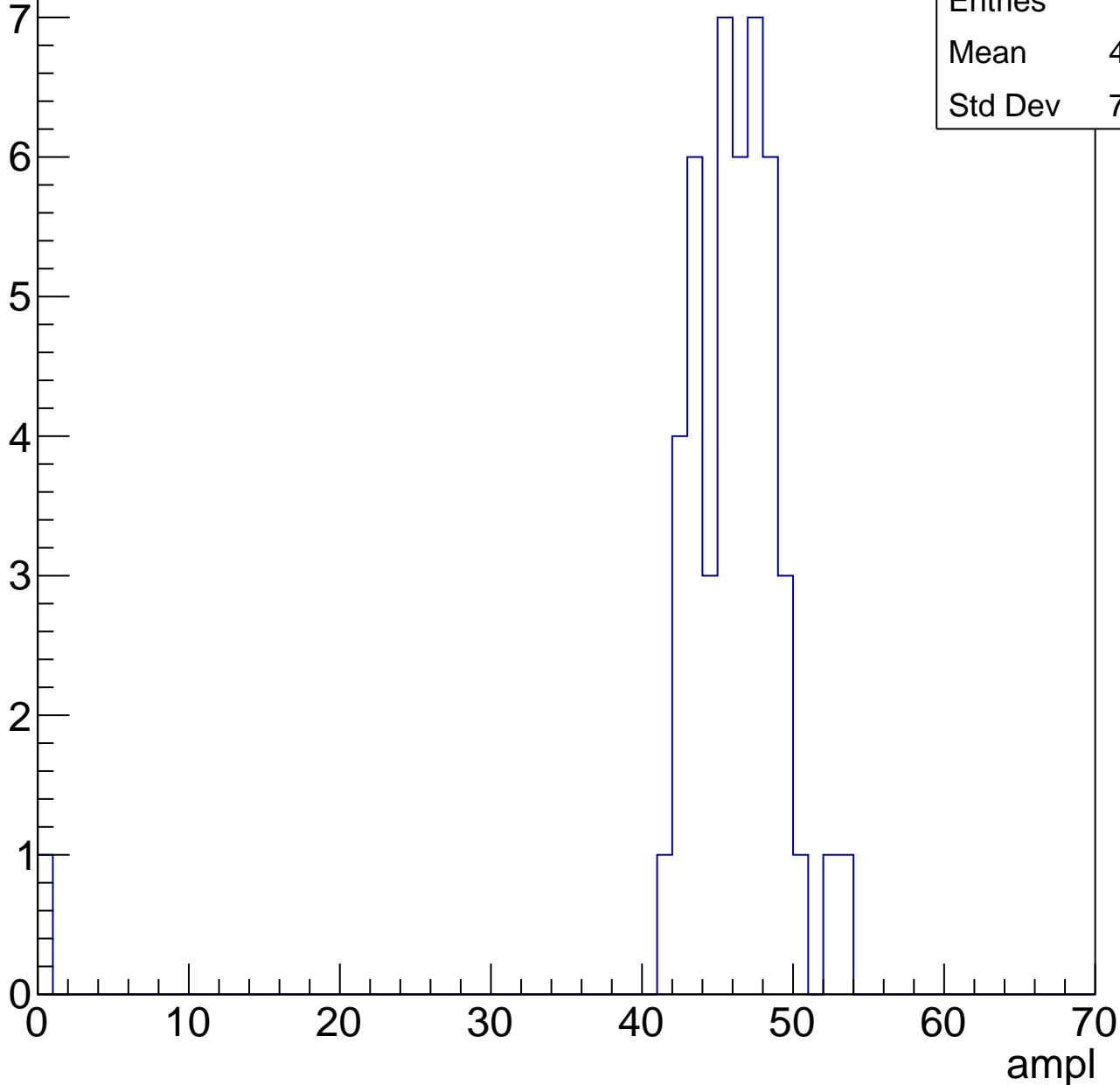


# B1L002S, U17-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	44.87
Std Dev	7.112

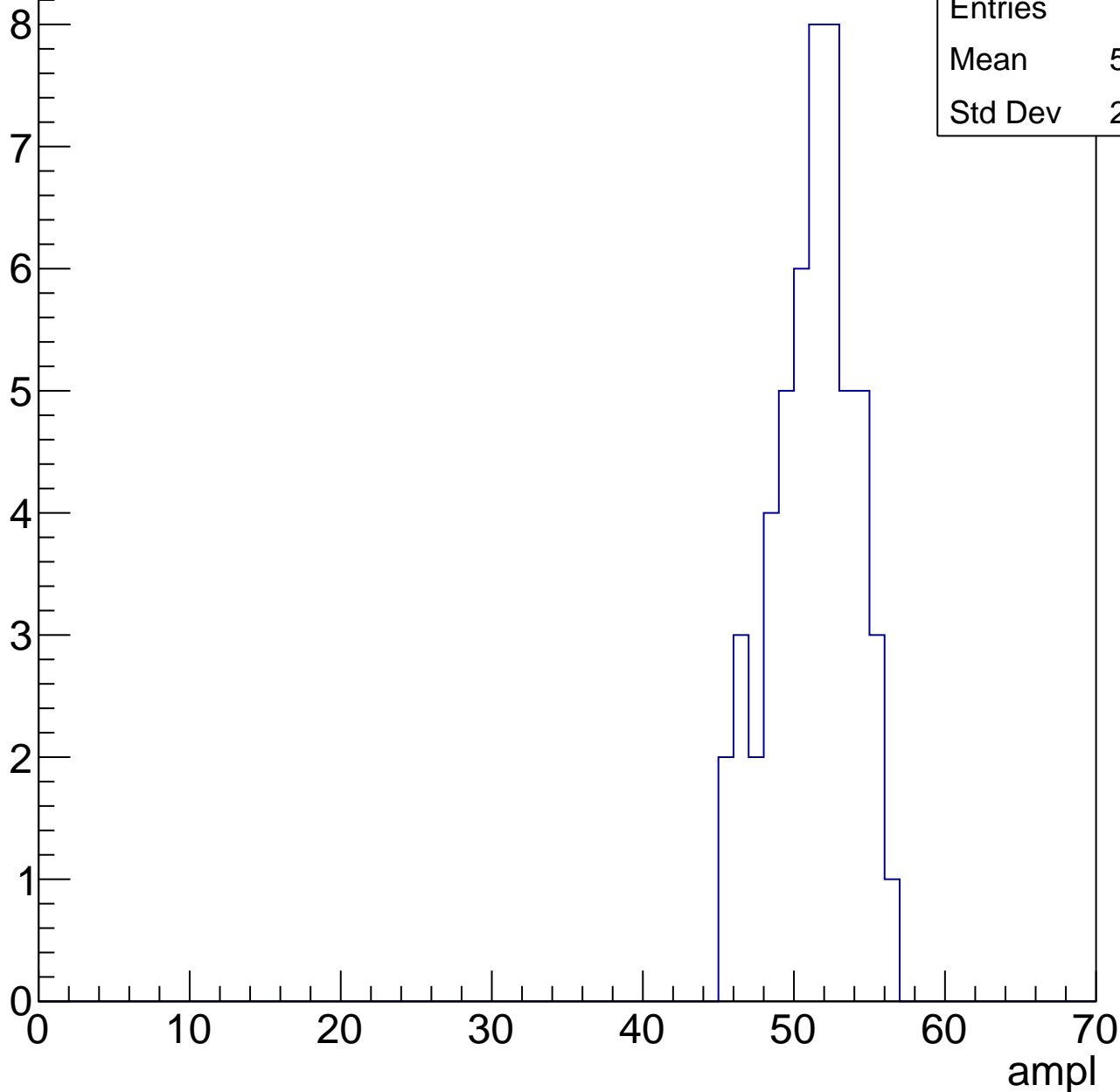


# B1L002S, U17-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

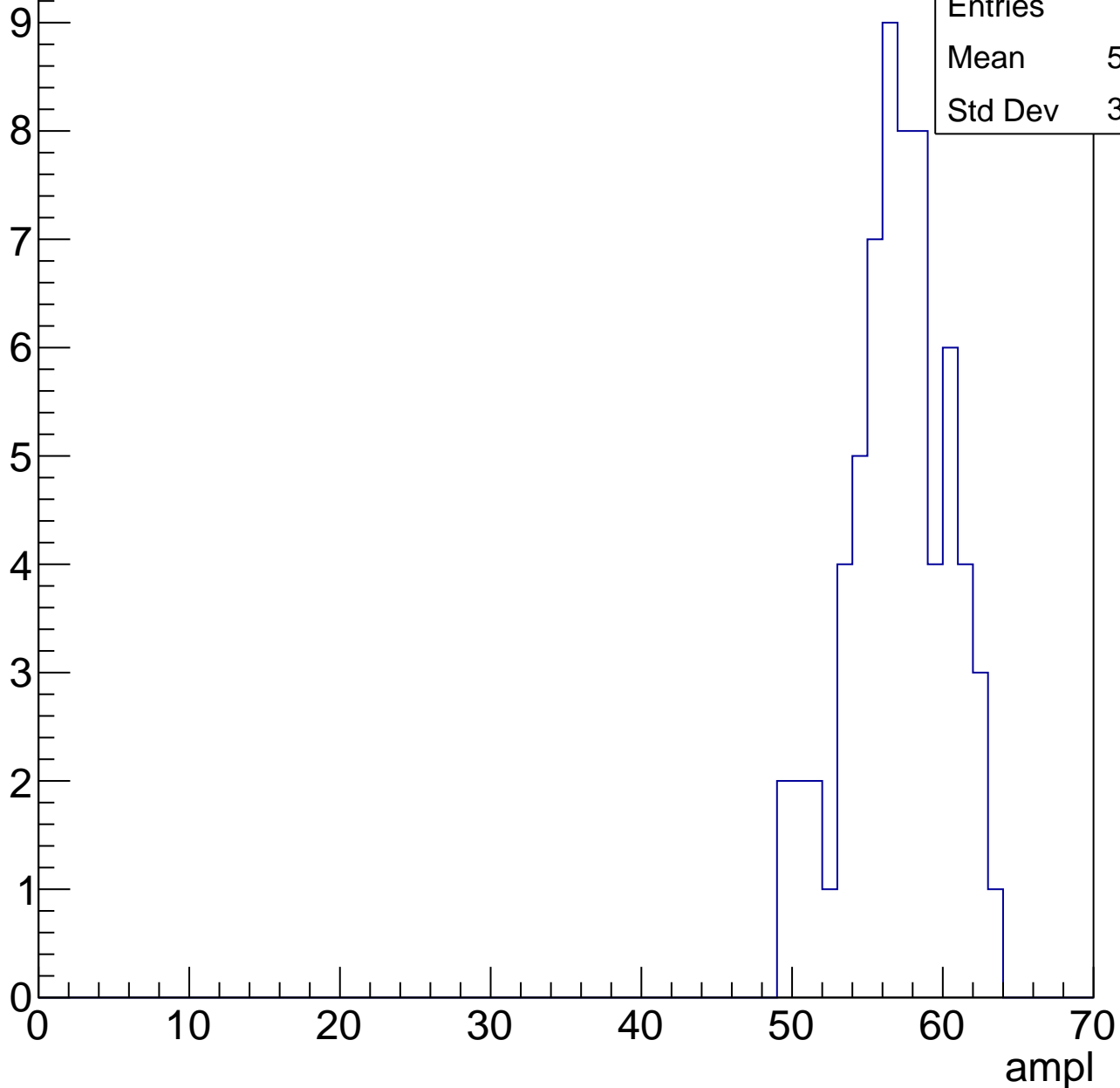
Entries	52
Mean	50.75
Std Dev	2.717



# B1L002S, U17-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

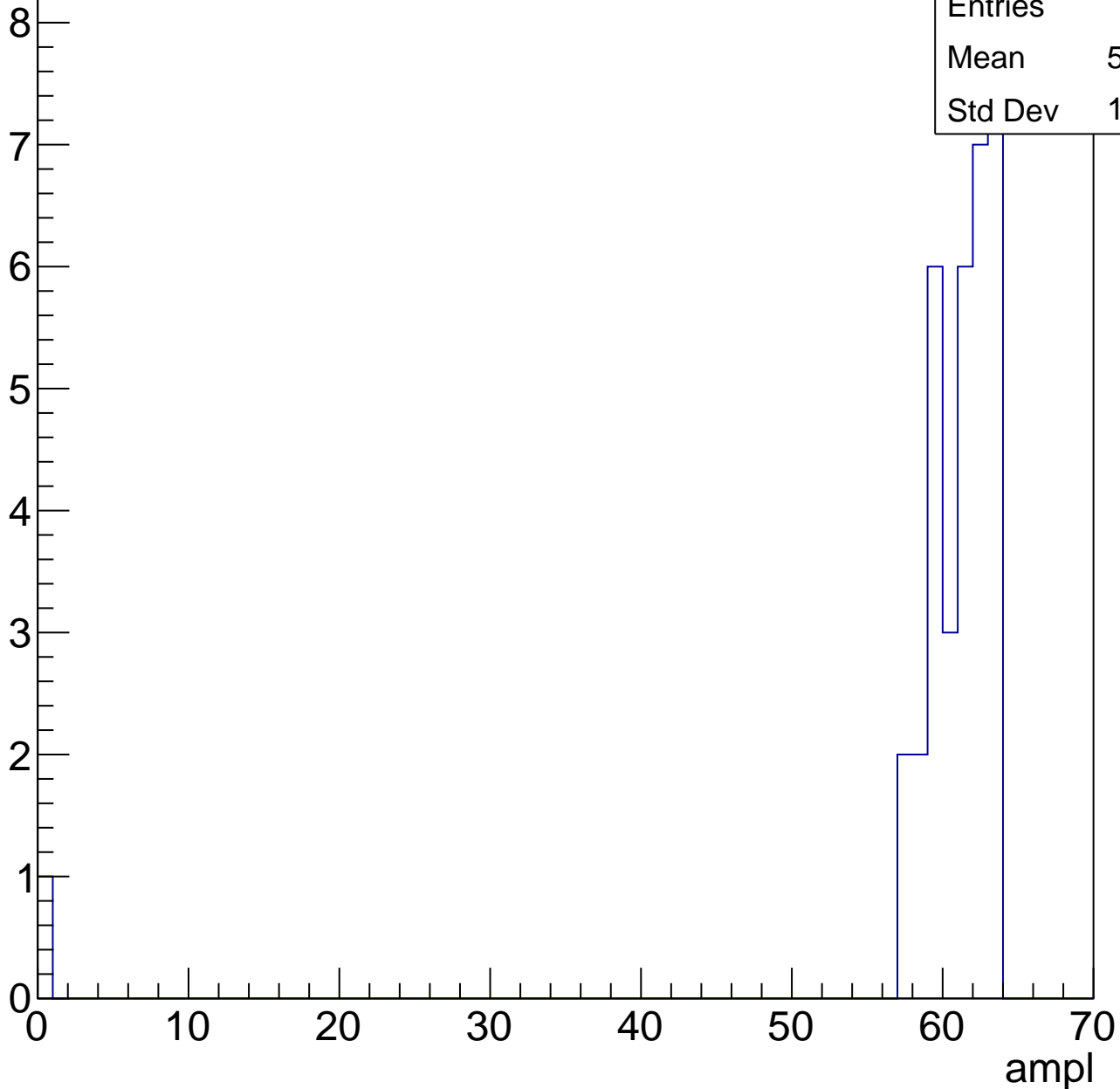


# B1L002S, U17-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	59.09
Std Dev	10.29

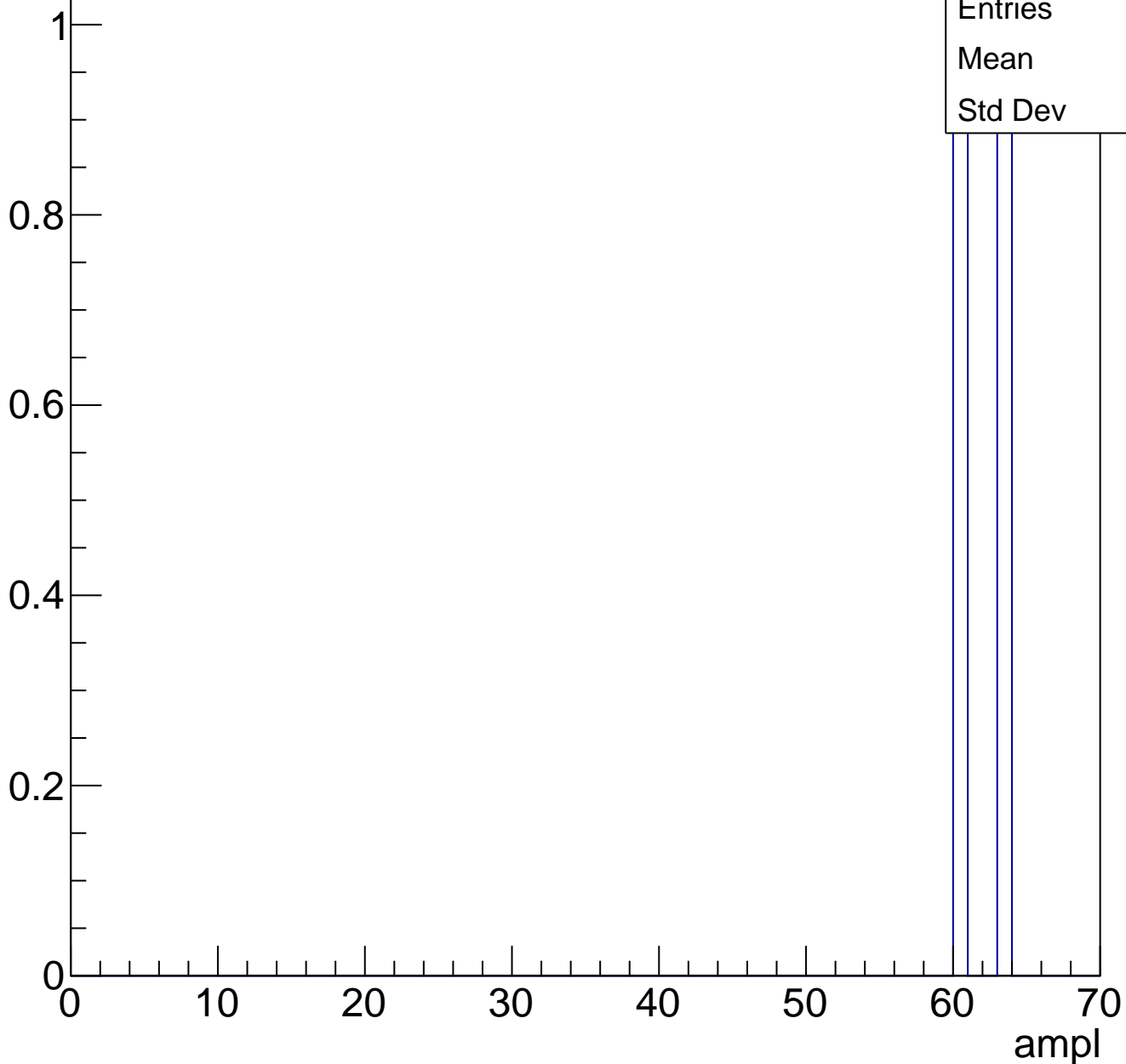




# B1L002S, U17-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch109, adc0

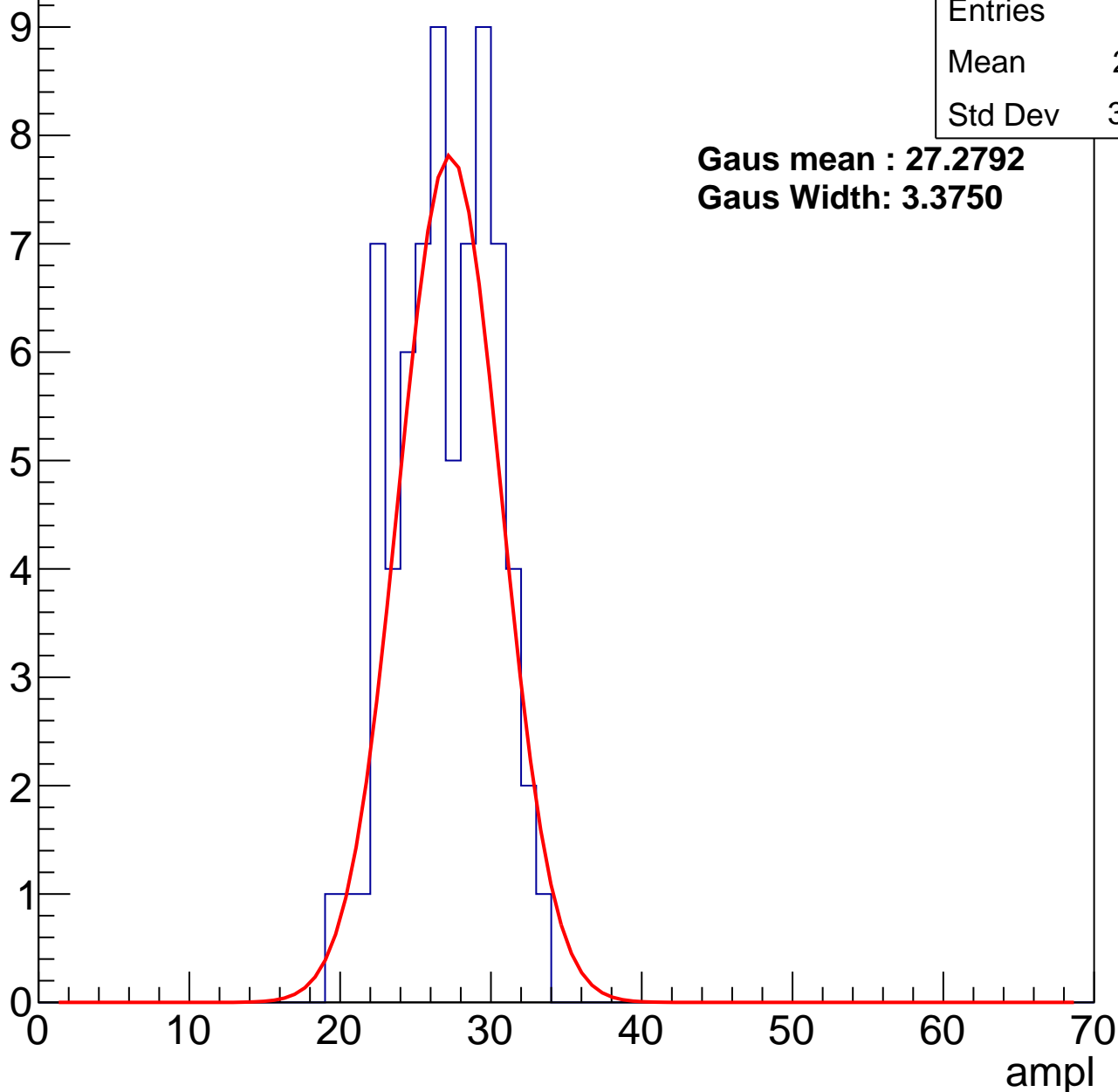
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	26.51
Std Dev	3.179

**Gaus mean : 27.2792**

**Gaus Width: 3.3750**



# B1L002S, U17-ch109, adc1

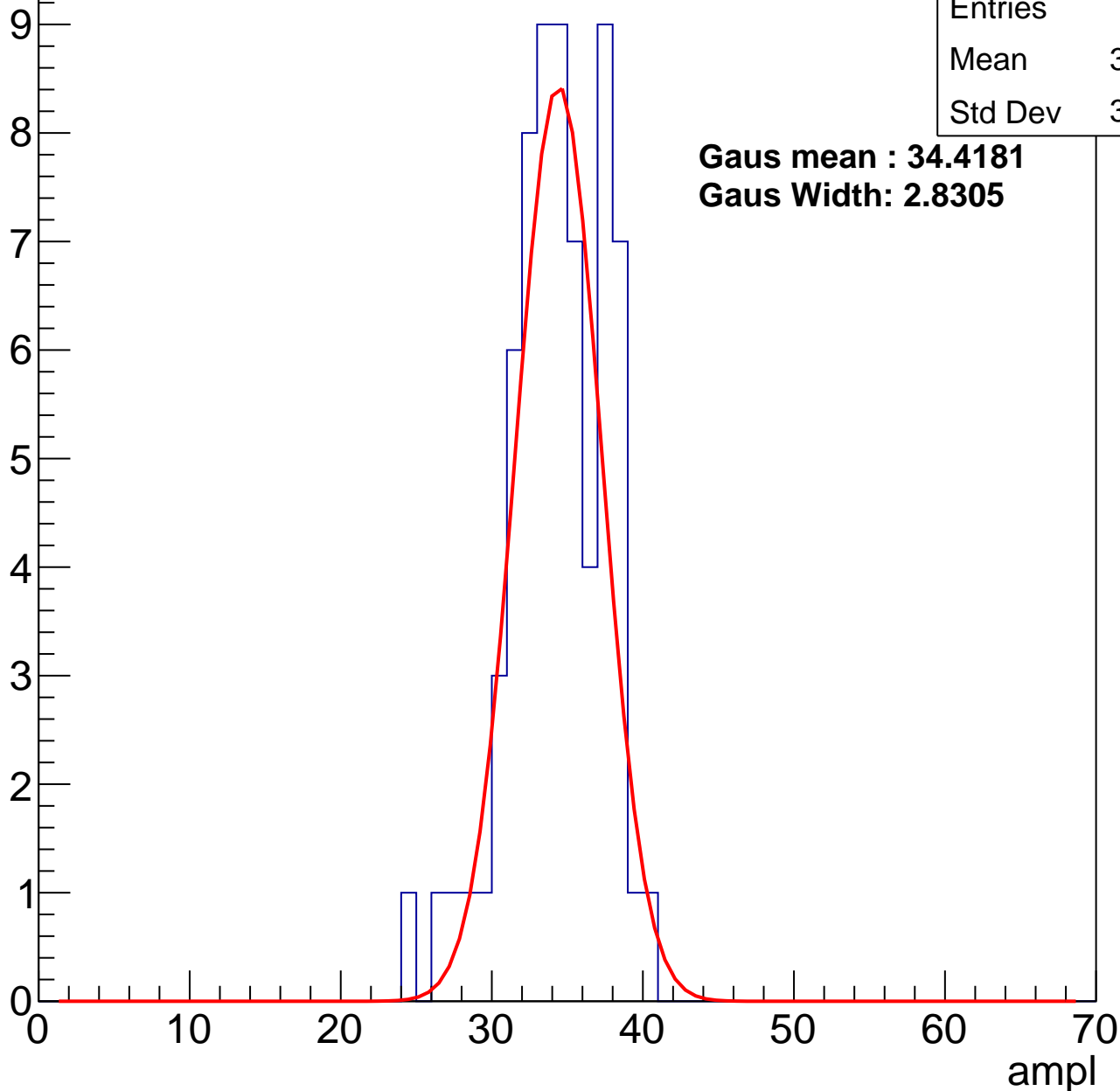
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	33.86
Std Dev	3.168

**Gaus mean : 34.4181**

**Gaus Width: 2.8305**



# B1L002S, U17-ch109, adc2

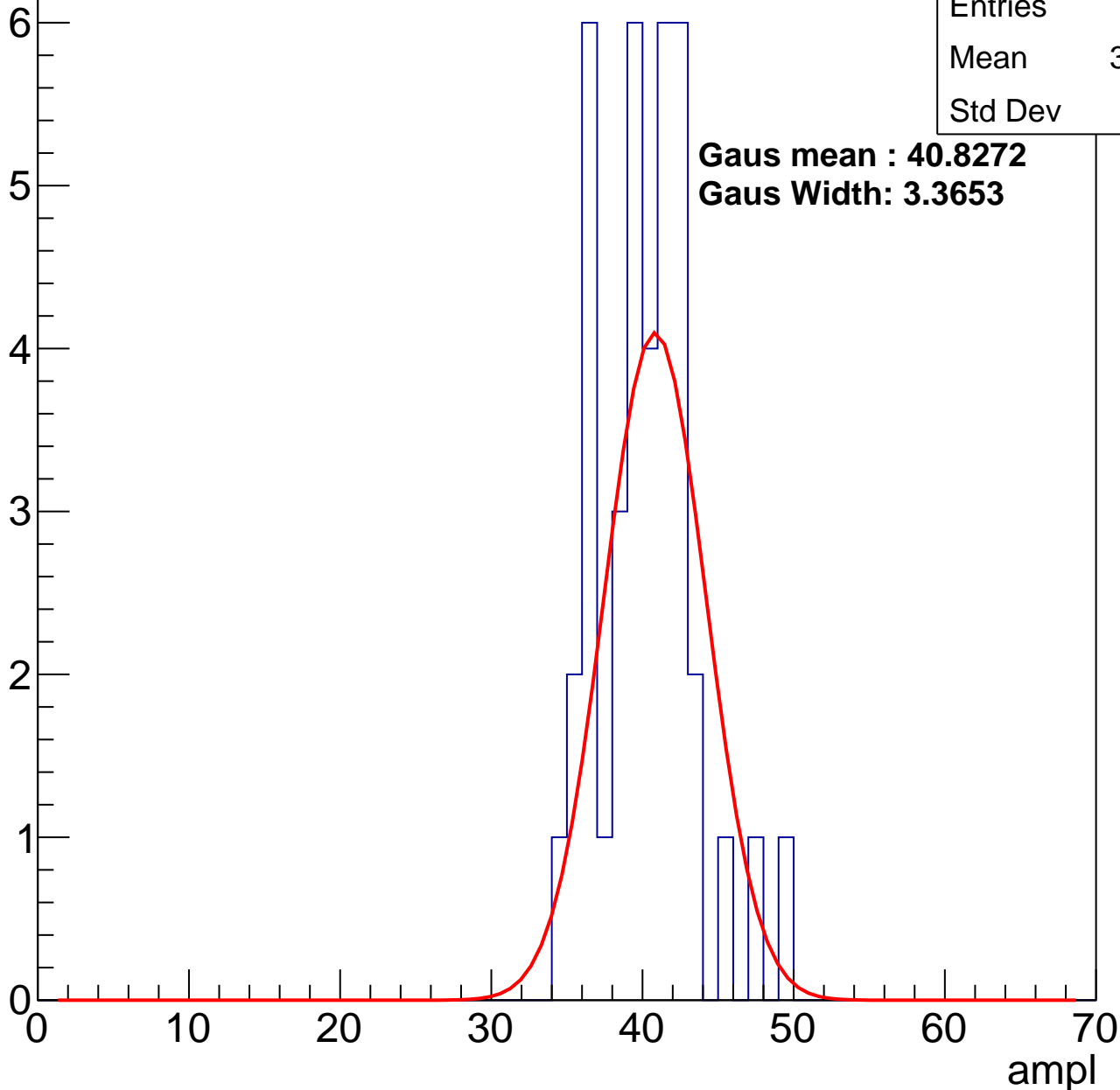
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	39.75
Std Dev	3.2

**Gaus mean : 40.8272**

**Gaus Width: 3.3653**

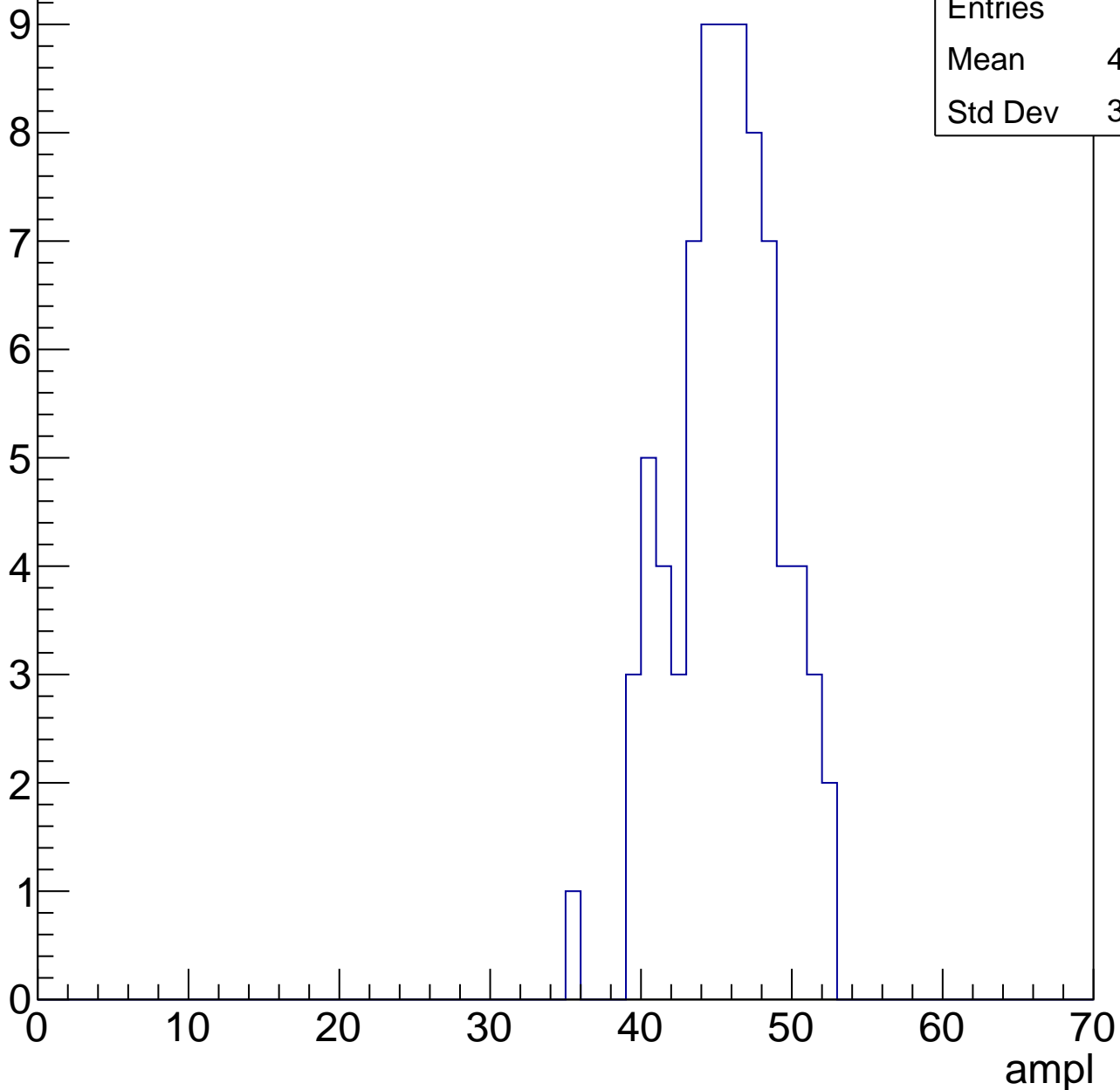


# B1L002S, U17-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	45.17
Std Dev	3.458

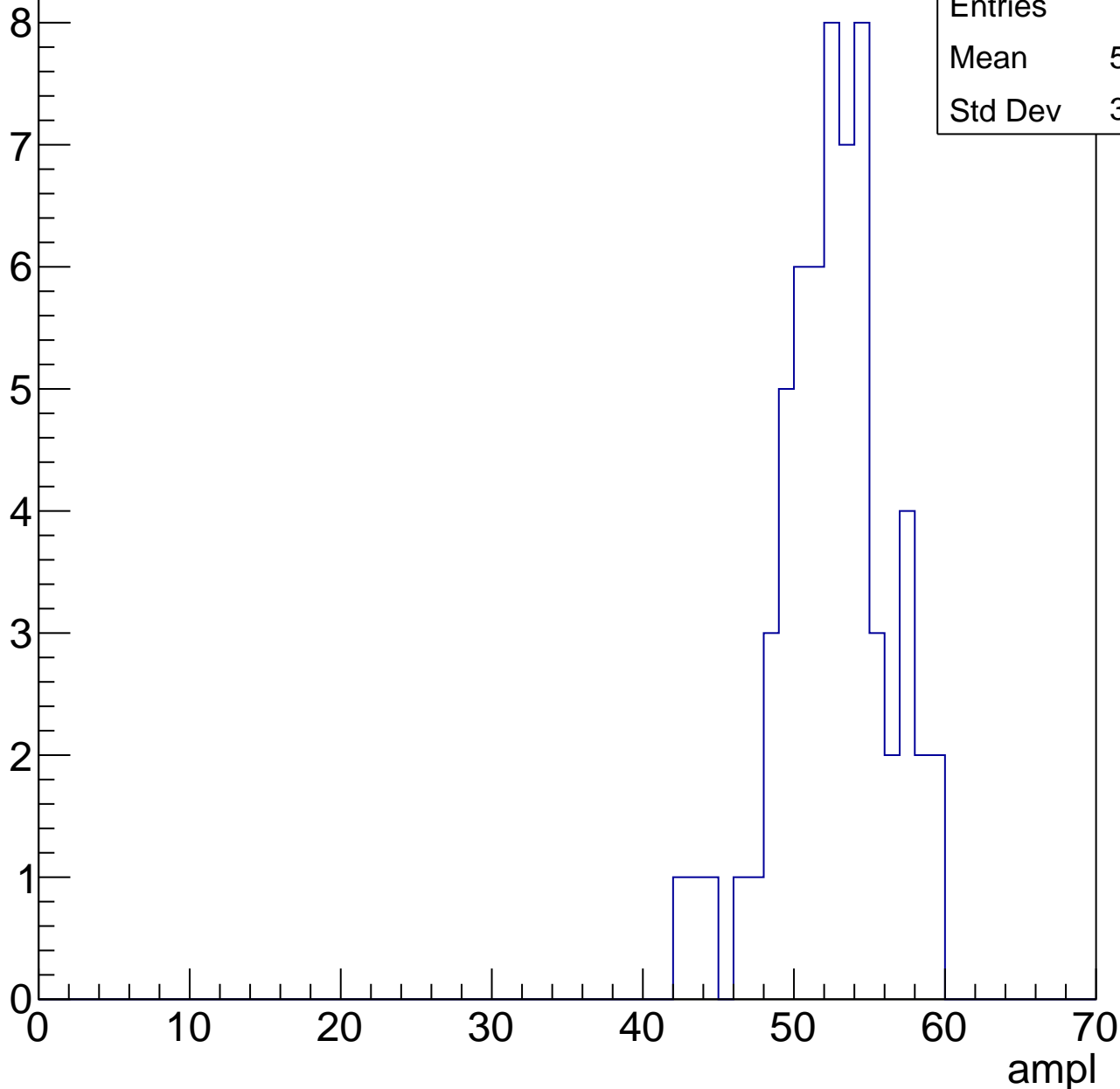


# B1L002S, U17-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	52.05
Std Dev	3.614

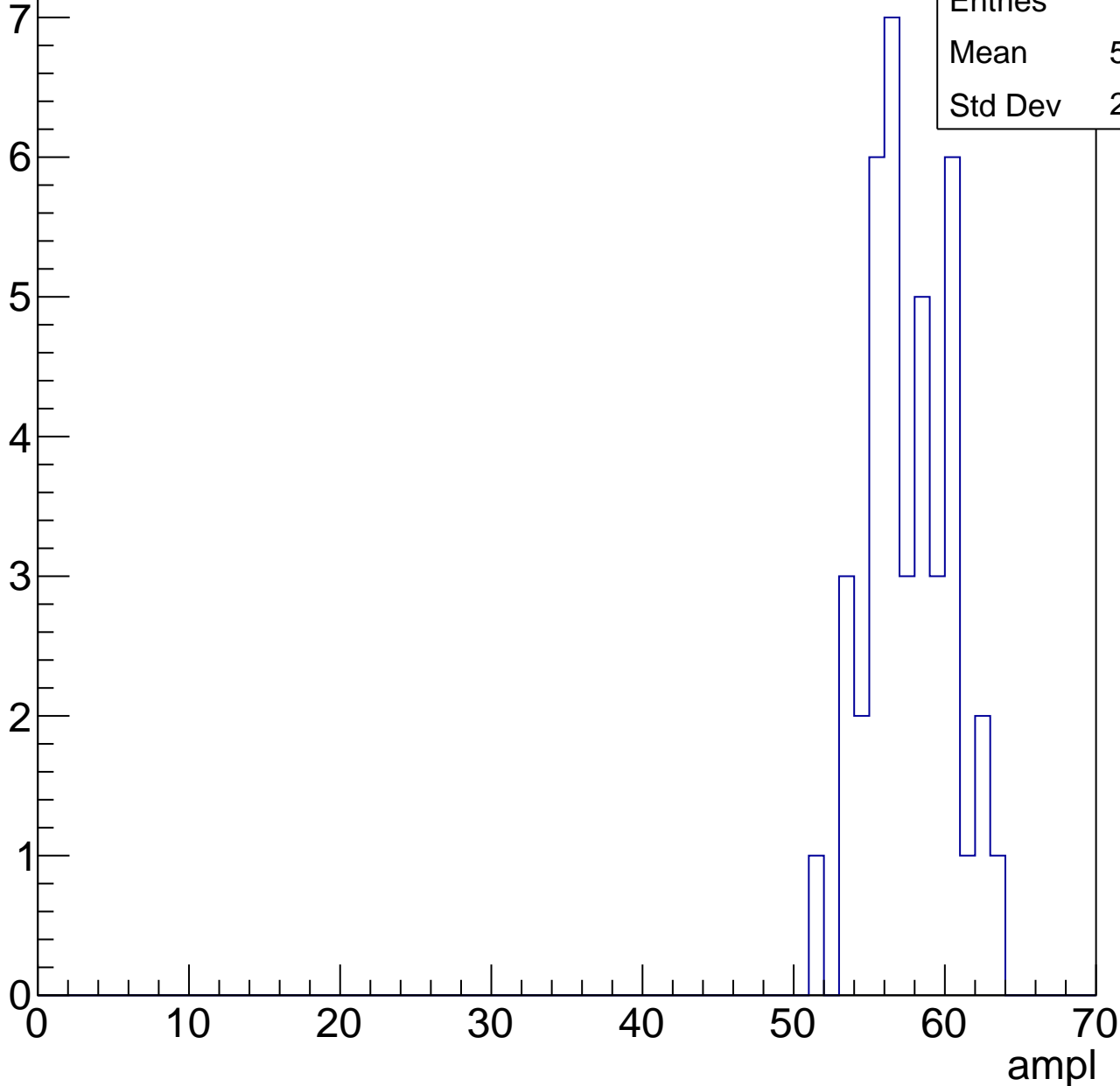


# B1L002S, U17-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	57.15
Std Dev	2.762

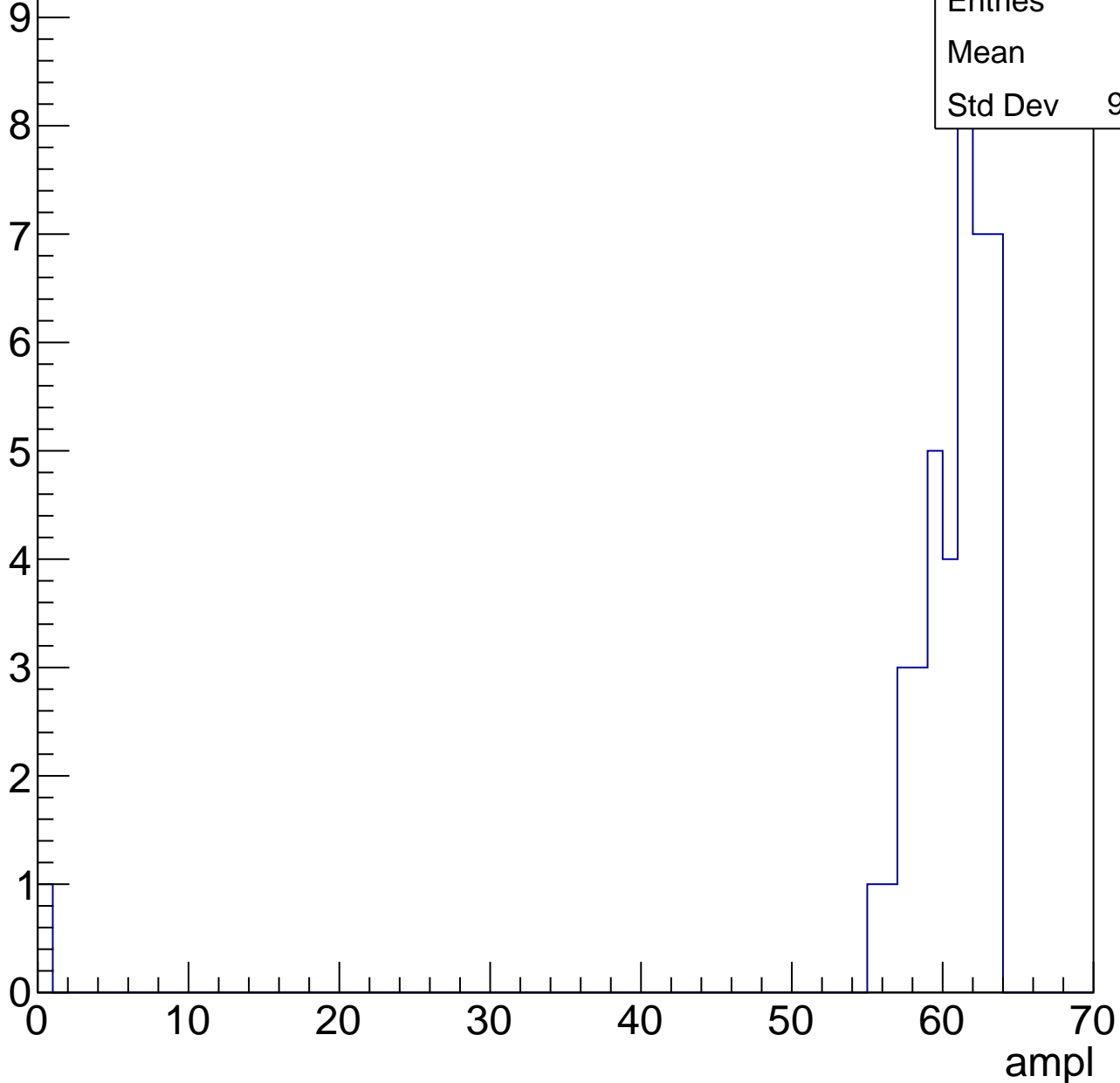


# B1L002S, U17-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	58.9
Std Dev	9.545

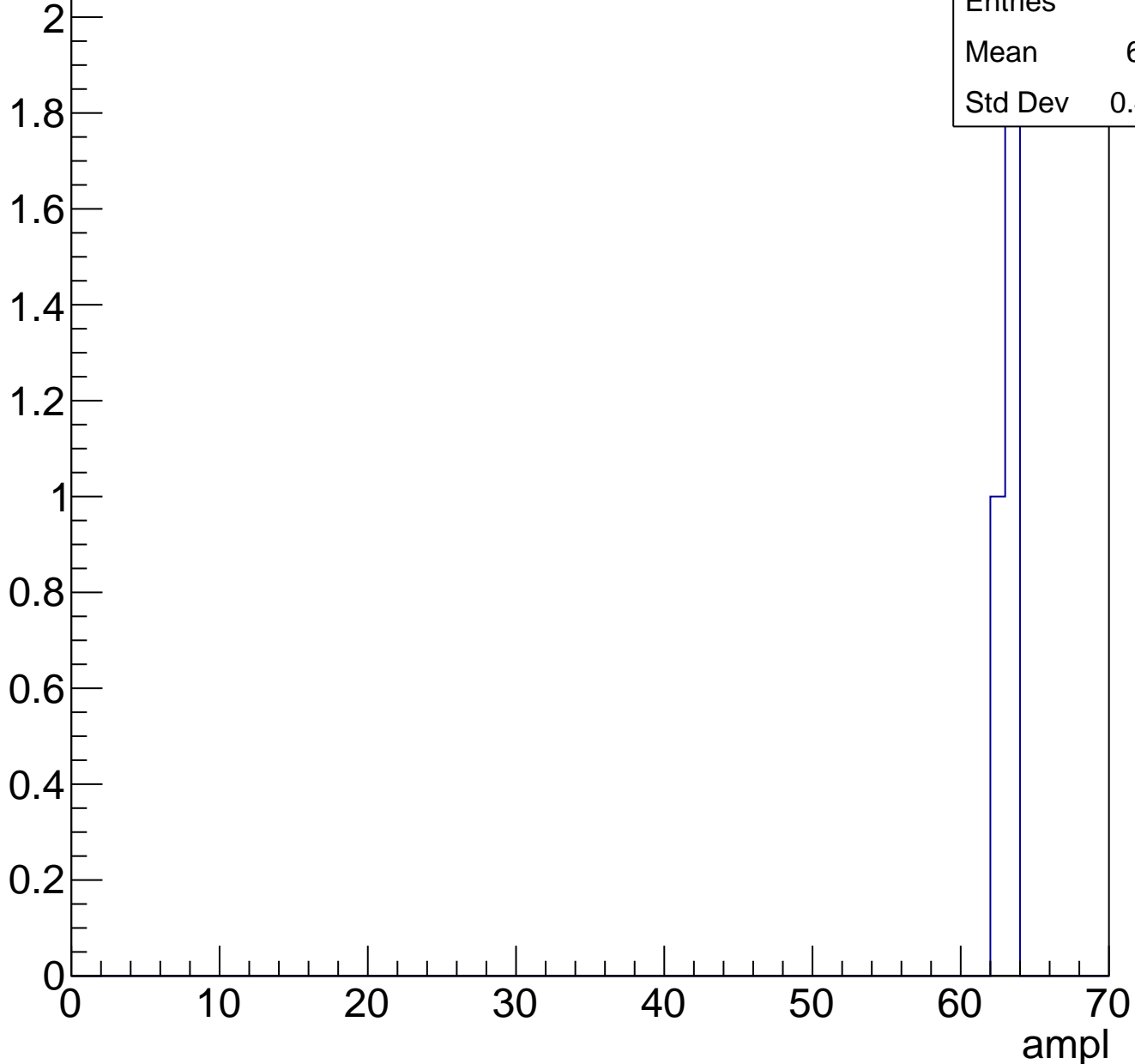




# B1L002S, U17-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	26.9
Std Dev	4.755

**Gaus mean : 27.6026**

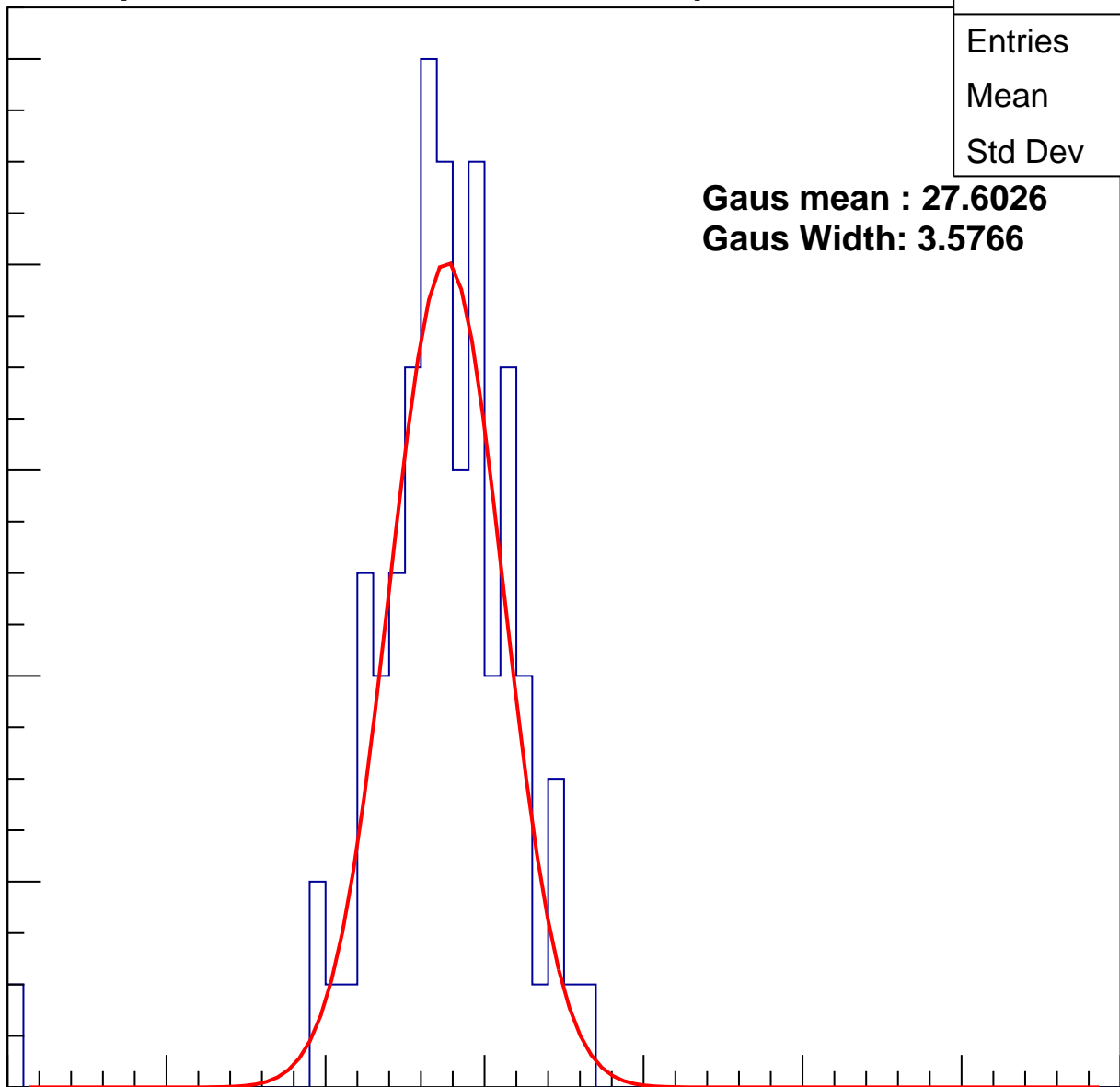
**Gaus Width: 3.5766**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch110, adc1

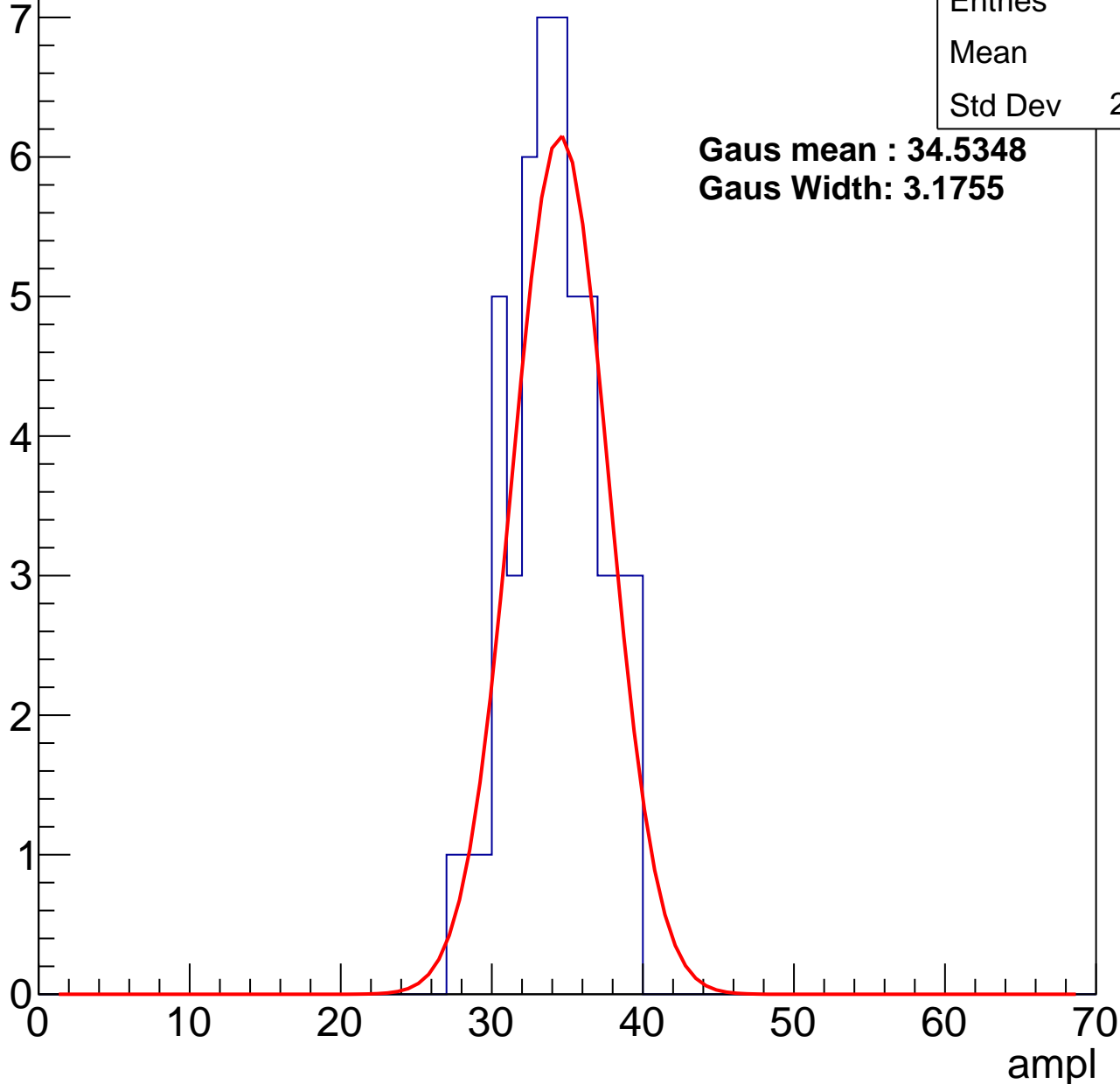
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	33.7
Std Dev	2.893

**Gaus mean : 34.5348**

**Gaus Width: 3.1755**



# B1L002S, U17-ch110, adc2

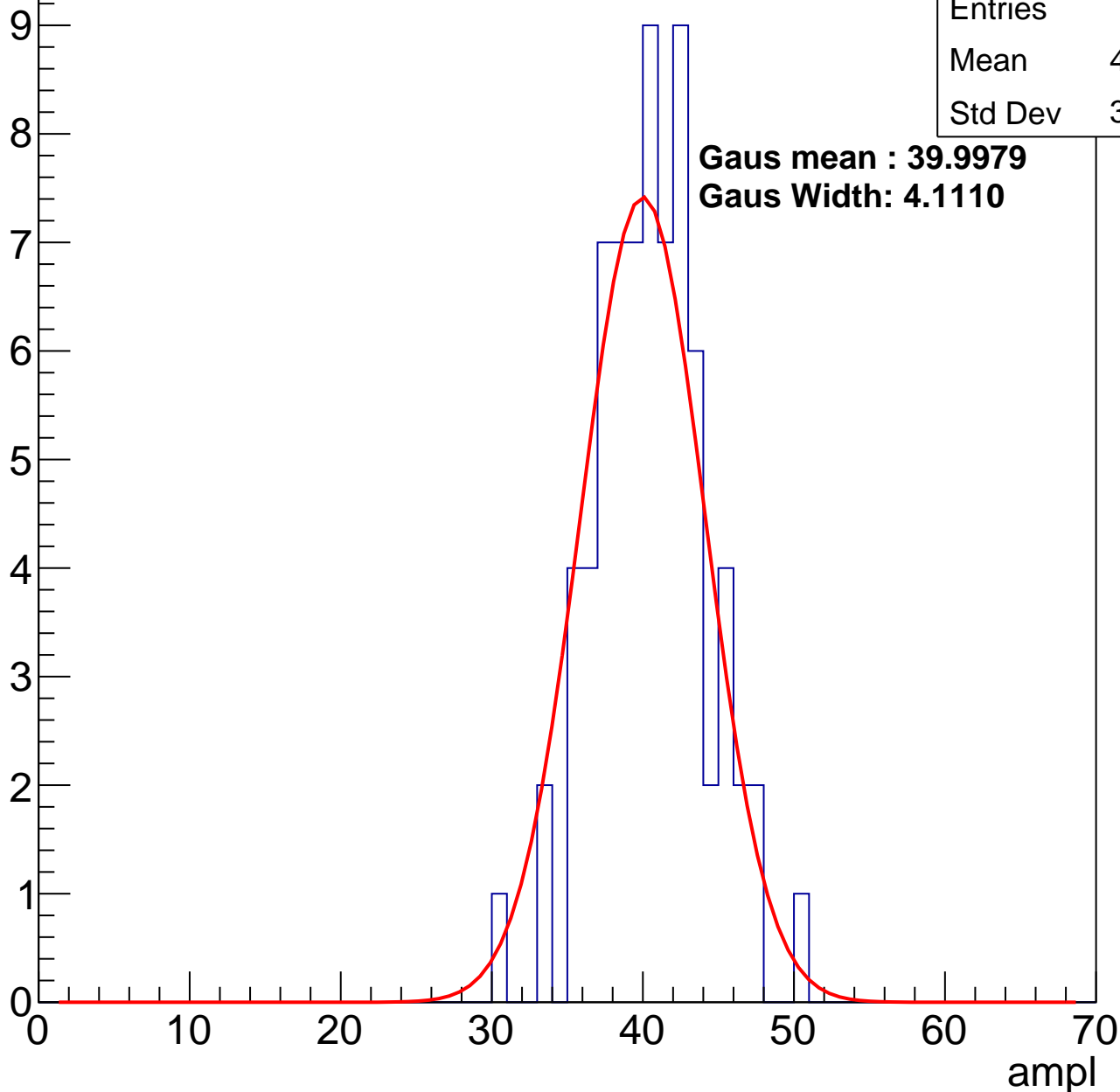
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	40.07
Std Dev	3.599

**Gaus mean : 39.9979**

**Gaus Width: 4.1110**

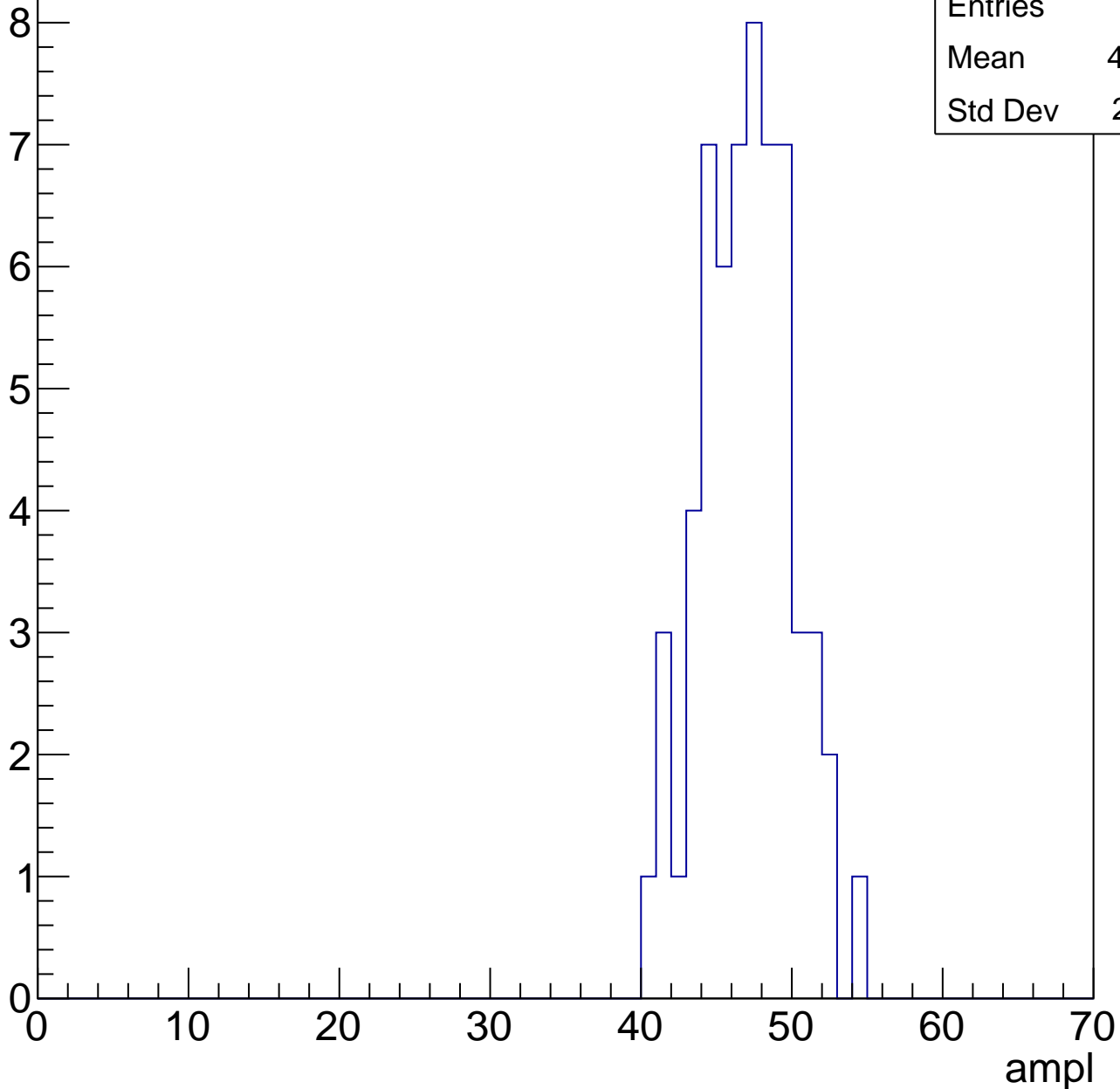


# B1L002S, U17-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.55
Std Dev	2.991

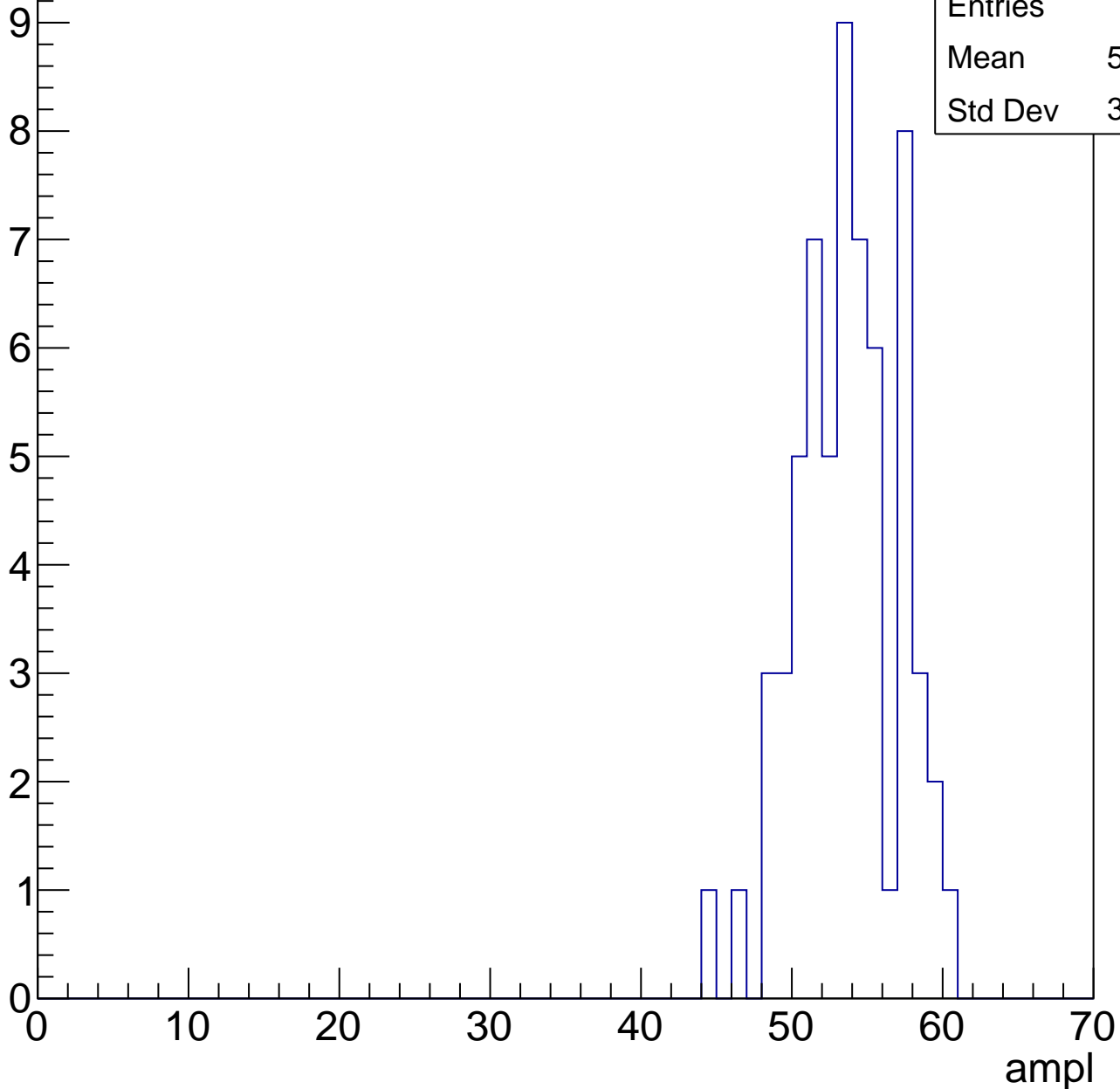


# B1L002S, U17-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	53.18
Std Dev	3.343

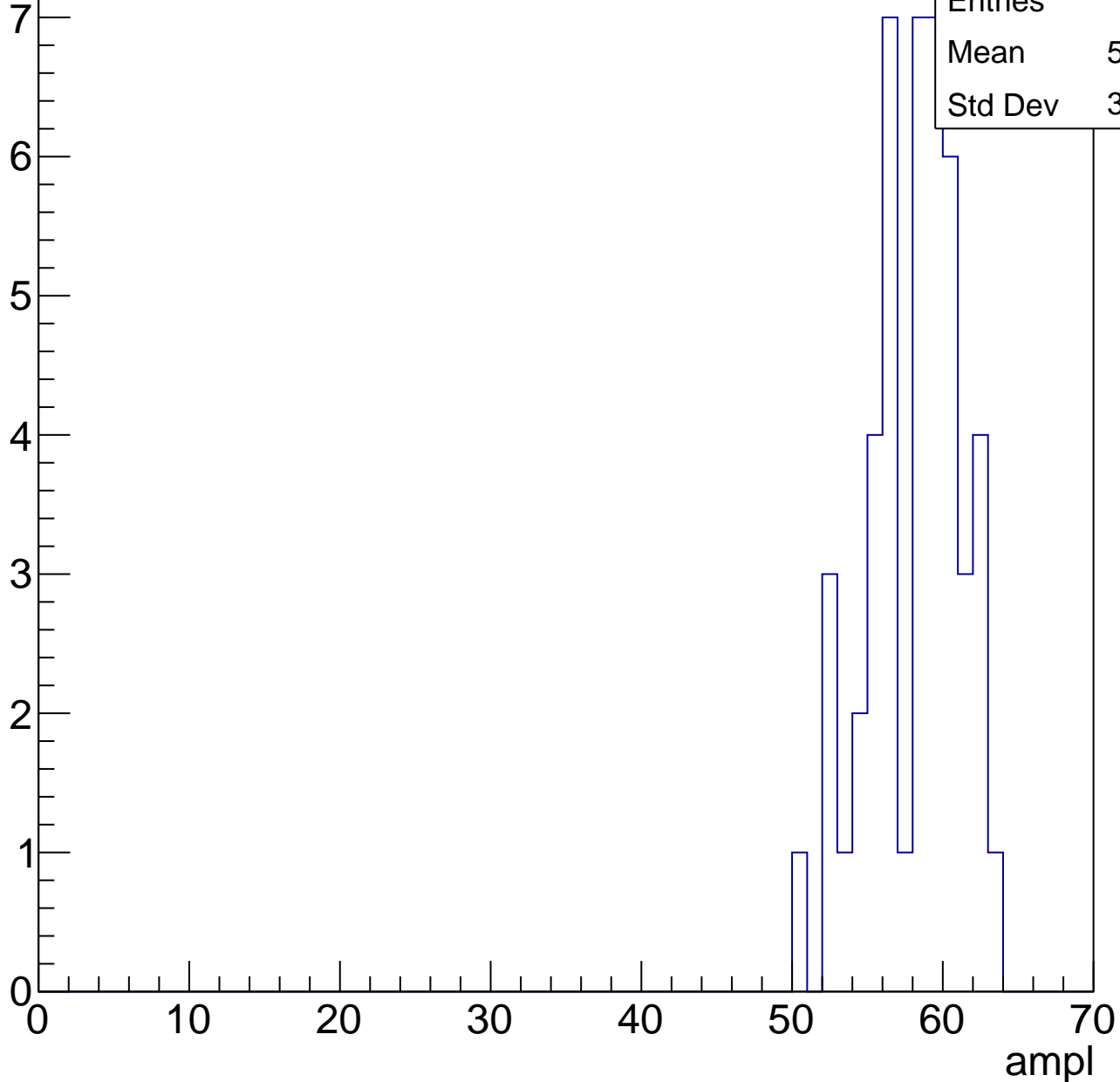


# B1L002S, U17-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.64
Std Dev	3.042

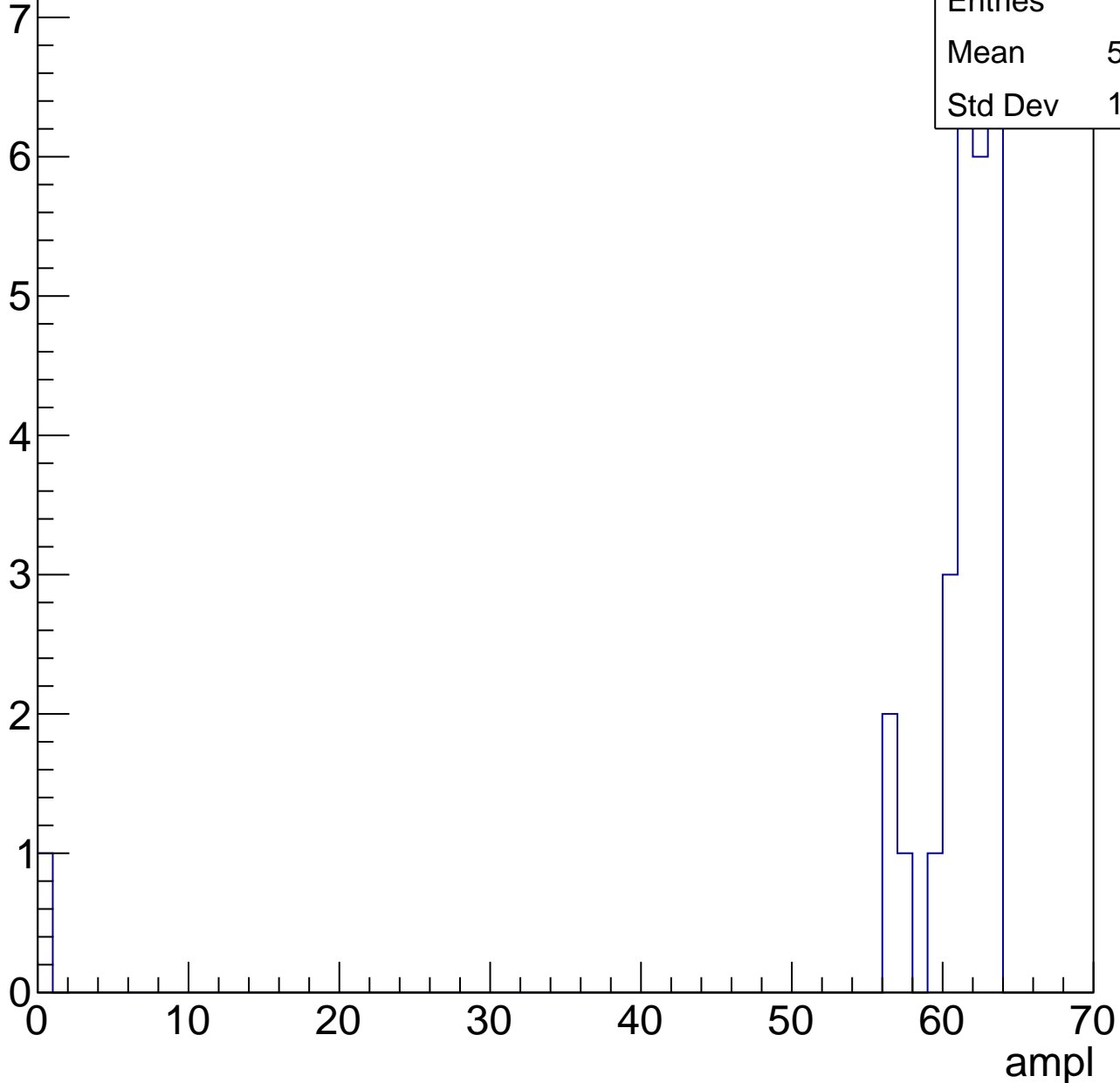


# B1L002S, U17-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.86
Std Dev	11.49





# B1L002S, U17-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	29.52
Std Dev	3.119

**Gaus mean : 30.4493**

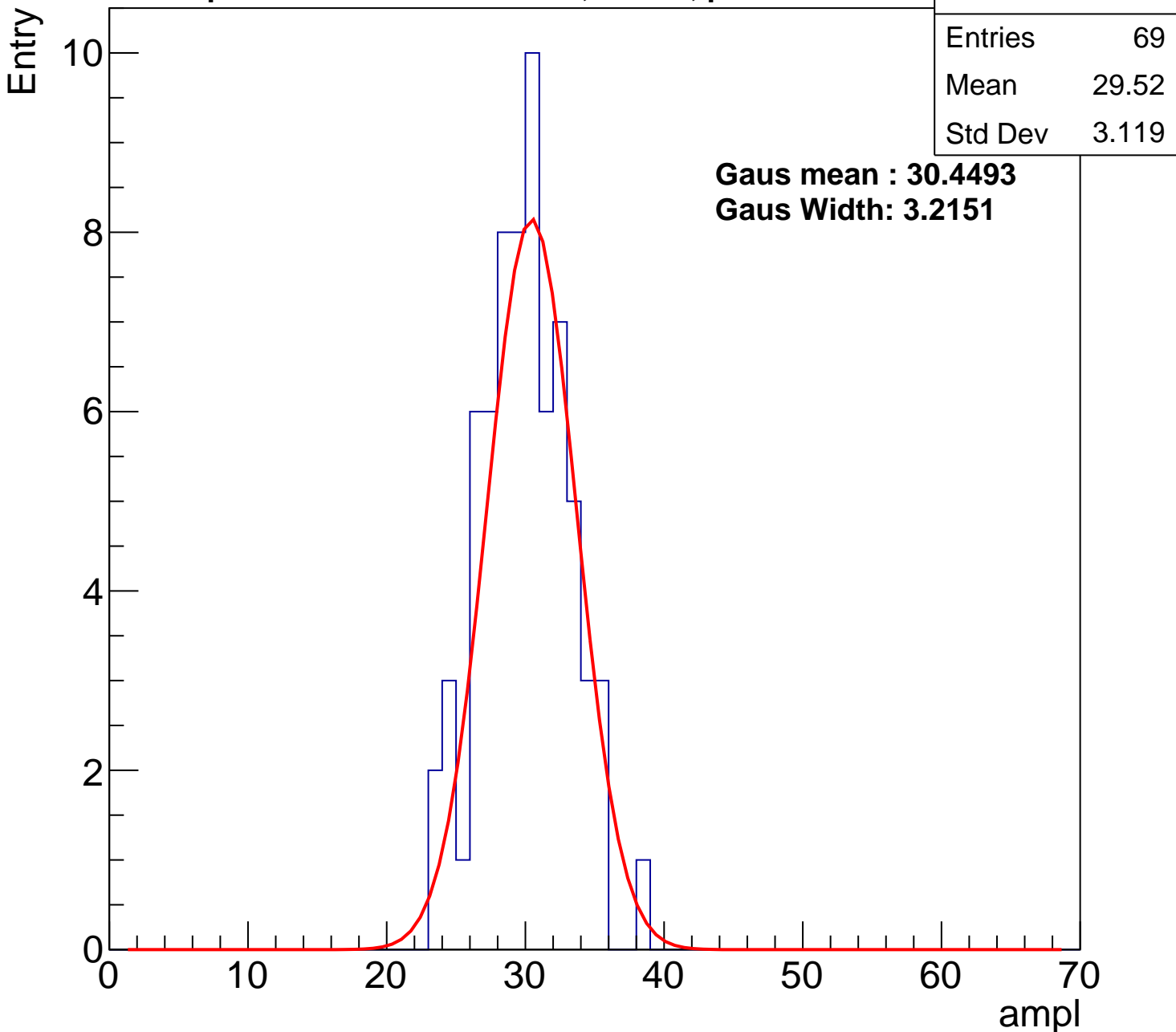
**Gaus Width: 3.2151**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch111, adc1

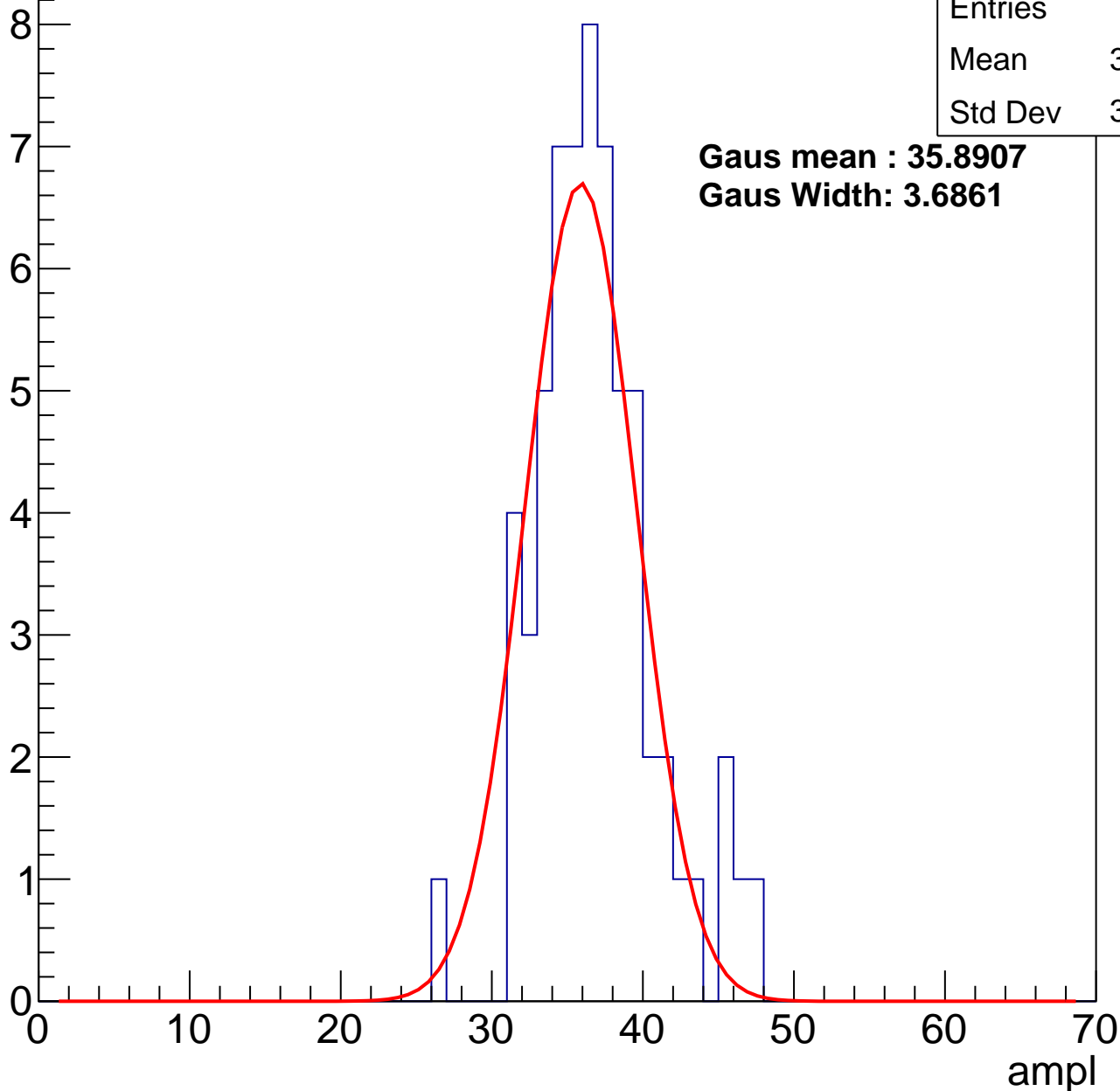
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	36.39
Std Dev	3.895

**Gaus mean : 35.8907**

**Gaus Width: 3.6861**



# B1L002S, U17-ch111, adc2

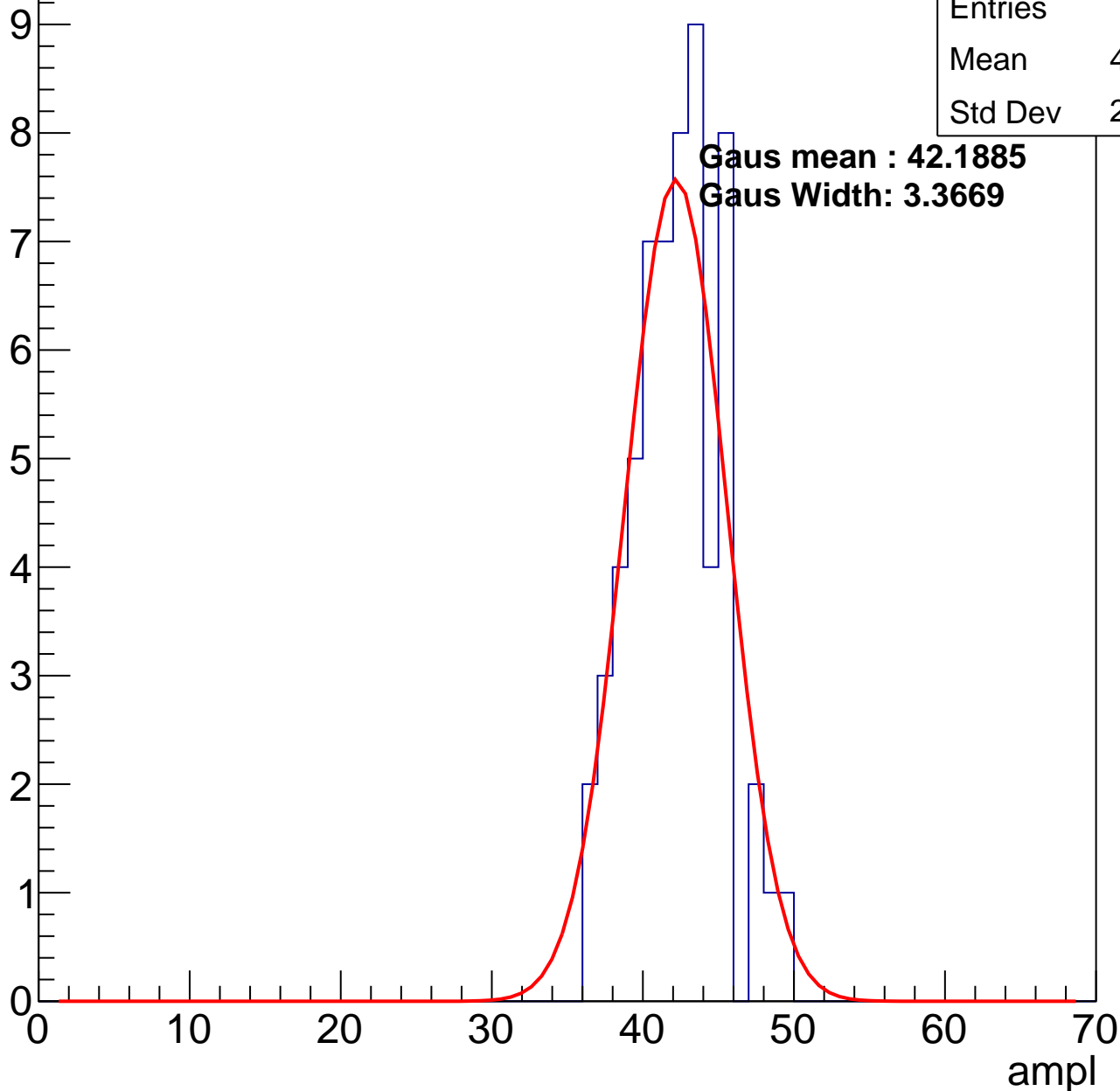
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.75
Std Dev	2.918

**Gaus mean : 42.1885**

**Gaus Width: 3.3669**

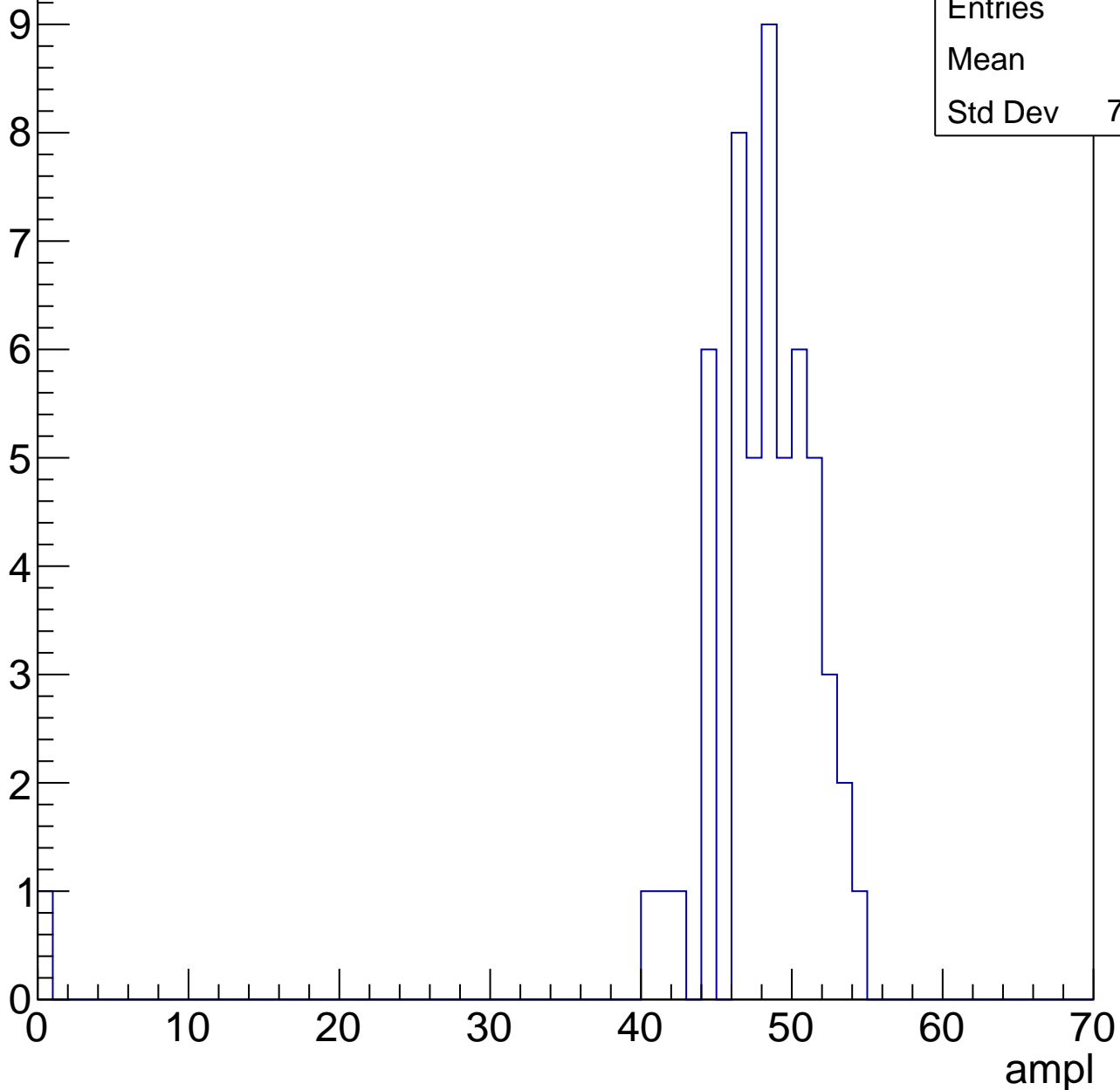


# B1L002S, U17-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	47
Std Dev	7.123

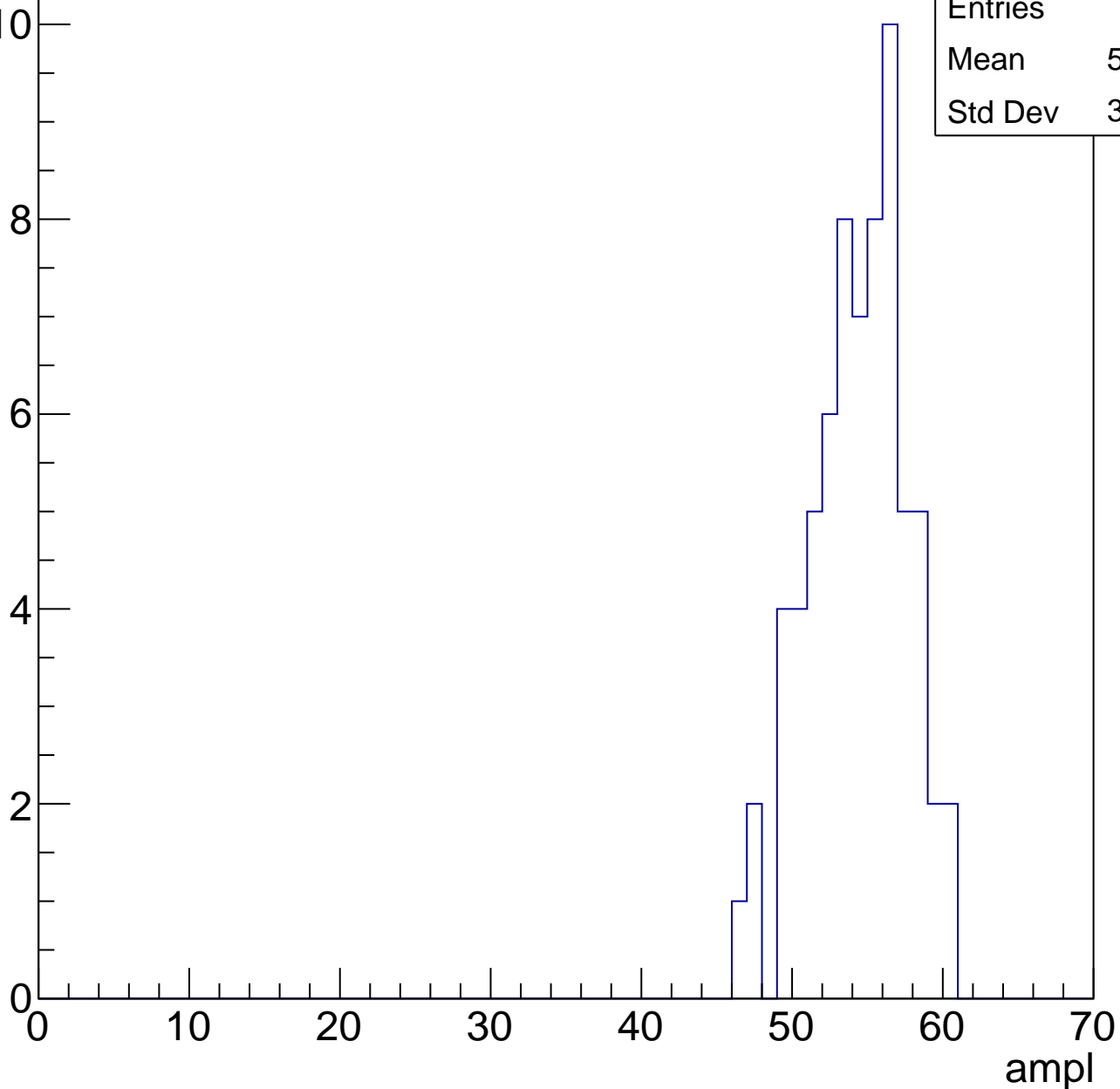


# B1L002S, U17-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	53.88
Std Dev	3.178

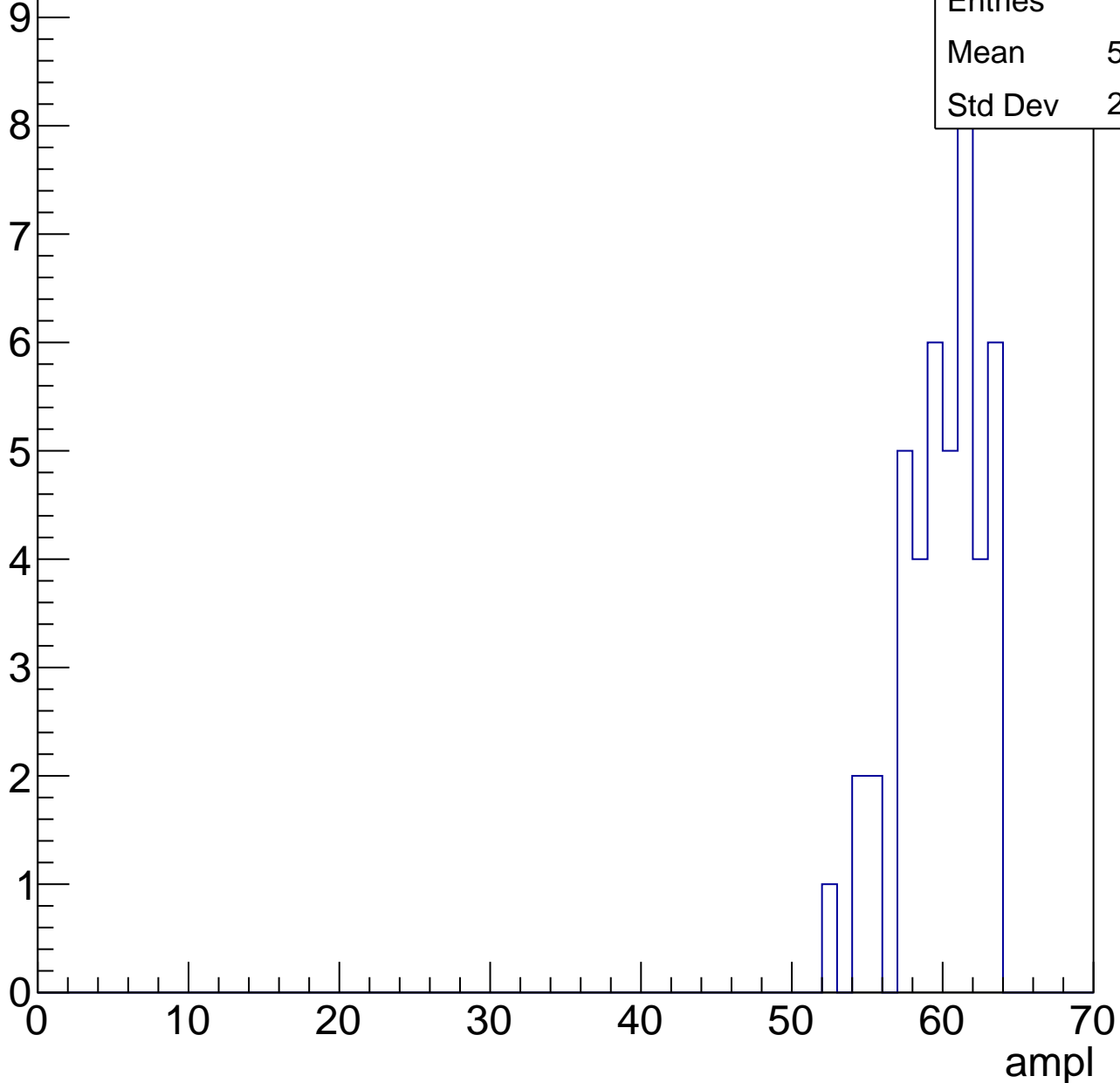


# B1L002S, U17-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	59.45
Std Dev	2.692

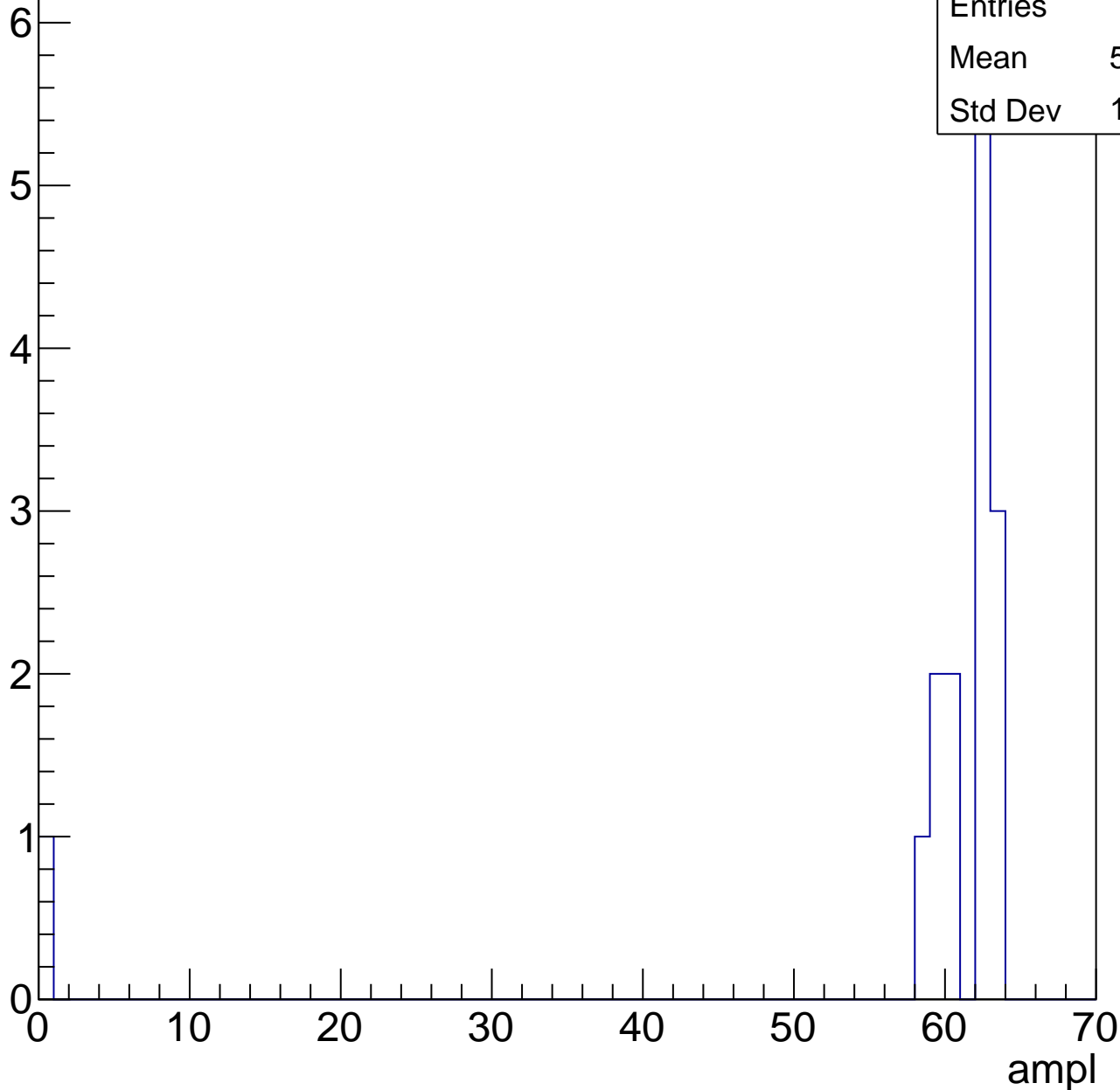


# B1L002S, U17-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	57.13
Std Dev	15.35

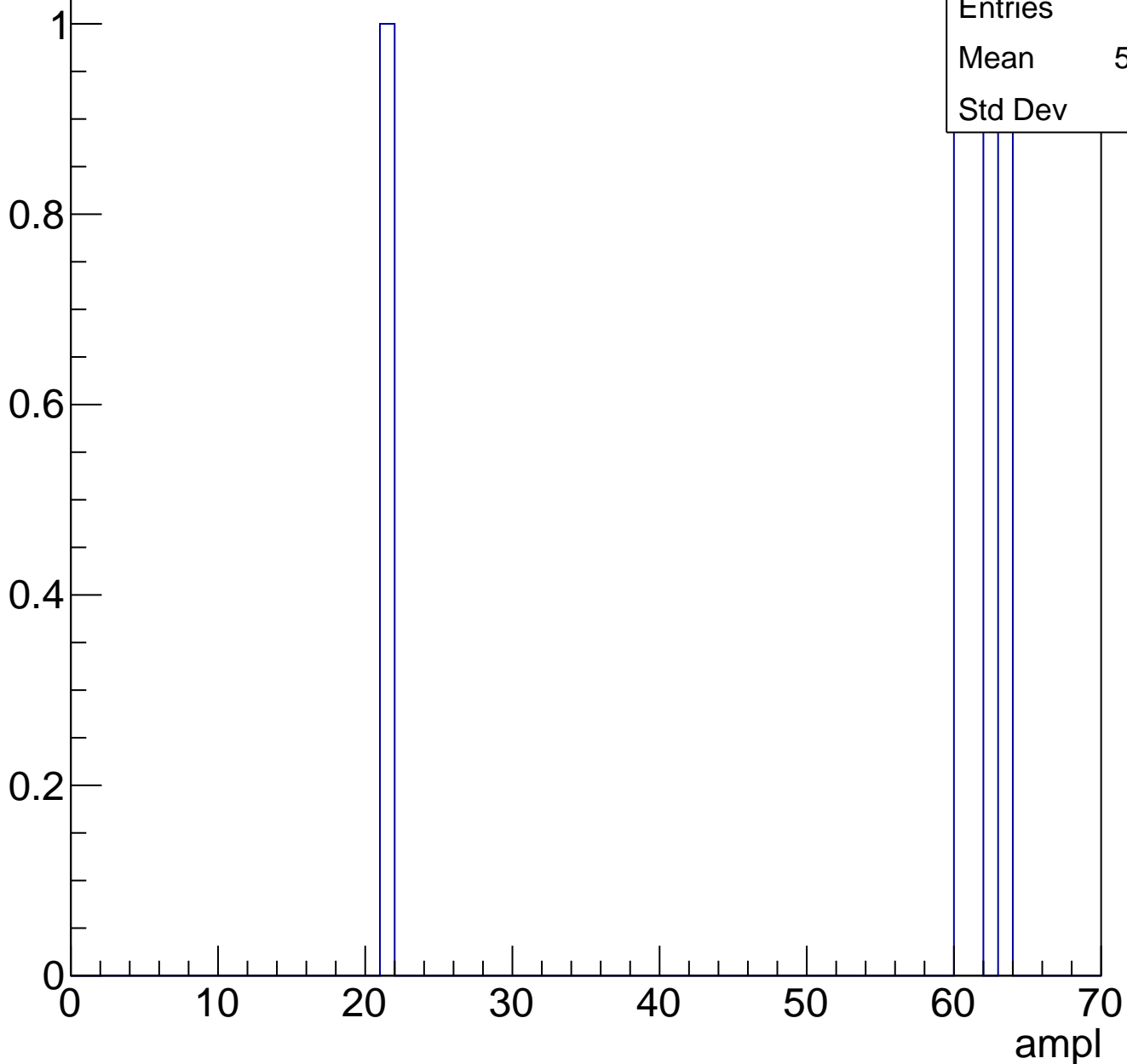




# B1L002S, U17-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch112, adc0

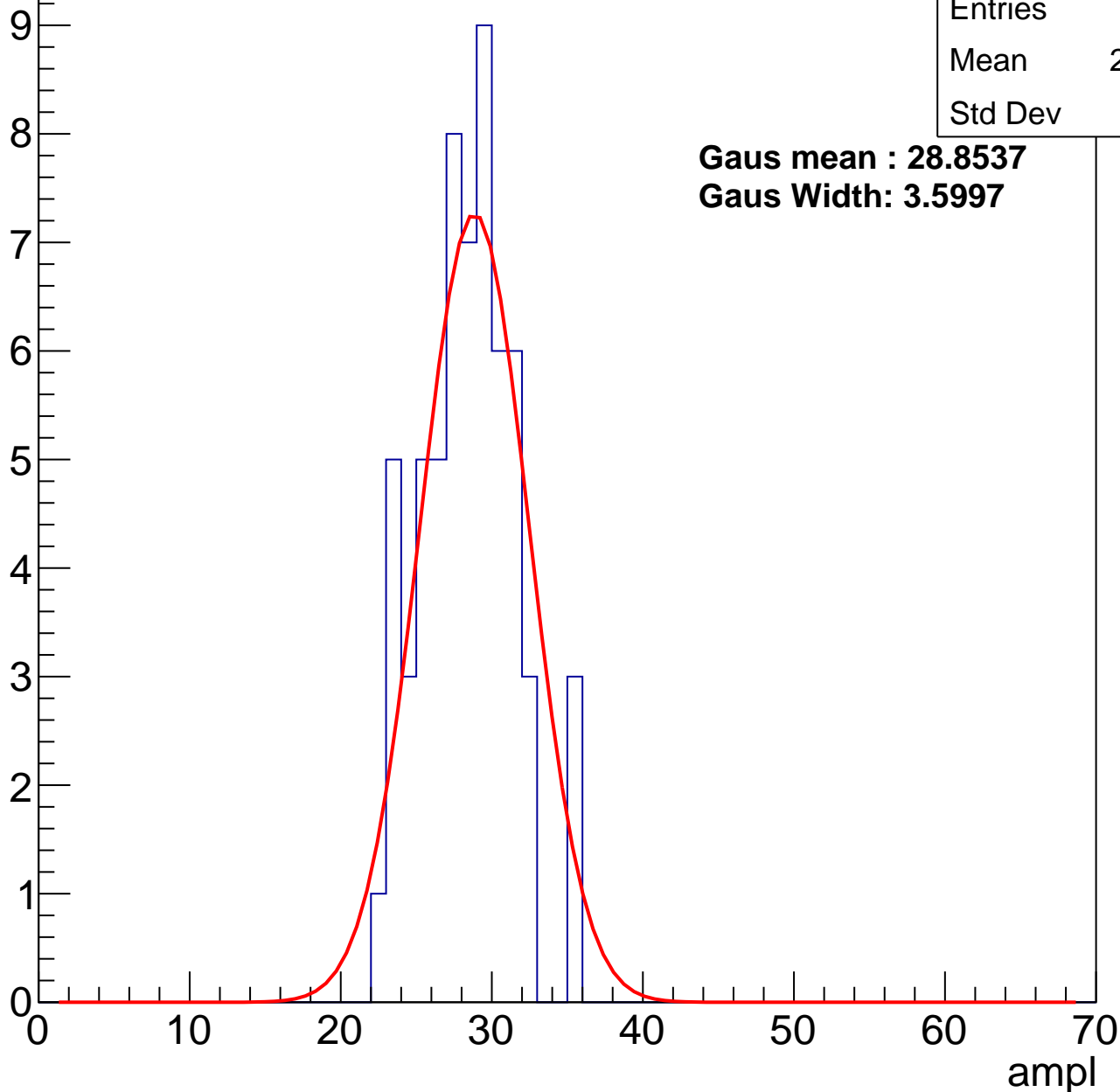
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.93
Std Dev	3.04

**Gaus mean : 28.8537**

**Gaus Width: 3.5997**



# B1L002S, U17-ch112, adc1

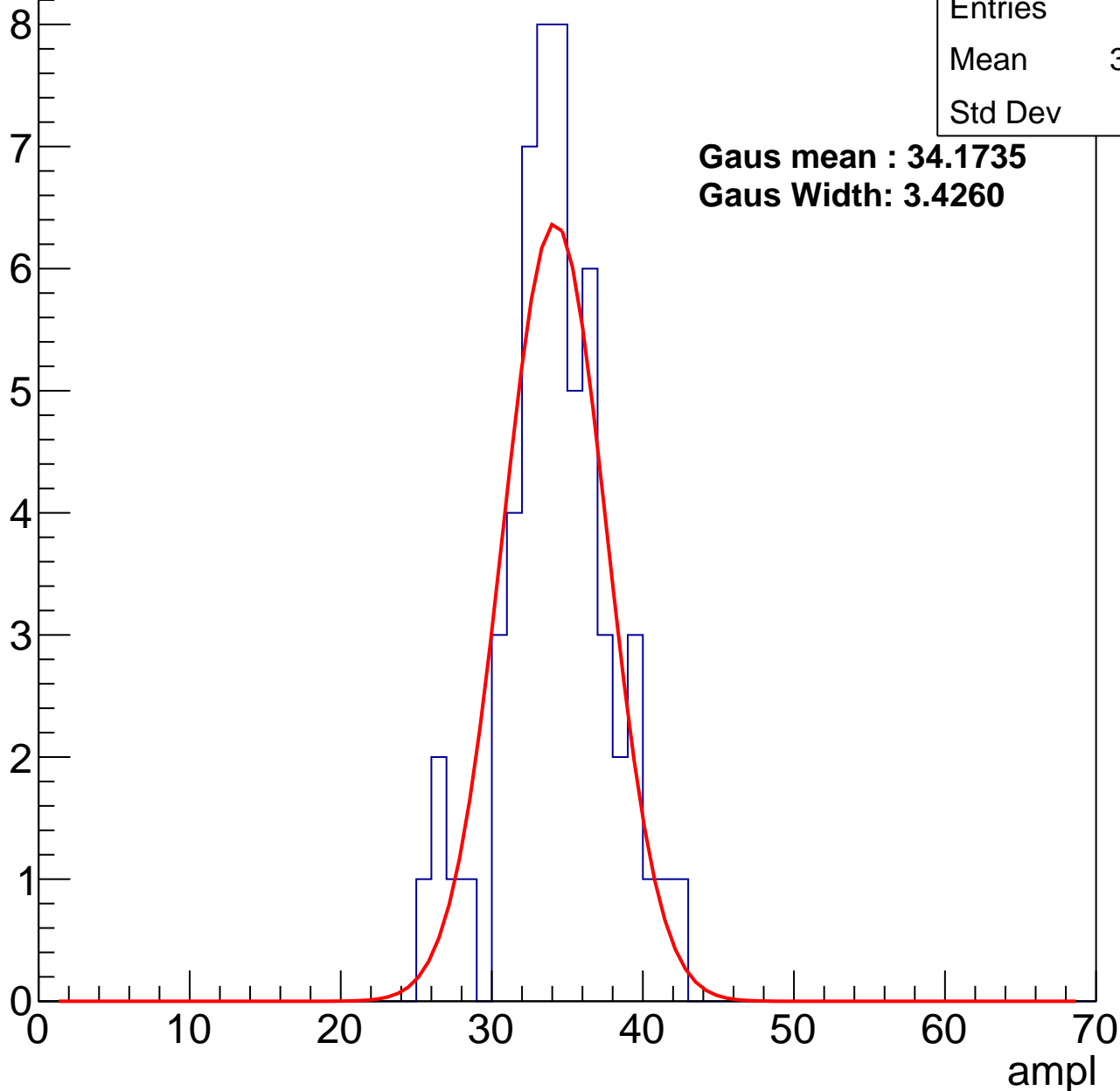
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.75
Std Dev	3.56

**Gaus mean : 34.1735**

**Gaus Width: 3.4260**



# B1L002S, U17-ch112, adc2

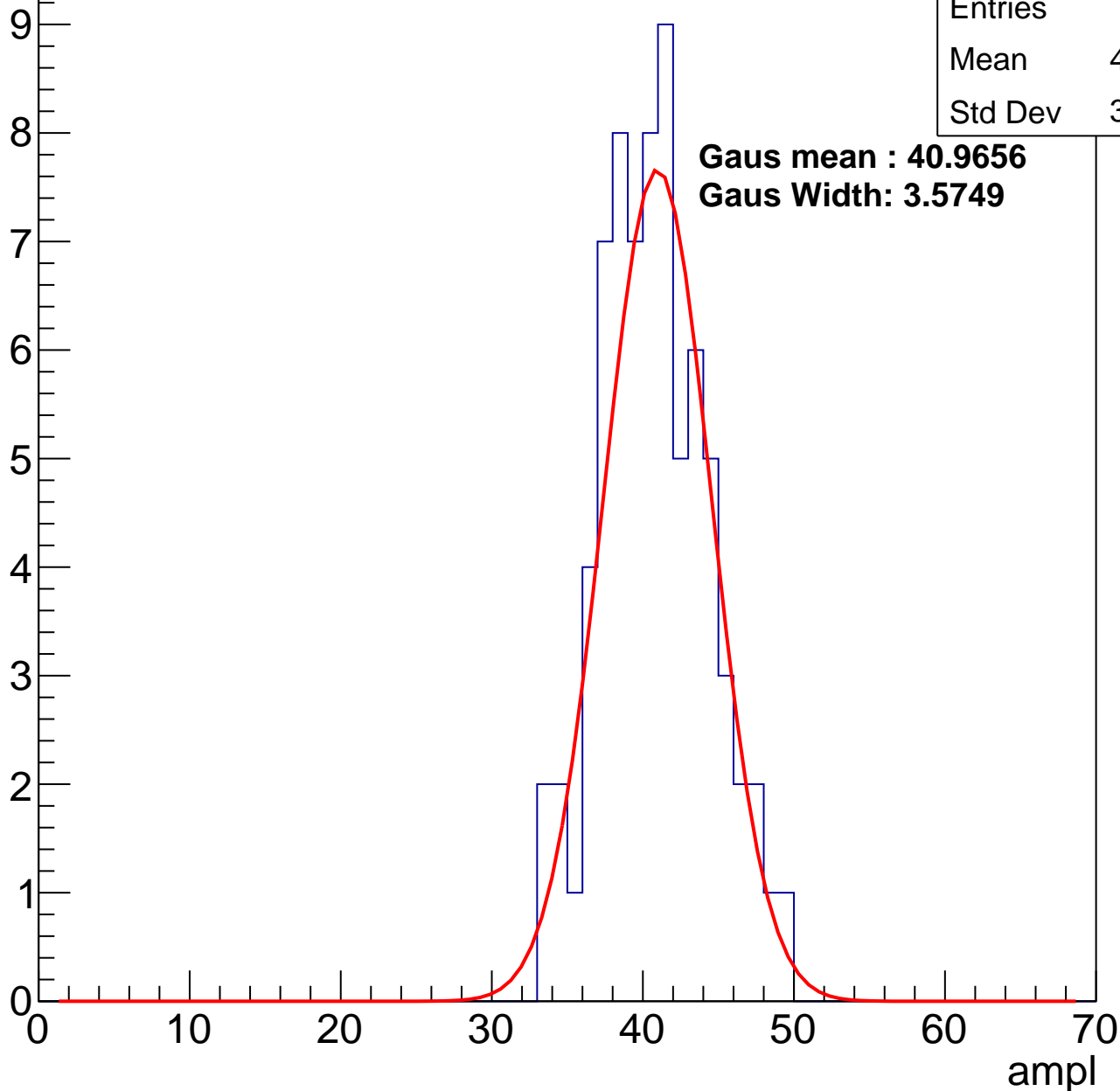
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	40.33
Std Dev	3.523

**Gaus mean : 40.9656**

**Gaus Width: 3.5749**

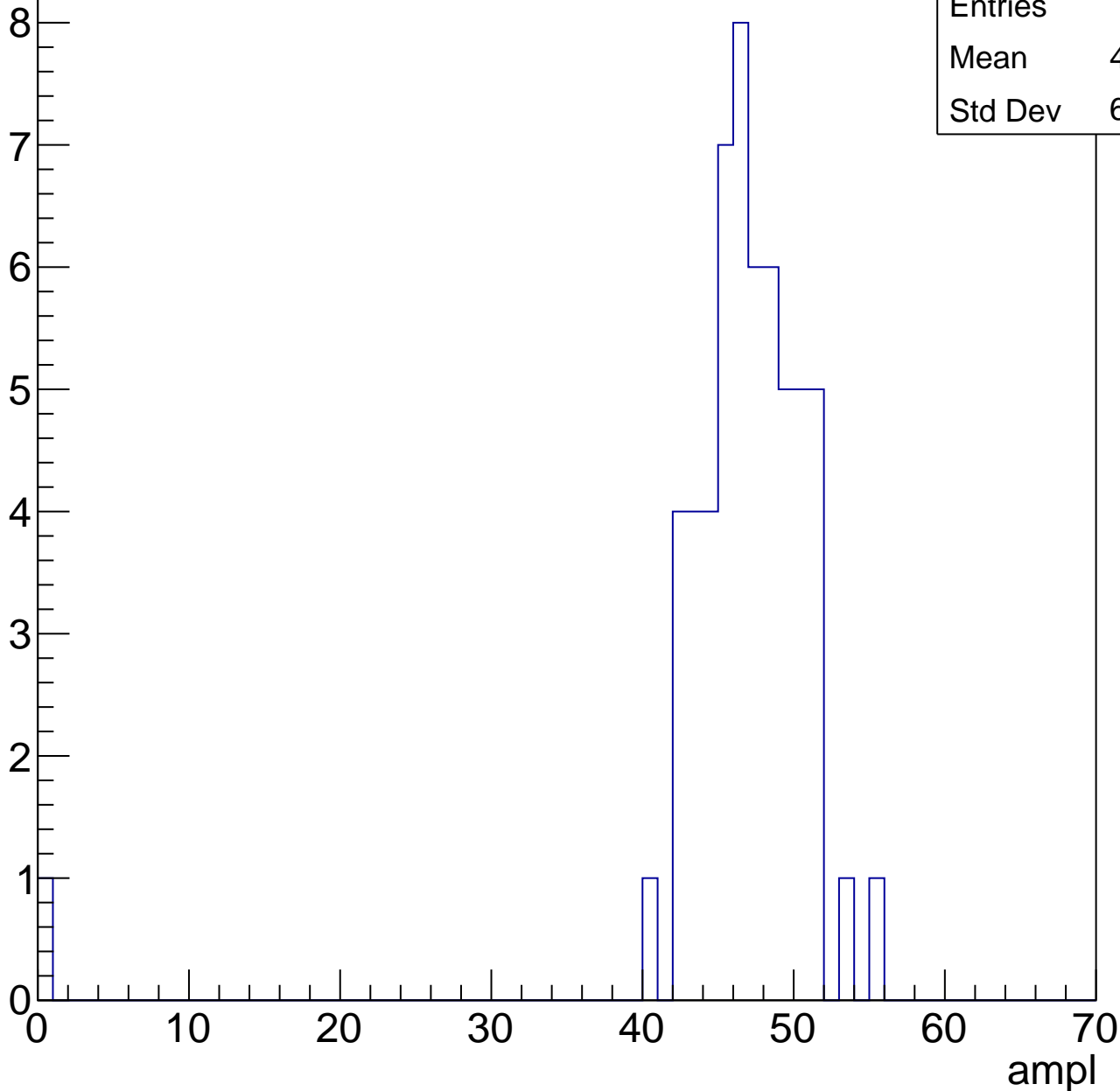


# B1L002S, U17-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.98
Std Dev	6.804

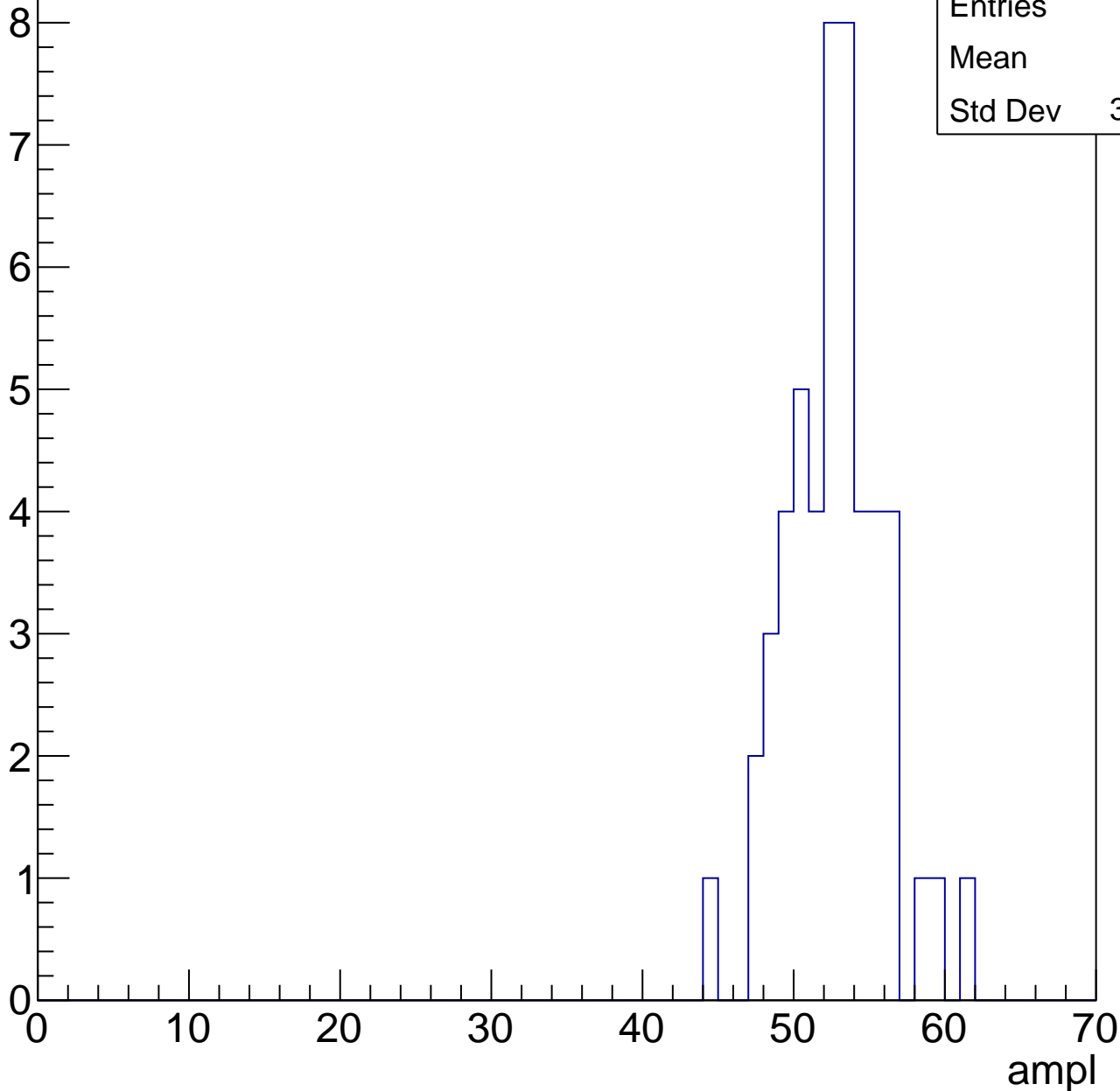


# B1L002S, U17-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	52.2
Std Dev	3.194

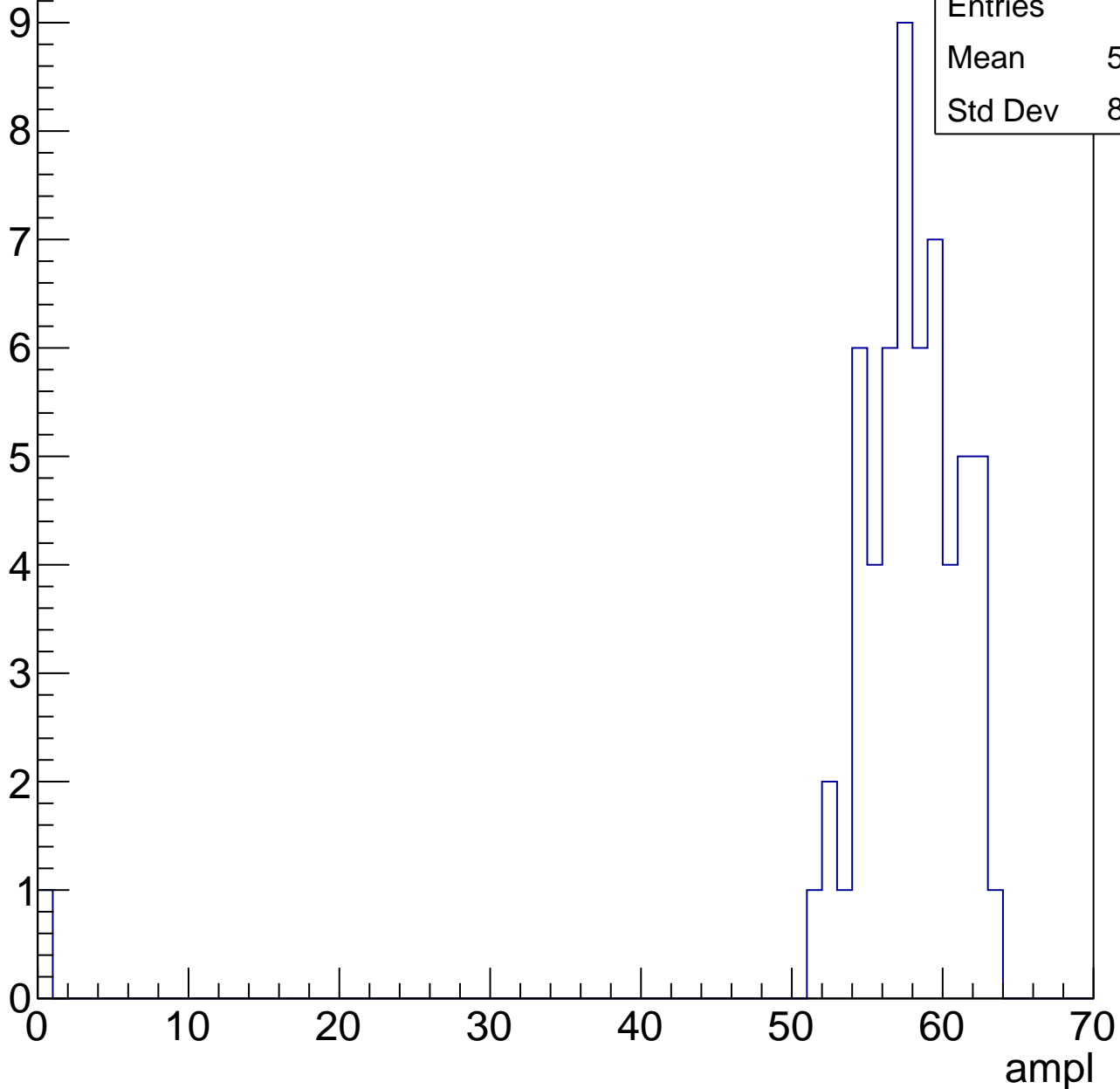


# B1L002S, U17-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.55
Std Dev	8.015

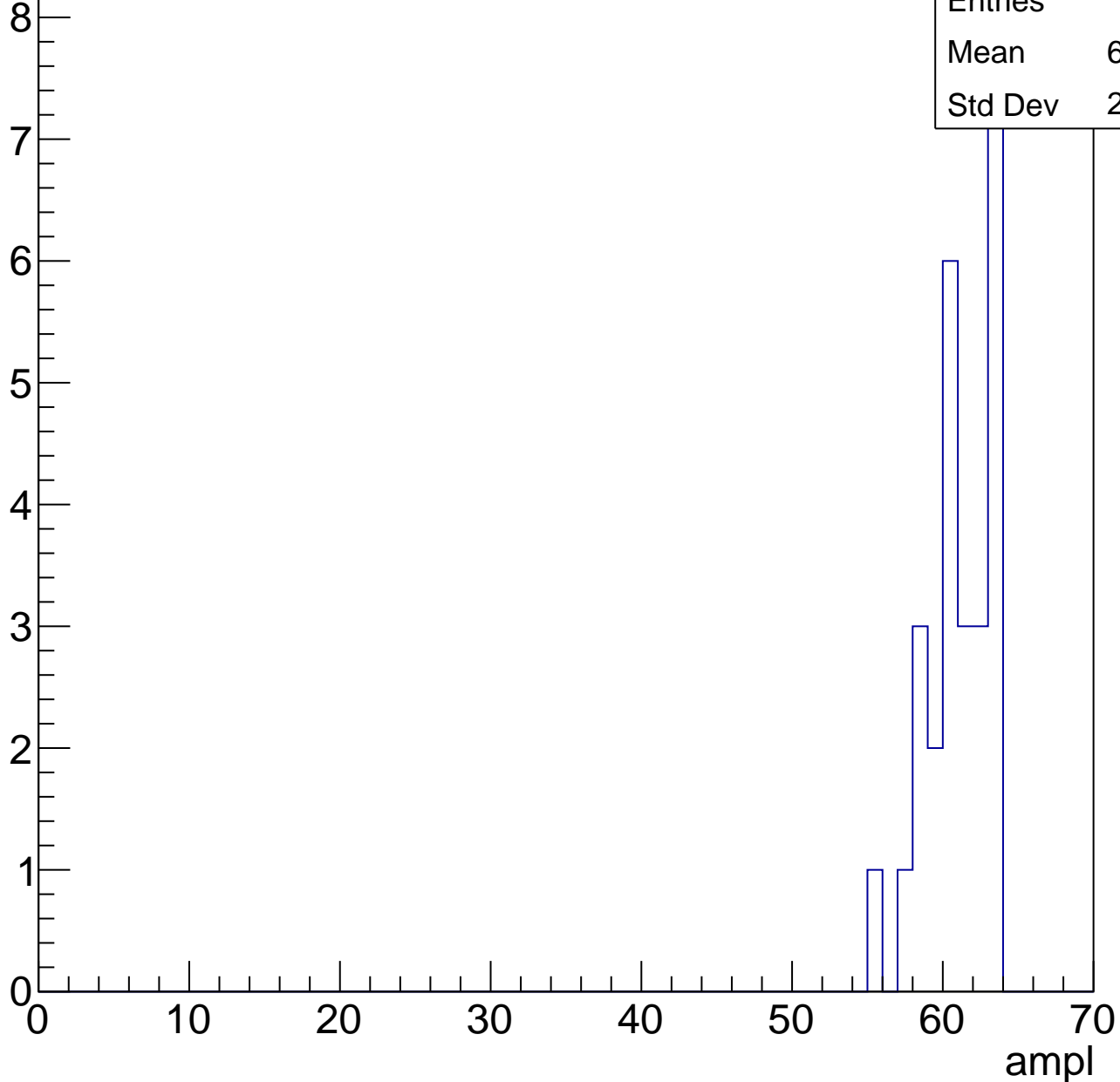


# B1L002S, U17-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.63
Std Dev	2.146

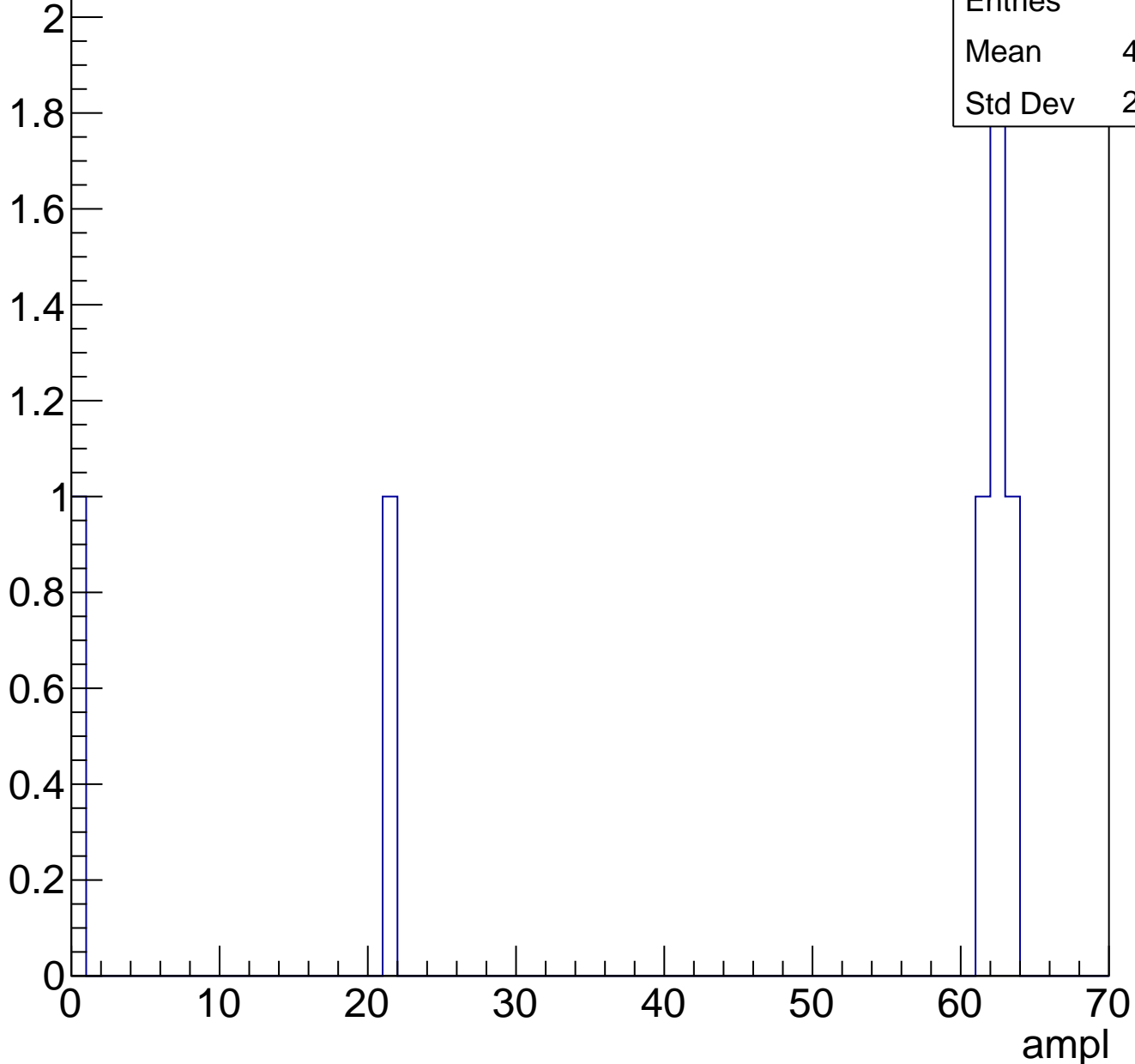




# B1L002S, U17-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	44.83
Std Dev	25.03

# B1L002S, U17-ch113, adc0

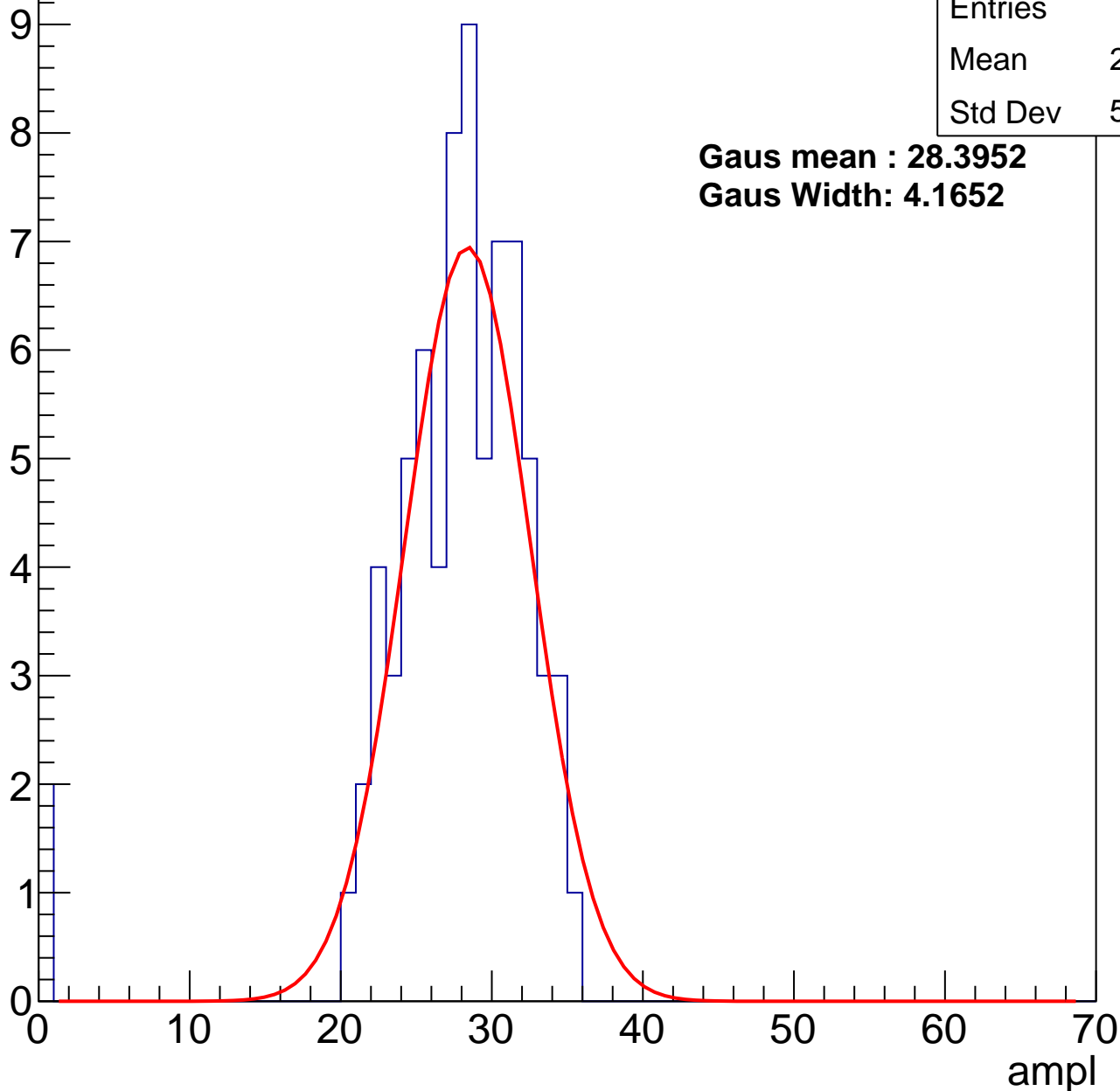
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	27.05
Std Dev	5.713

**Gaus mean : 28.3952**

**Gaus Width: 4.1652**



# B1L002S, U17-ch113, adc1

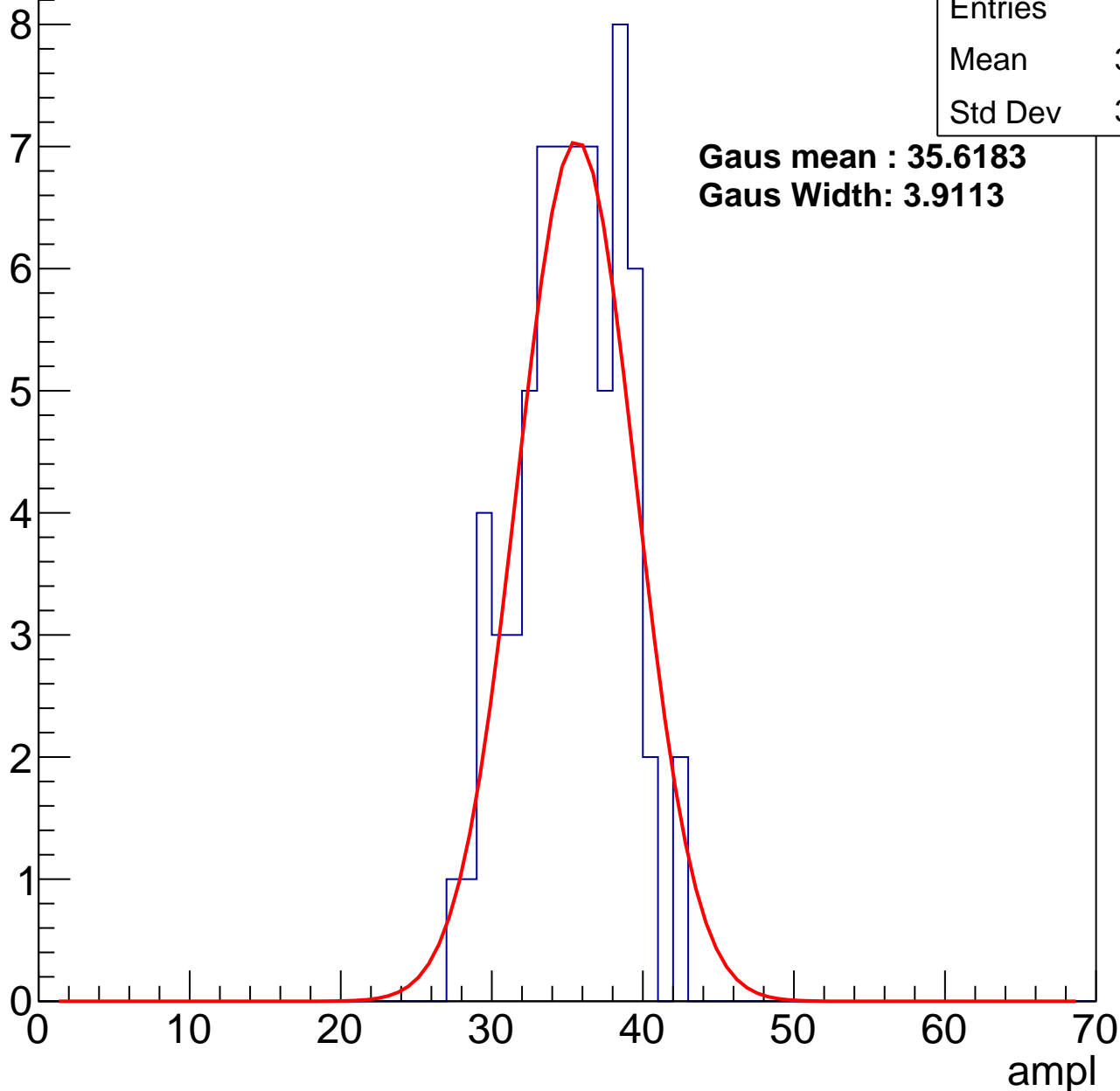
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.81
Std Dev	3.431

**Gaus mean : 35.6183**

**Gaus Width: 3.9113**



# B1L002S, U17-ch113, adc2

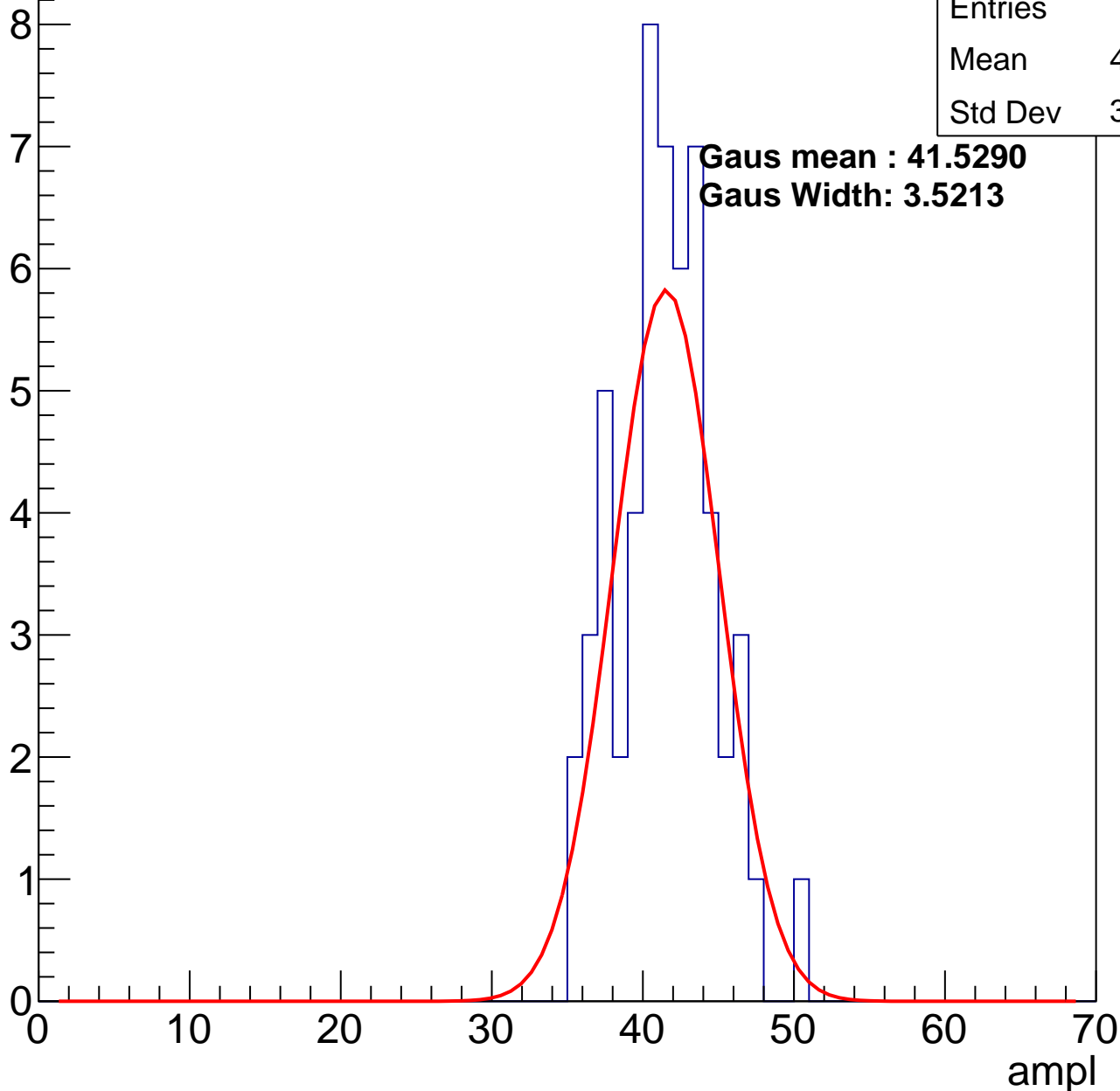
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.02
Std Dev	3.199

**Gaus mean : 41.5290**

**Gaus Width: 3.5213**

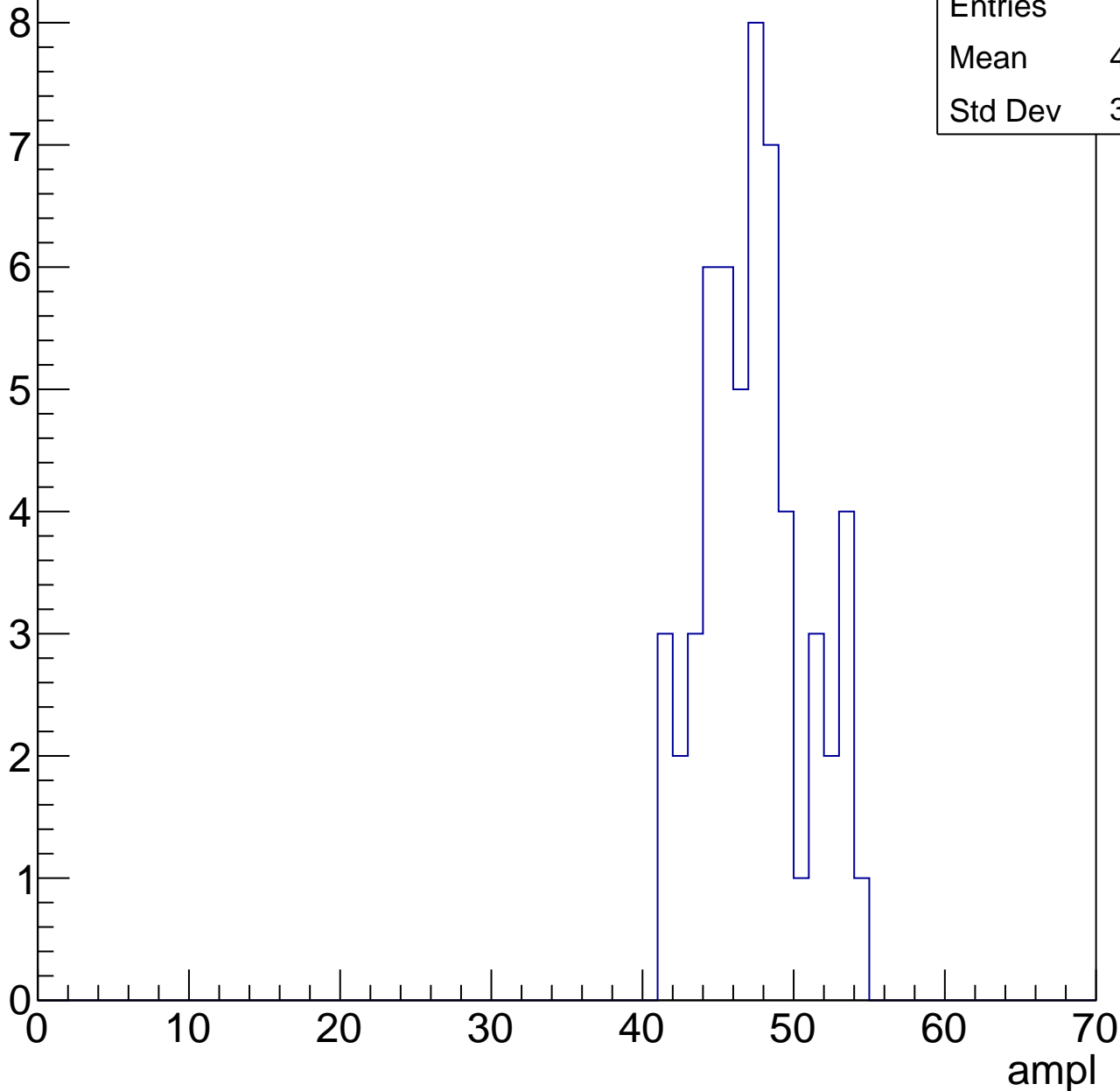


# B1L002S, U17-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.93
Std Dev	3.335

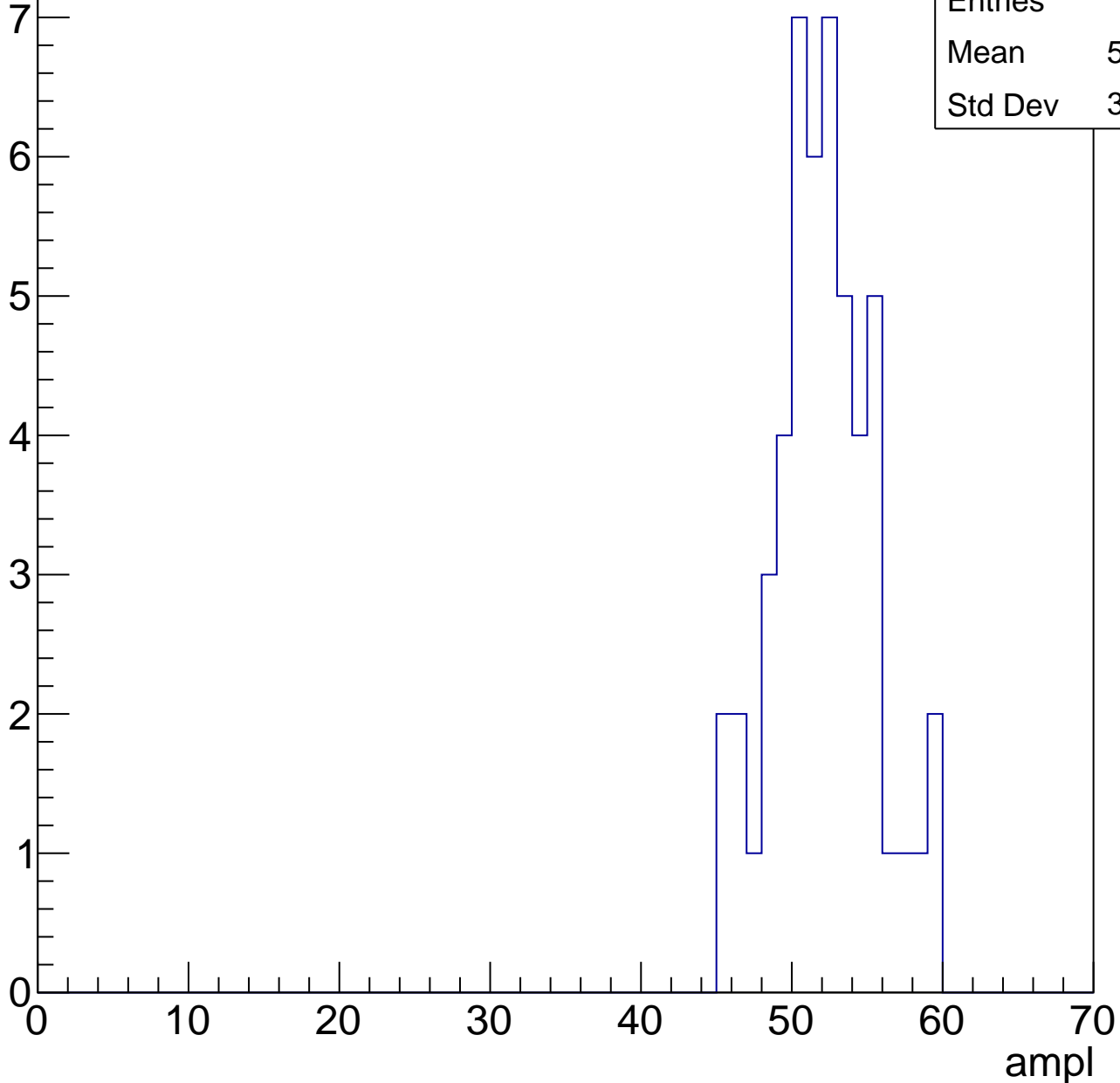


# B1L002S, U17-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

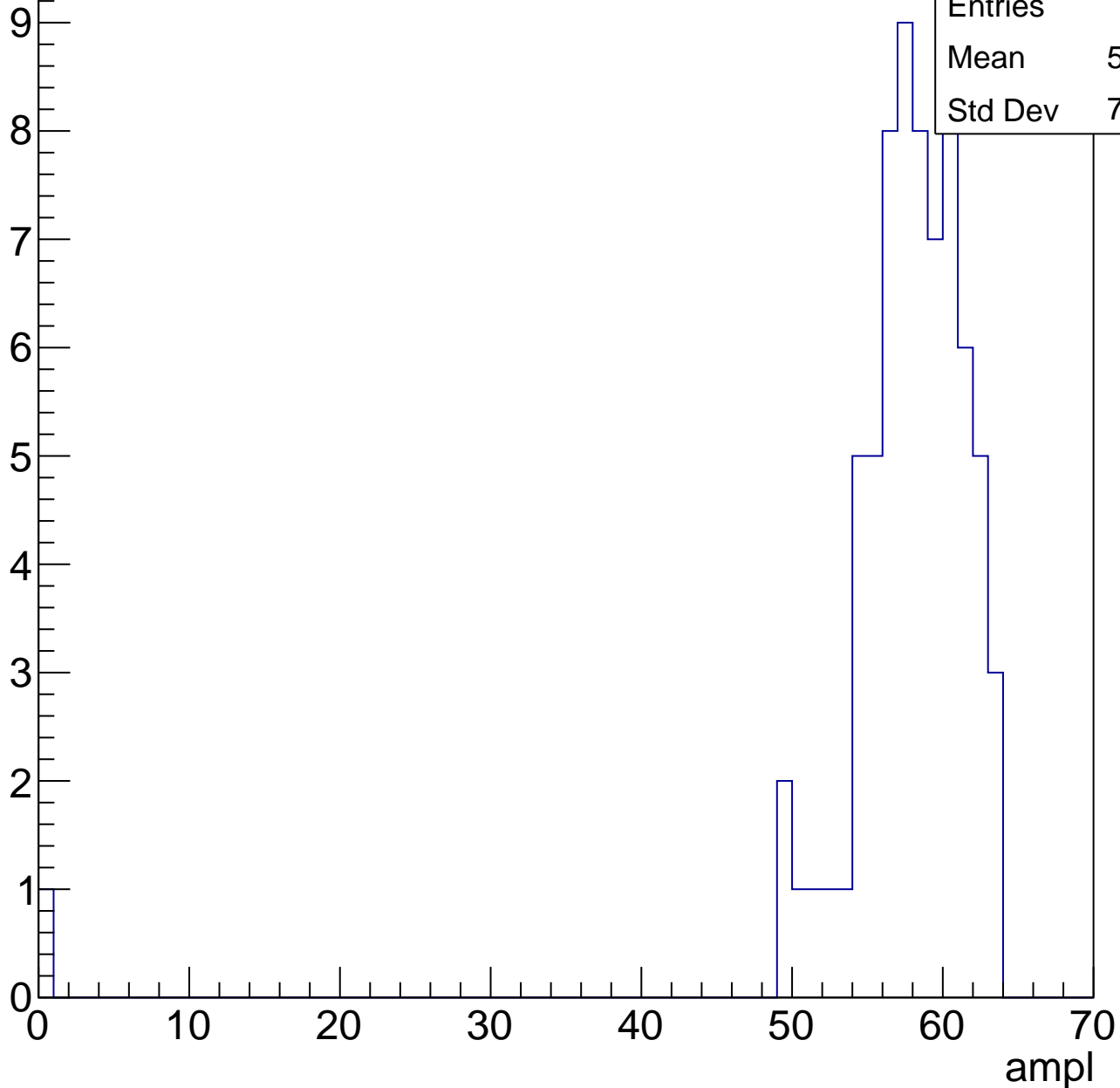
Entries	51
Mean	51.65
Std Dev	3.277



# B1L002S, U17-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

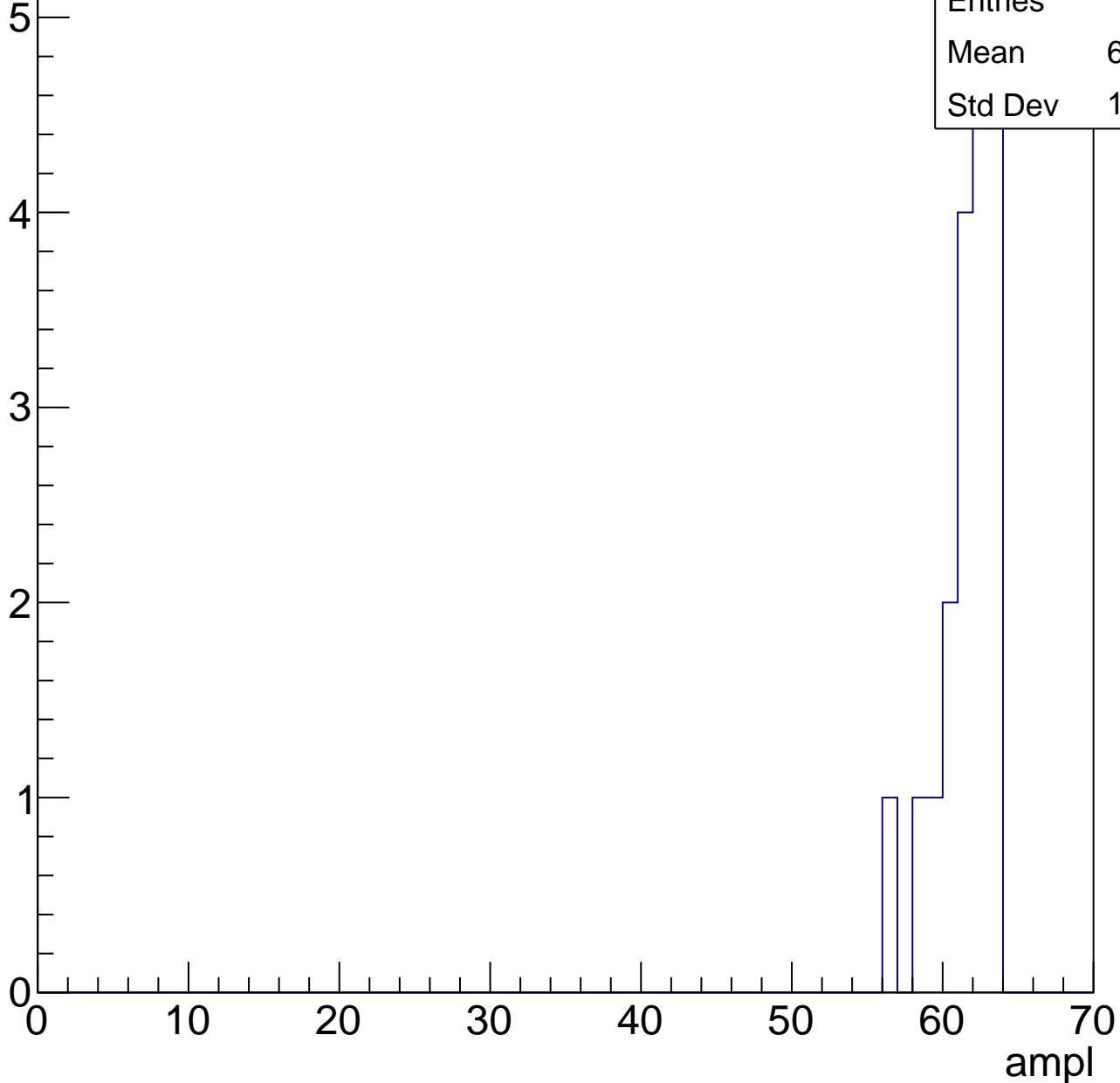


# B1L002S, U17-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.16
Std Dev	1.843





# B1L002S, U17-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	48.33
Std Dev	20.74

0 10 20 30 40 50 60 70

ampl

1

# B1L002S, U17-ch114, adc0

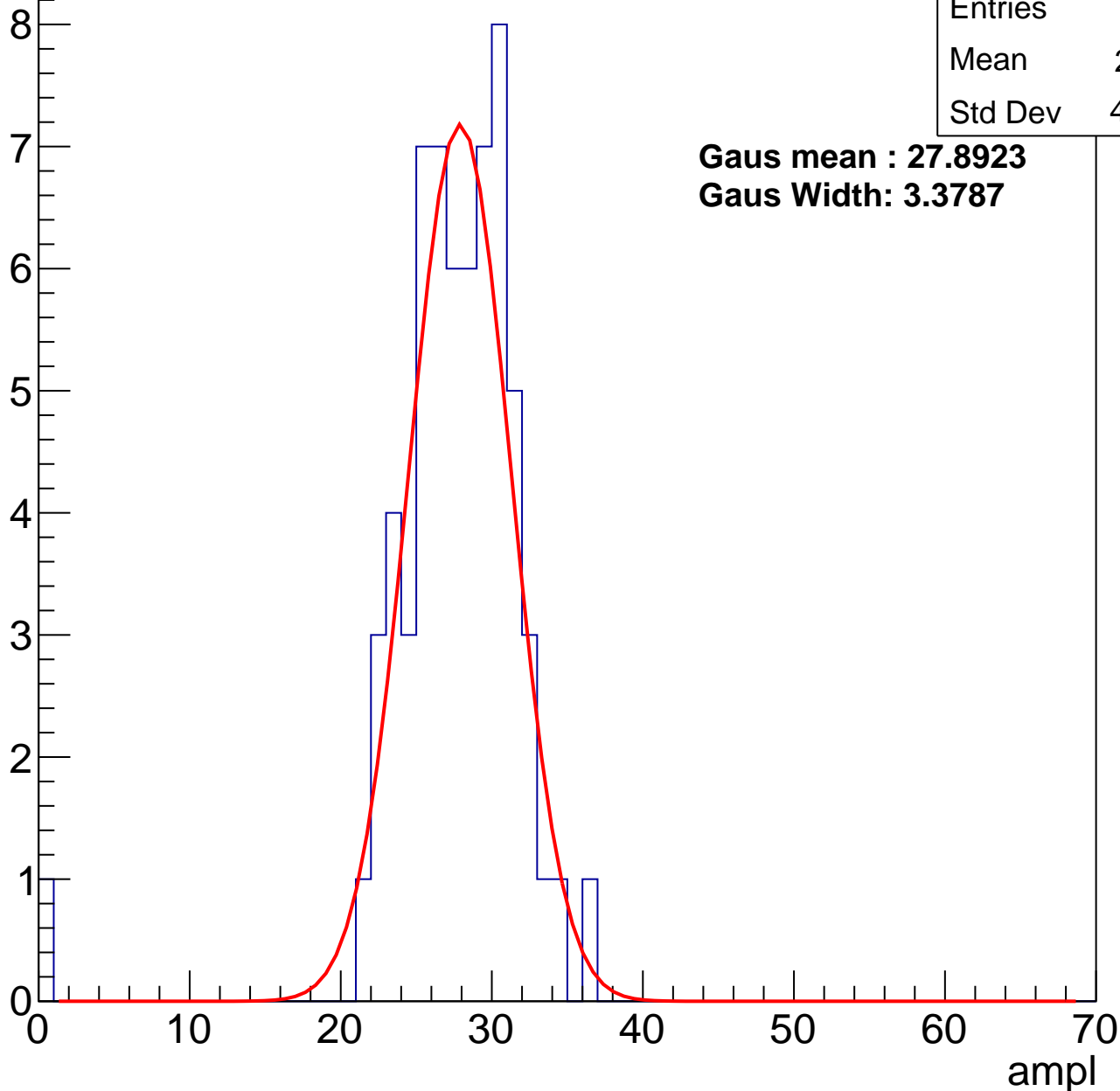
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.11
Std Dev	4.664

**Gaus mean : 27.8923**

**Gaus Width: 3.3787**



# B1L002S, U17-ch114, adc1

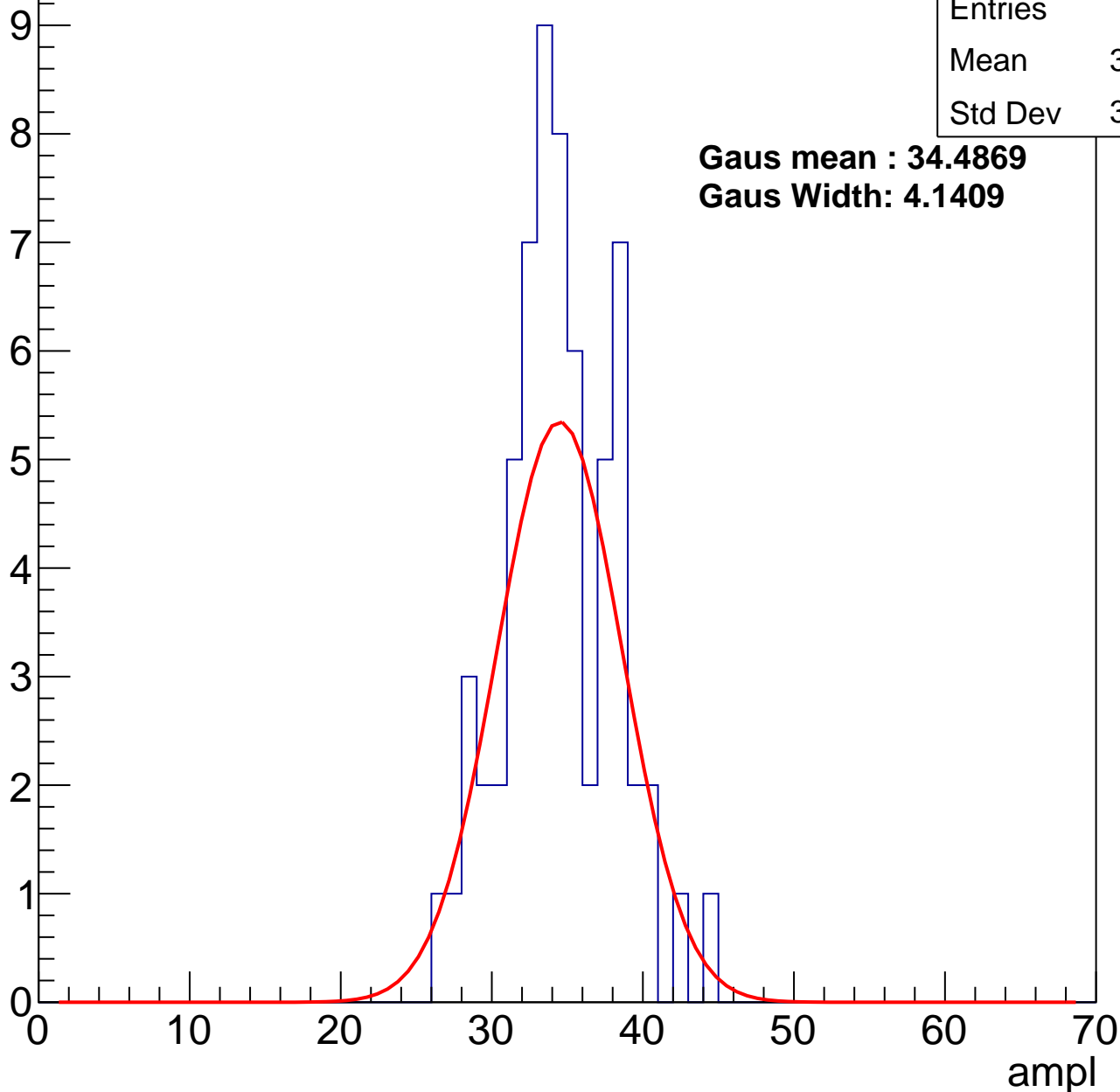
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.06
Std Dev	3.627

**Gaus mean : 34.4869**

**Gaus Width: 4.1409**



# B1L002S, U17-ch114, adc2

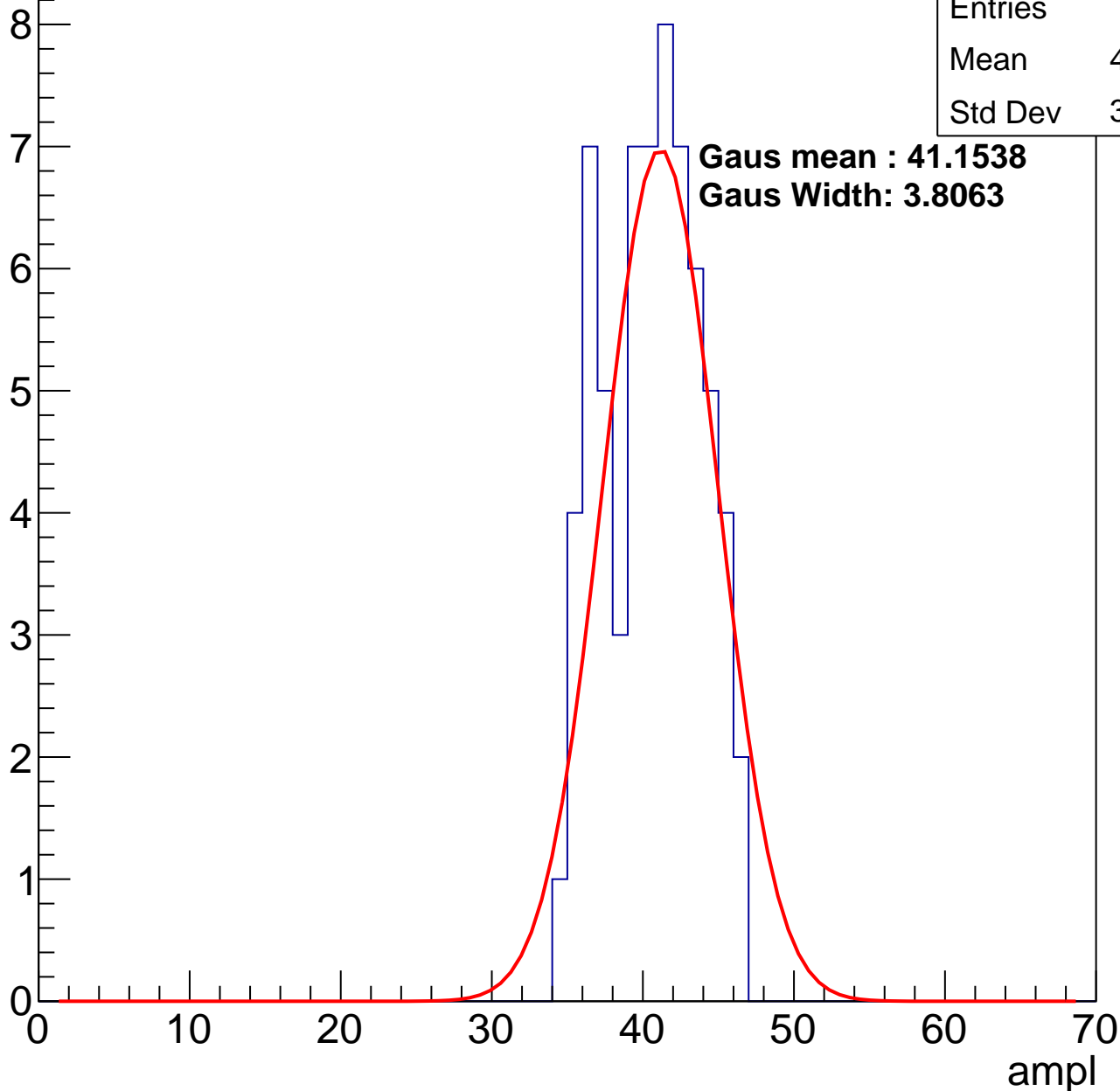
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.15
Std Dev	3.144

**Gaus mean : 41.1538**

**Gaus Width: 3.8063**

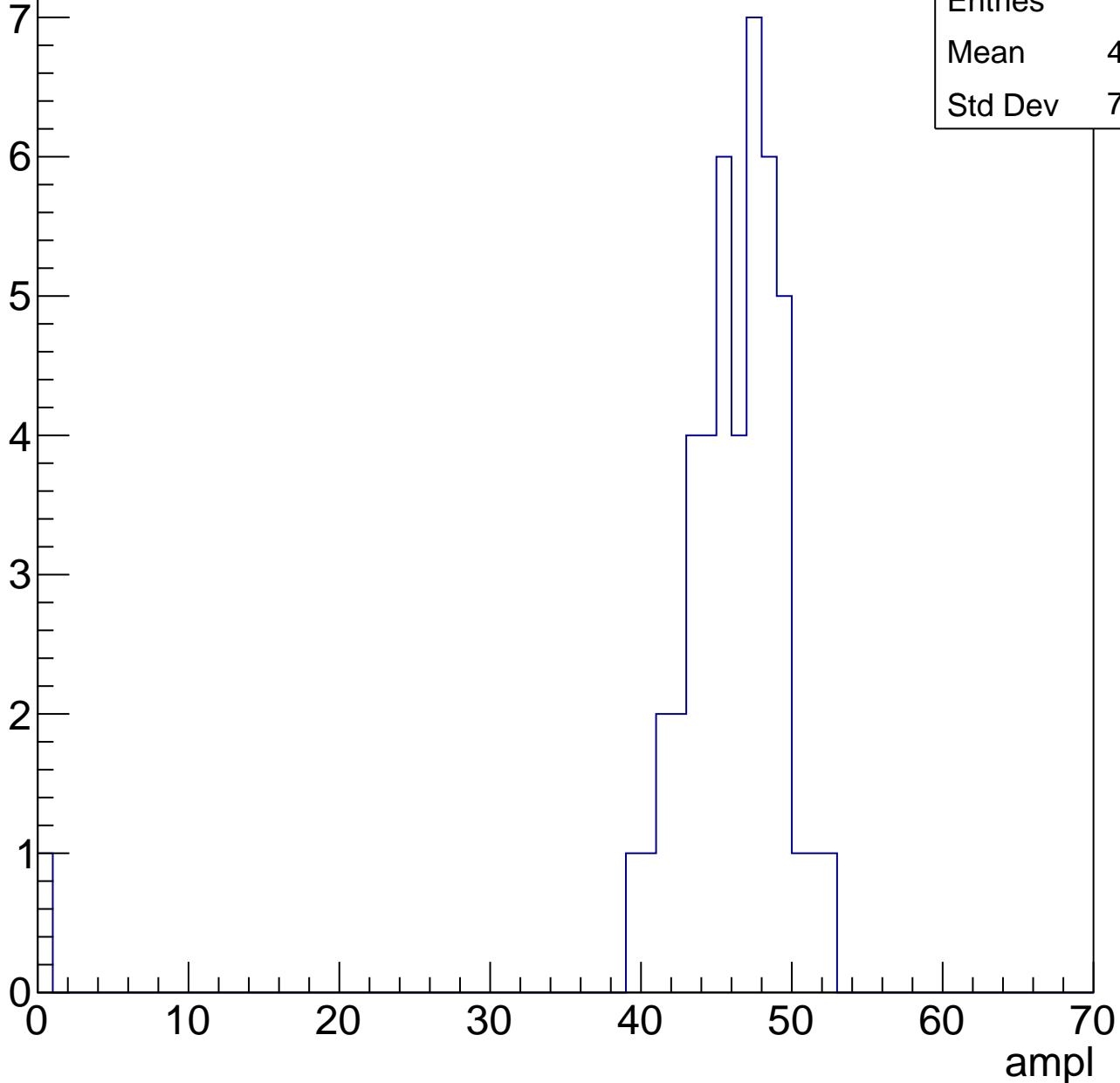


# B1L002S, U17-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	44.83
Std Dev	7.269

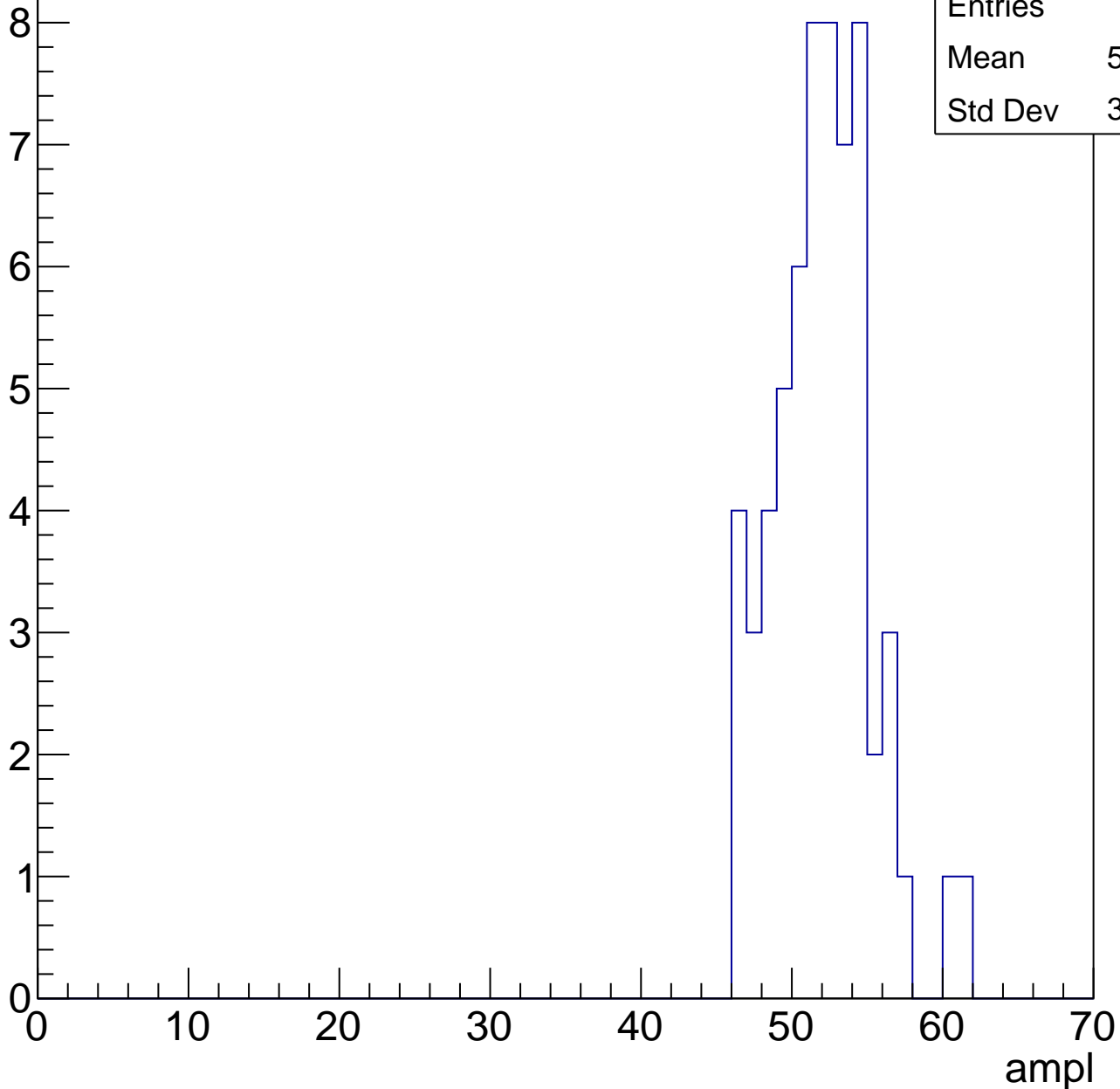


# B1L002S, U17-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

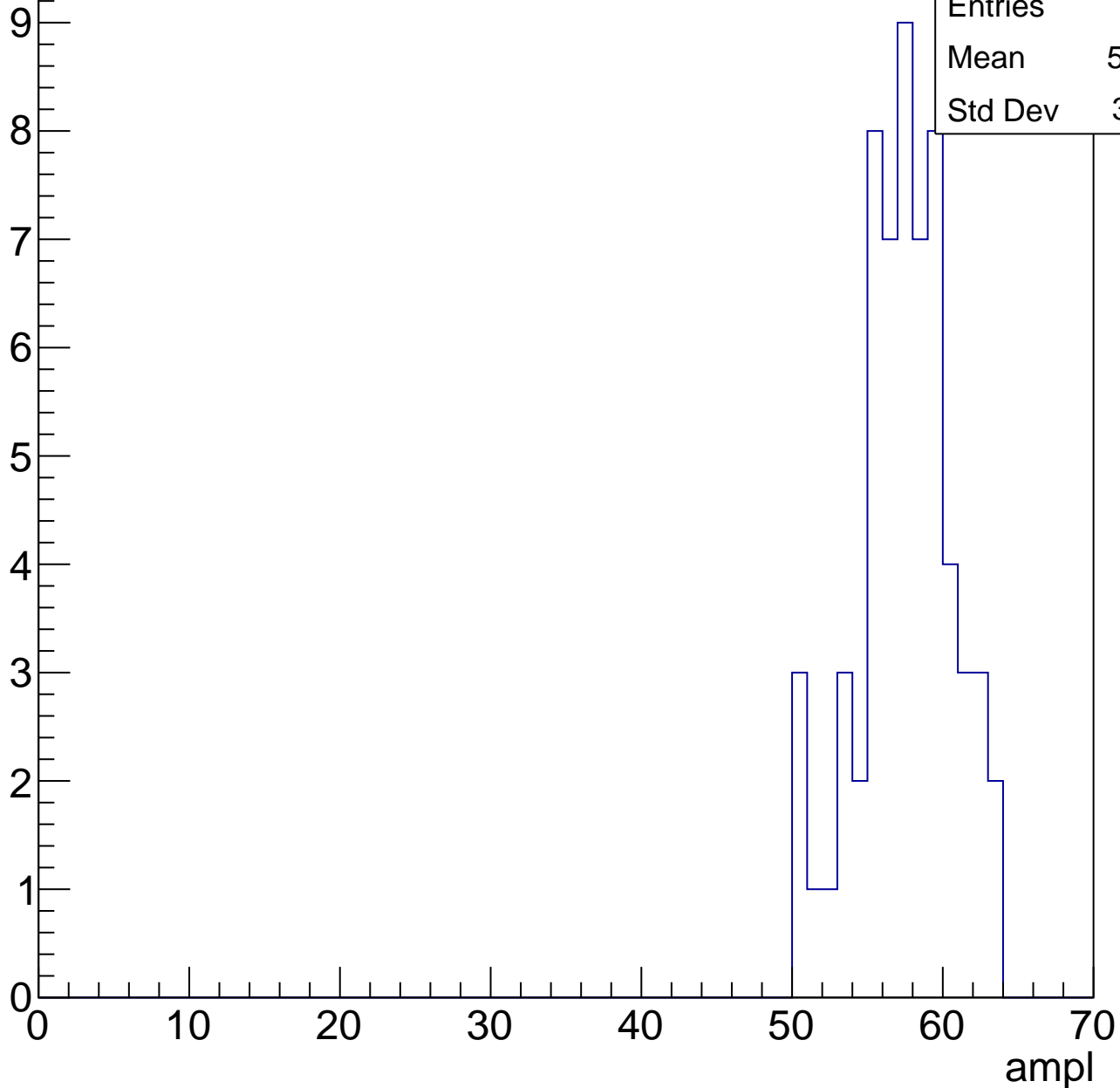
Entries	61
Mean	51.56
Std Dev	3.196



# B1L002S, U17-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



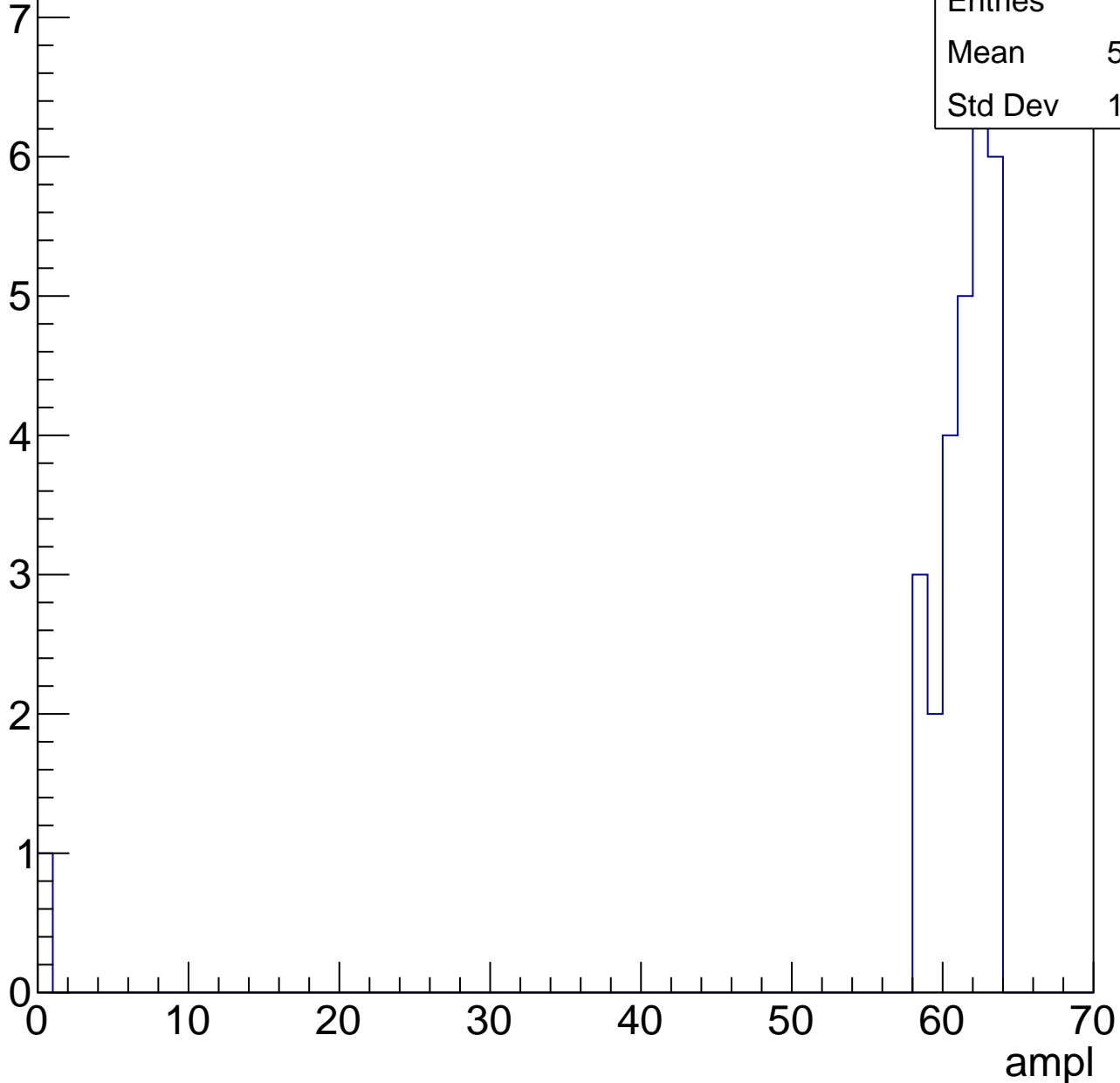
Entries	61
Mean	57.02
Std Dev	3.091

# B1L002S, U17-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.89
Std Dev	11.44

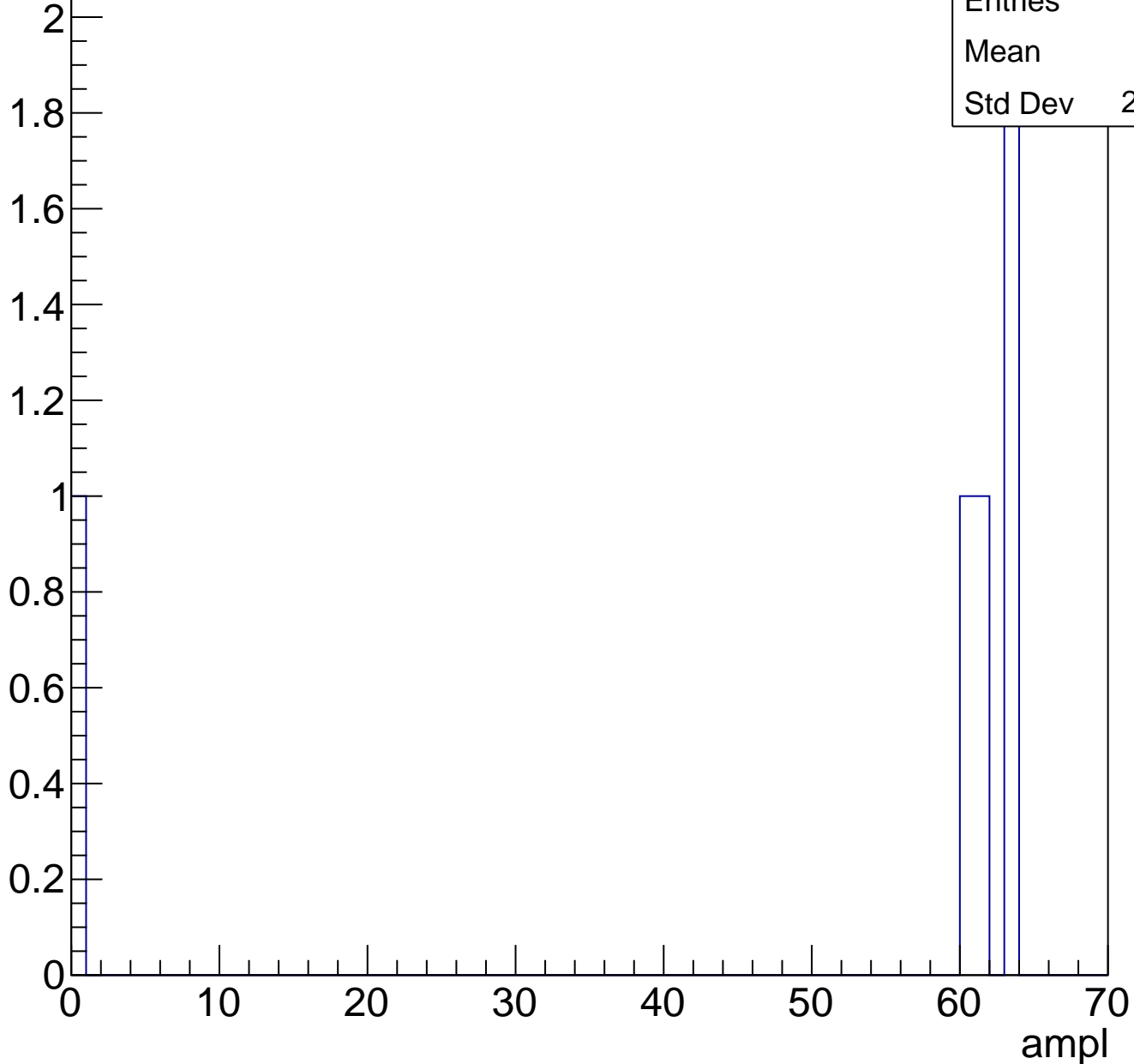




# B1L002S, U17-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch115, adc0

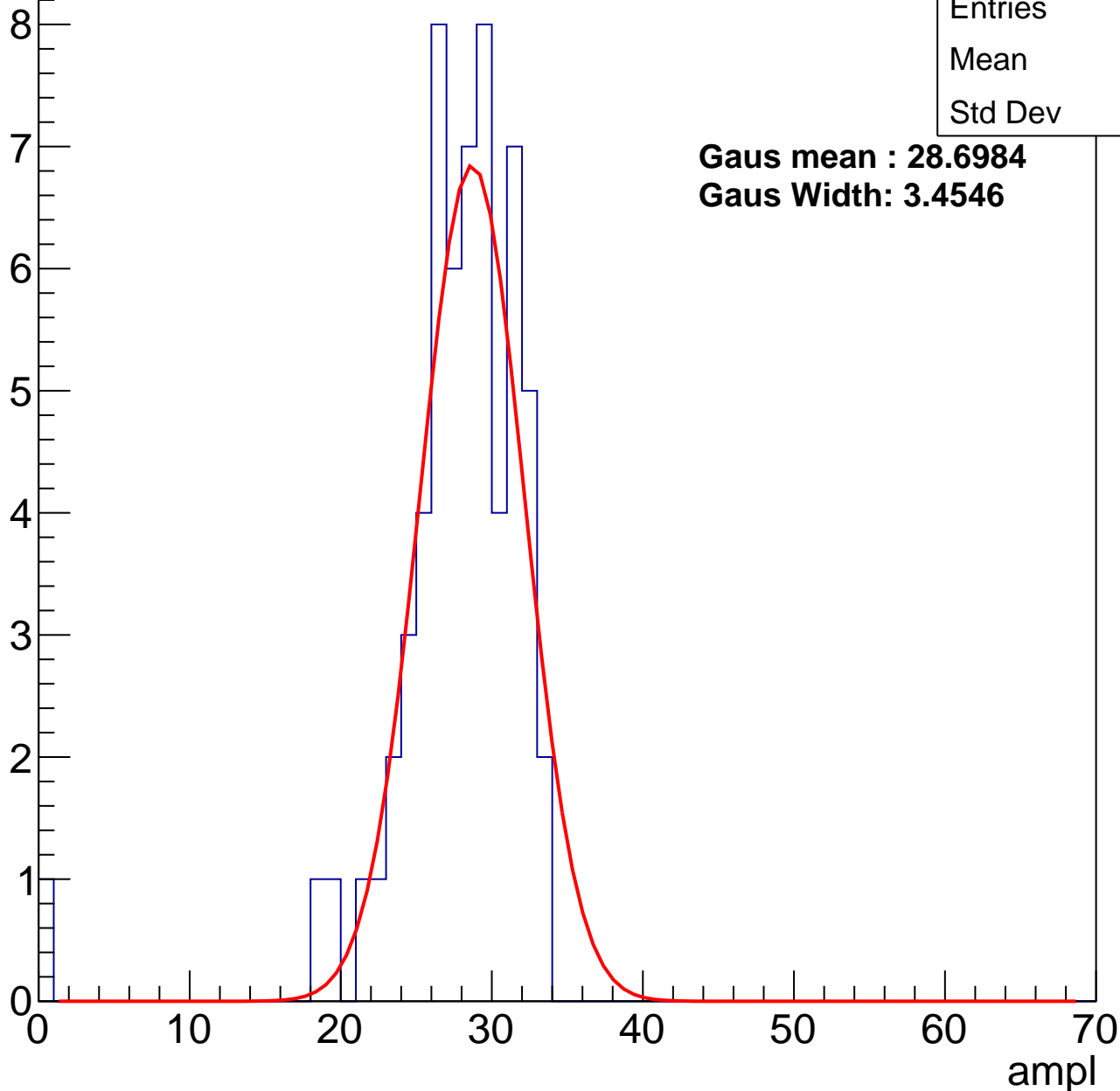
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.2
Std Dev	4.79

**Gaus mean : 28.6984**

**Gaus Width: 3.4546**



# B1L002S, U17-ch115, adc1

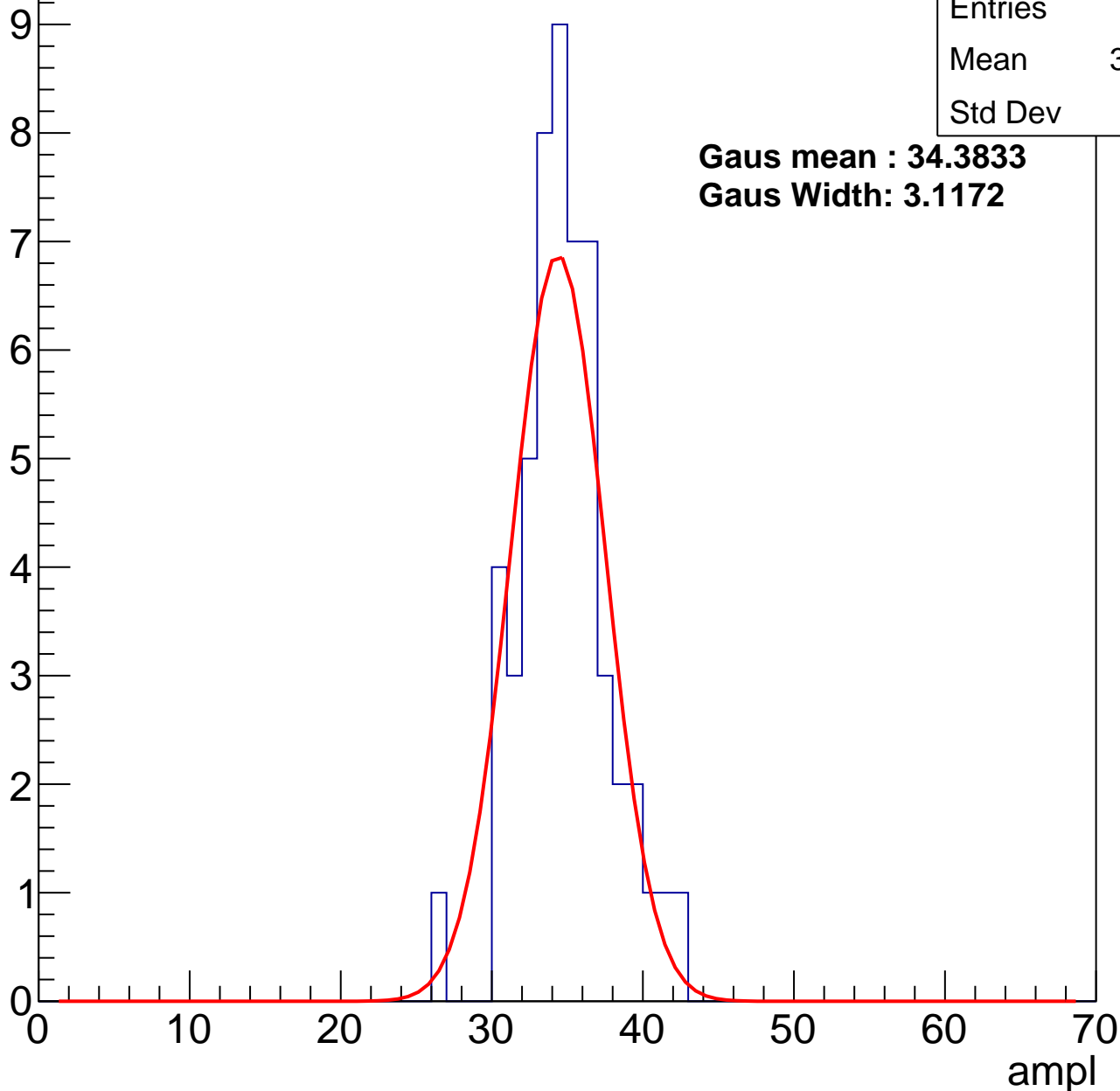
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.33
Std Dev	2.95

**Gaus mean : 34.3833**

**Gaus Width: 3.1172**



# B1L002S, U17-ch115, adc2

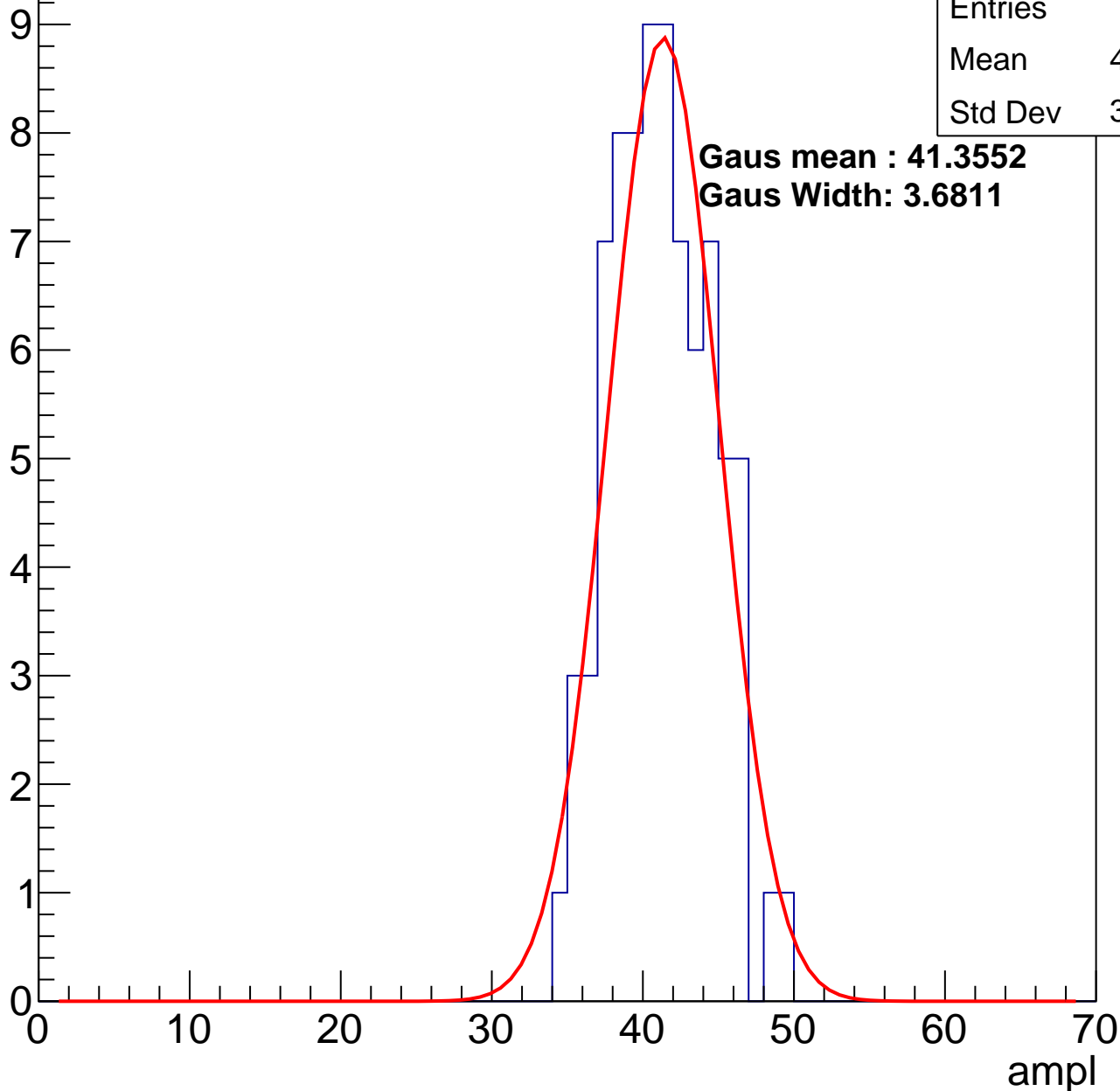
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	40.79
Std Dev	3.289

**Gaus mean : 41.3552**

**Gaus Width: 3.6811**

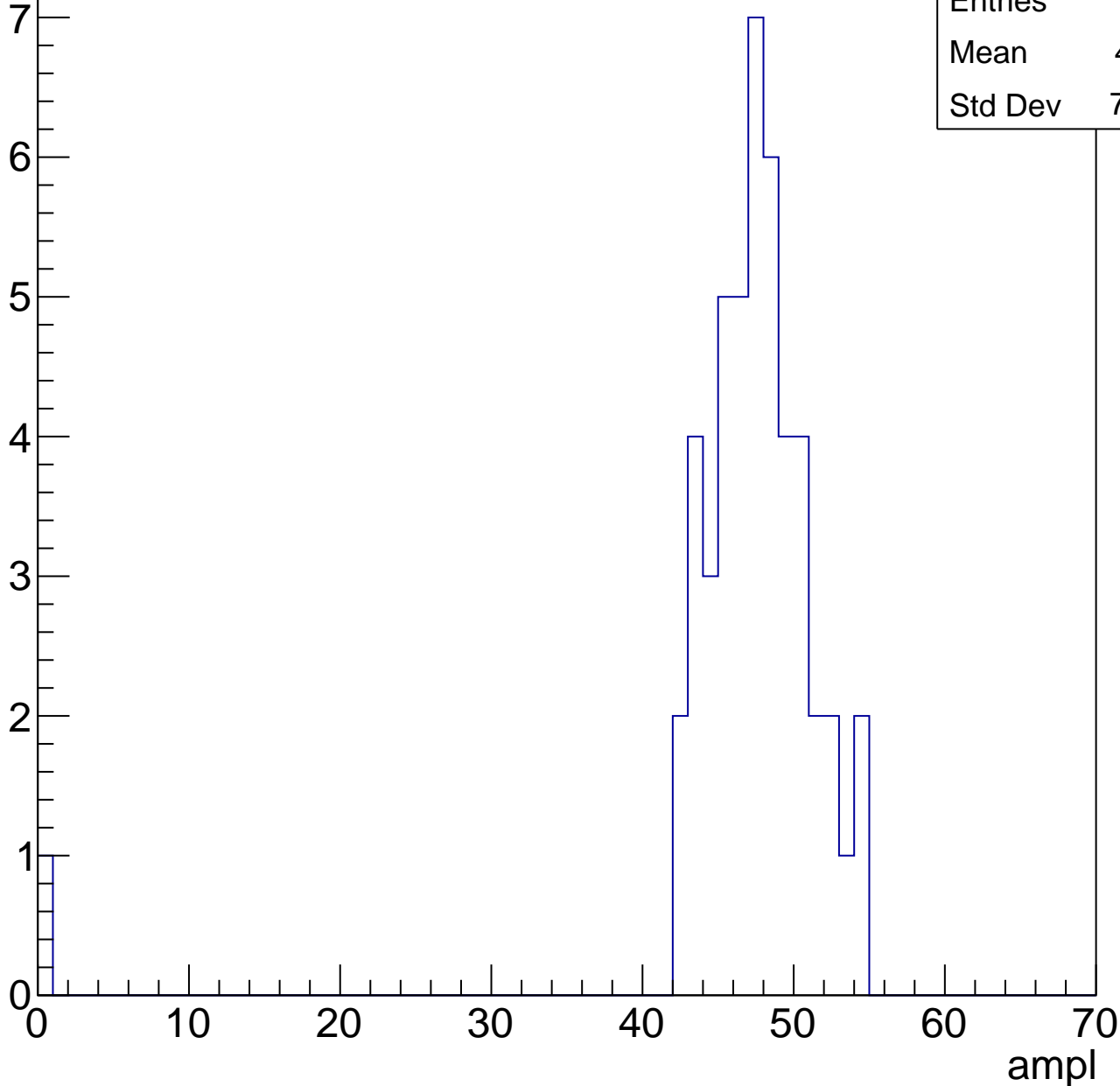


# B1L002S, U17-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	46.31
Std Dev	7.397

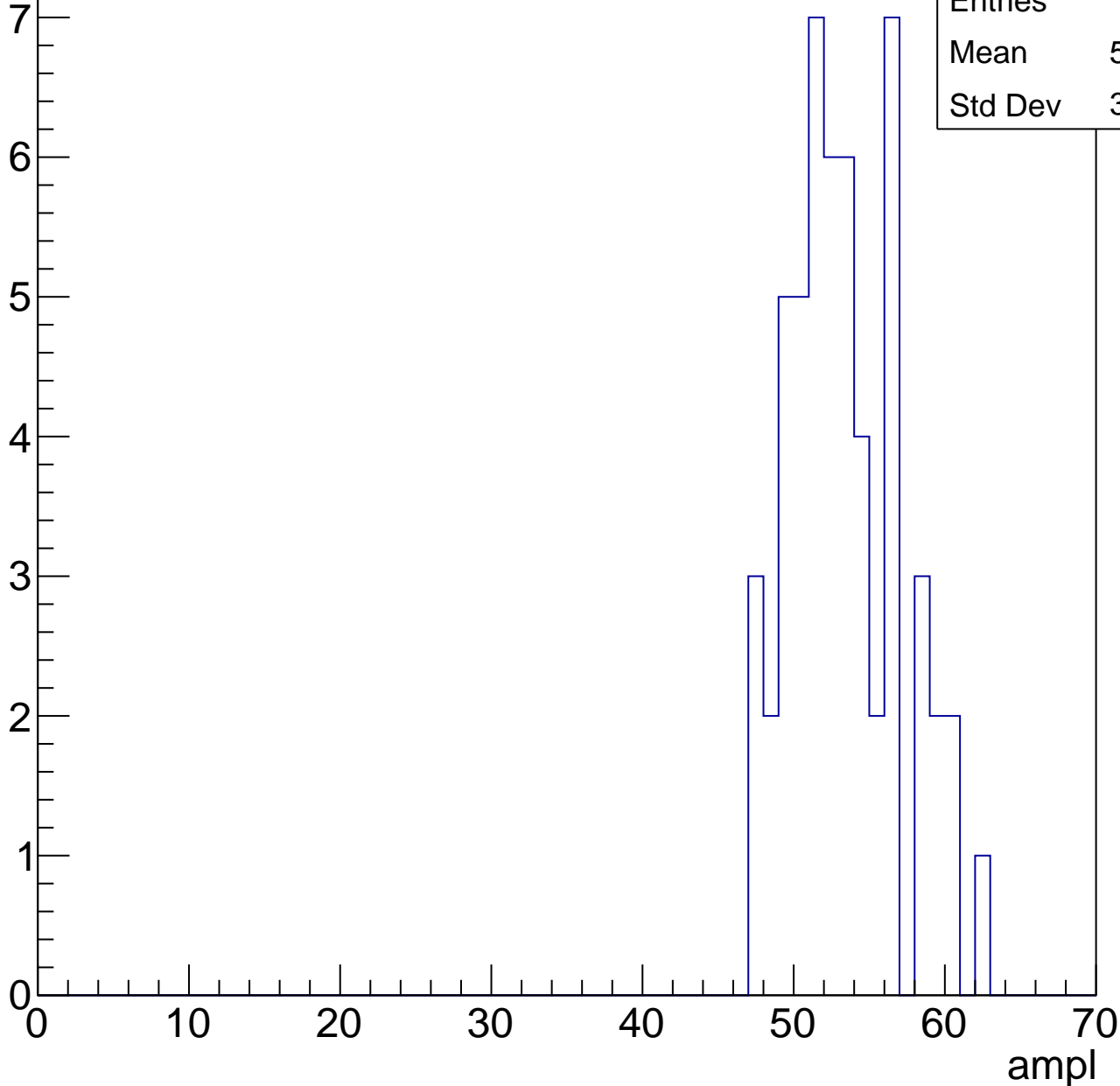


# B1L002S, U17-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.93
Std Dev	3.612

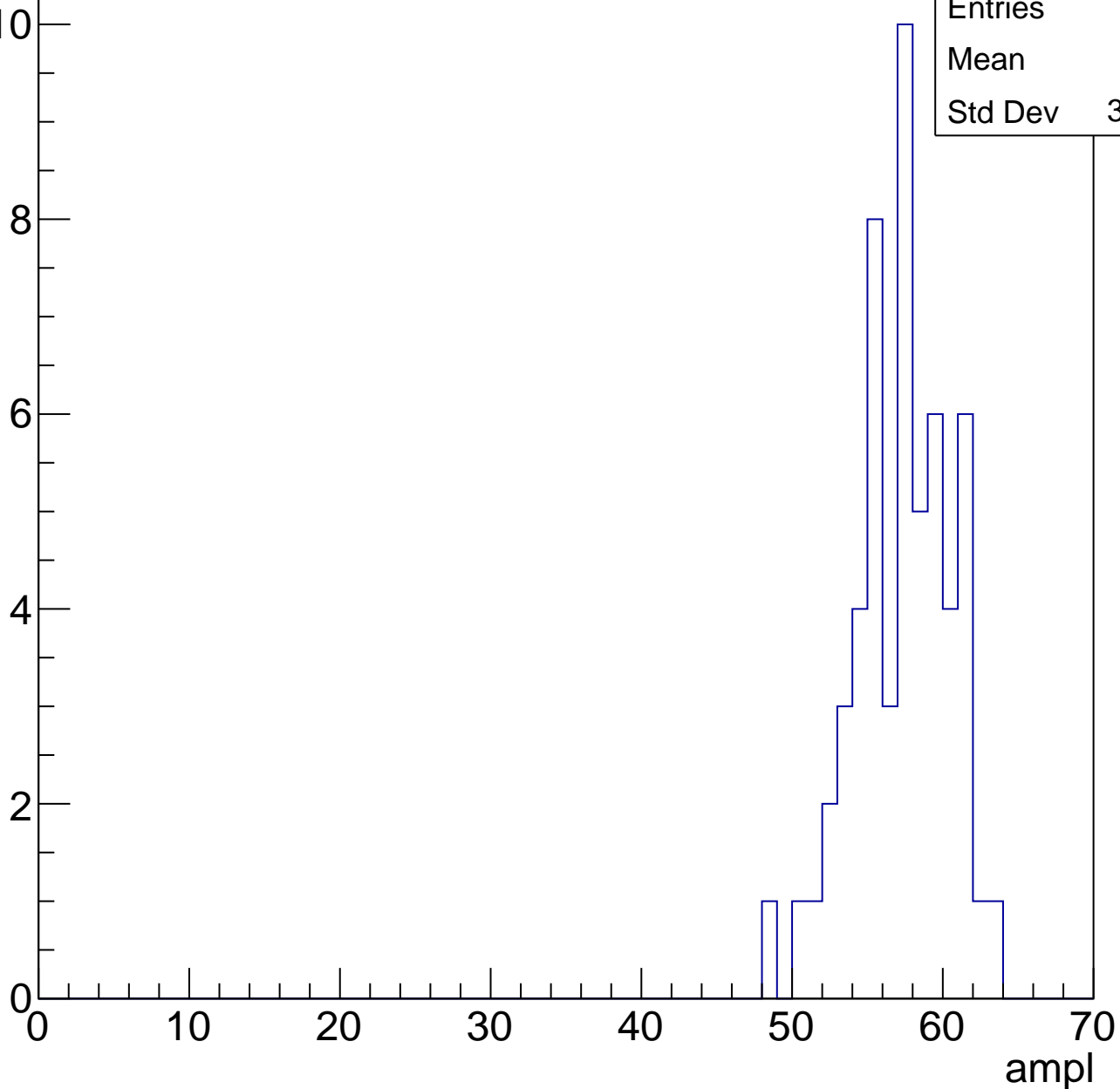


# B1L002S, U17-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.8
Std Dev	3.148

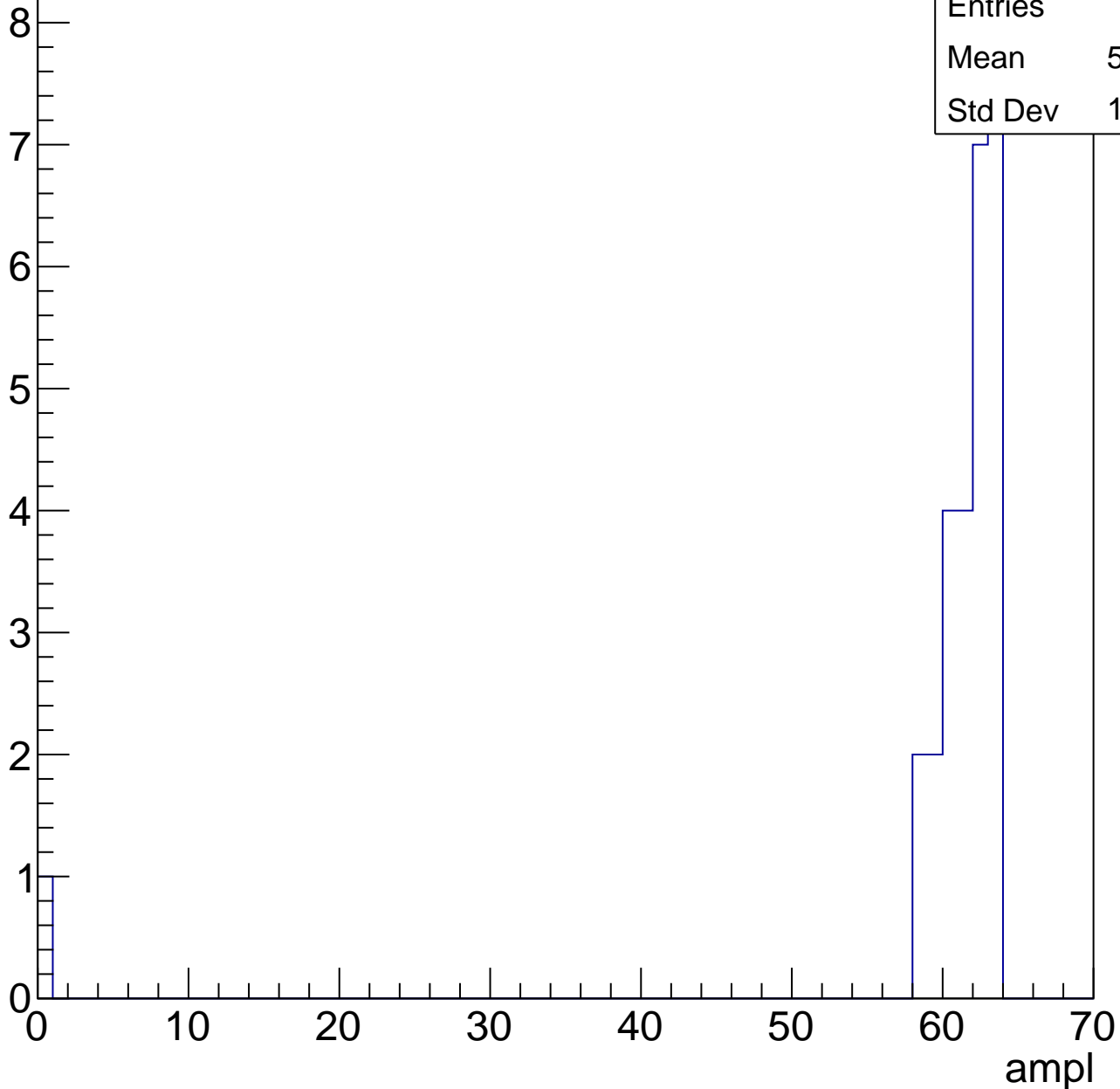


# B1L002S, U17-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59.14
Std Dev	11.49





# B1L002S, U17-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	31.25
Std Dev	31.25

# B1L002S, U17-ch116, adc0

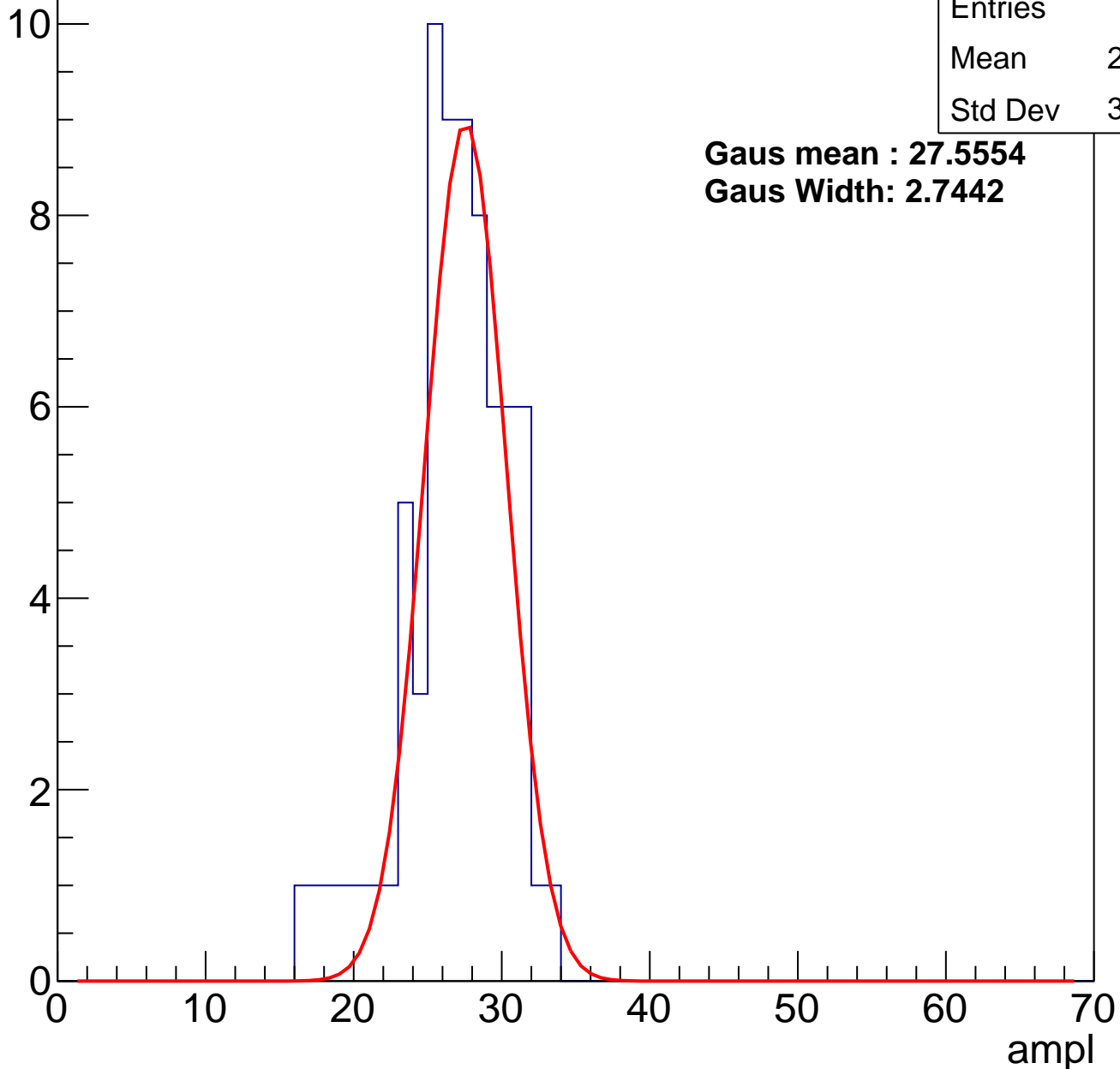
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	26.42
Std Dev	3.463

**Gaus mean : 27.5554**

**Gaus Width: 2.7442**

Entry



# B1L002S, U17-ch116, adc1

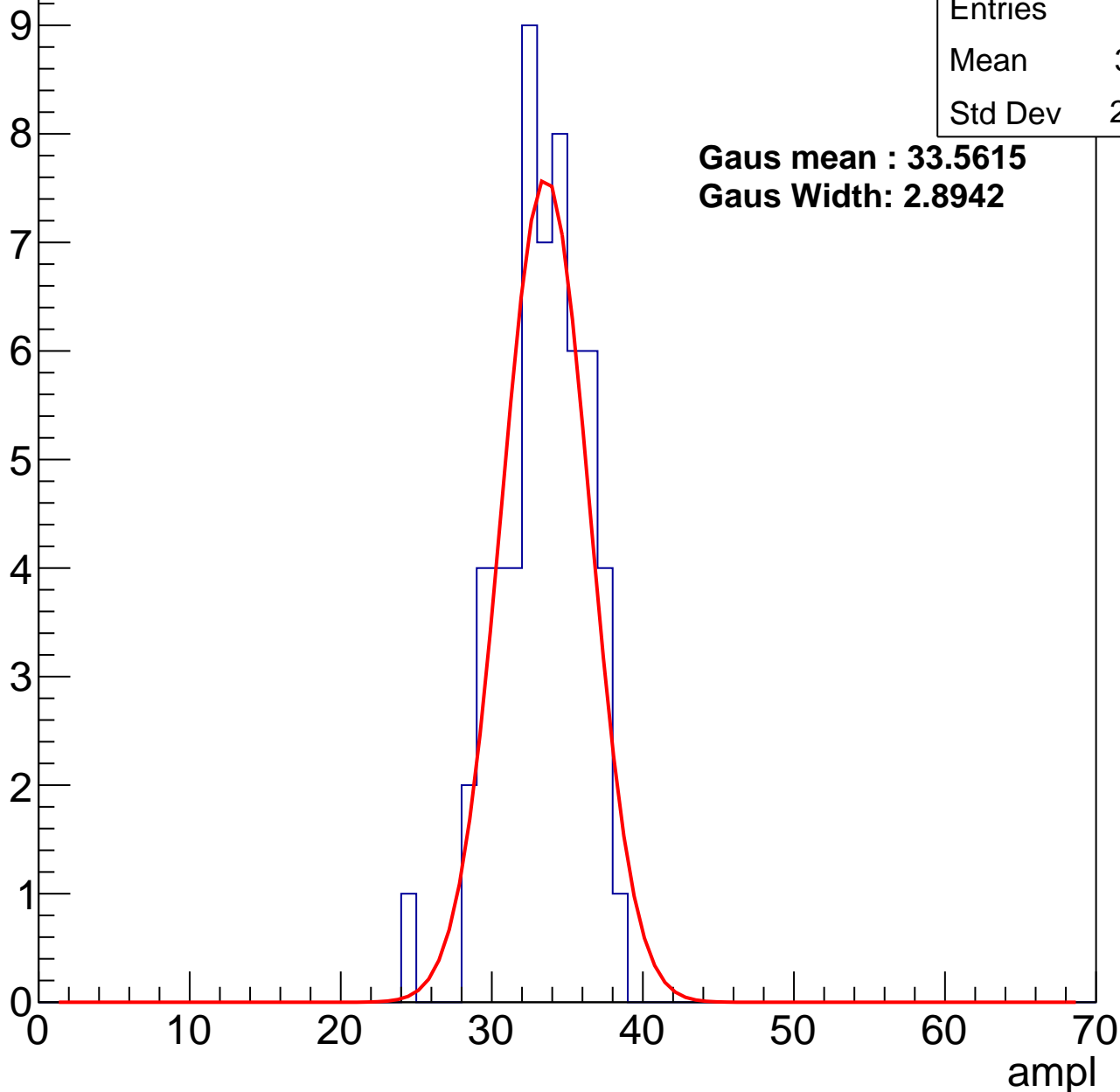
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	32.91
Std Dev	2.773

**Gaus mean : 33.5615**

**Gaus Width: 2.8942**



# B1L002S, U17-ch116, adc2

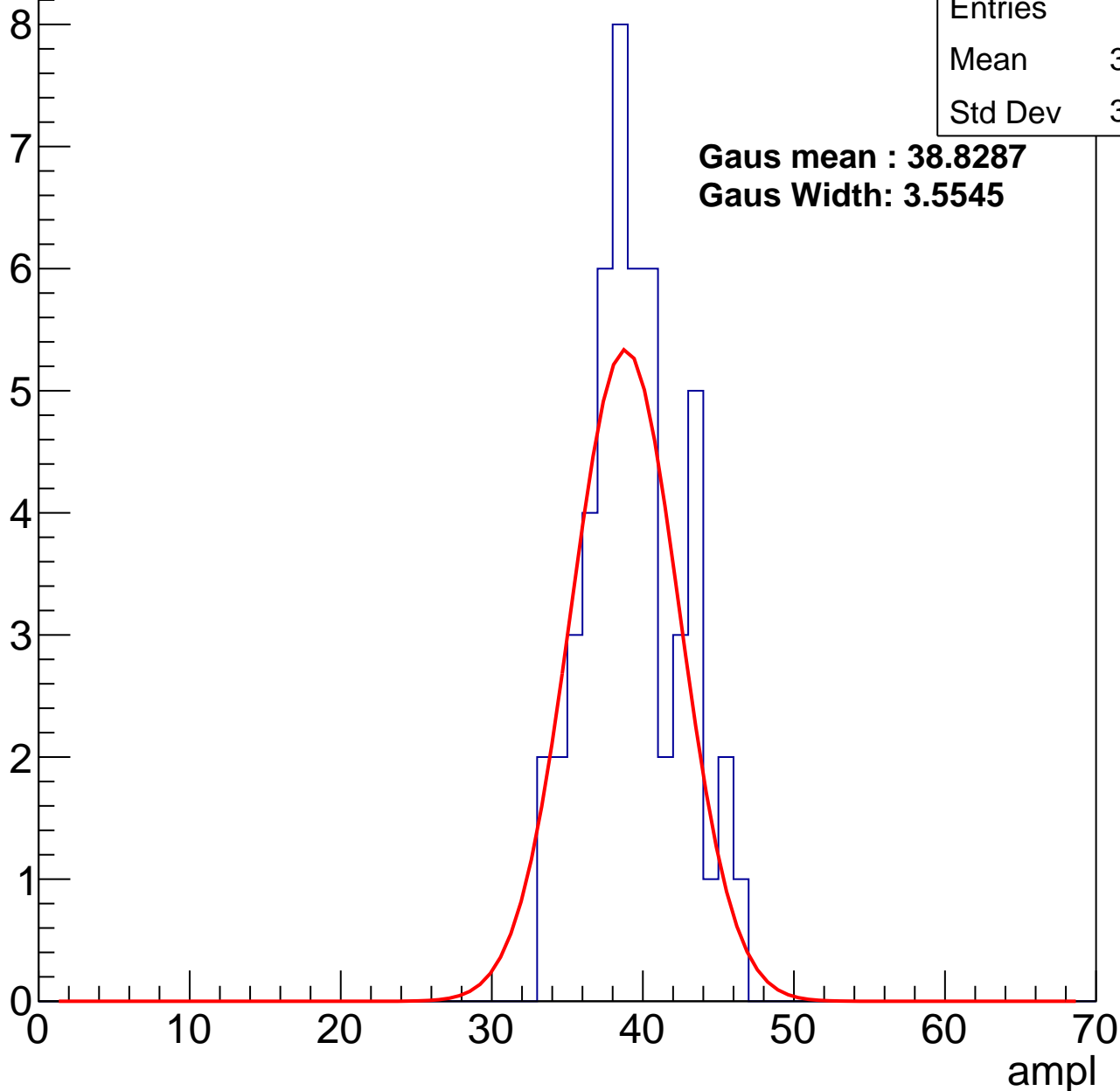
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	38.94
Std Dev	3.146

**Gaus mean : 38.8287**

**Gaus Width: 3.5545**

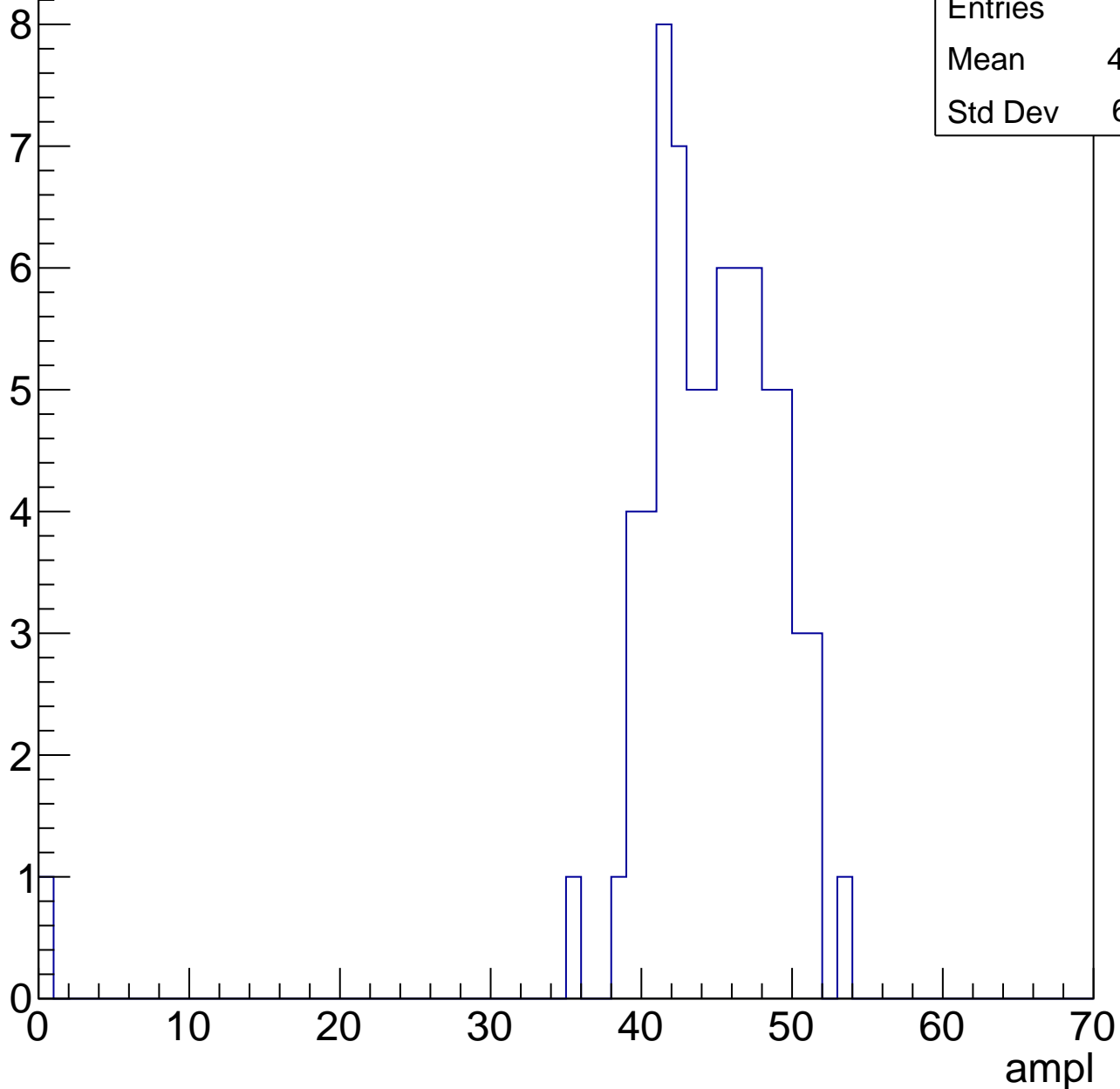


# B1L002S, U17-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	43.87
Std Dev	6.441

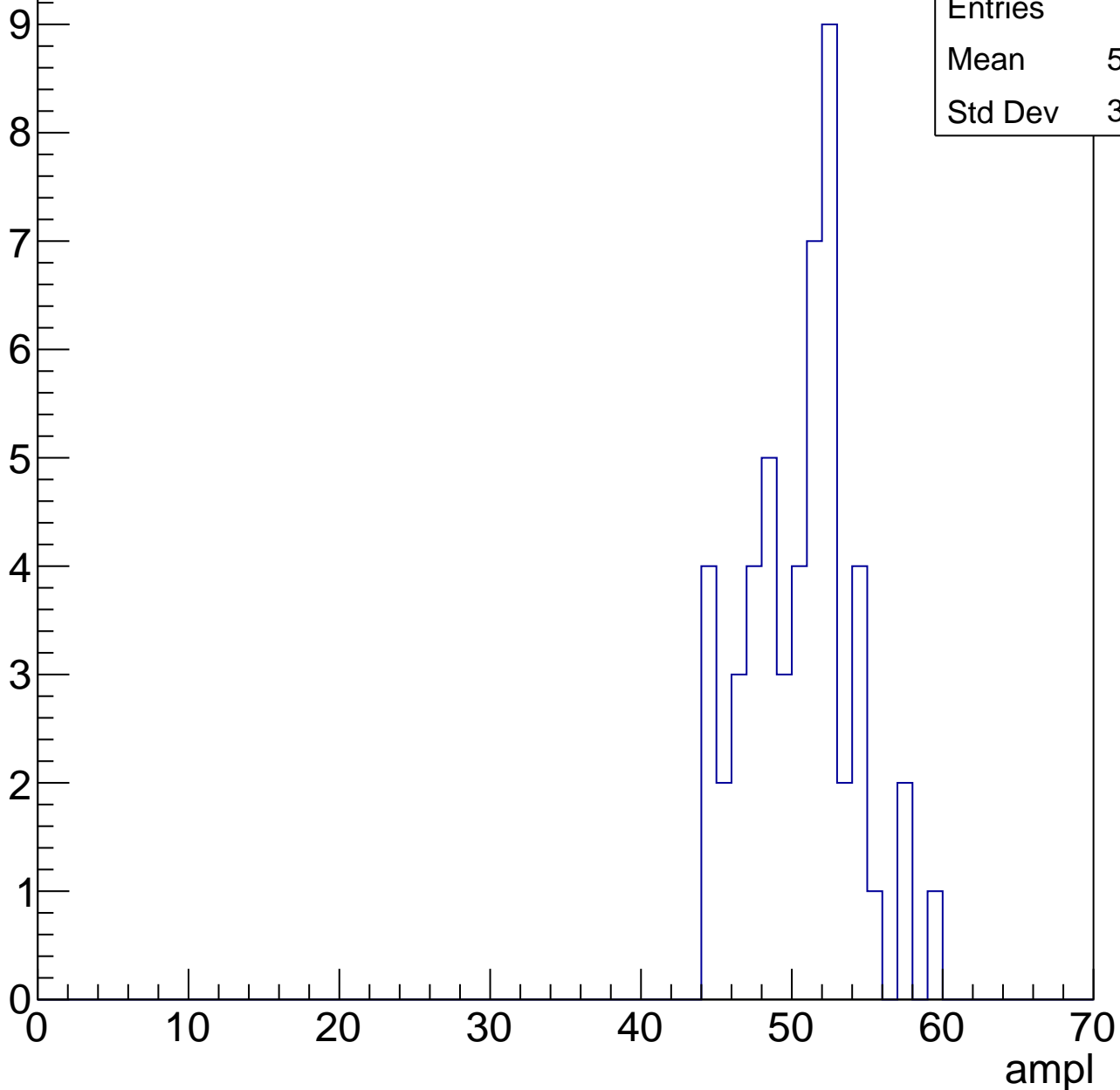


# B1L002S, U17-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	50.08
Std Dev	3.514

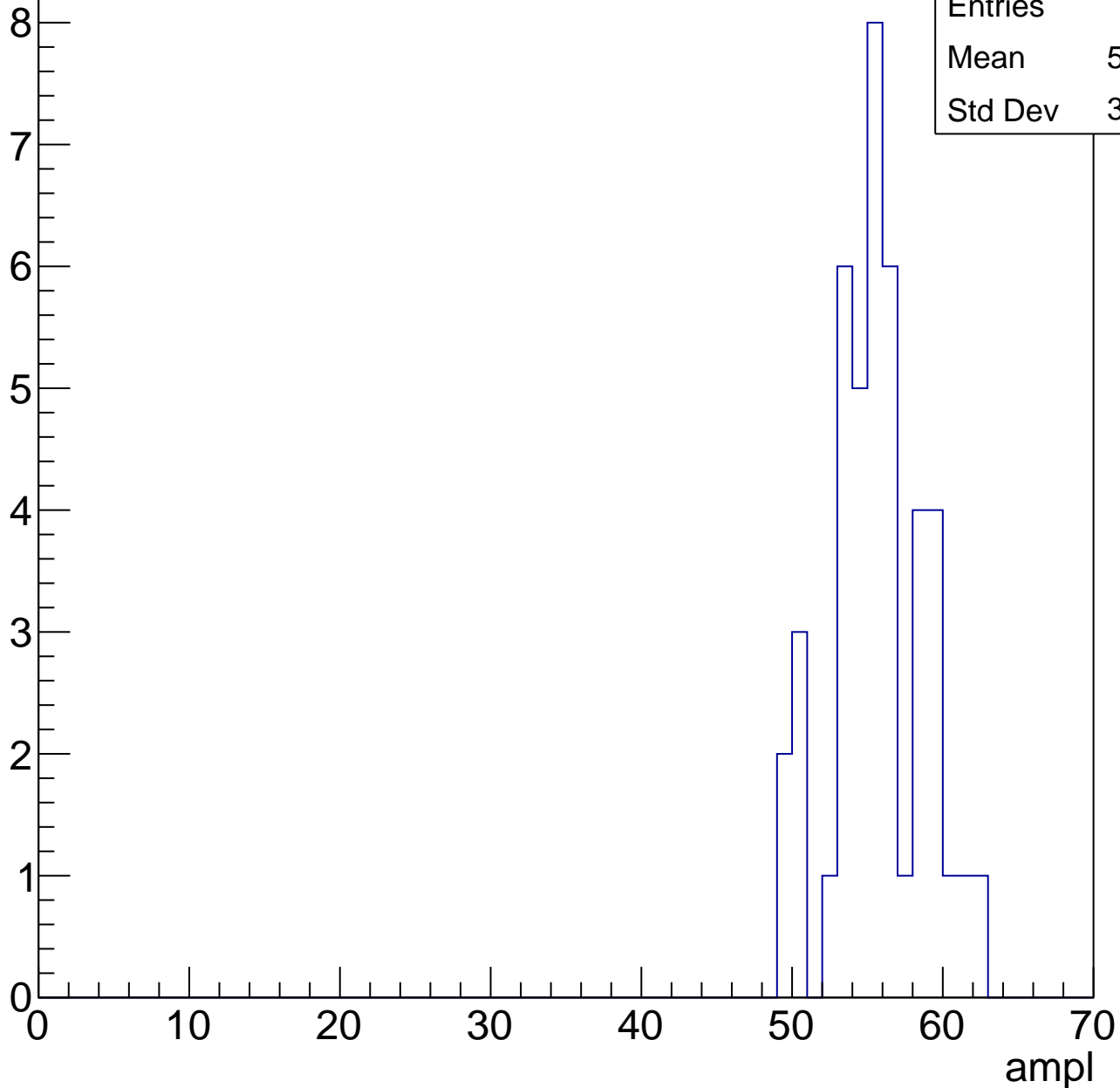


# B1L002S, U17-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	55.16
Std Dev	3.065

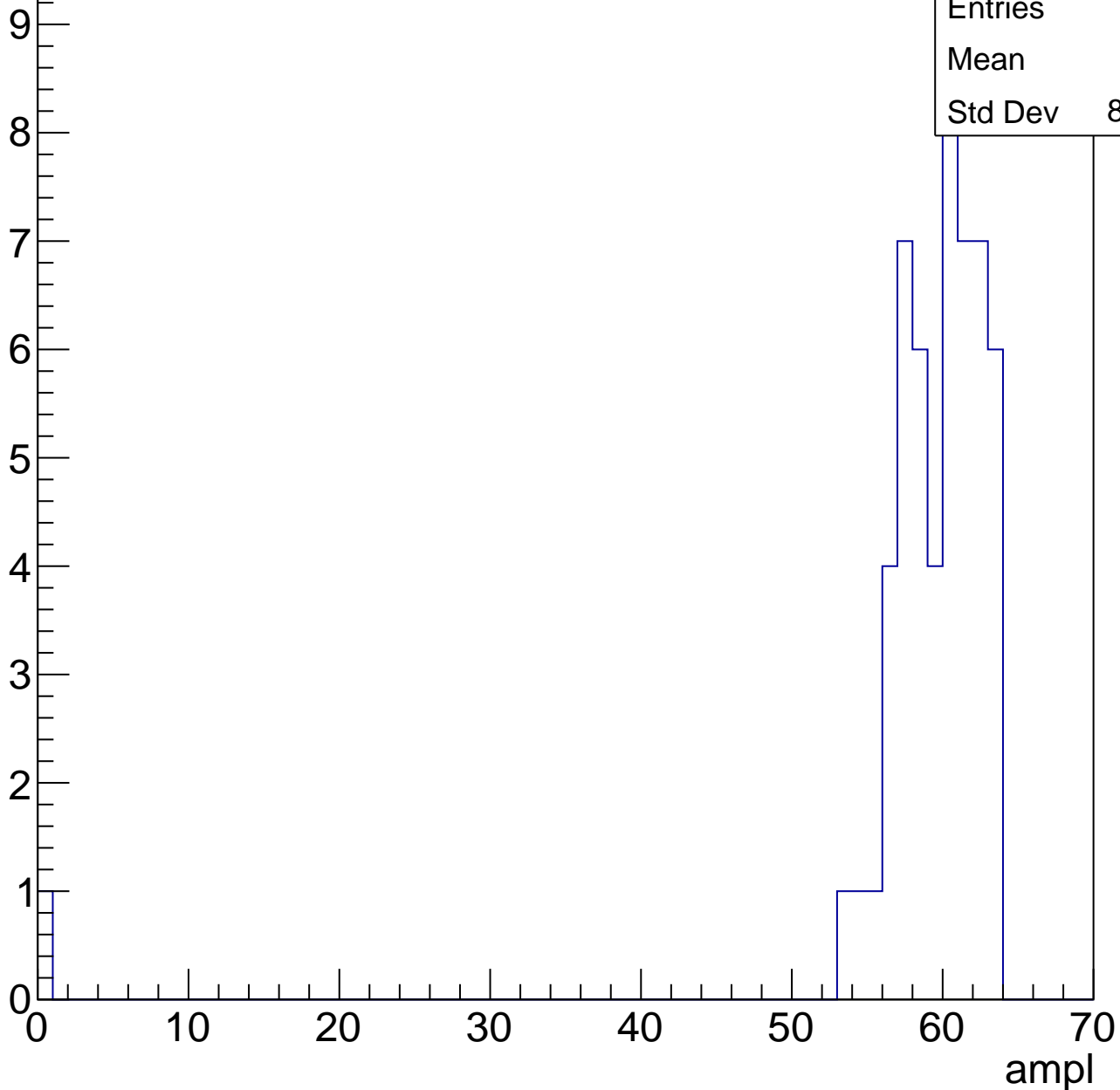


# B1L002S, U17-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.3
Std Dev	8.386

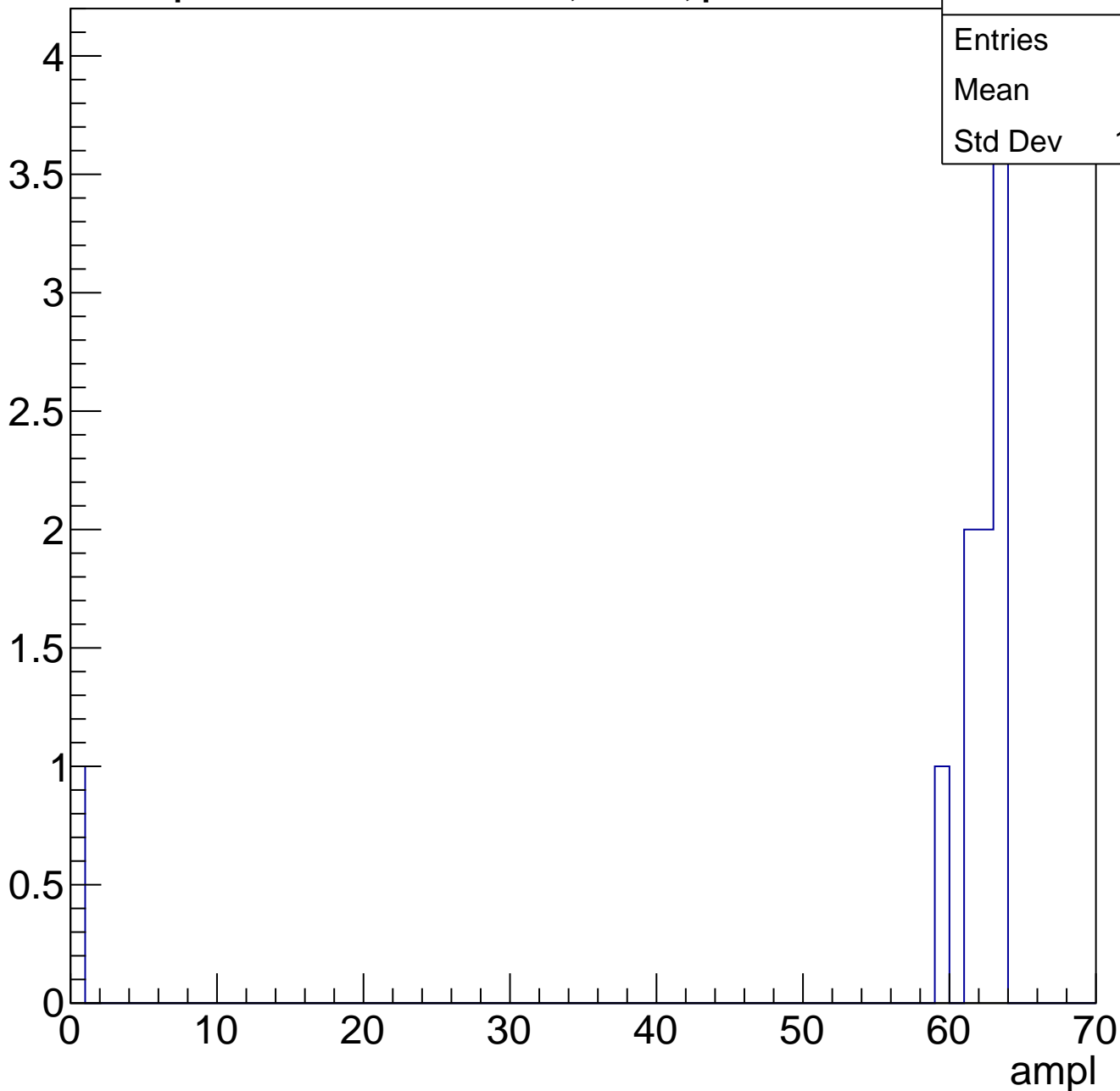




# B1L002S, U17-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch117, adc0

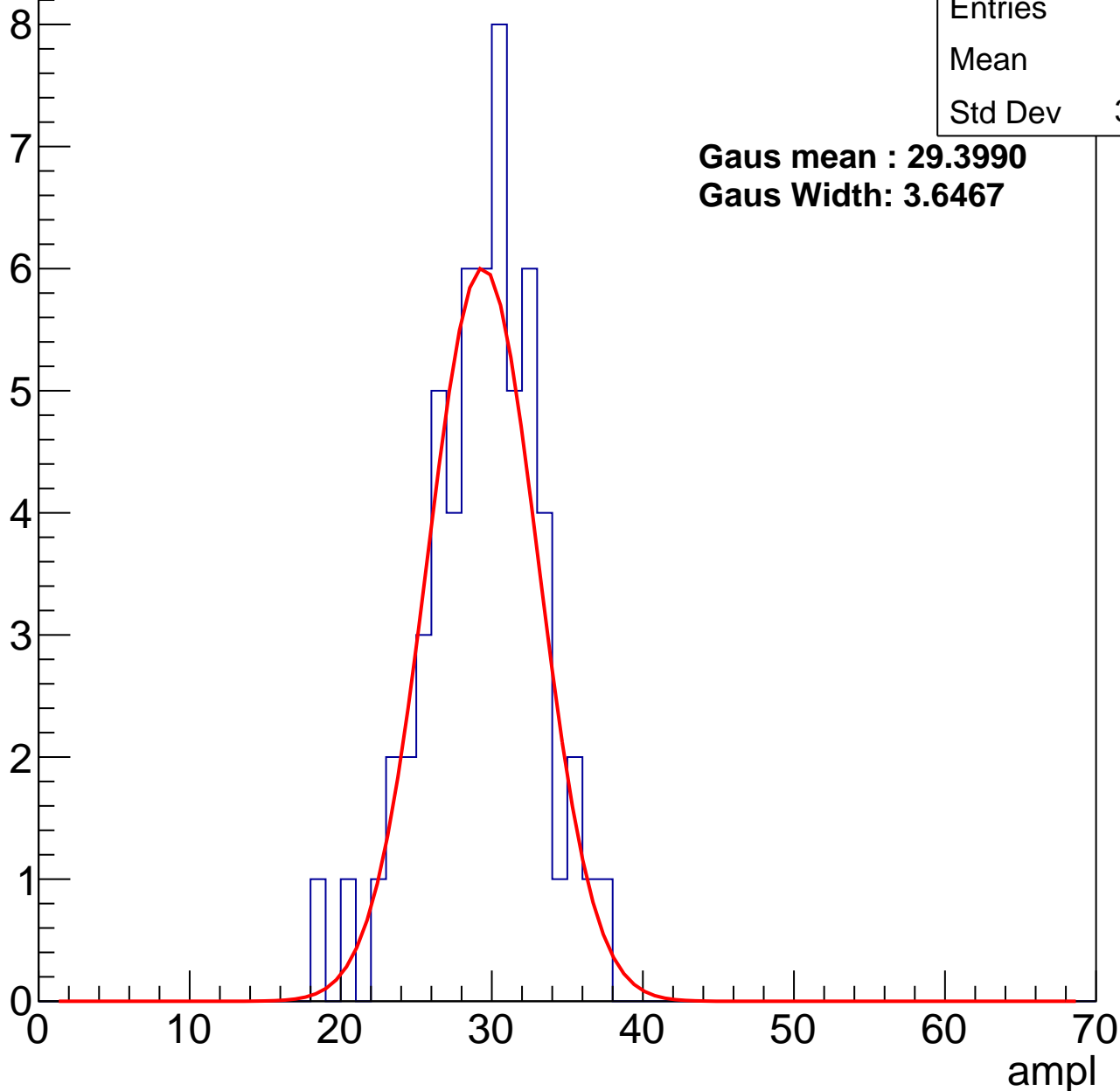
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.9
Std Dev	3.781

**Gaus mean : 29.3990**

**Gaus Width: 3.6467**



# B1L002S, U17-ch117, adc1

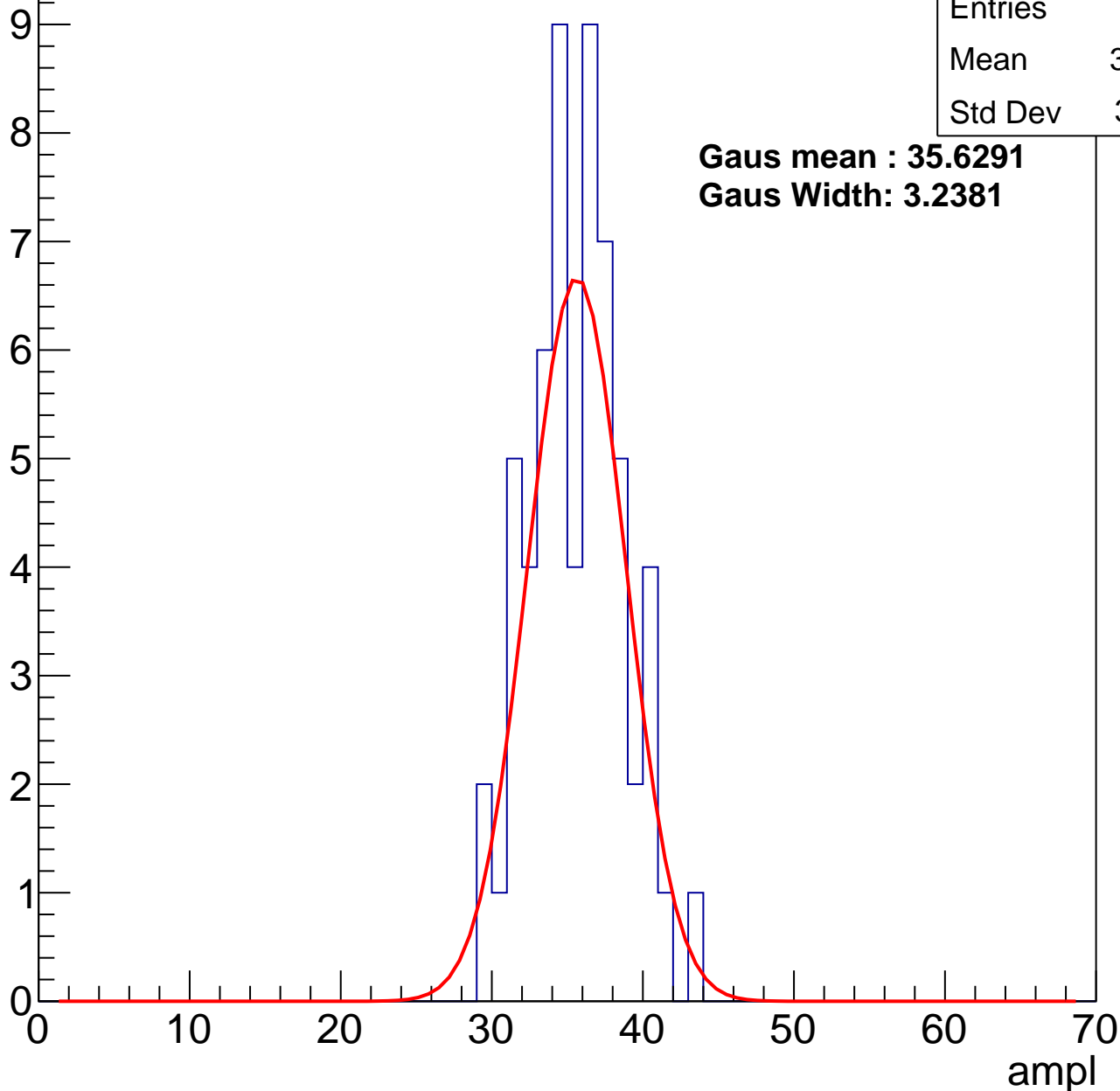
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.17
Std Dev	3.051

**Gaus mean : 35.6291**

**Gaus Width: 3.2381**



# B1L002S, U17-ch117, adc2

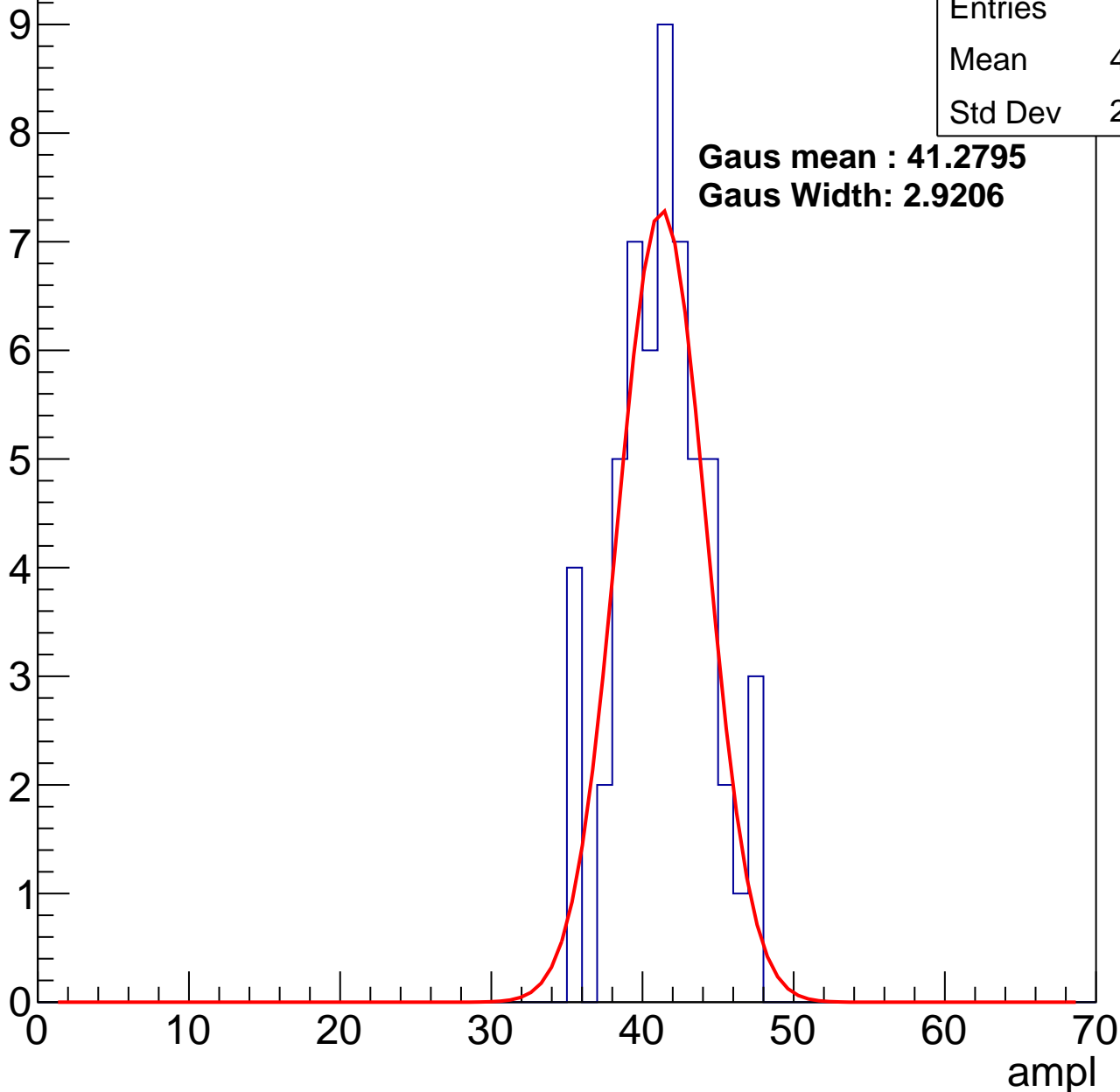
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.93
Std Dev	2.963

**Gaus mean : 41.2795**

**Gaus Width: 2.9206**

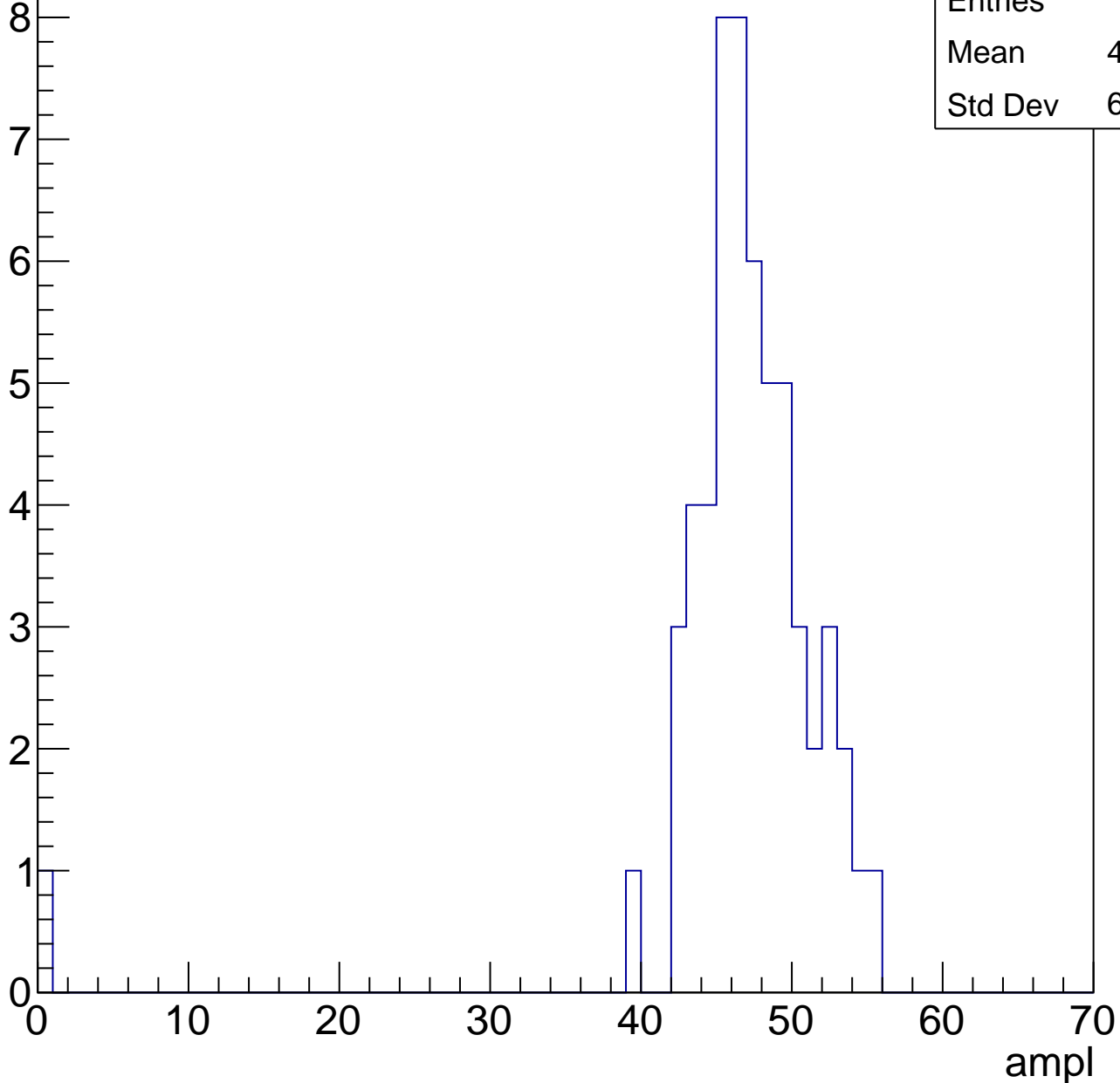


# B1L002S, U17-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.16
Std Dev	6.998

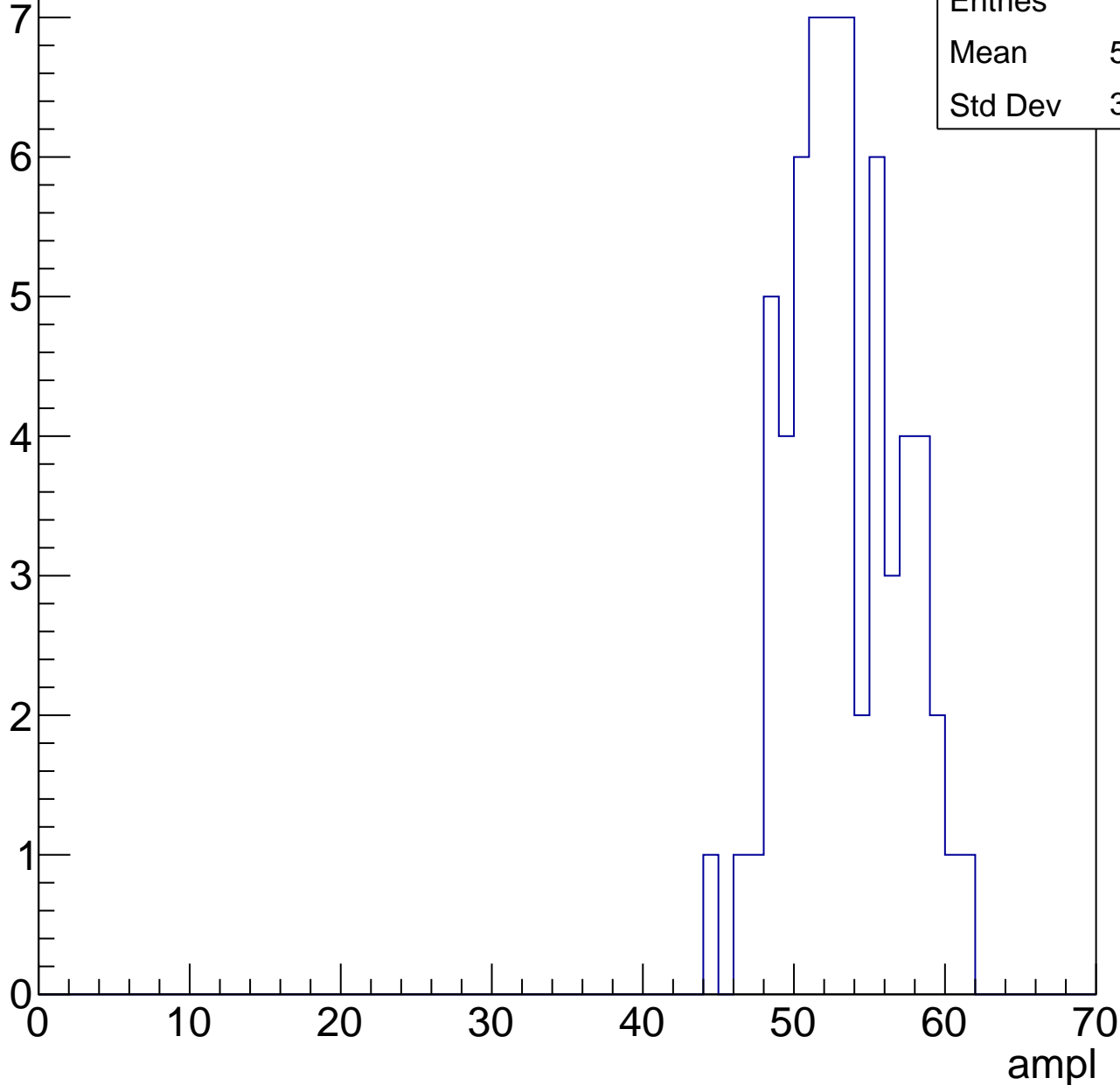


# B1L002S, U17-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	52.74
Std Dev	3.685

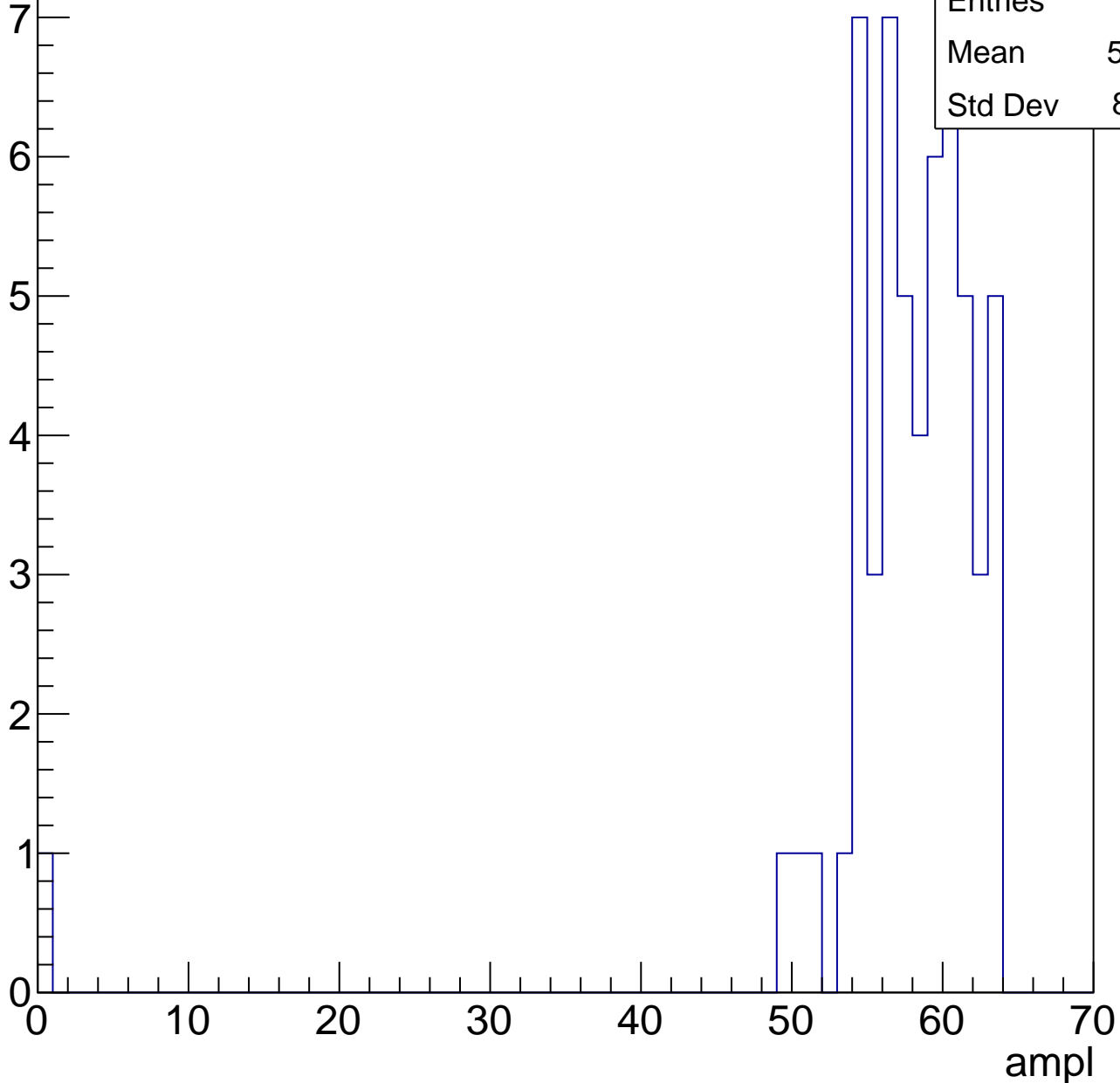


# B1L002S, U17-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	56.75
Std Dev	8.291

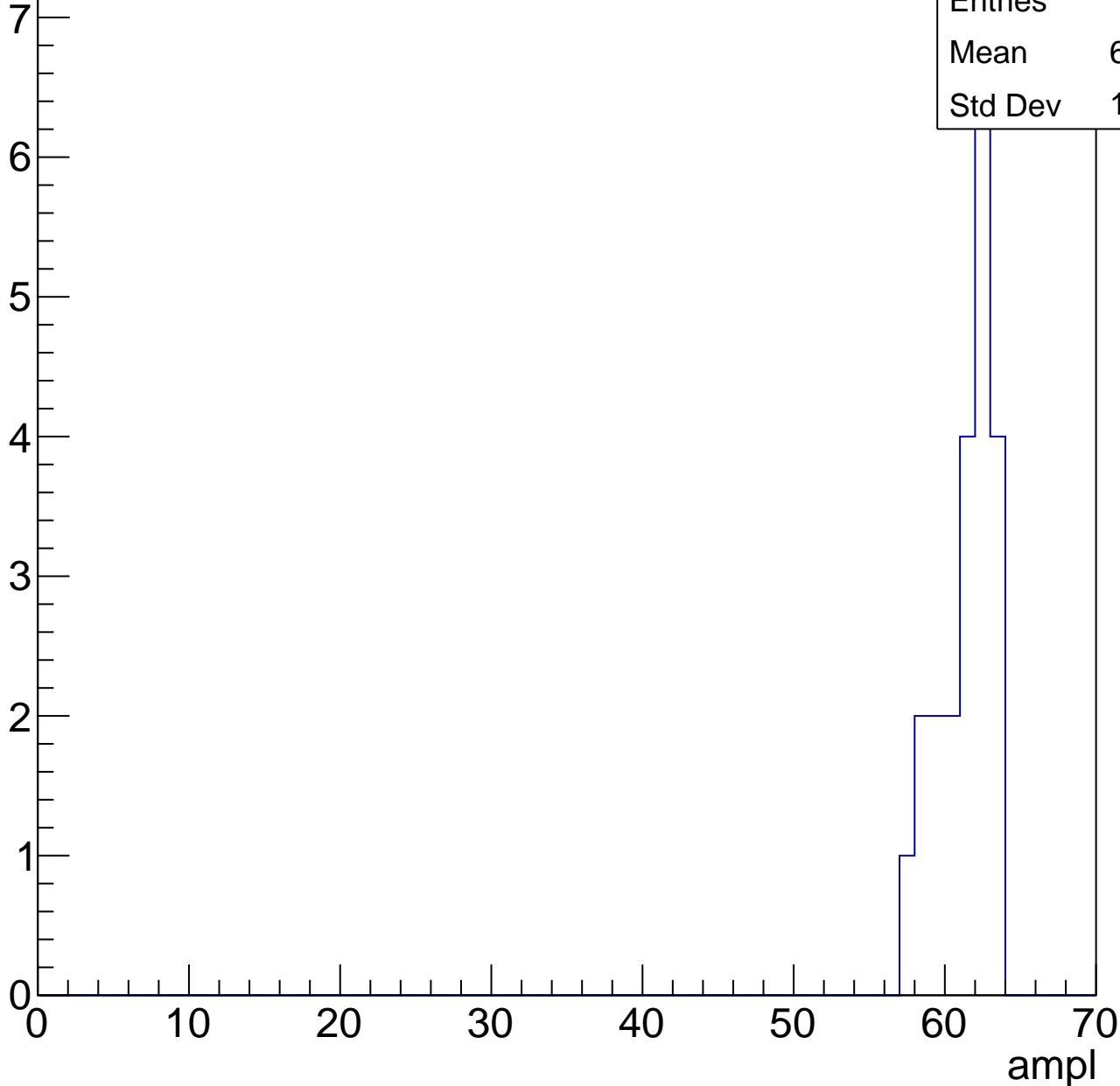


# B1L002S, U17-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	60.95
Std Dev	1.745

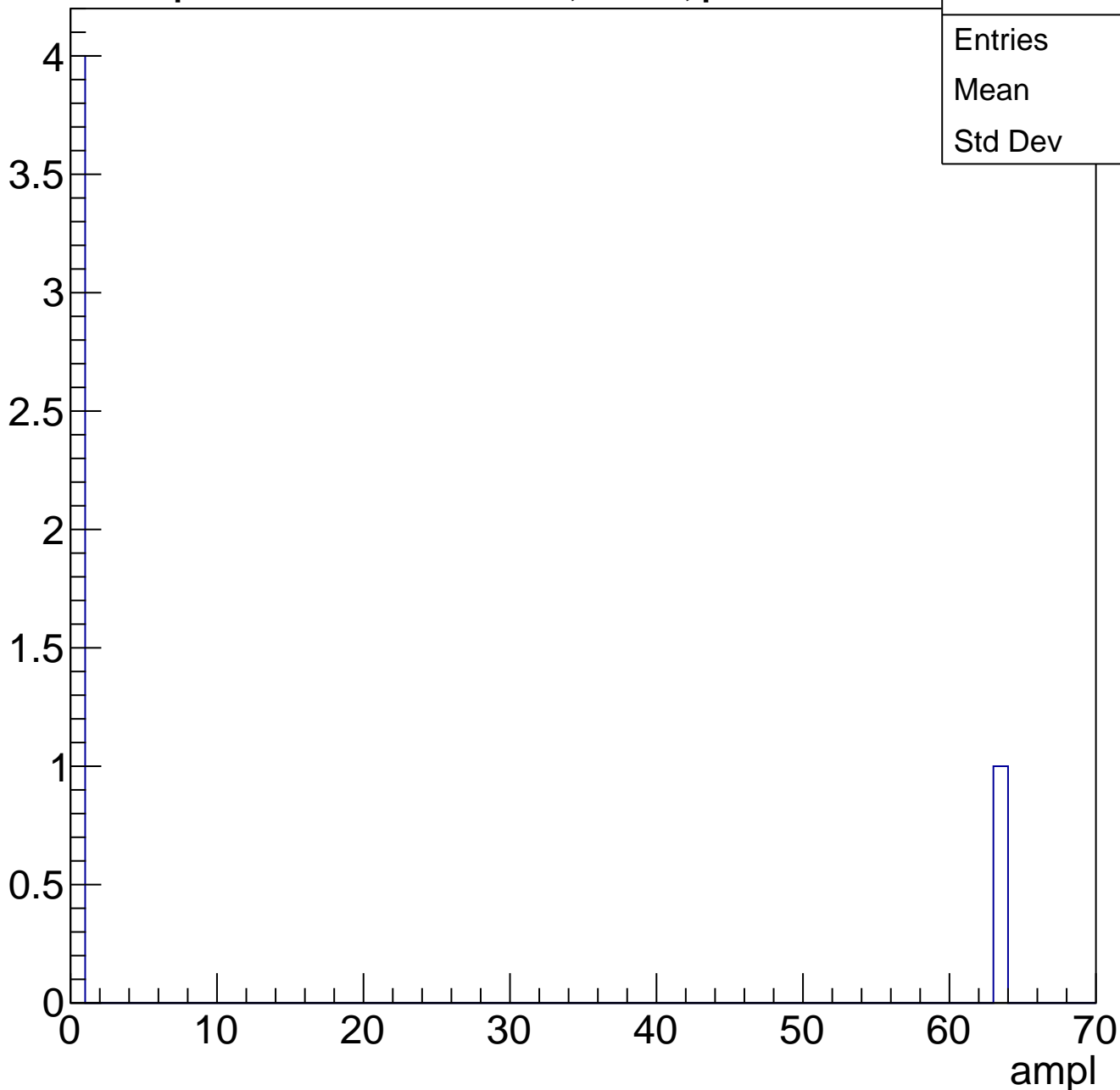




# B1L002S, U17-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch118, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	52
Mean	27.96
Std Dev	2.993

**Gaus mean : 28.8634**

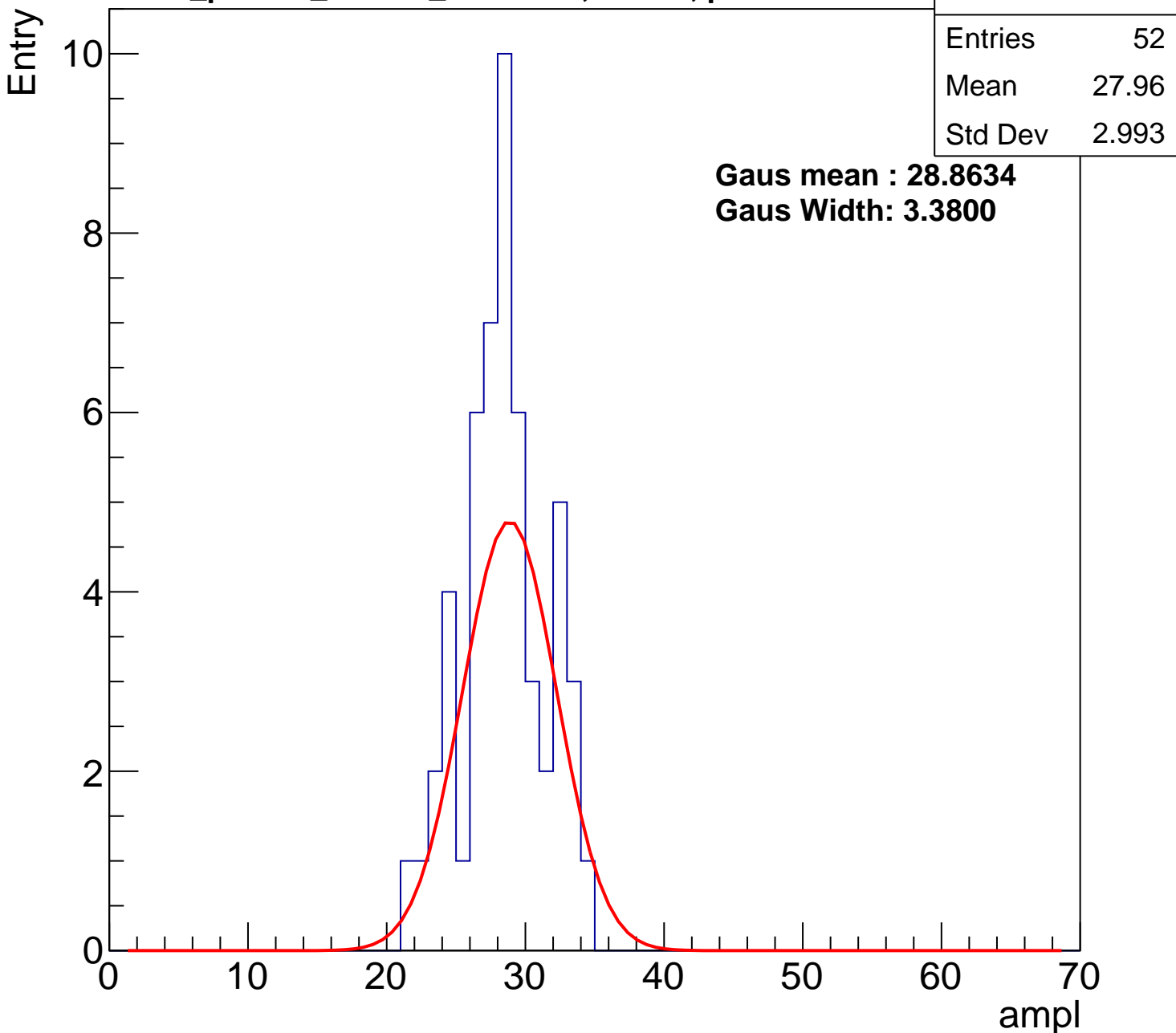
**Gaus Width: 3.3800**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch118, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	34.05
Std Dev	3.005

**Gaus mean : 34.2155**

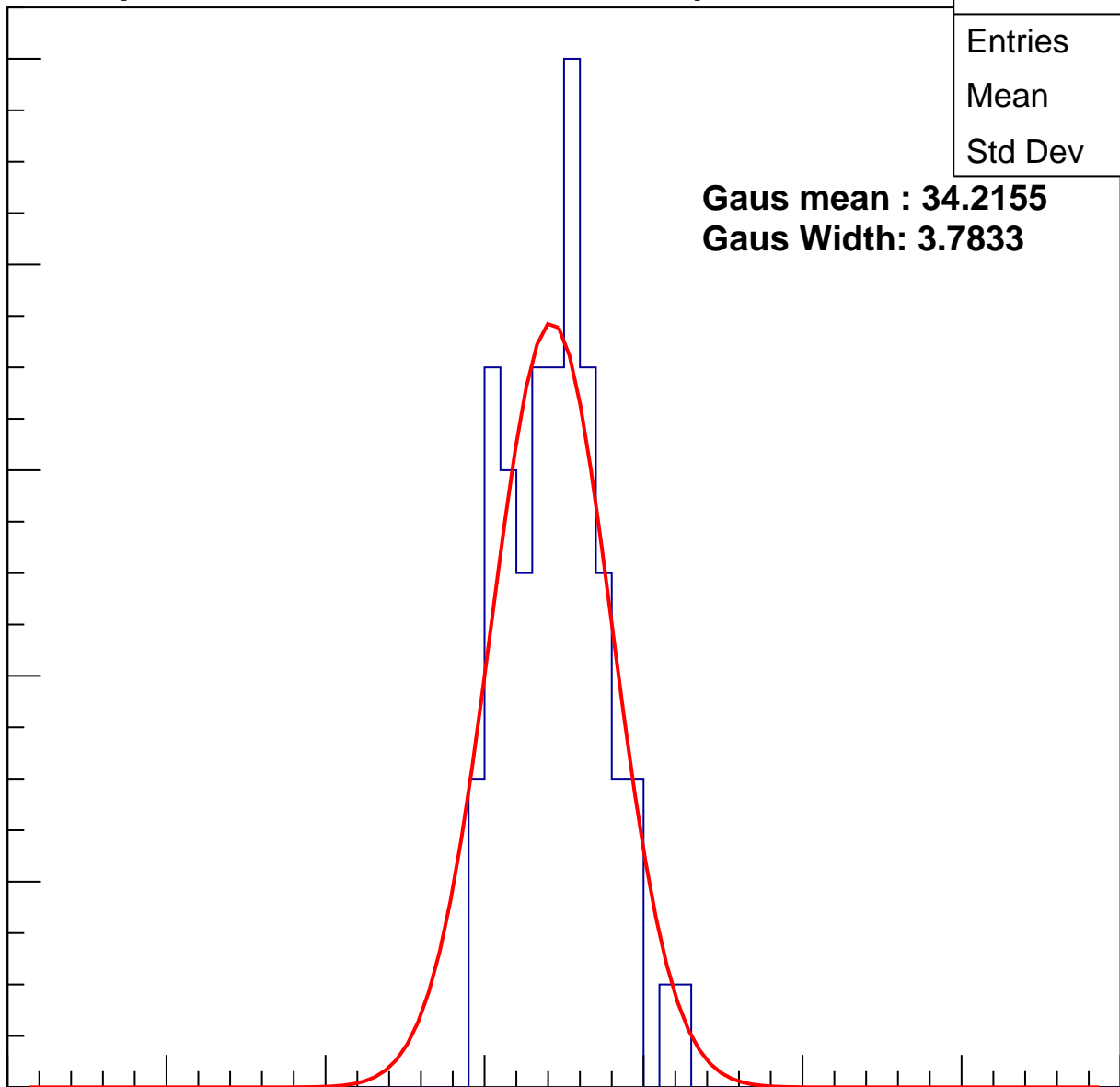
**Gaus Width: 3.7833**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch118, adc2

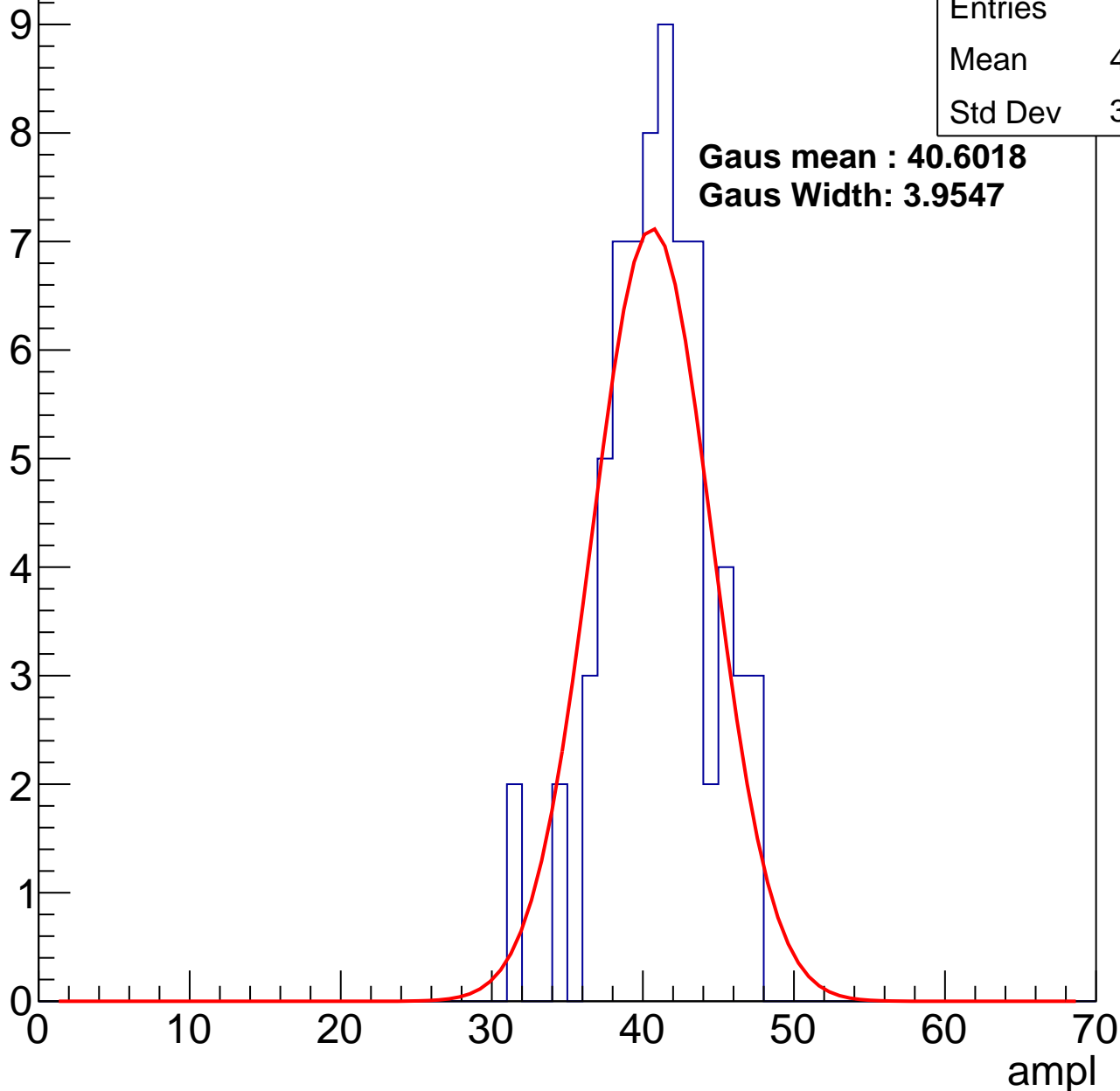
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.48
Std Dev	3.475

**Gaus mean : 40.6018**

**Gaus Width: 3.9547**

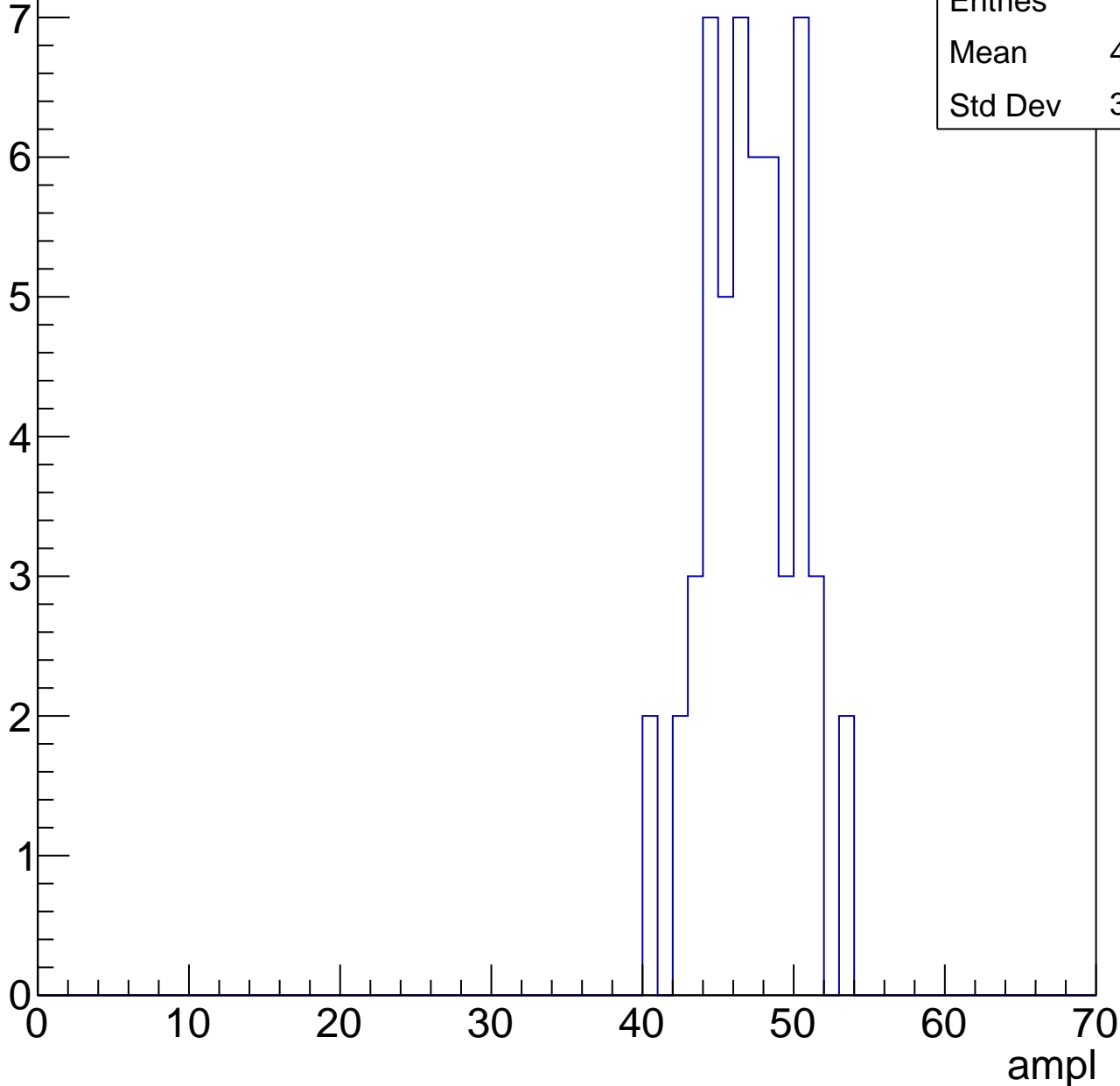


# B1L002S, U17-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.68
Std Dev	3.014

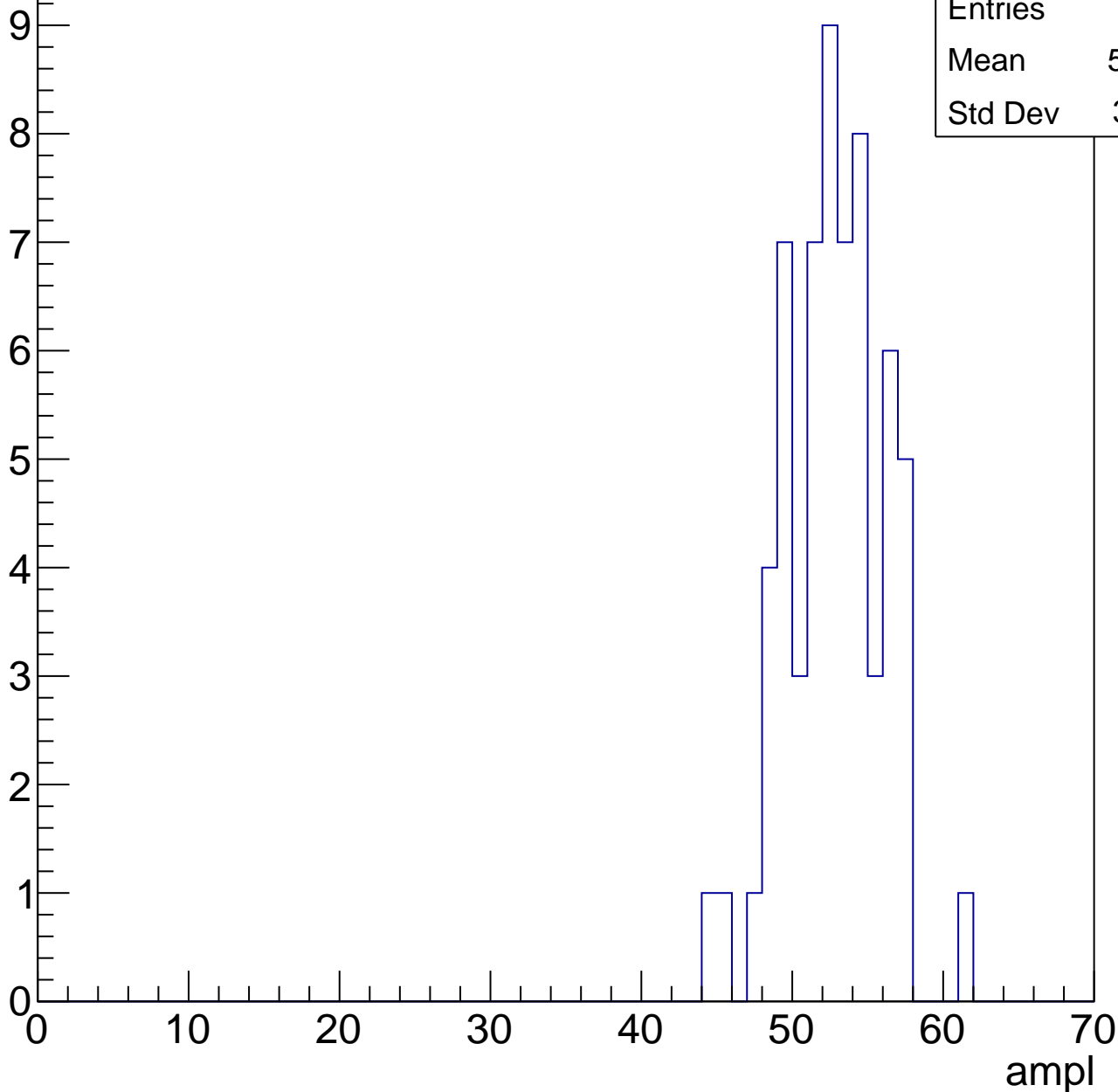


# B1L002S, U17-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	52.32
Std Dev	3.201

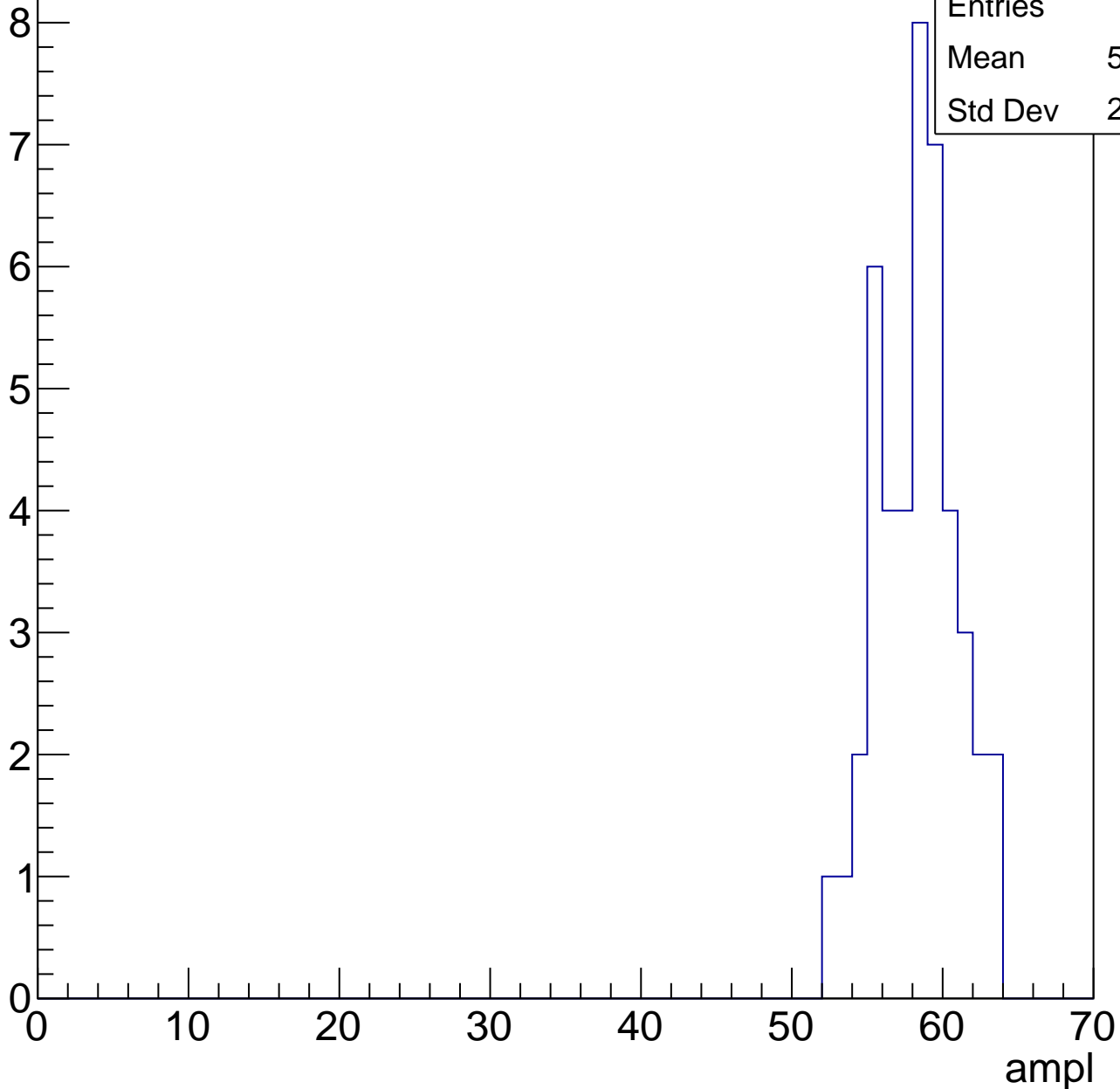


# B1L002S, U17-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	57.84
Std Dev	2.602

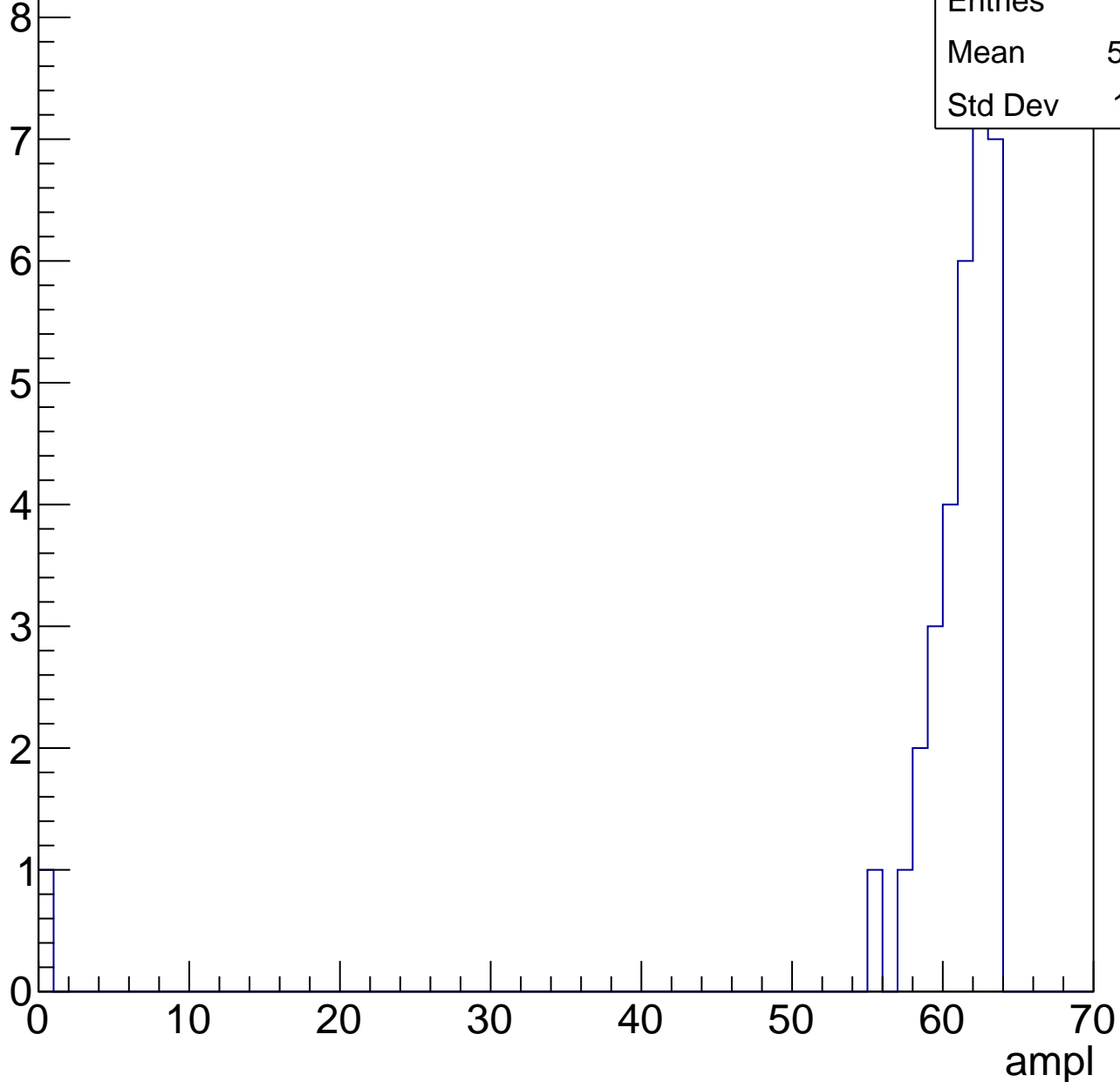


# B1L002S, U17-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	59.03
Std Dev	10.61

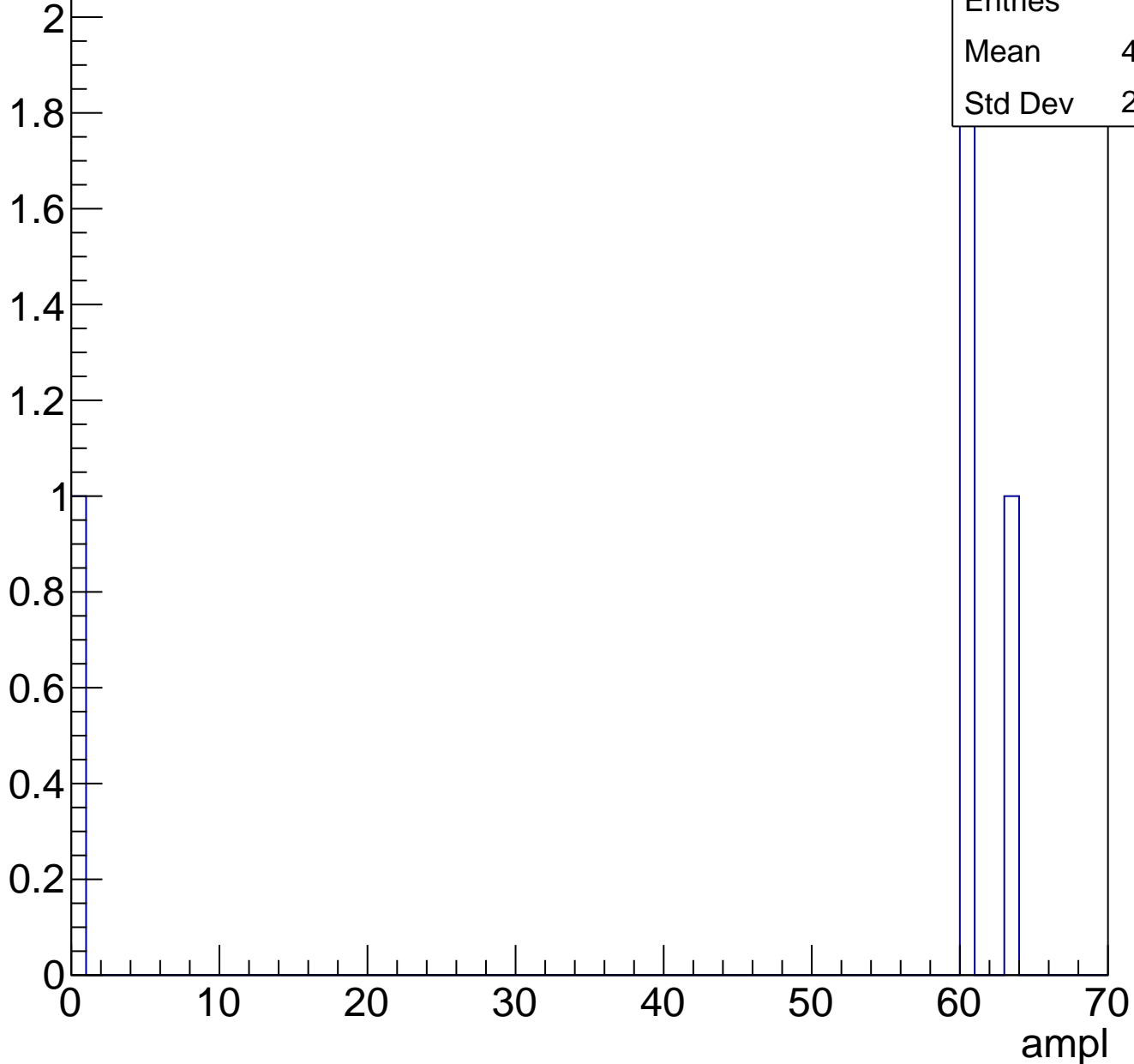




# B1L002S, U17-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	45.75
Std Dev	26.44

# B1L002S, U17-ch119, adc0

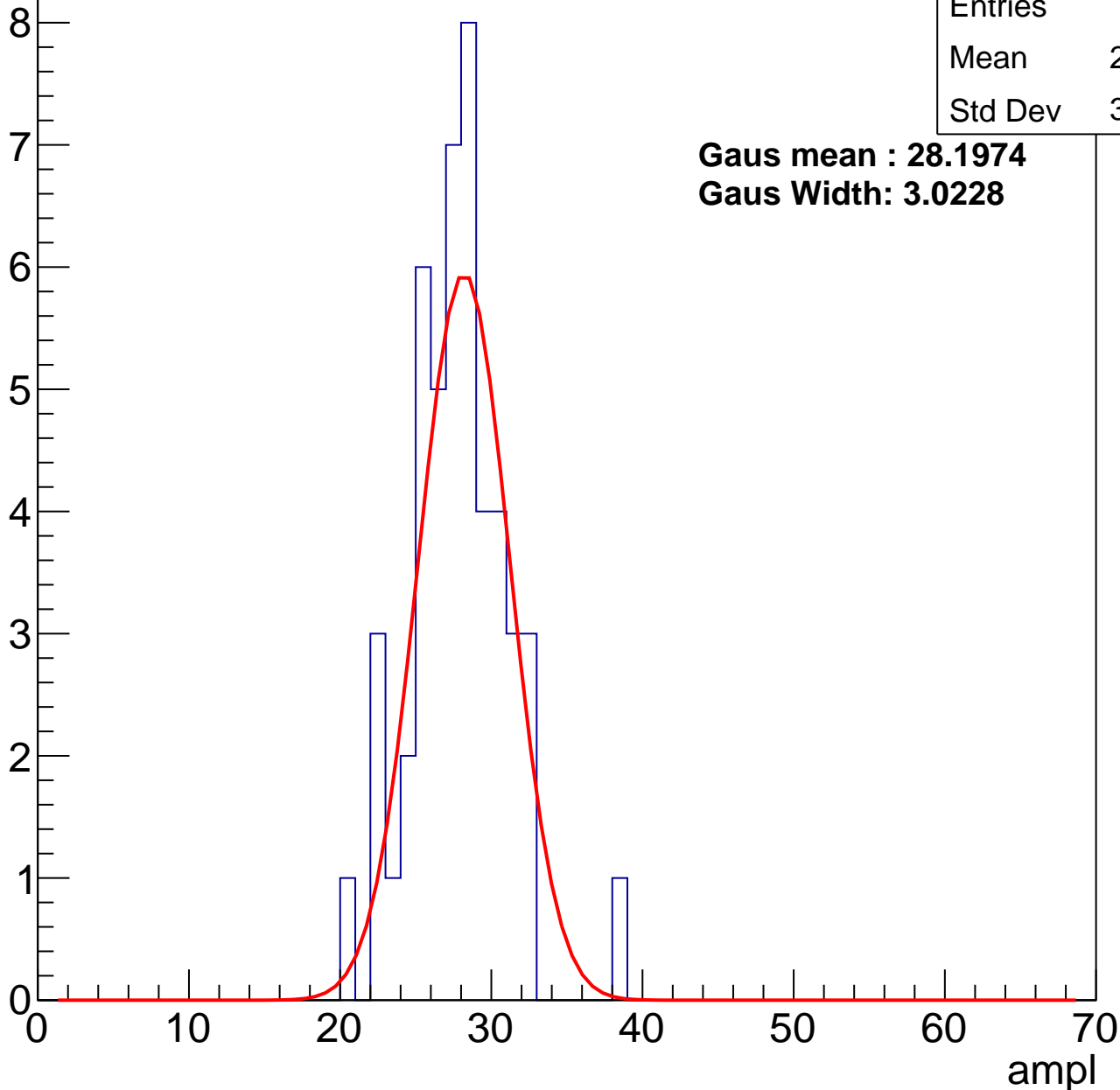
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	27.35
Std Dev	3.179

**Gaus mean : 28.1974**

**Gaus Width: 3.0228**



# B1L002S, U17-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	33.03
Std Dev	2.99

**Gaus mean : 33.7685**

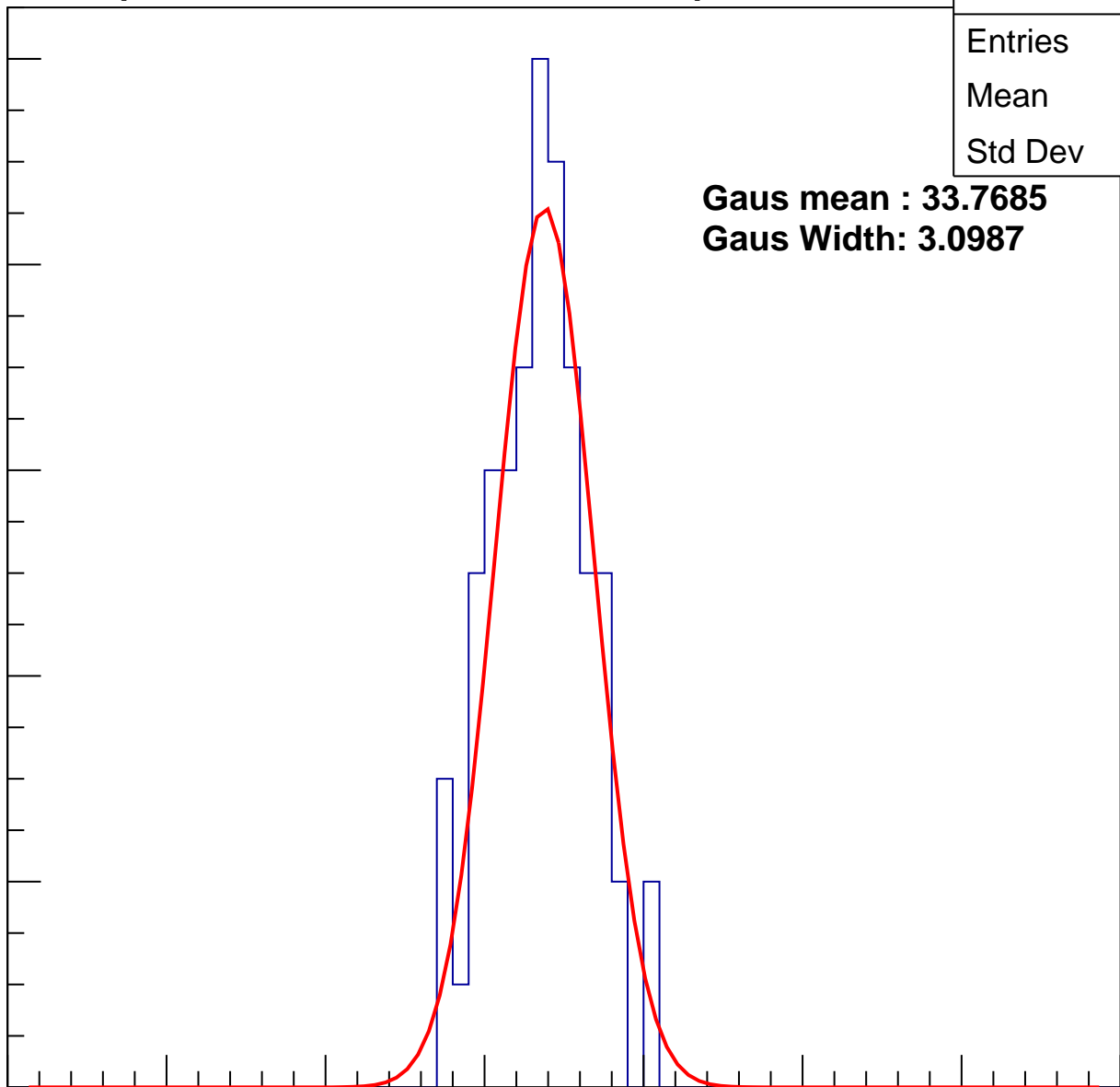
**Gaus Width: 3.0987**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch119, adc2

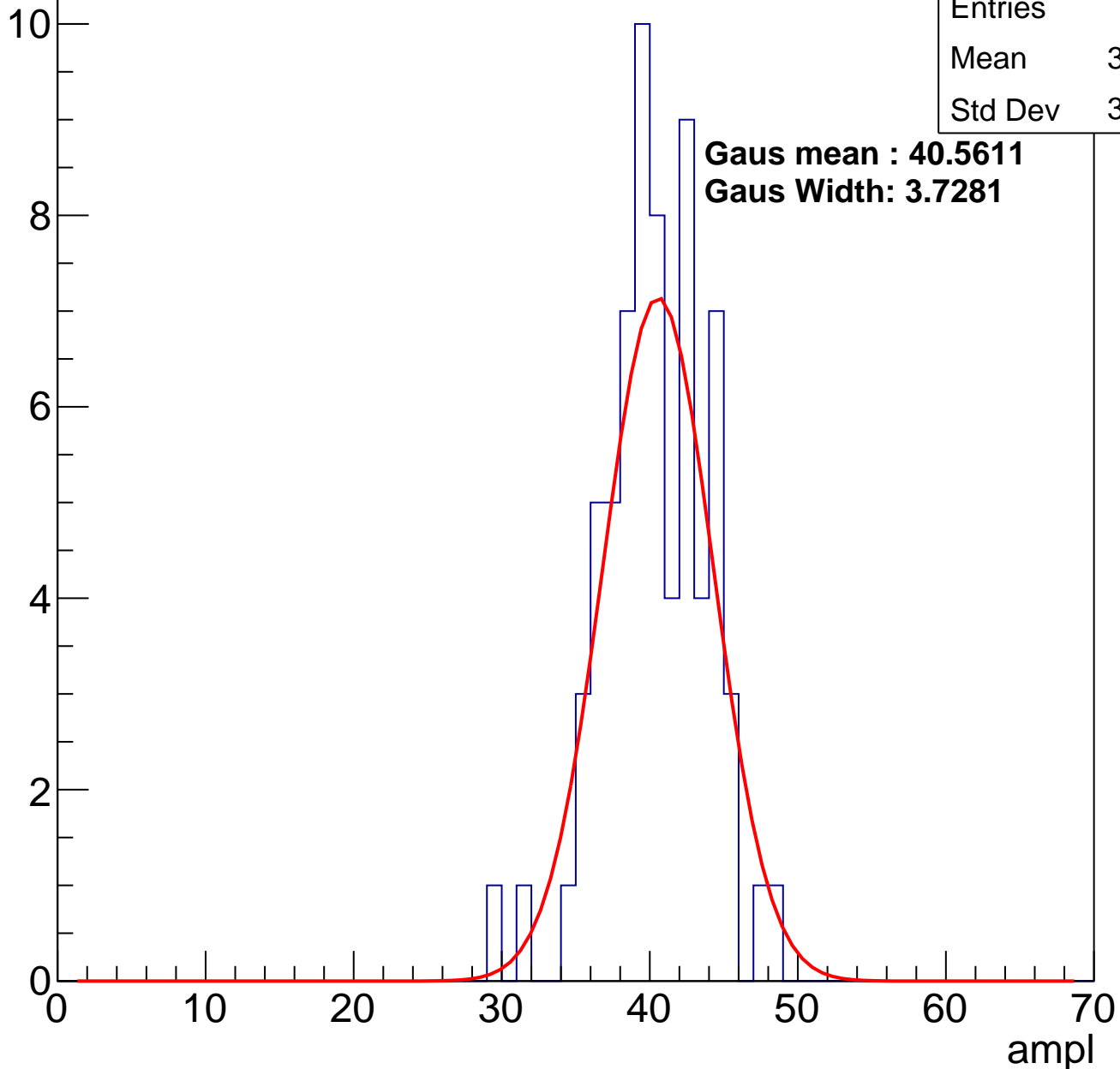
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	39.89
Std Dev	3.487

**Gaus mean : 40.5611**

**Gaus Width: 3.7281**

Entry

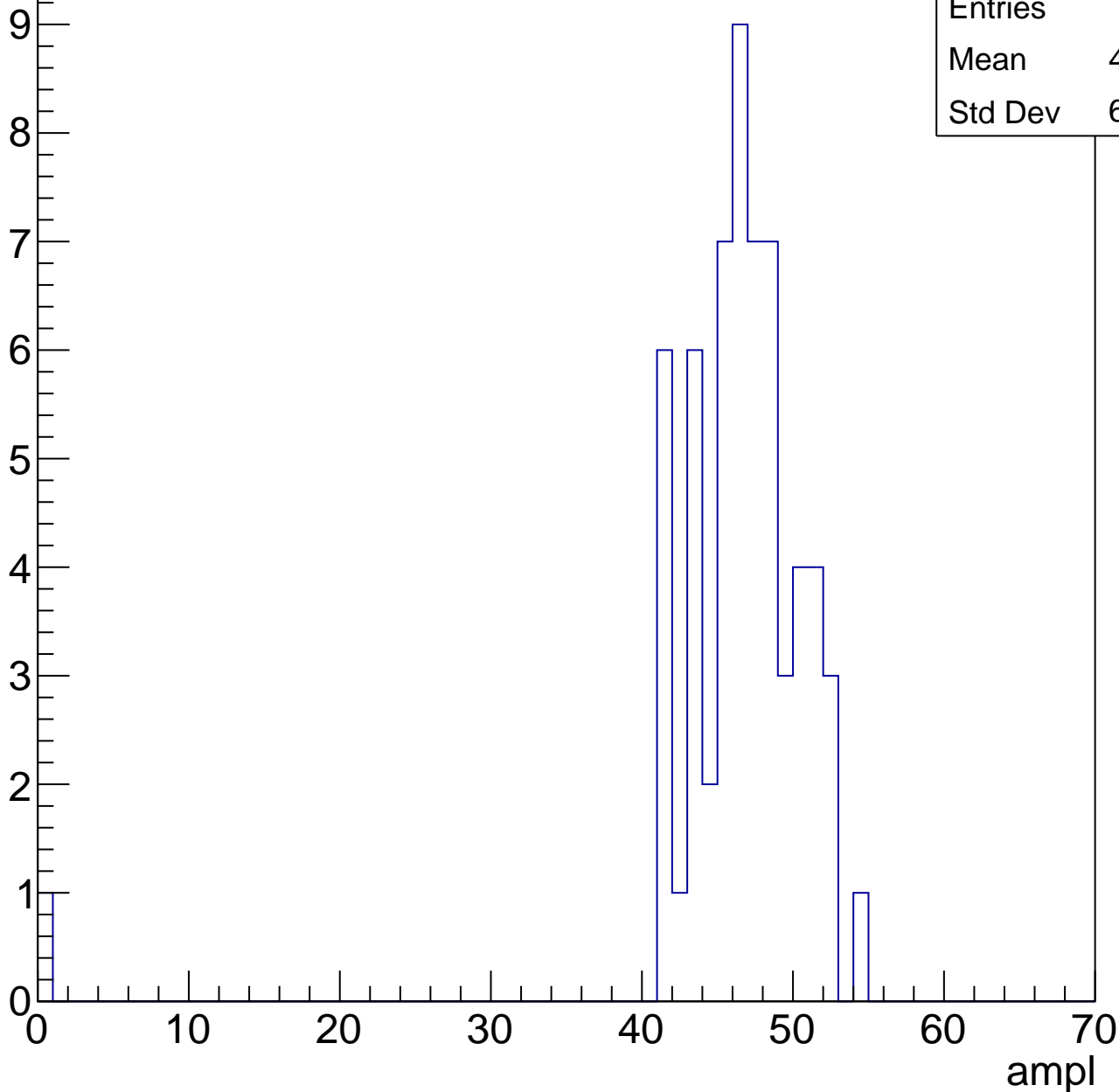


# B1L002S, U17-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.72
Std Dev	6.707

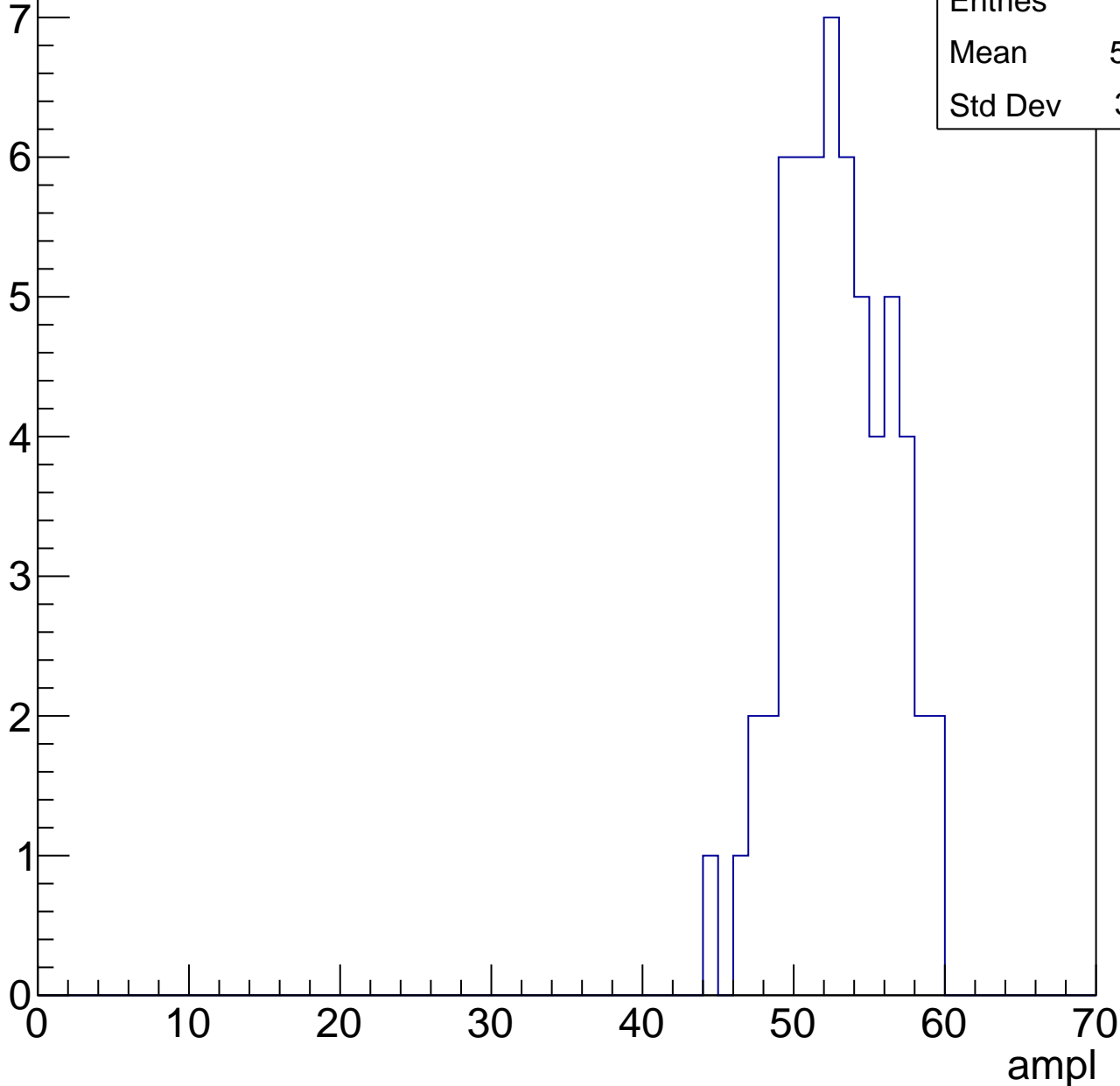


# B1L002S, U17-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	52.44
Std Dev	3.361

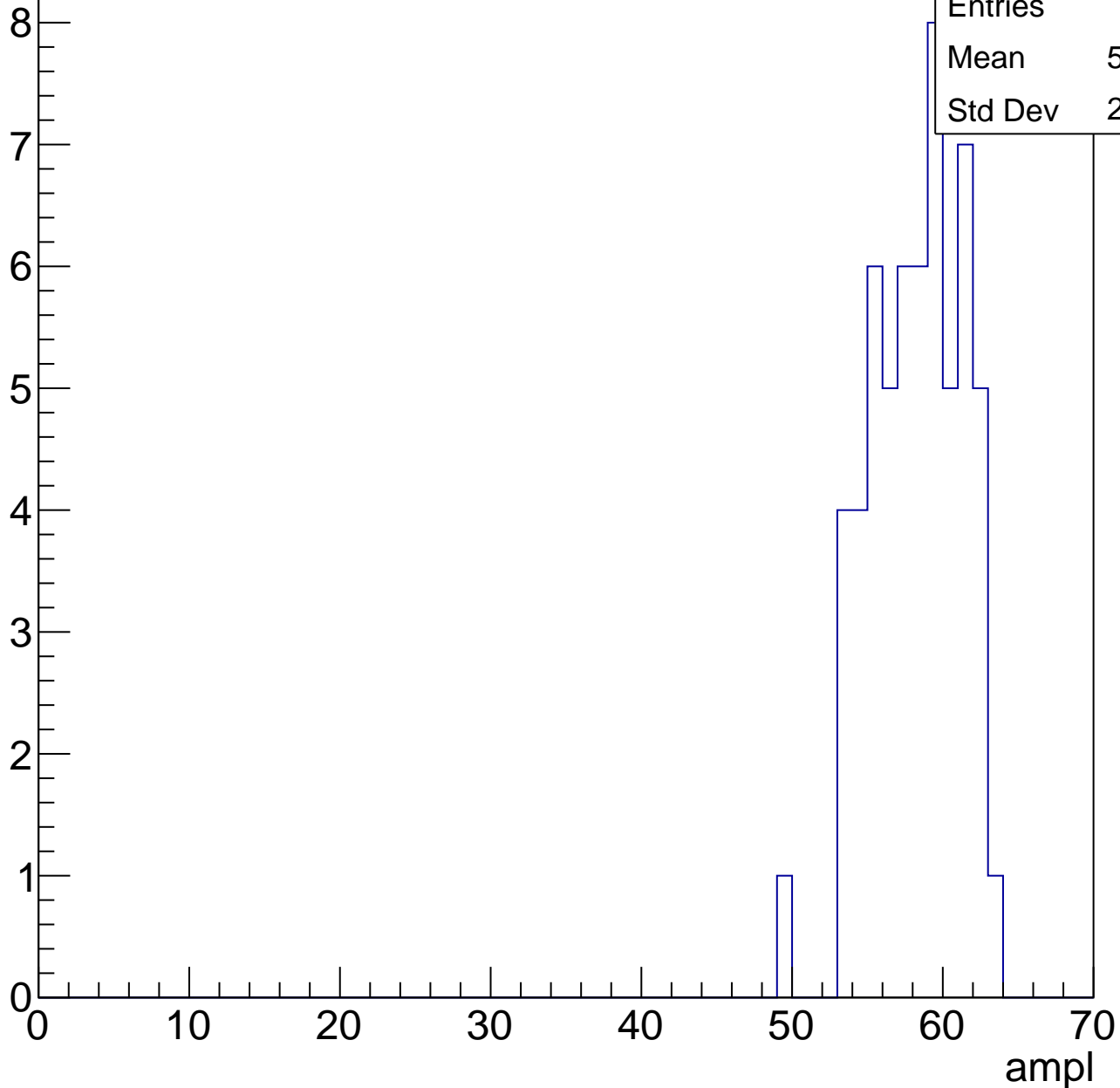


# B1L002S, U17-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.74
Std Dev	2.986

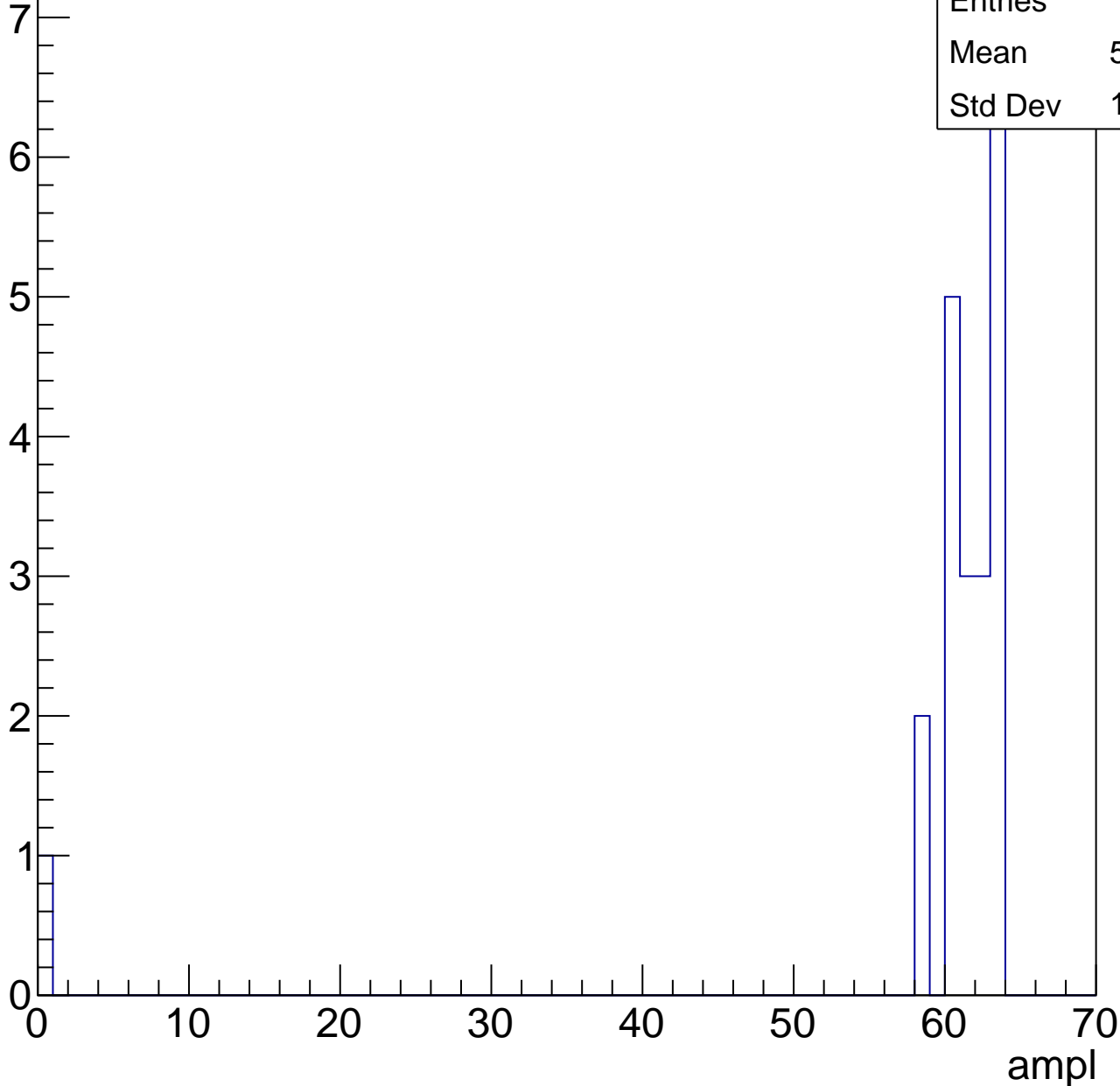


# B1L002S, U17-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.38
Std Dev	13.15

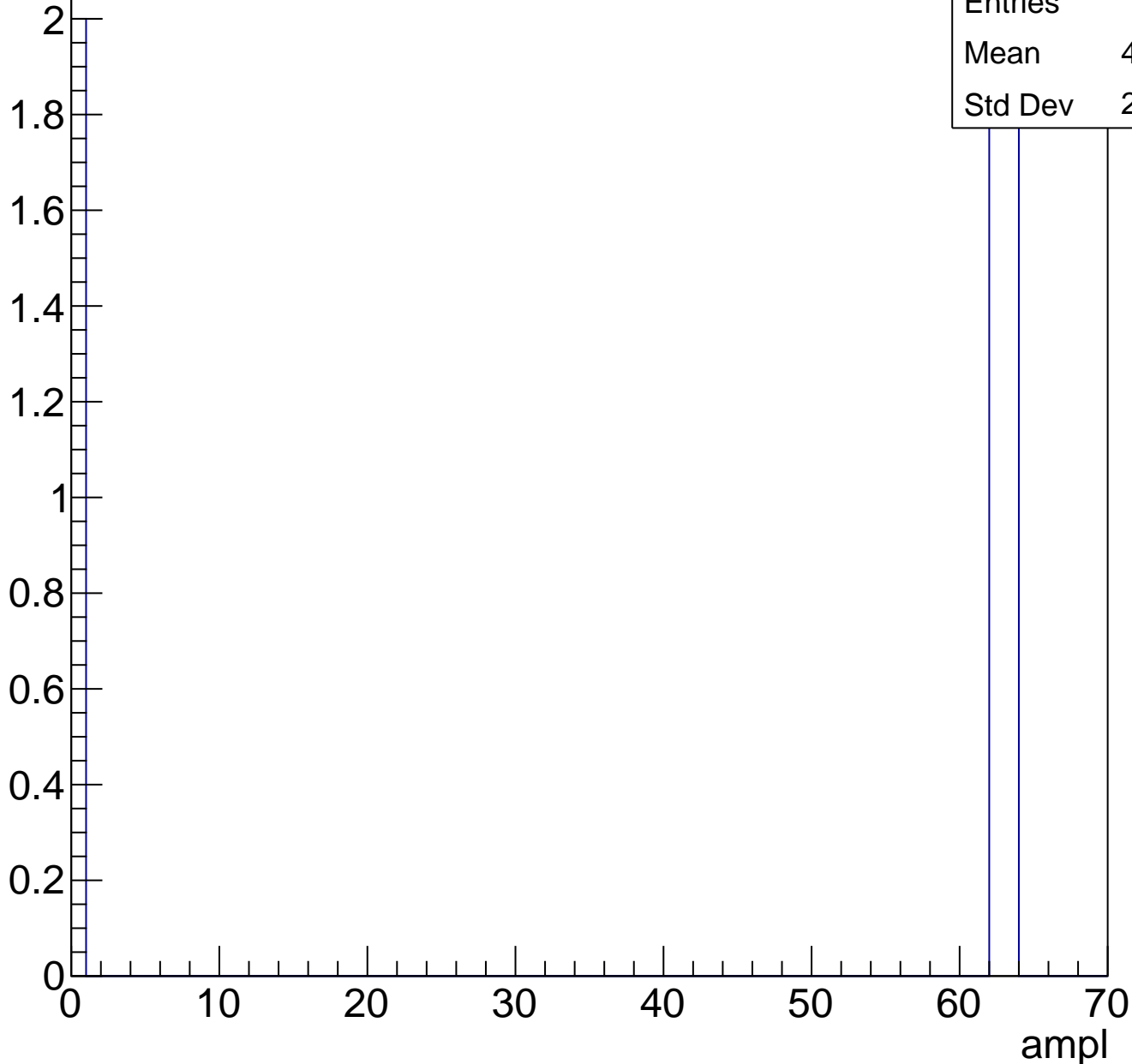




# B1L002S, U17-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch120, adc0

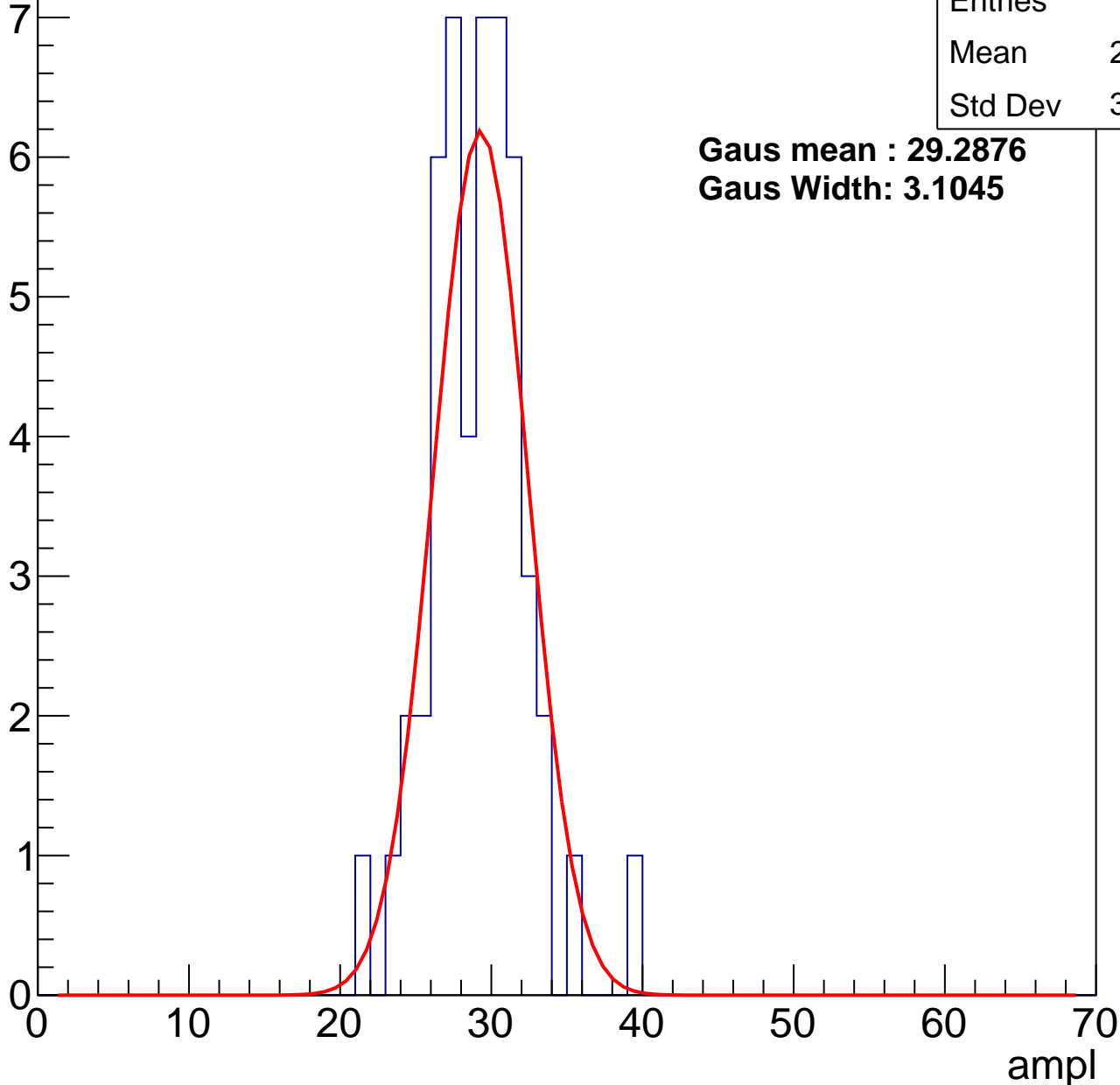
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	28.68
Std Dev	3.127

**Gaus mean : 29.2876**

**Gaus Width: 3.1045**



# B1L002S, U17-ch120, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	34.33
Std Dev	3.454

**Gaus mean : 34.9218**

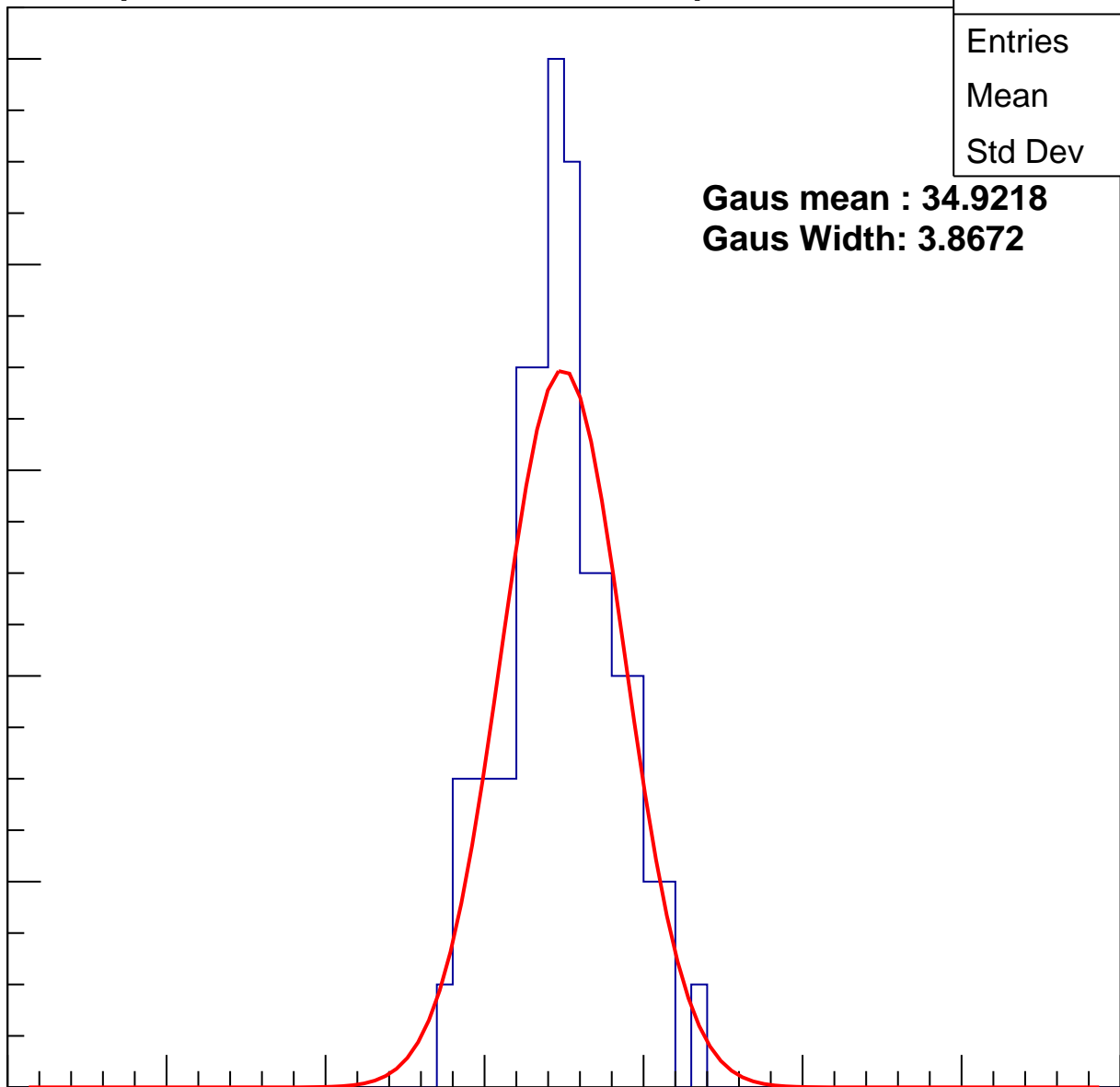
**Gaus Width: 3.8672**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch120, adc2

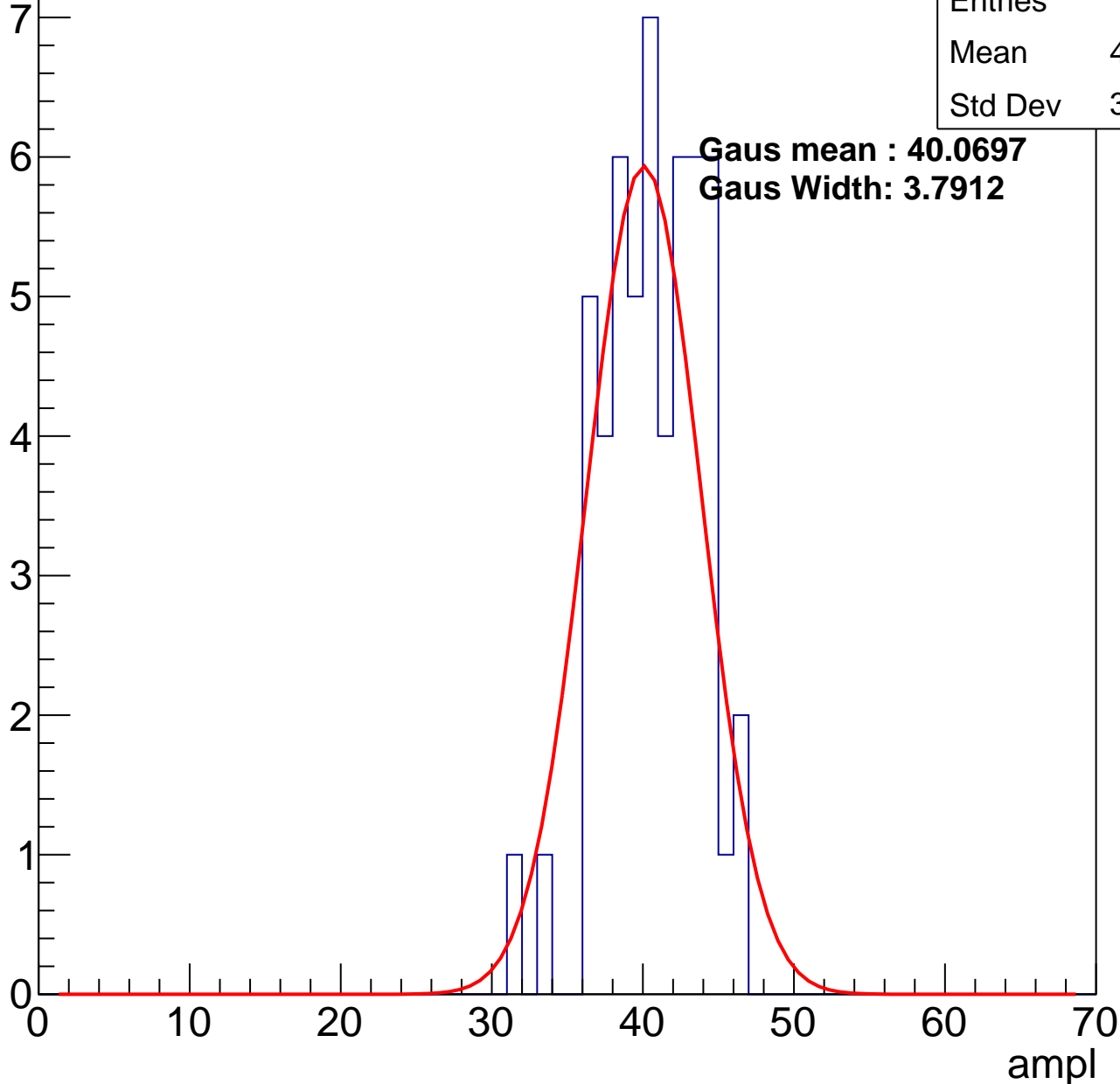
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.19
Std Dev	3.186

**Gaus mean : 40.0697**

**Gaus Width: 3.7912**

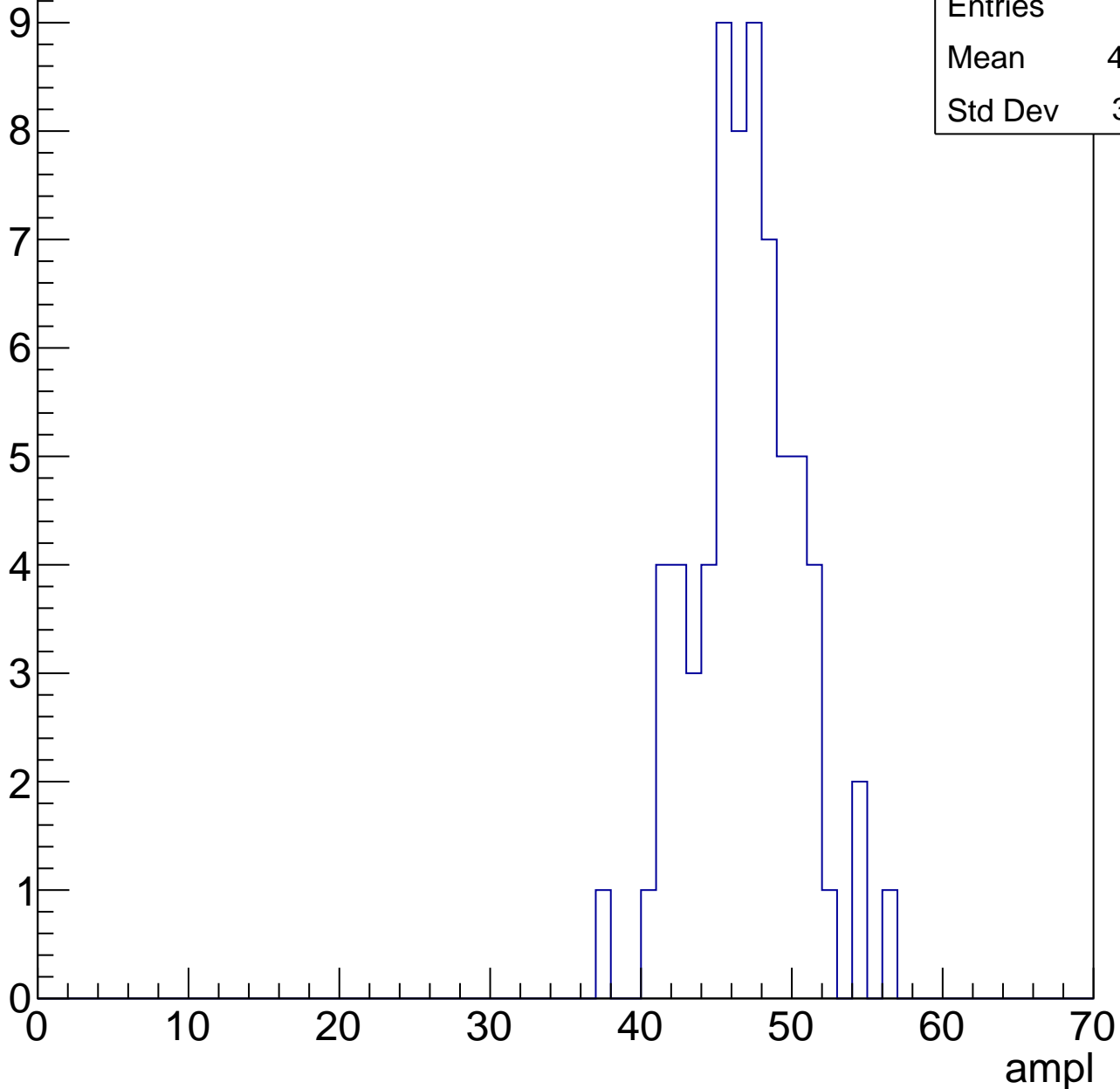


# B1L002S, U17-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	46.49
Std Dev	3.521

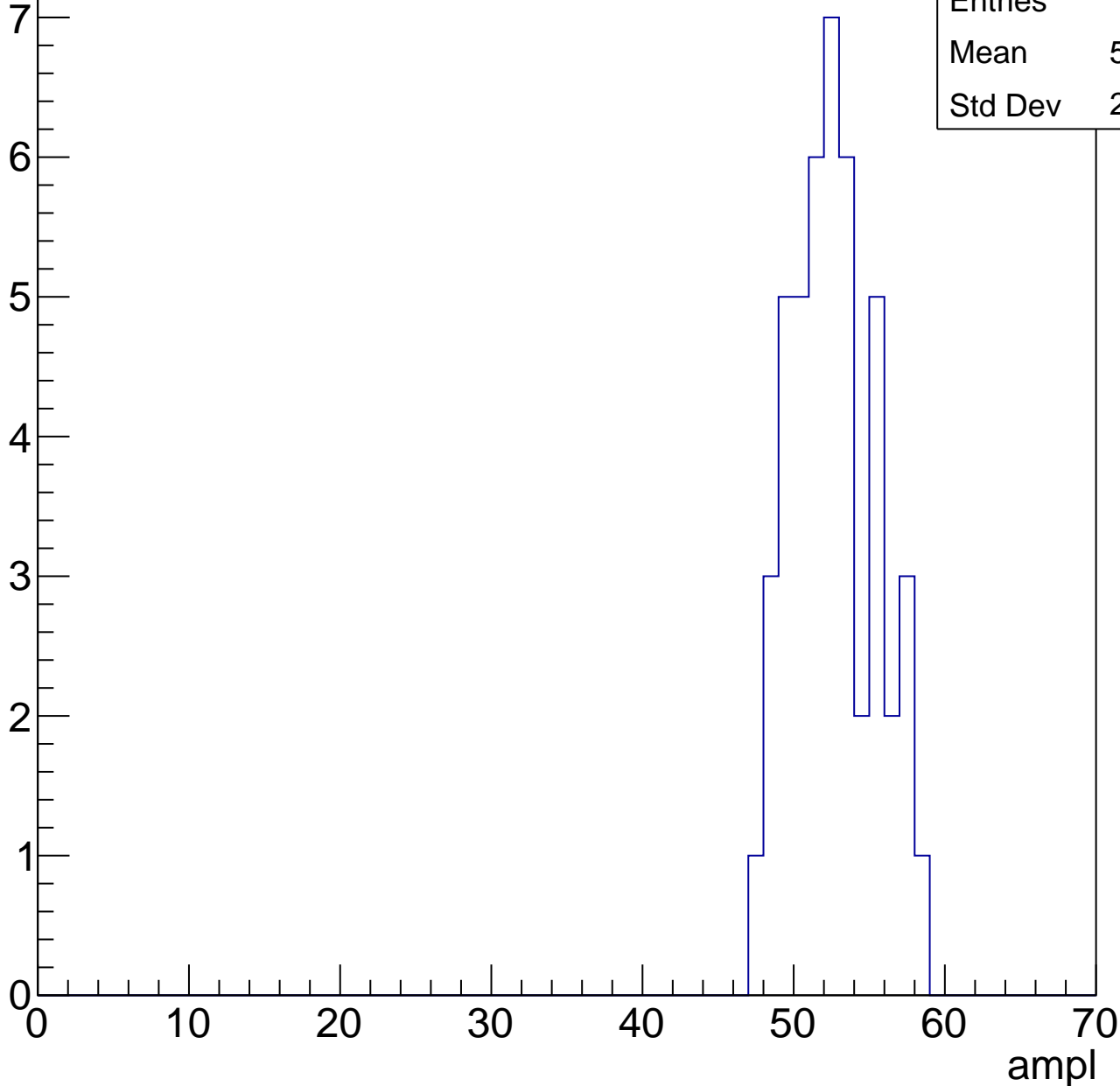


# B1L002S, U17-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	52.13
Std Dev	2.739

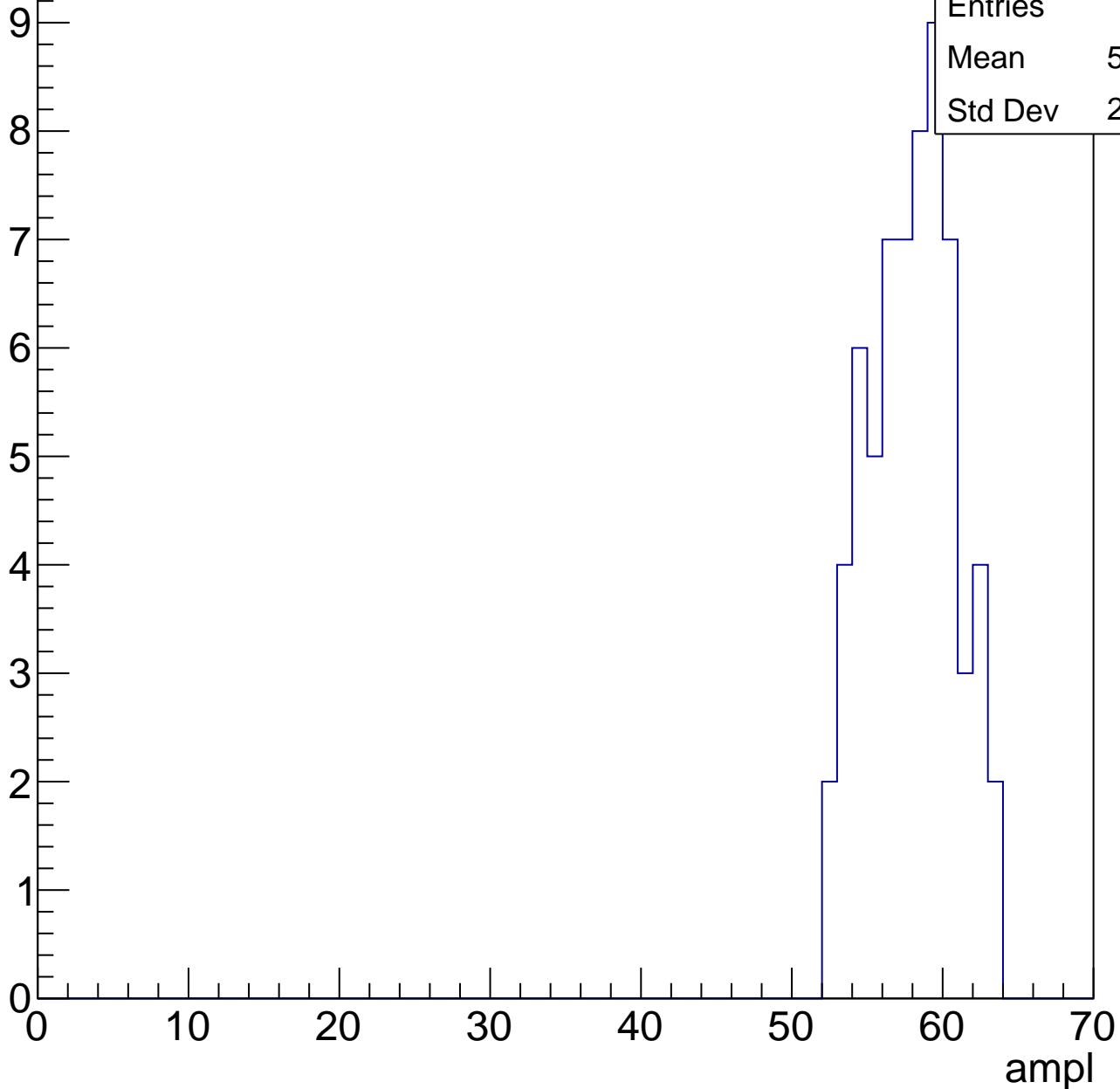


# B1L002S, U17-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.47
Std Dev	2.817

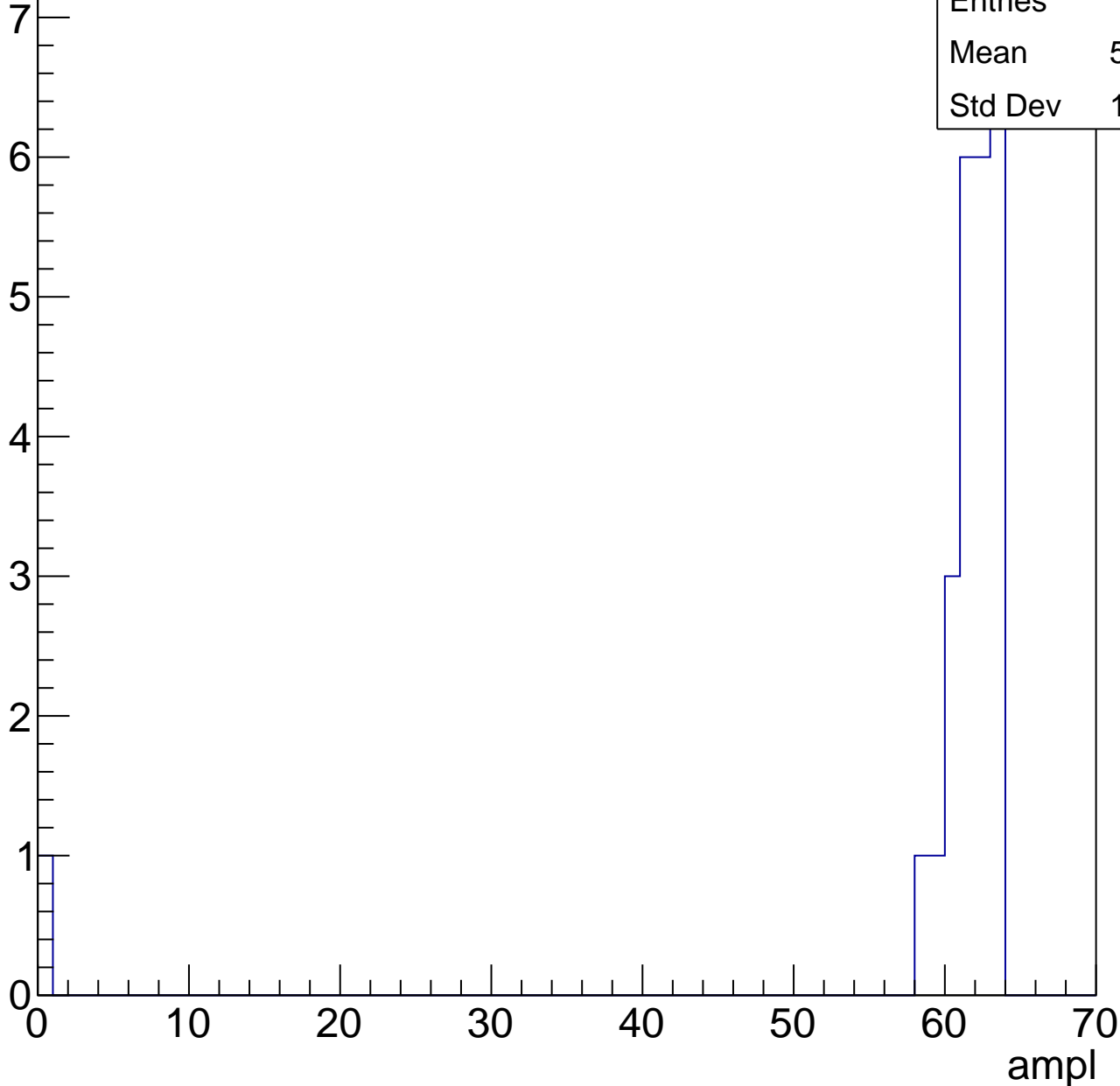


# B1L002S, U17-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	59.04
Std Dev	12.12

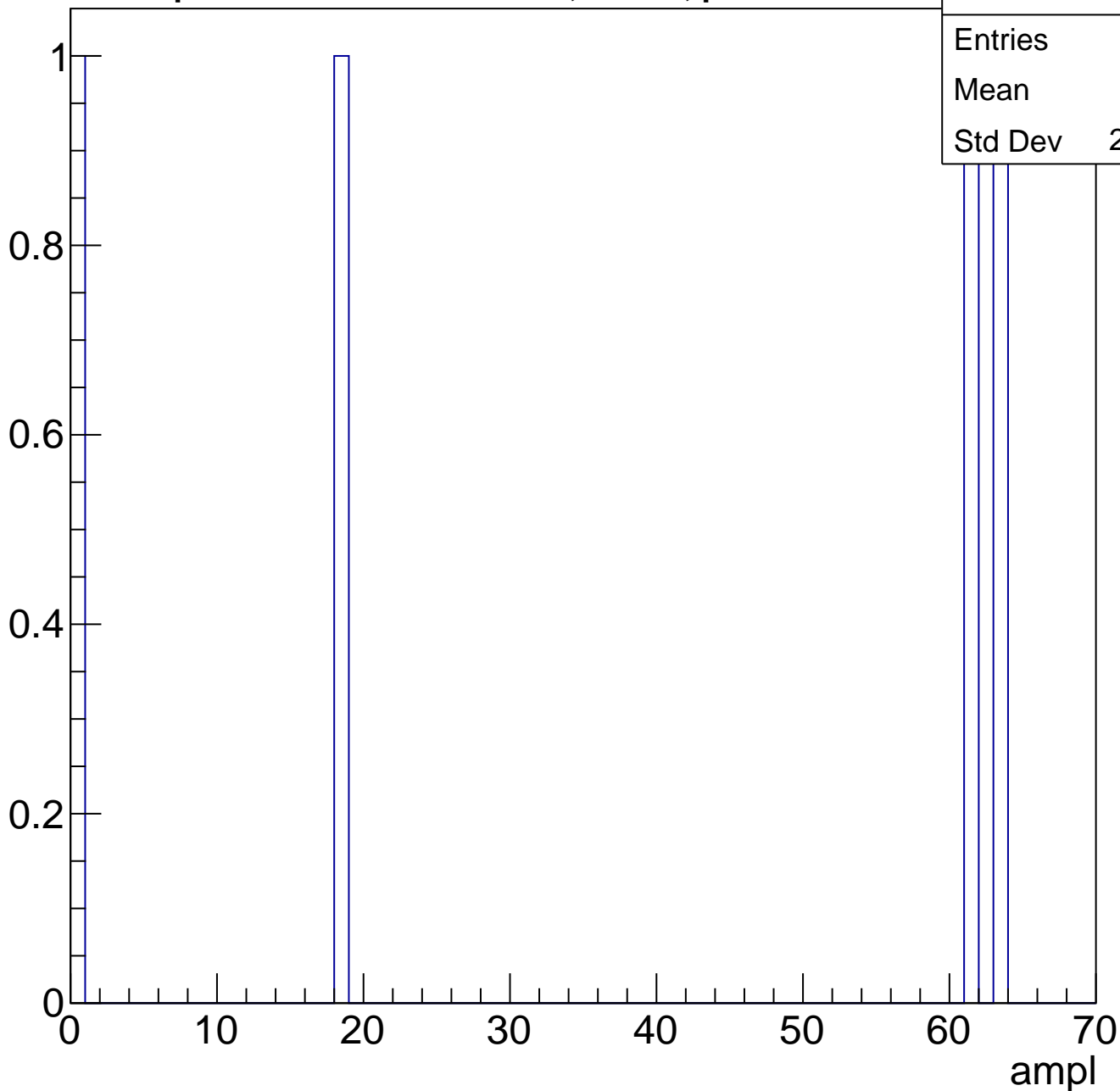




# B1L002S, U17-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	35.5
Std Dev	27.26

# B1L002S, U17-ch121, adc0

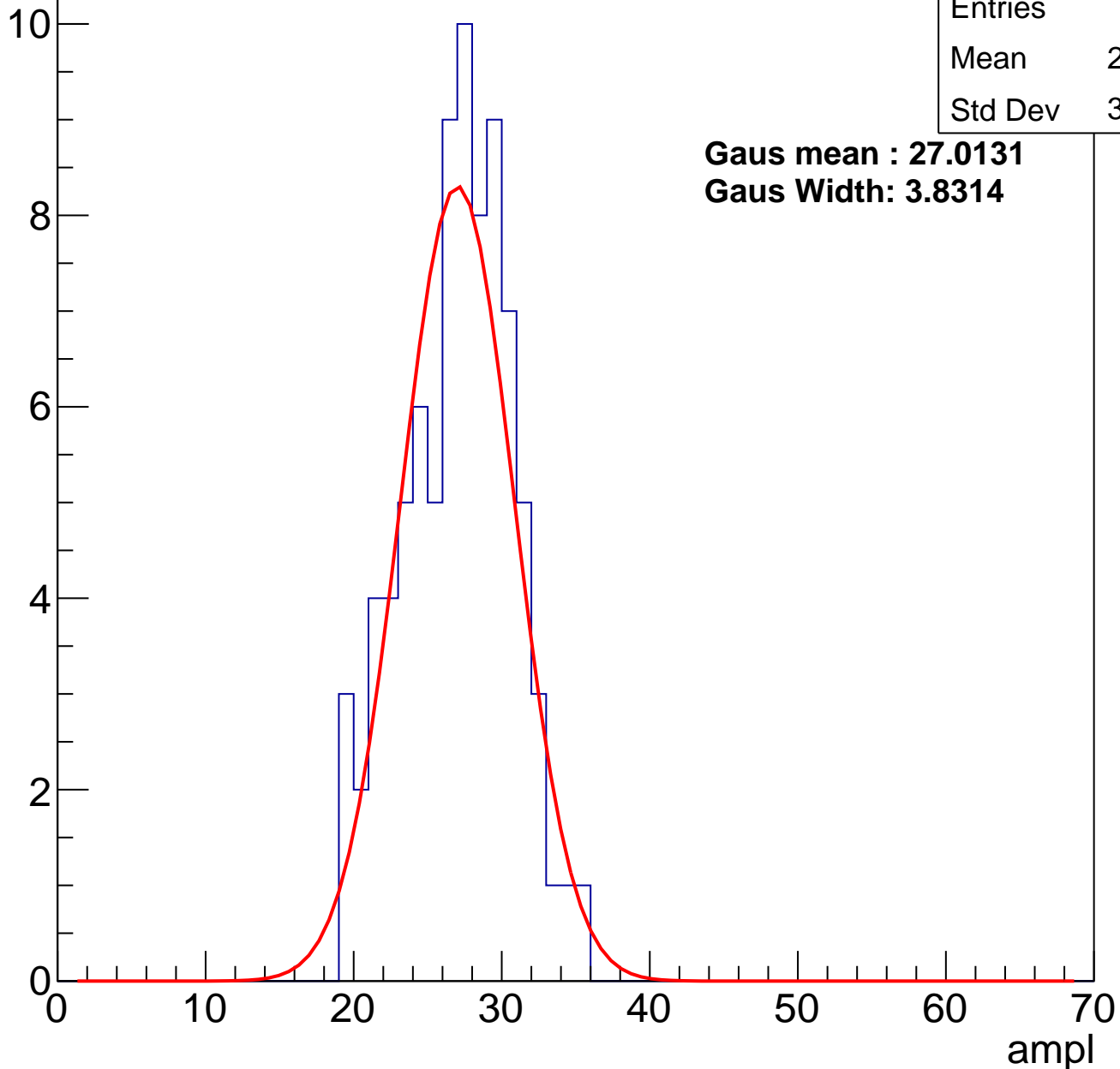
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	26.57
Std Dev	3.628

**Gaus mean : 27.0131**

**Gaus Width: 3.8314**

Entry



# B1L002S, U17-ch121, adc1

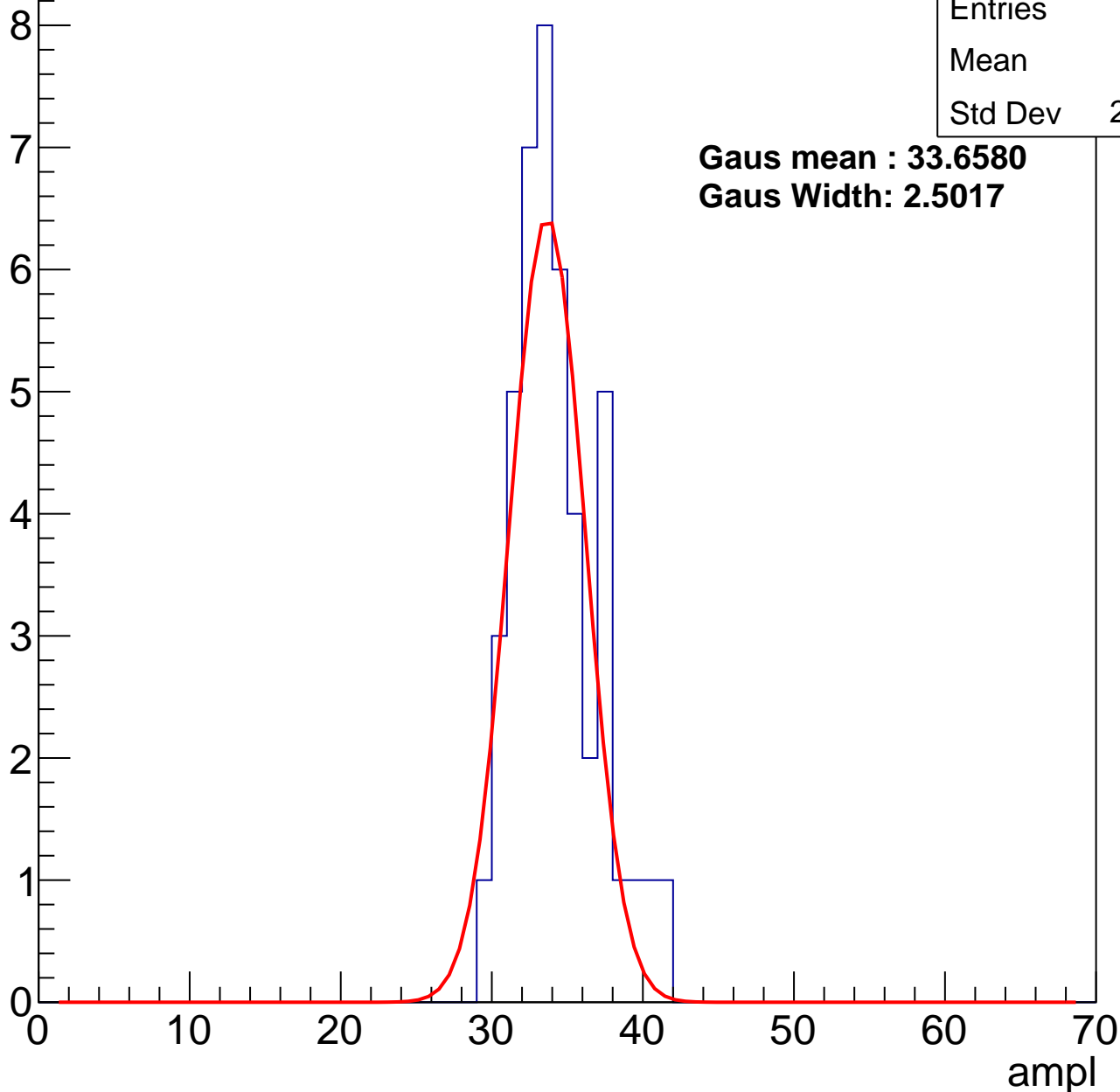
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	33.8
Std Dev	2.729

**Gaus mean : 33.6580**

**Gaus Width: 2.5017**



# B1L002S, U17-ch121, adc2

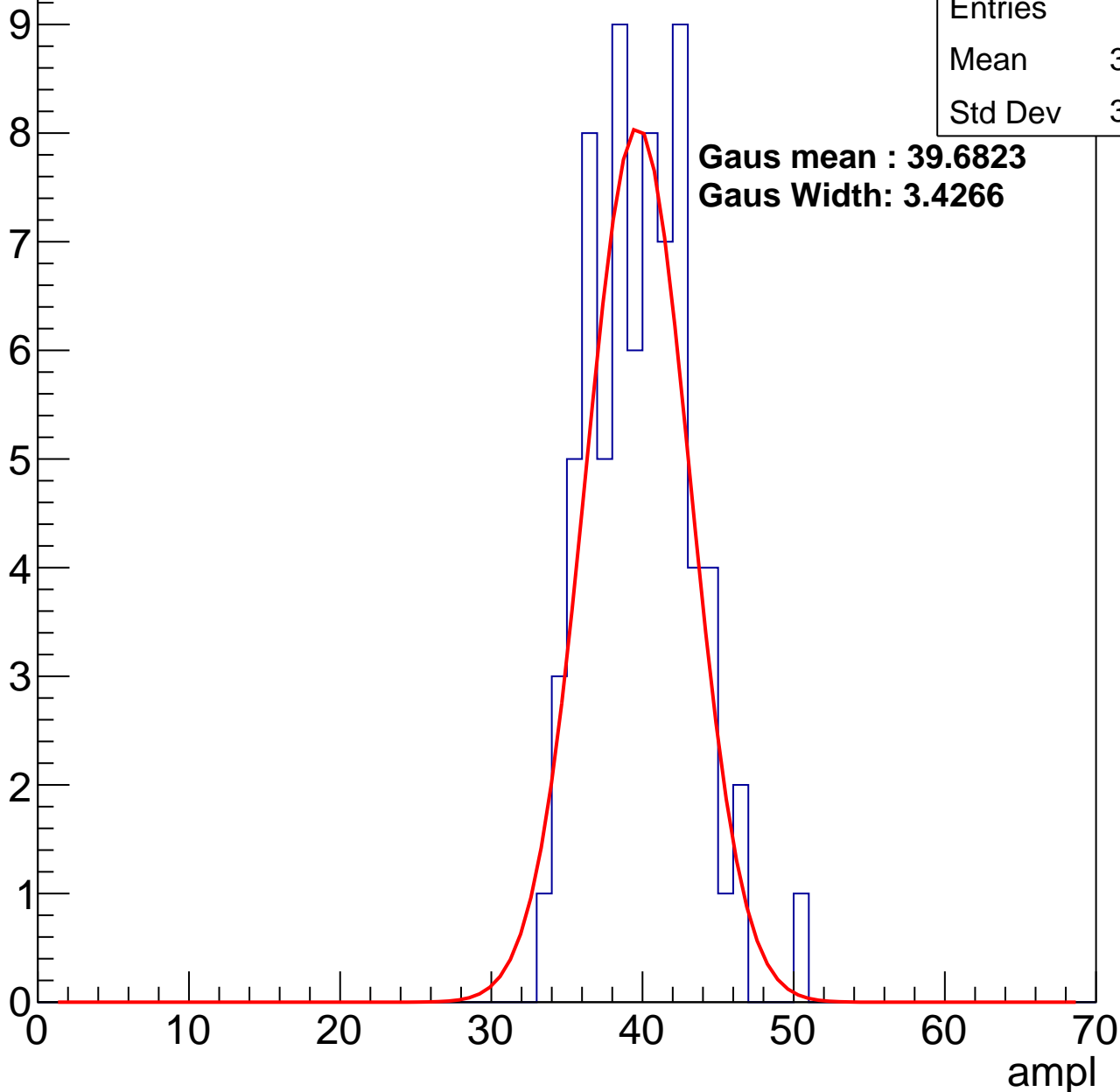
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	39.44
Std Dev	3.339

**Gaus mean : 39.6823**

**Gaus Width: 3.4266**

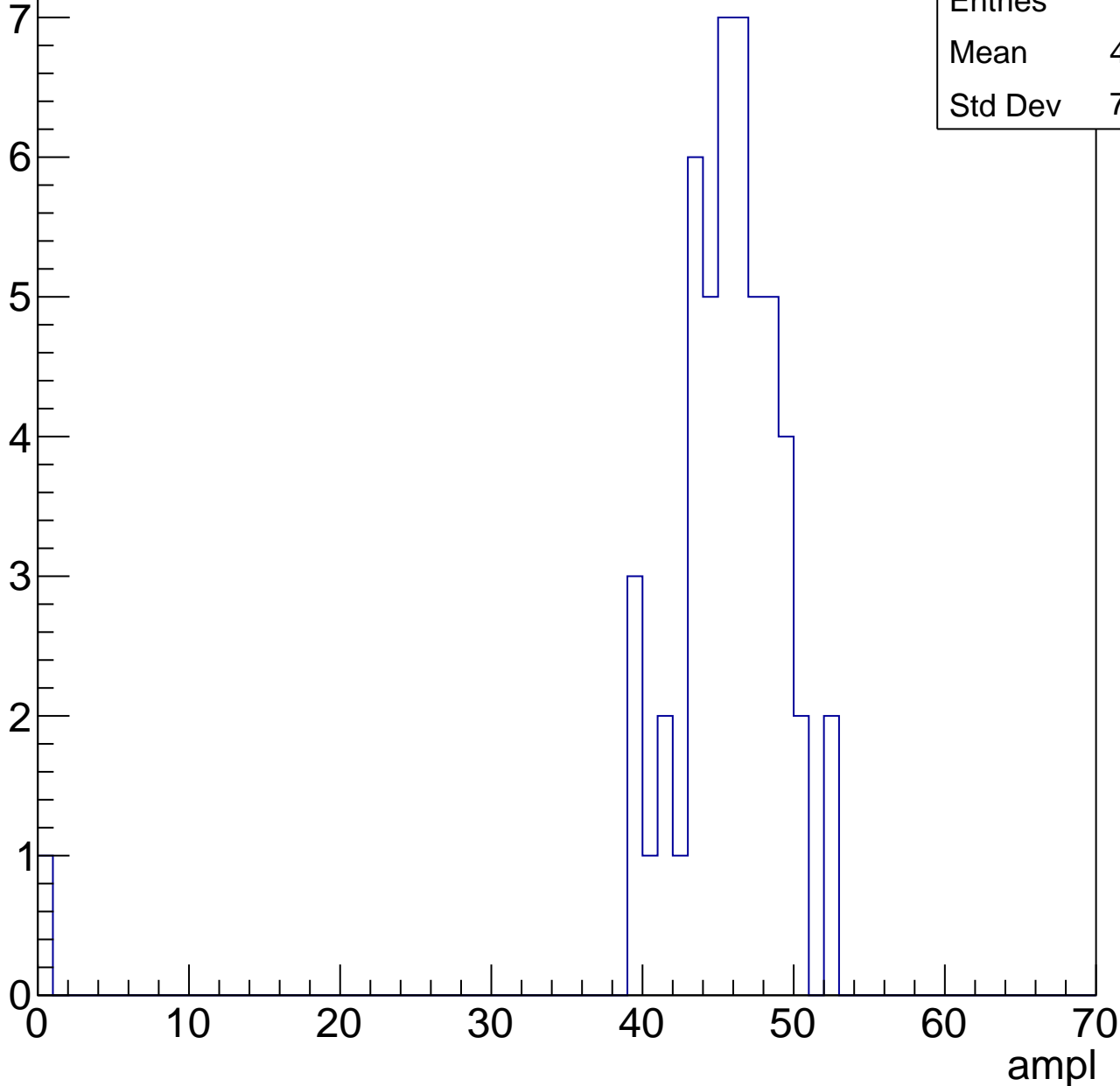


# B1L002S, U17-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	44.53
Std Dev	7.002

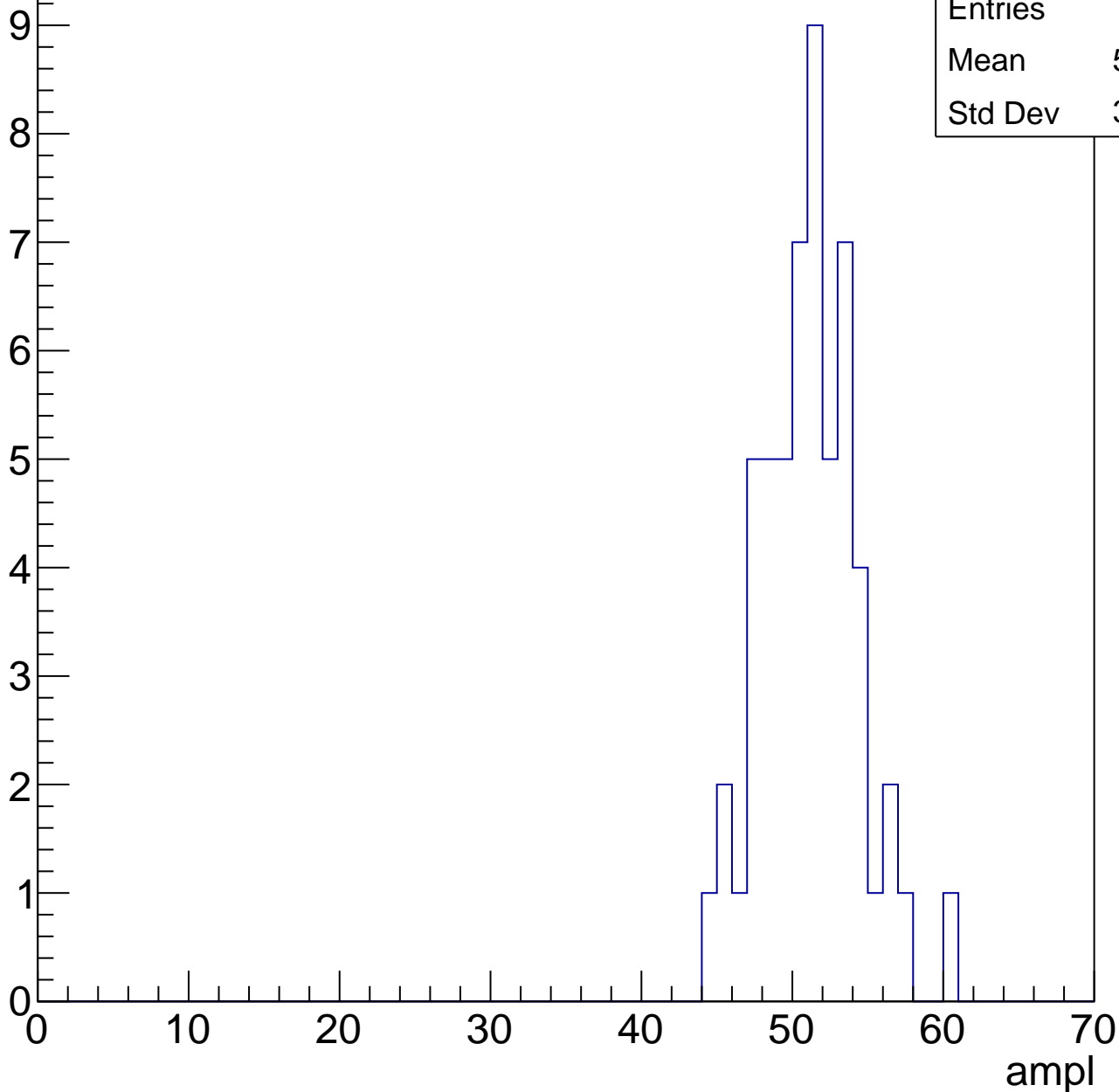


# B1L002S, U17-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	50.71
Std Dev	3.121

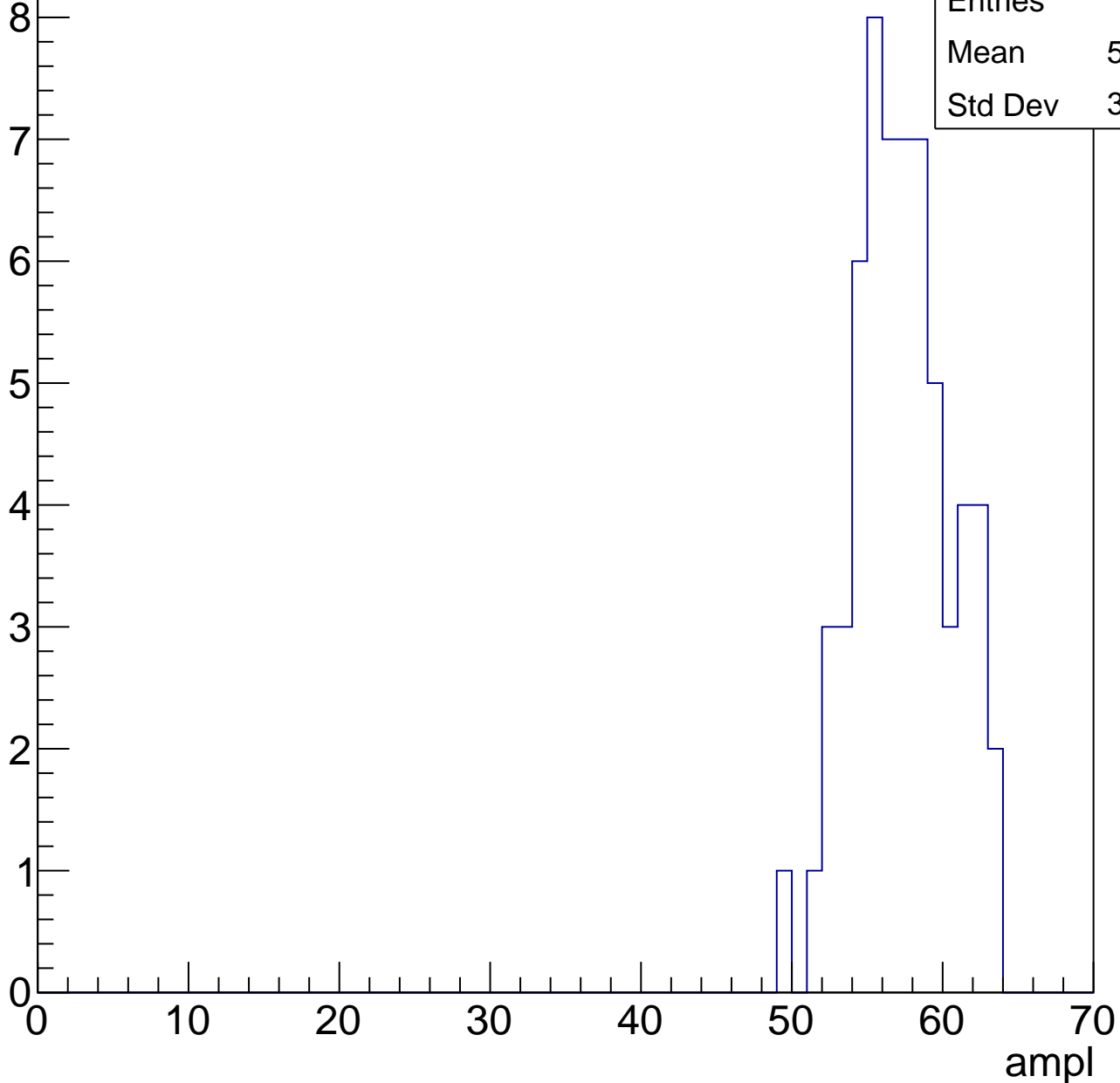


# B1L002S, U17-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	56.87
Std Dev	3.149

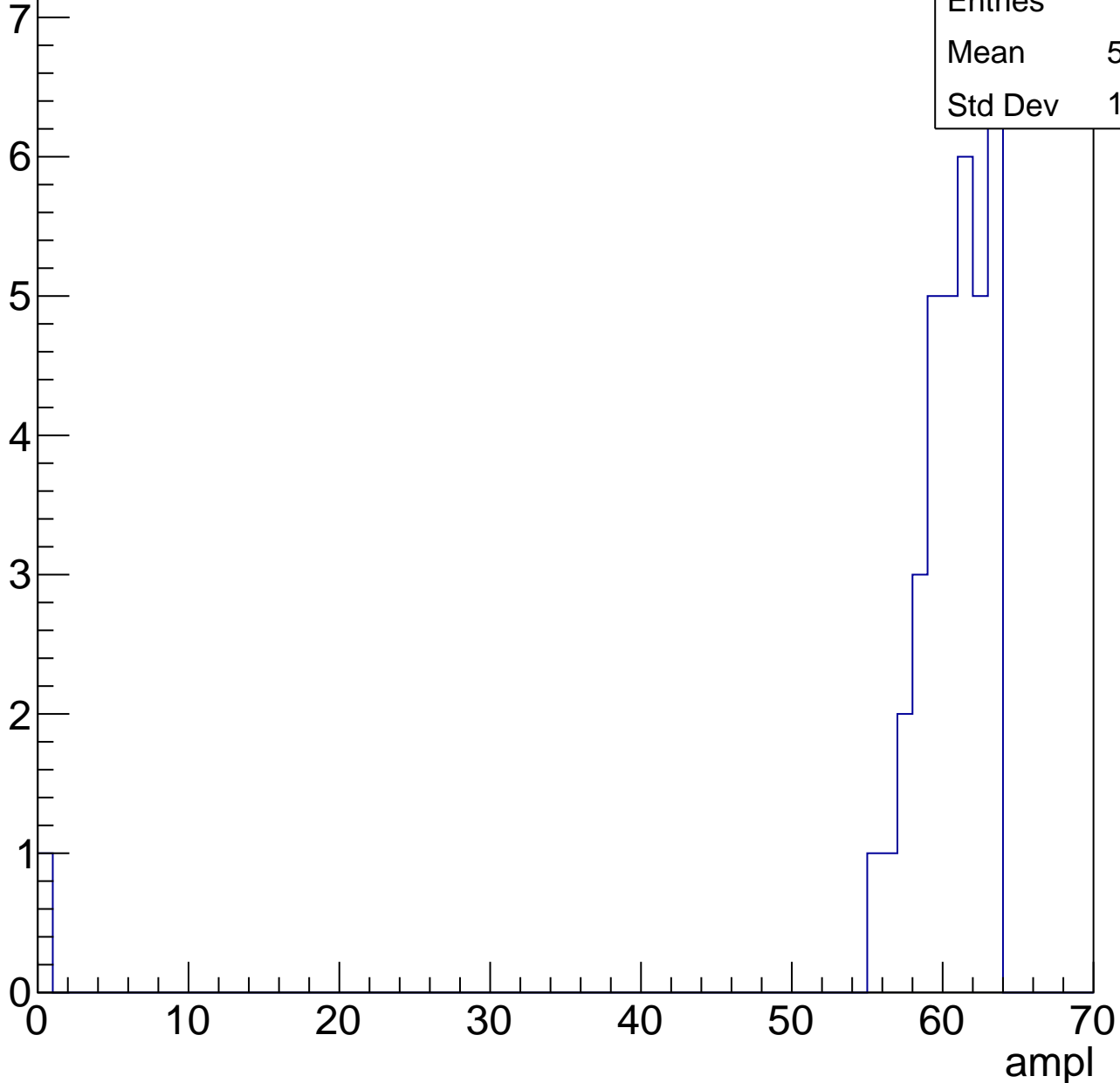


# B1L002S, U17-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.64
Std Dev	10.14

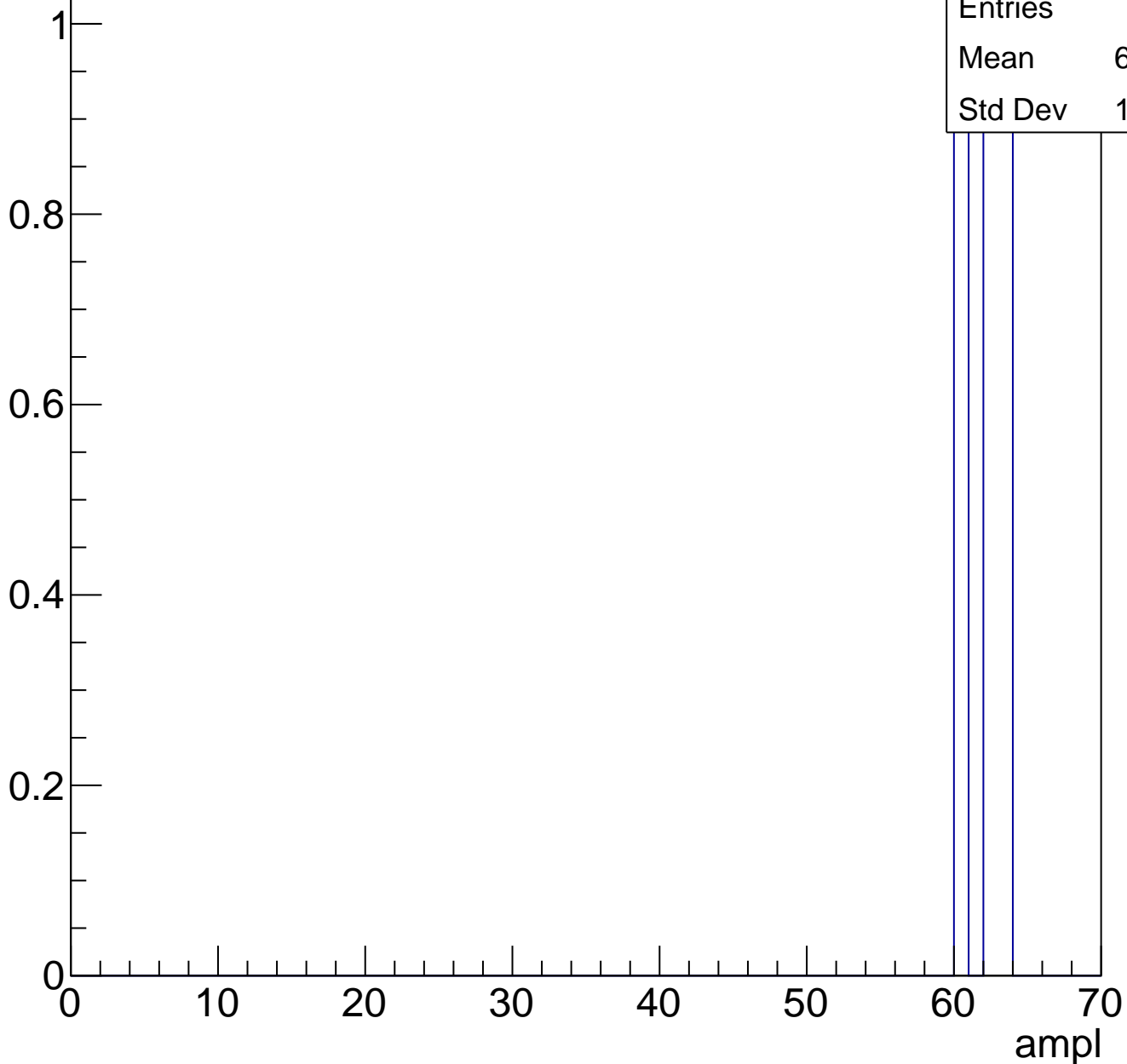




# B1L002S, U17-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	27.77
Std Dev	4.434

**Gaus mean : 28.6549**

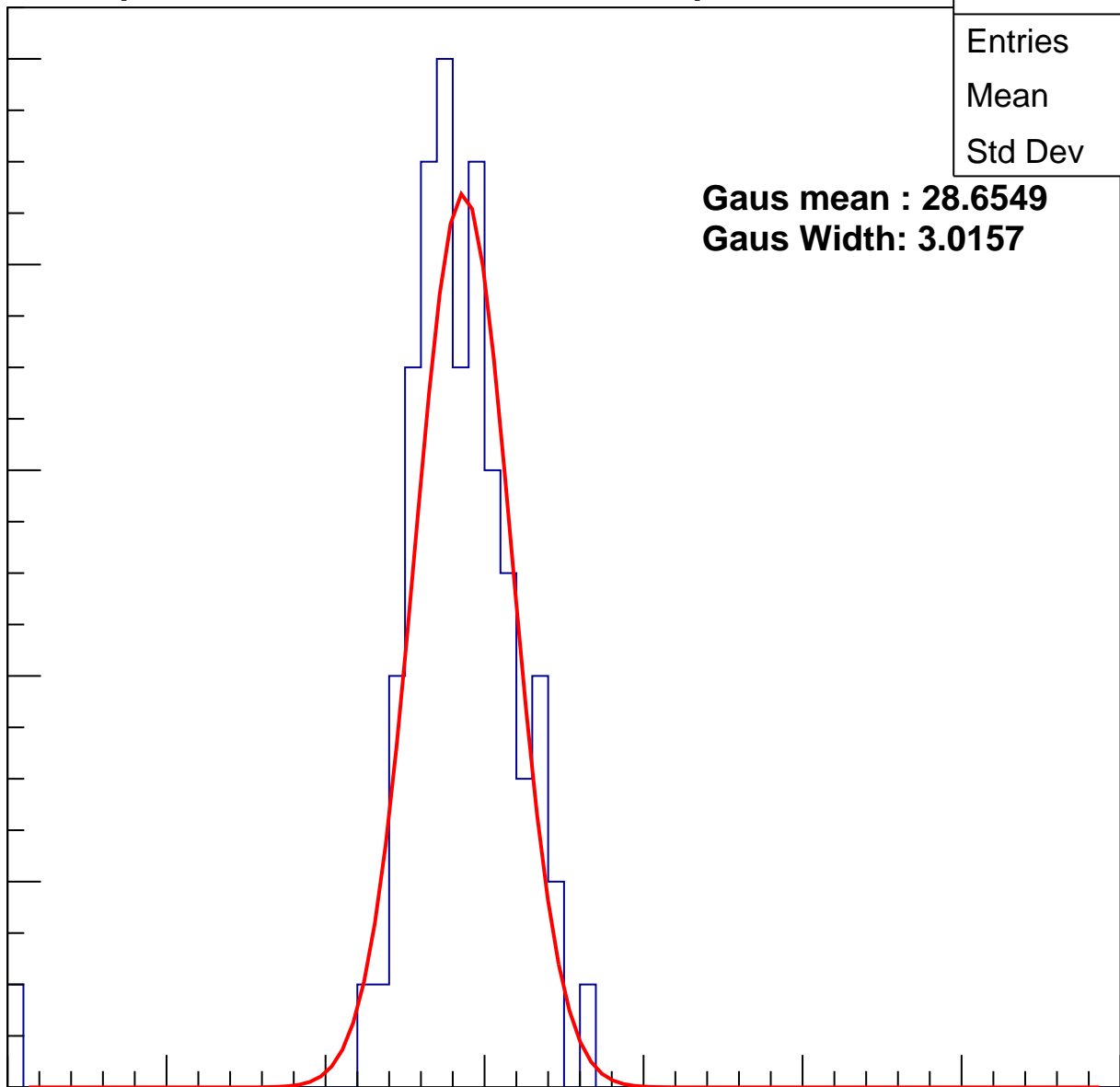
**Gaus Width: 3.0157**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch122, adc1

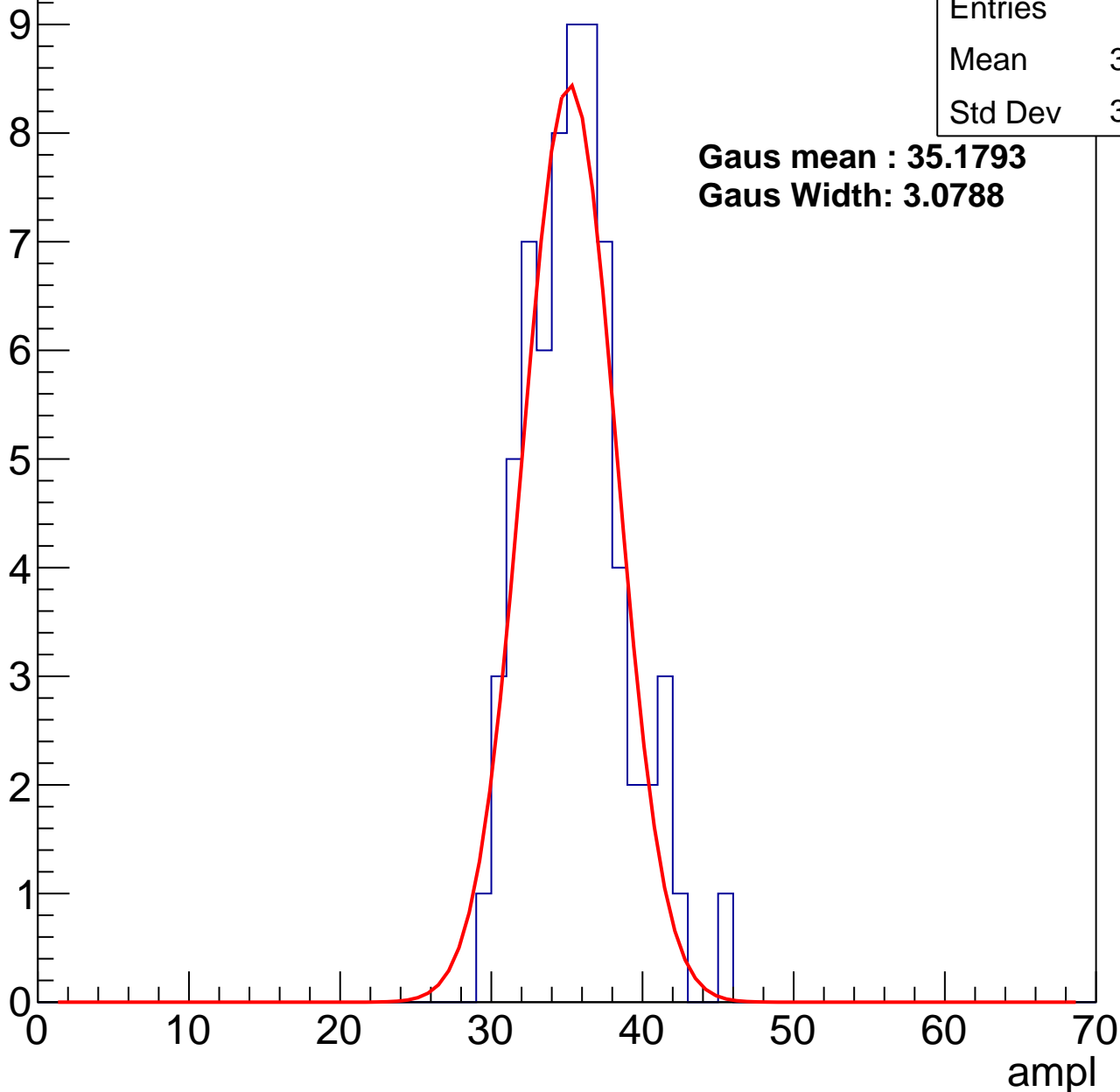
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.09
Std Dev	3.203

**Gaus mean : 35.1793**

**Gaus Width: 3.0788**



# B1L002S, U17-ch122, adc2

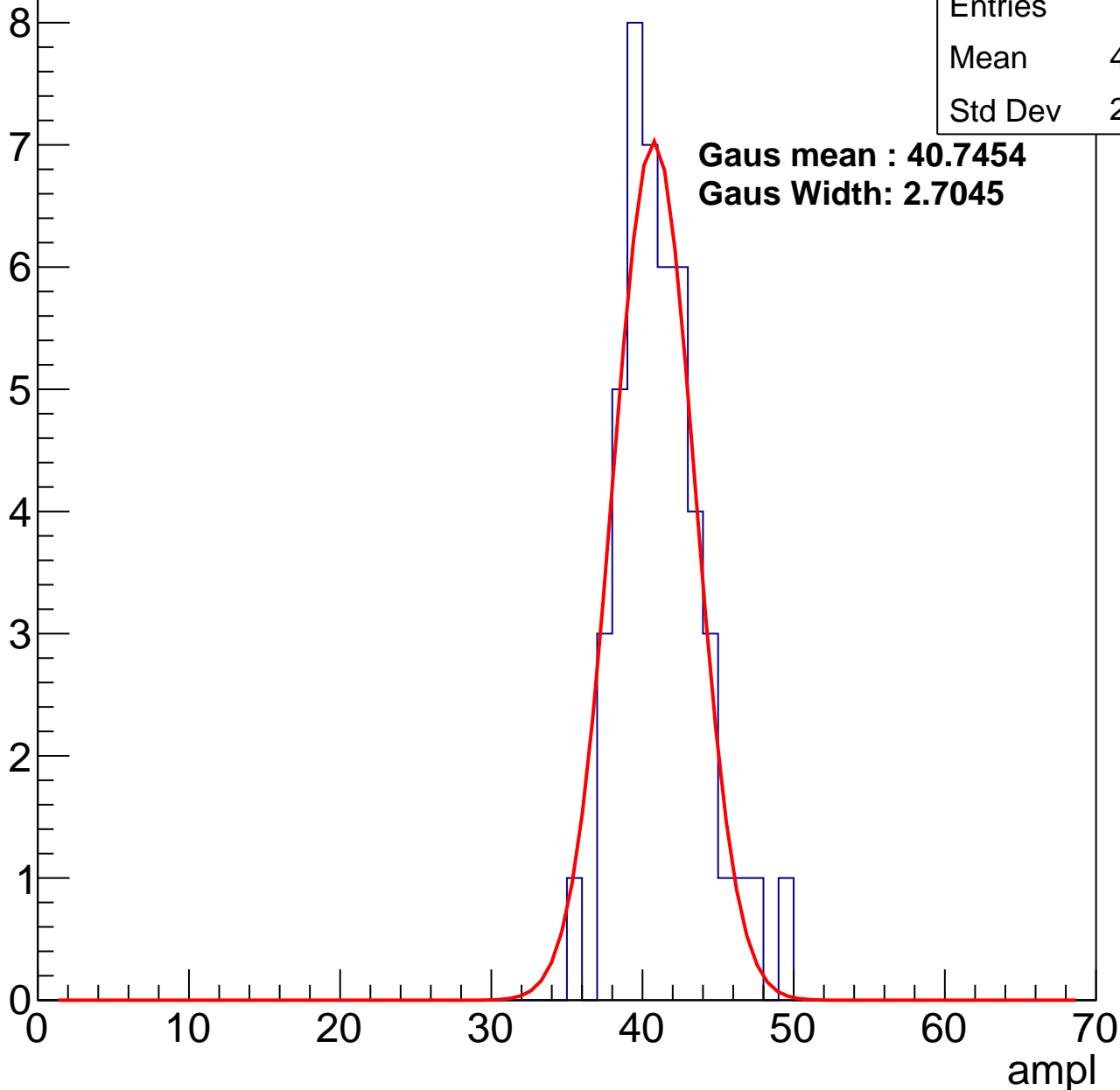
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	40.79
Std Dev	2.752

**Gaus mean : 40.7454**

**Gaus Width: 2.7045**

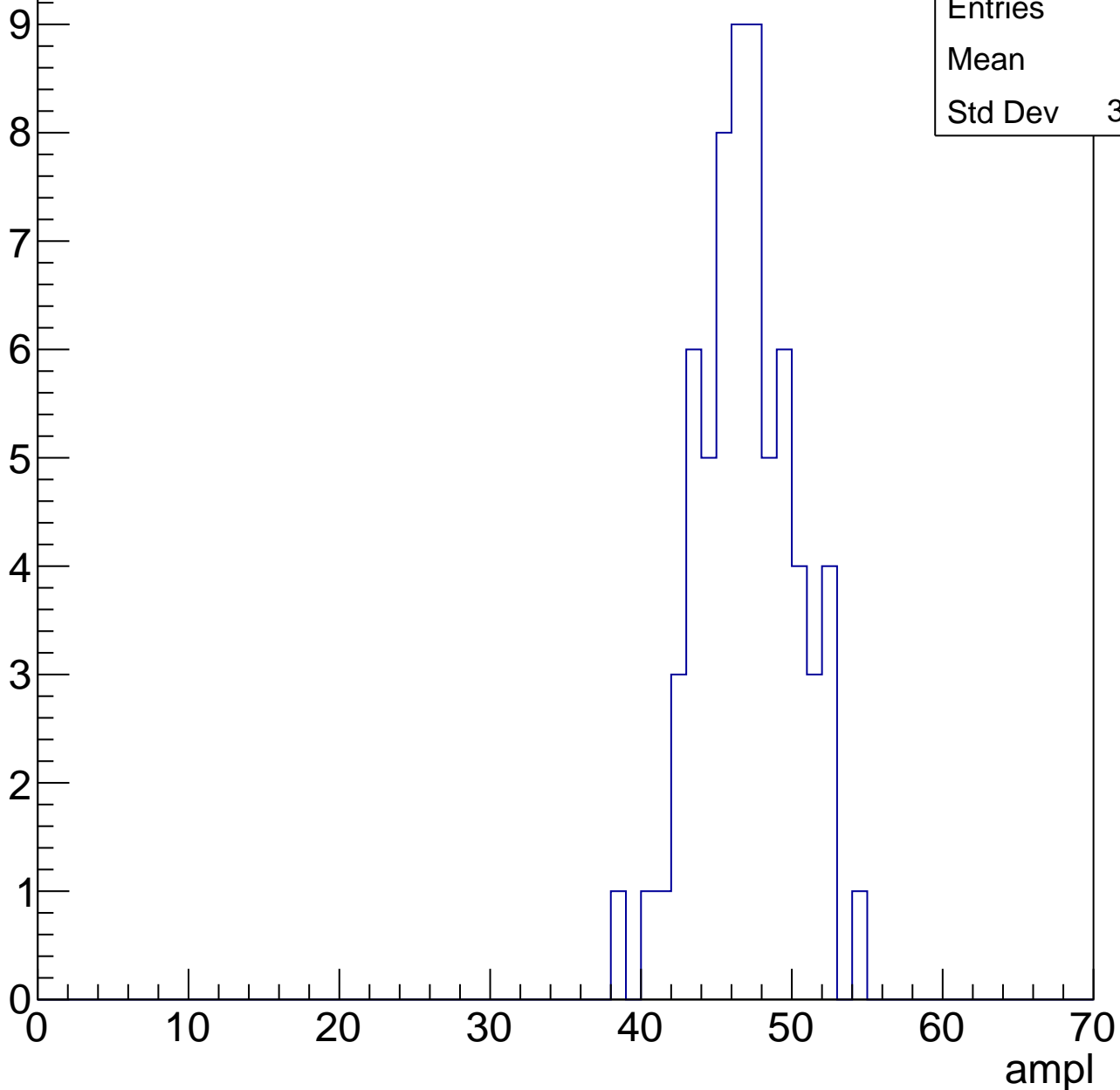


# B1L002S, U17-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.5
Std Dev	3.183

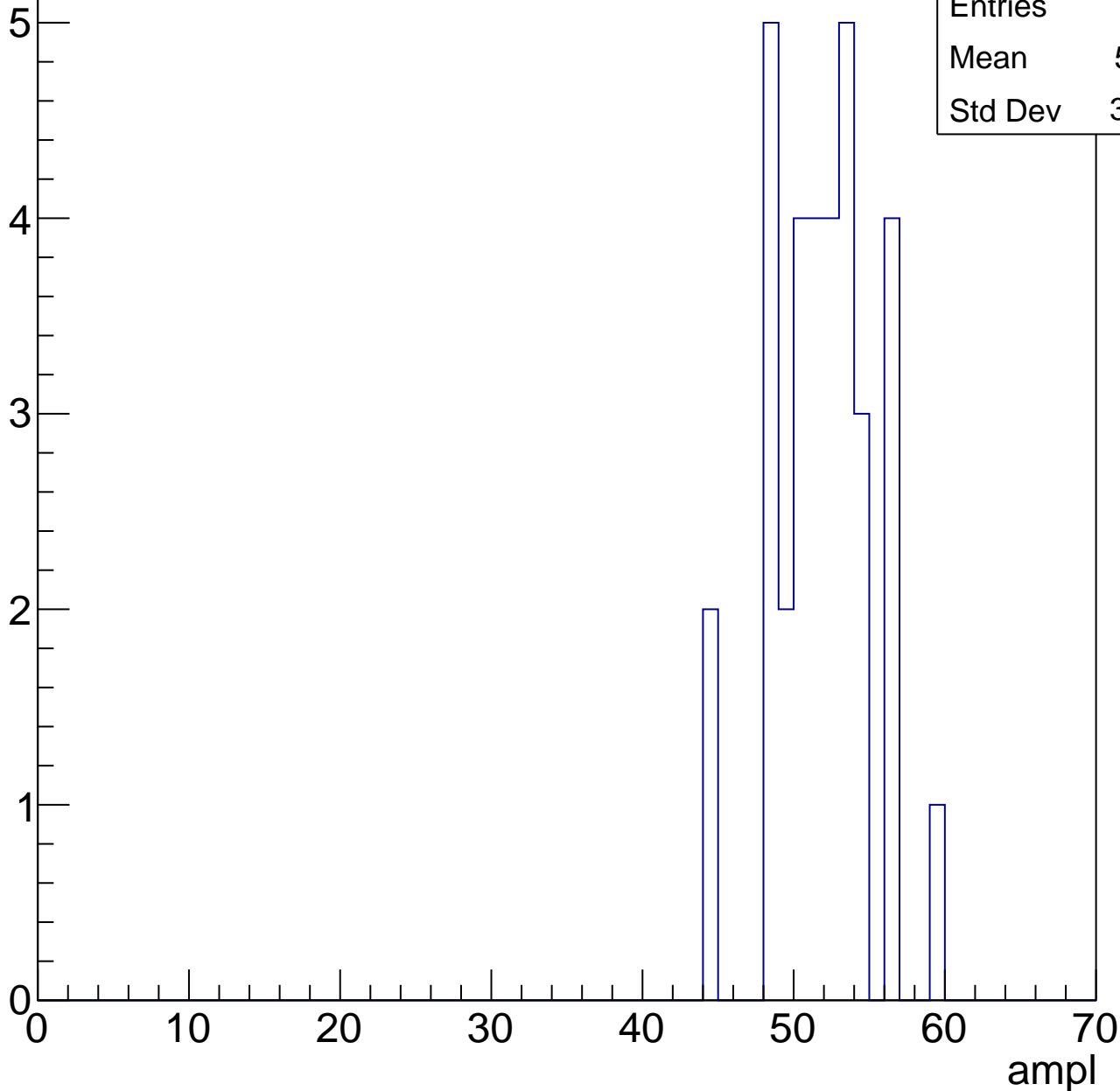


# B1L002S, U17-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	51.41
Std Dev	3.273

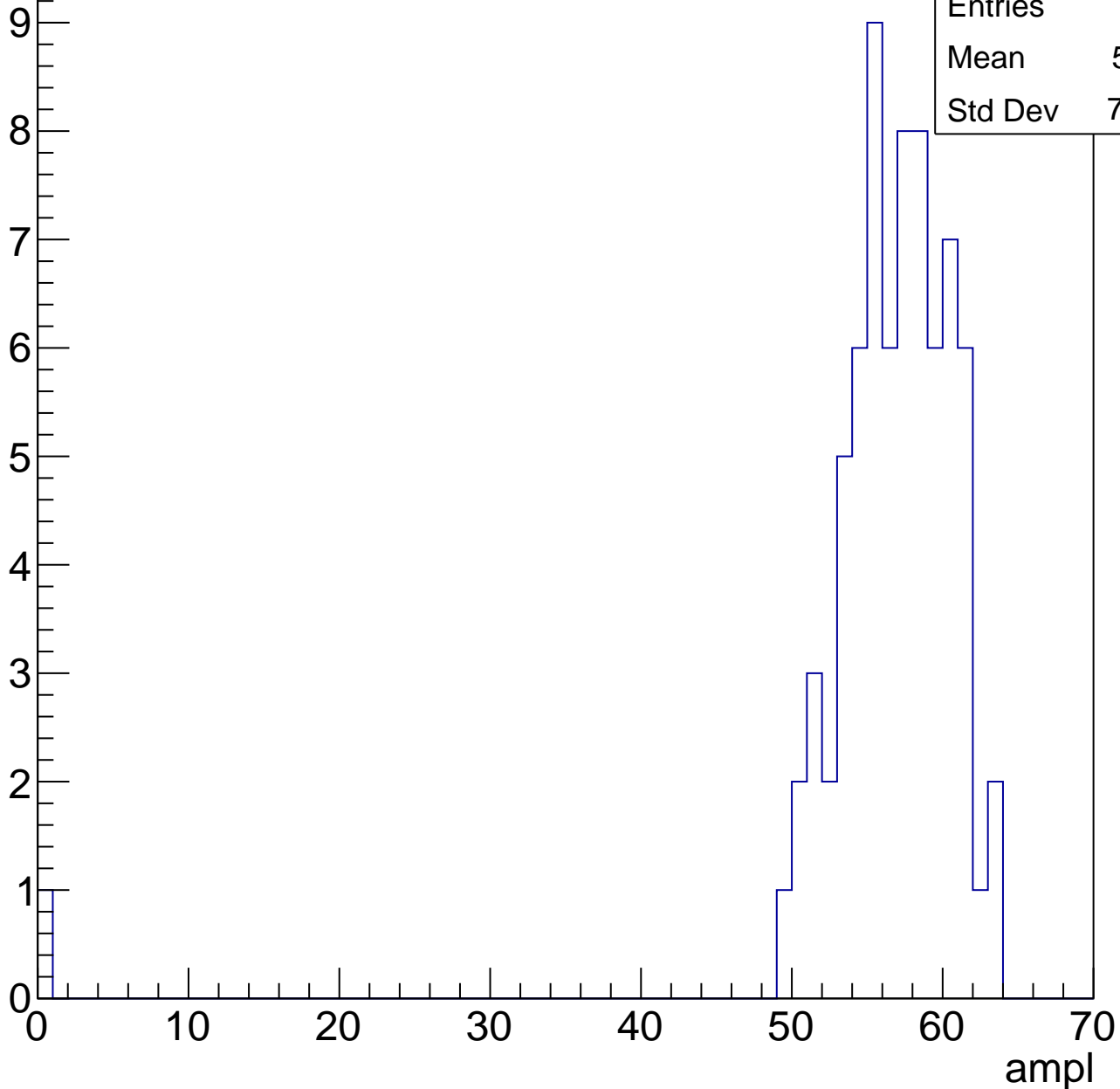


# B1L002S, U17-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	55.81
Std Dev	7.337

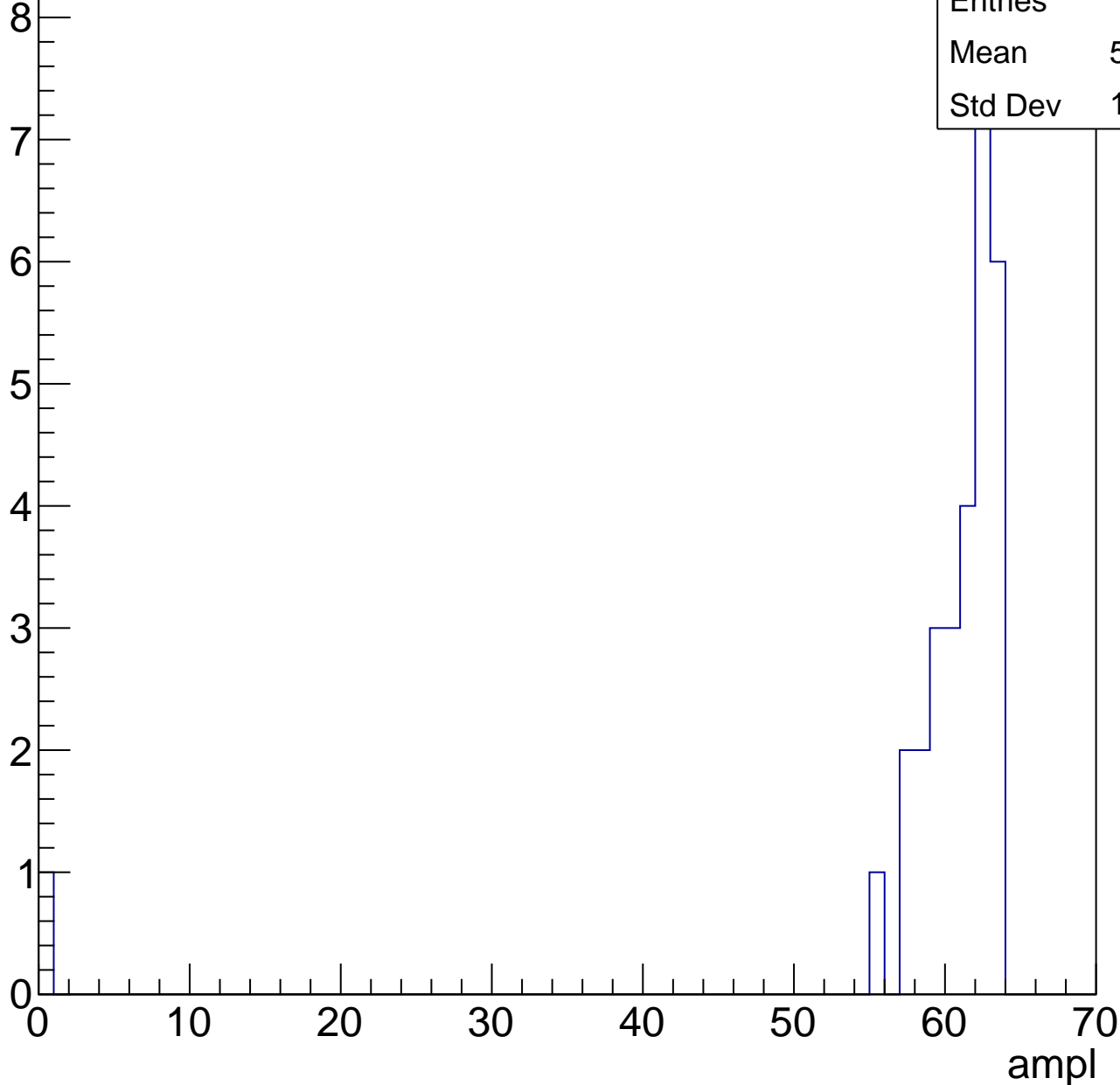


# B1L002S, U17-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.67
Std Dev	11.09

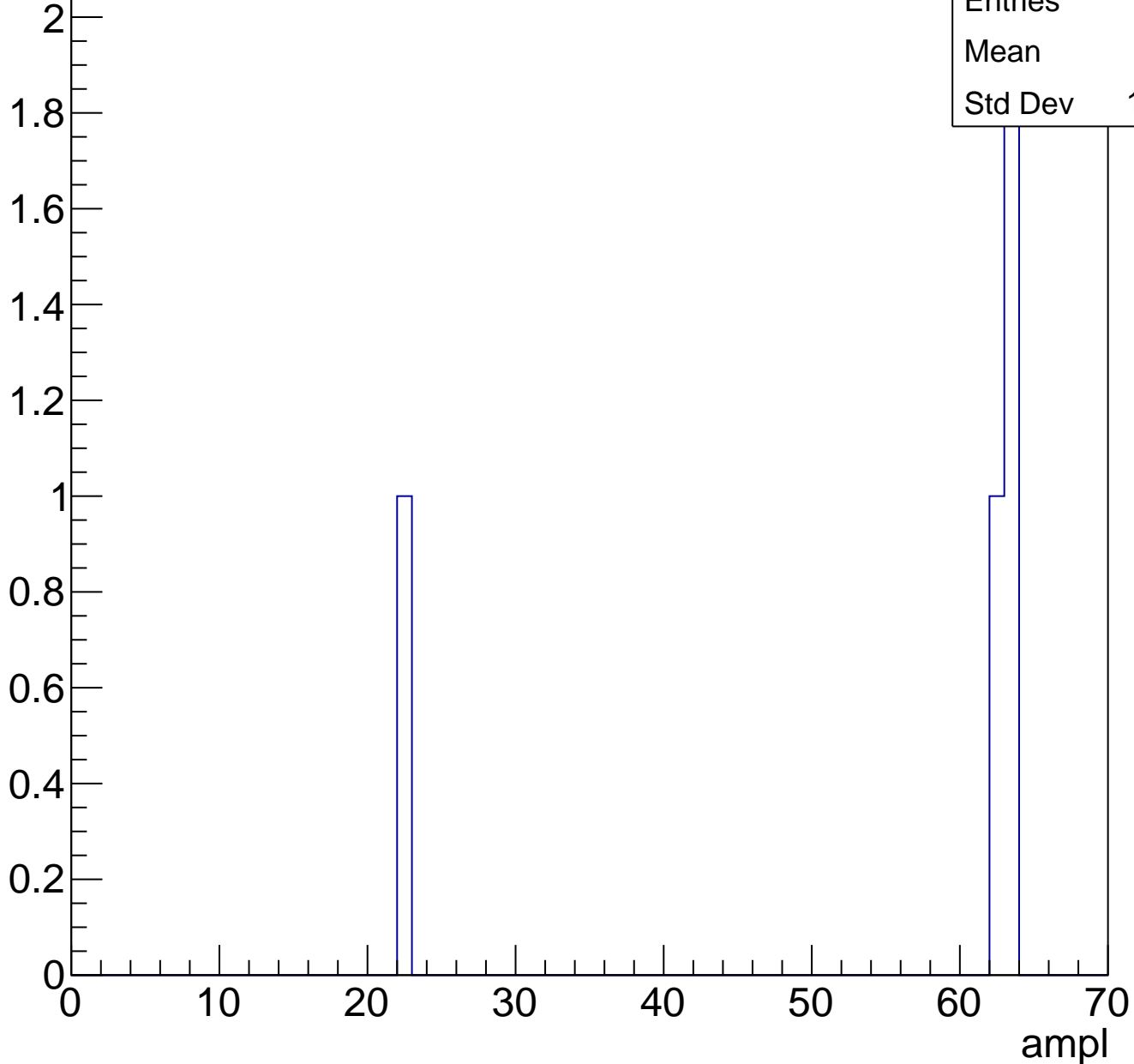




# B1L002S, U17-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	52.5
Std Dev	17.61

# B1L002S, U17-ch123, adc0

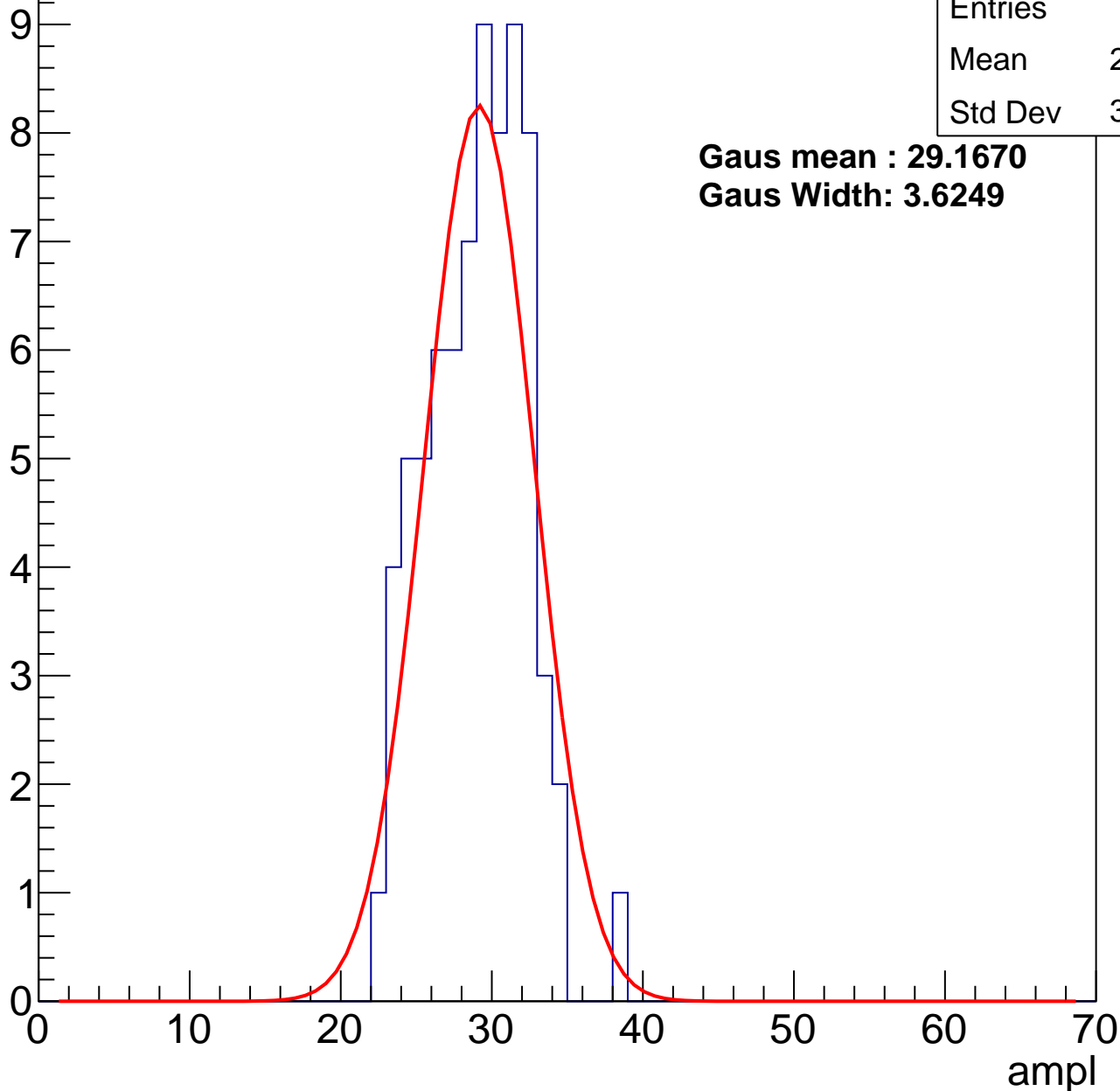
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	28.57
Std Dev	3.213

**Gaus mean : 29.1670**

**Gaus Width: 3.6249**



# B1L002S, U17-ch123, adc1

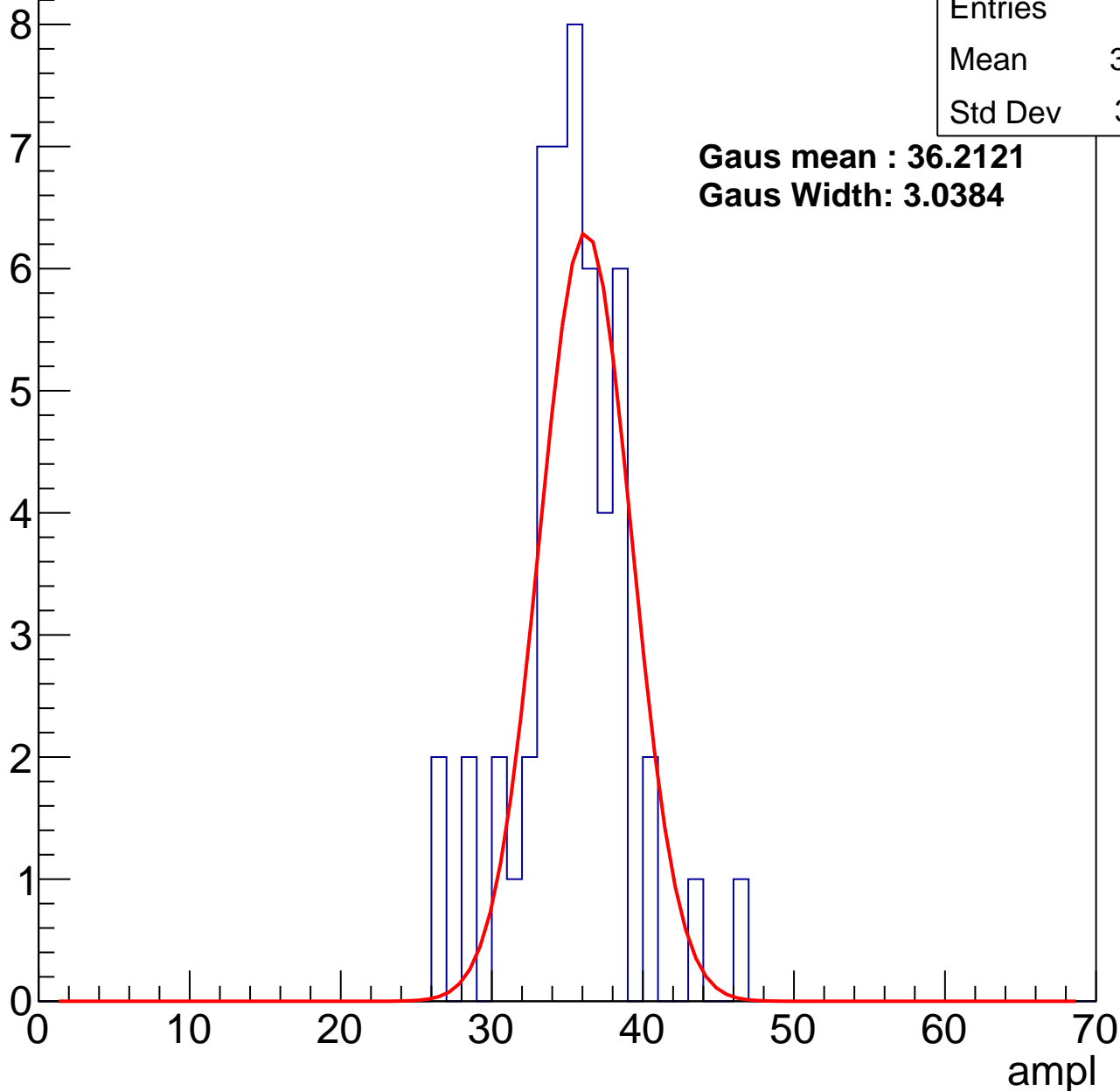
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	34.76
Std Dev	3.671

**Gaus mean : 36.2121**

**Gaus Width: 3.0384**



# B1L002S, U17-ch123, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	41.33
Std Dev	3.51

**Gaus mean : 41.9502**

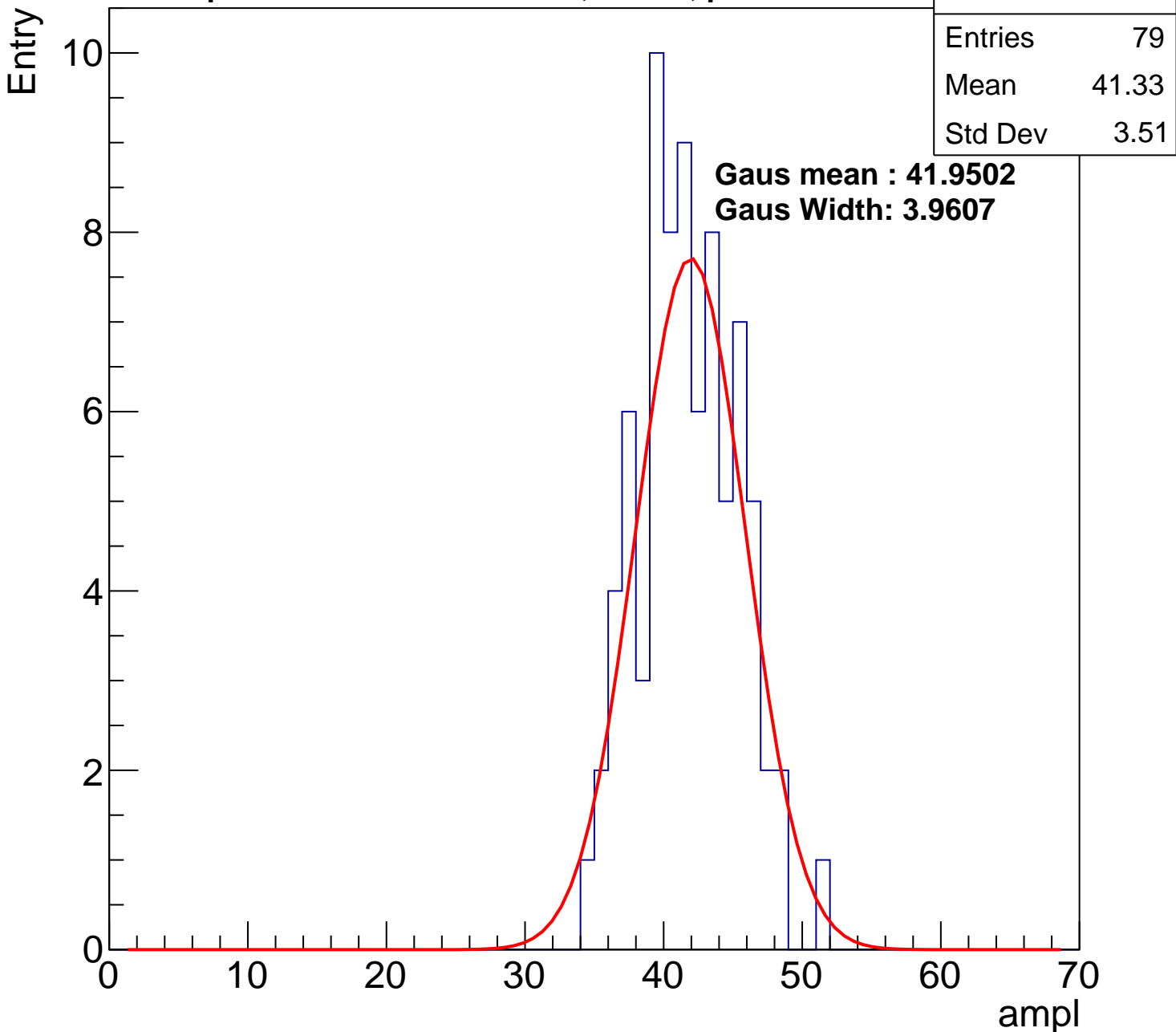
**Gaus Width: 3.9607**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

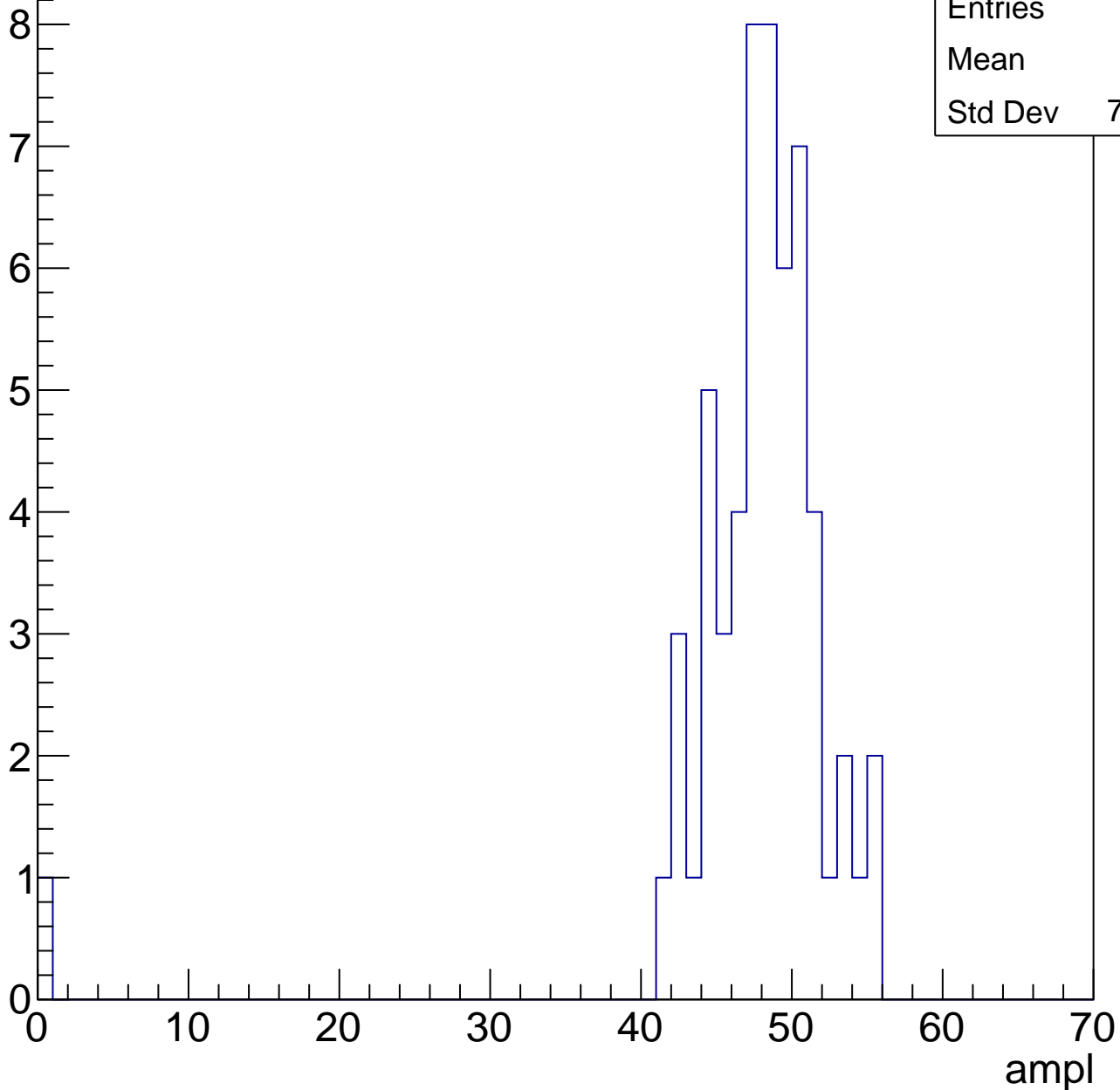


# B1L002S, U17-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	47
Std Dev	7.046

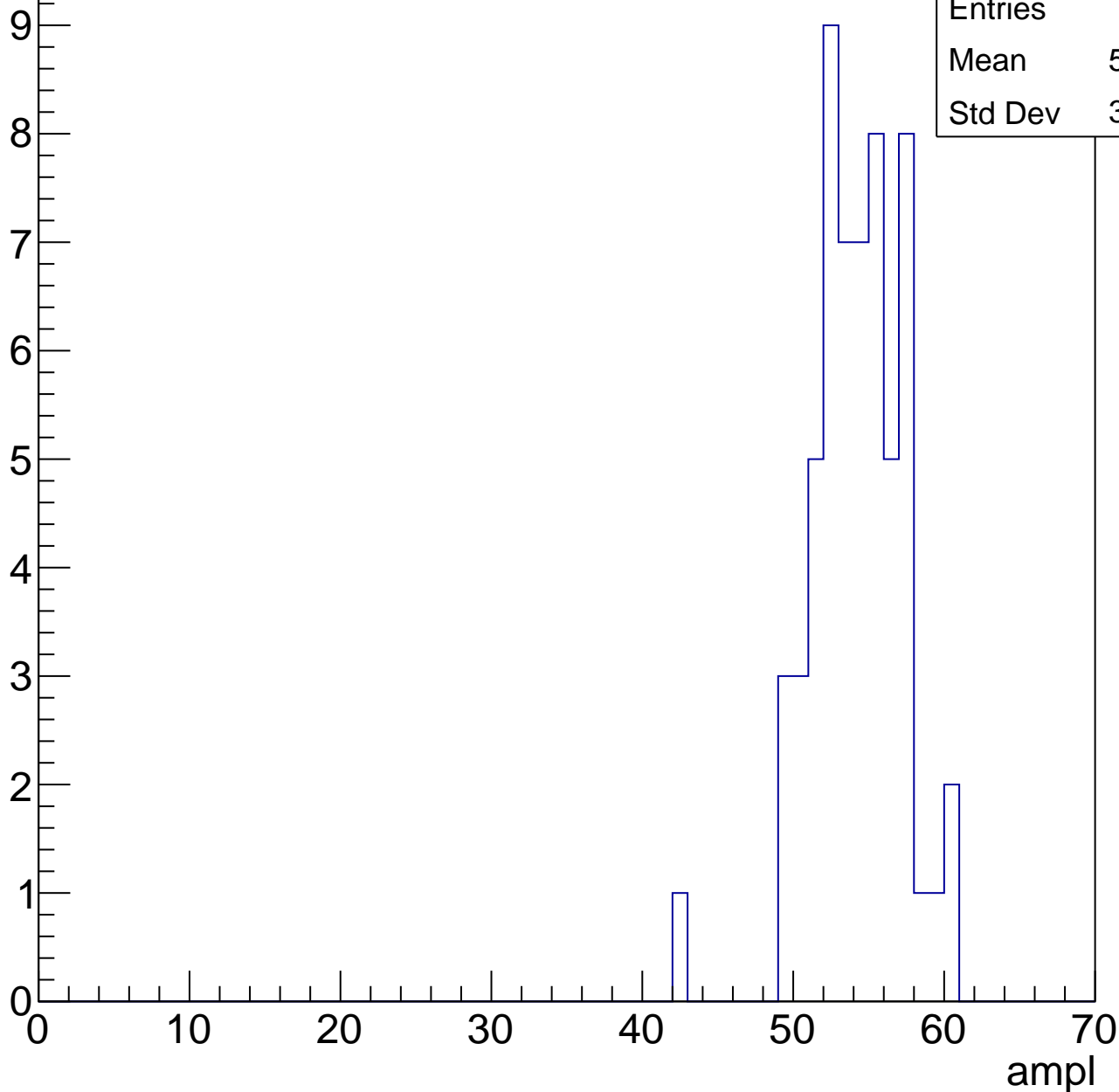


# B1L002S, U17-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	53.73
Std Dev	3.065

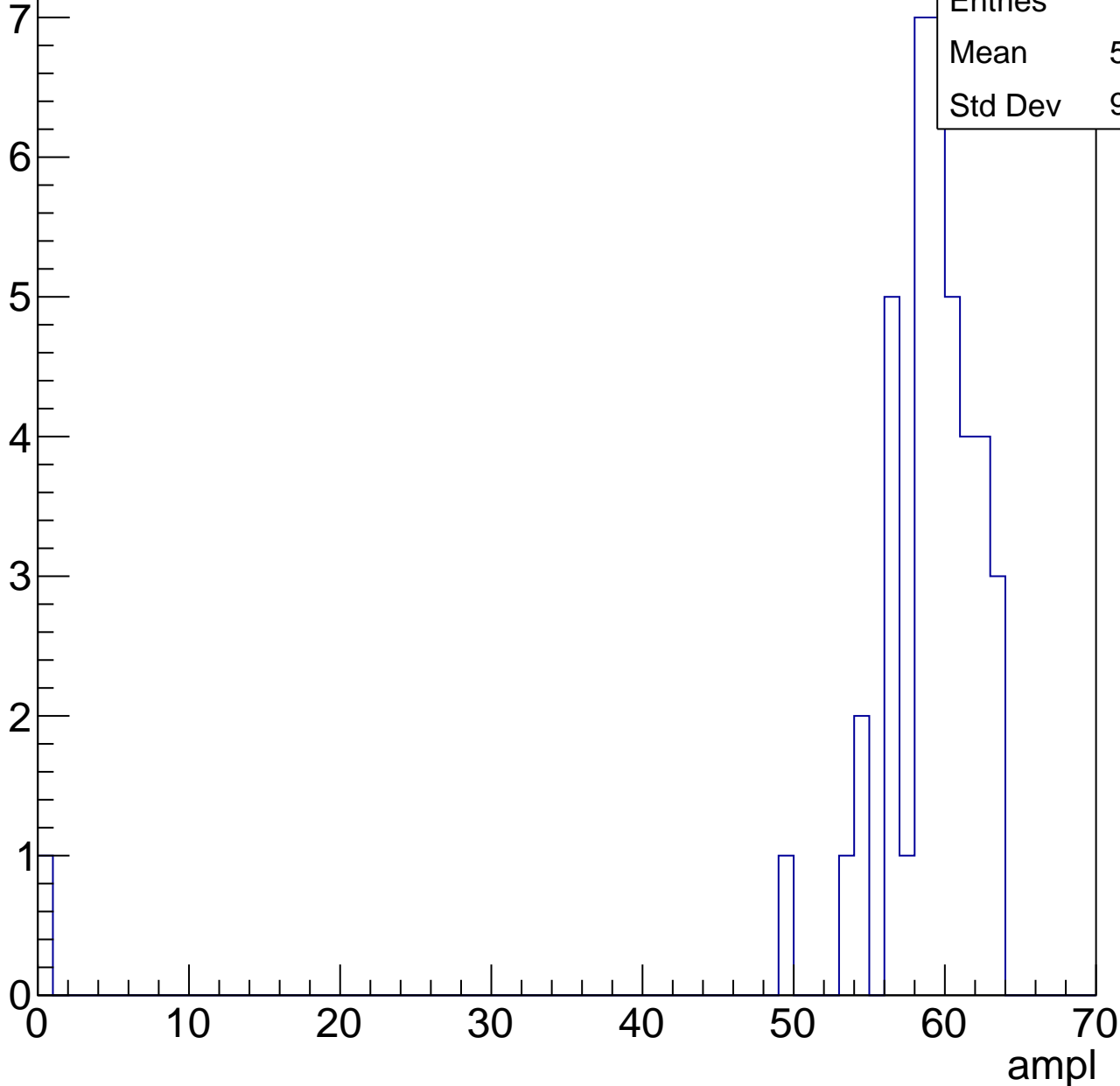


# B1L002S, U17-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	57.24
Std Dev	9.502

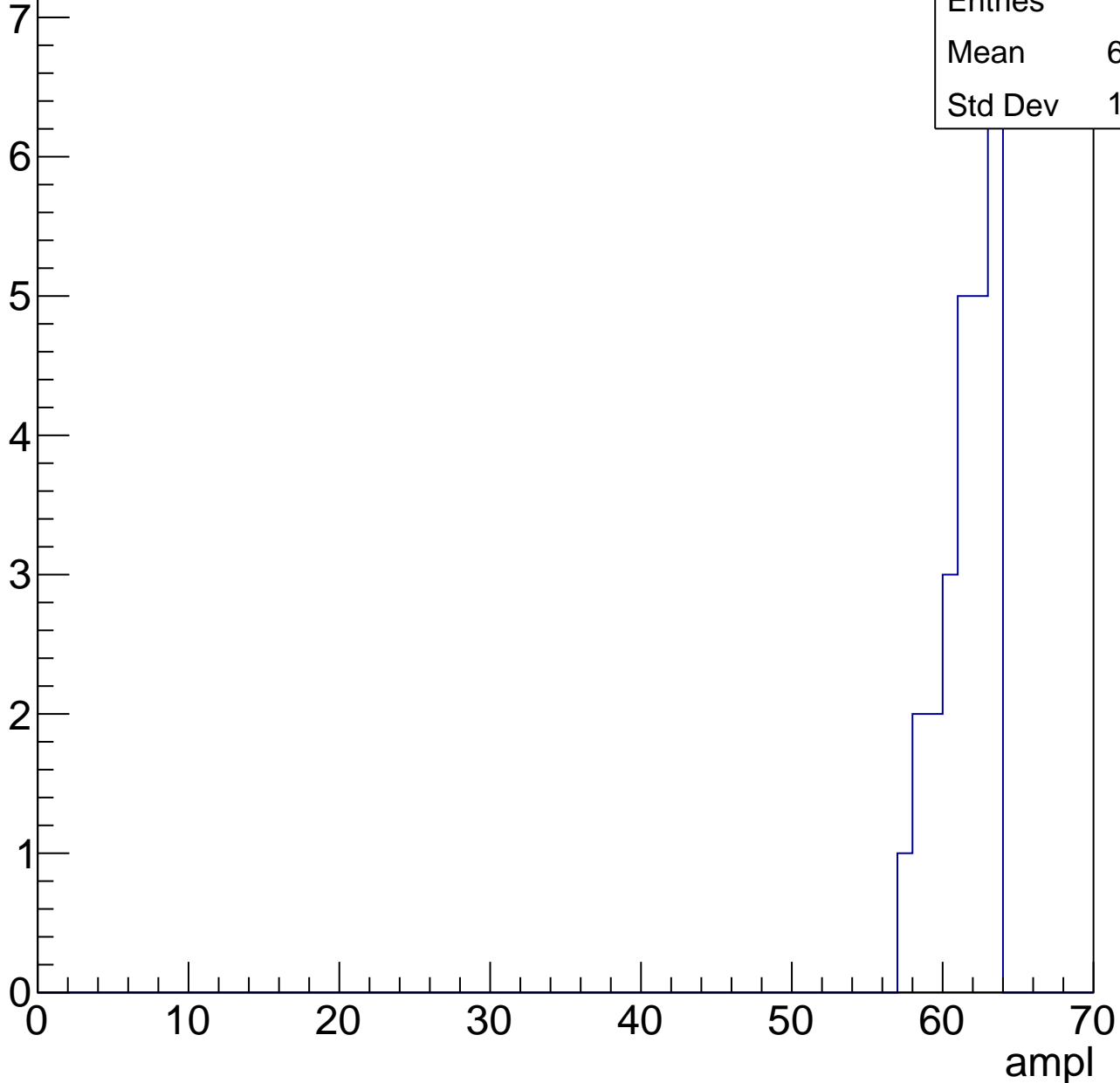


# B1L002S, U17-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.08
Std Dev	1.765

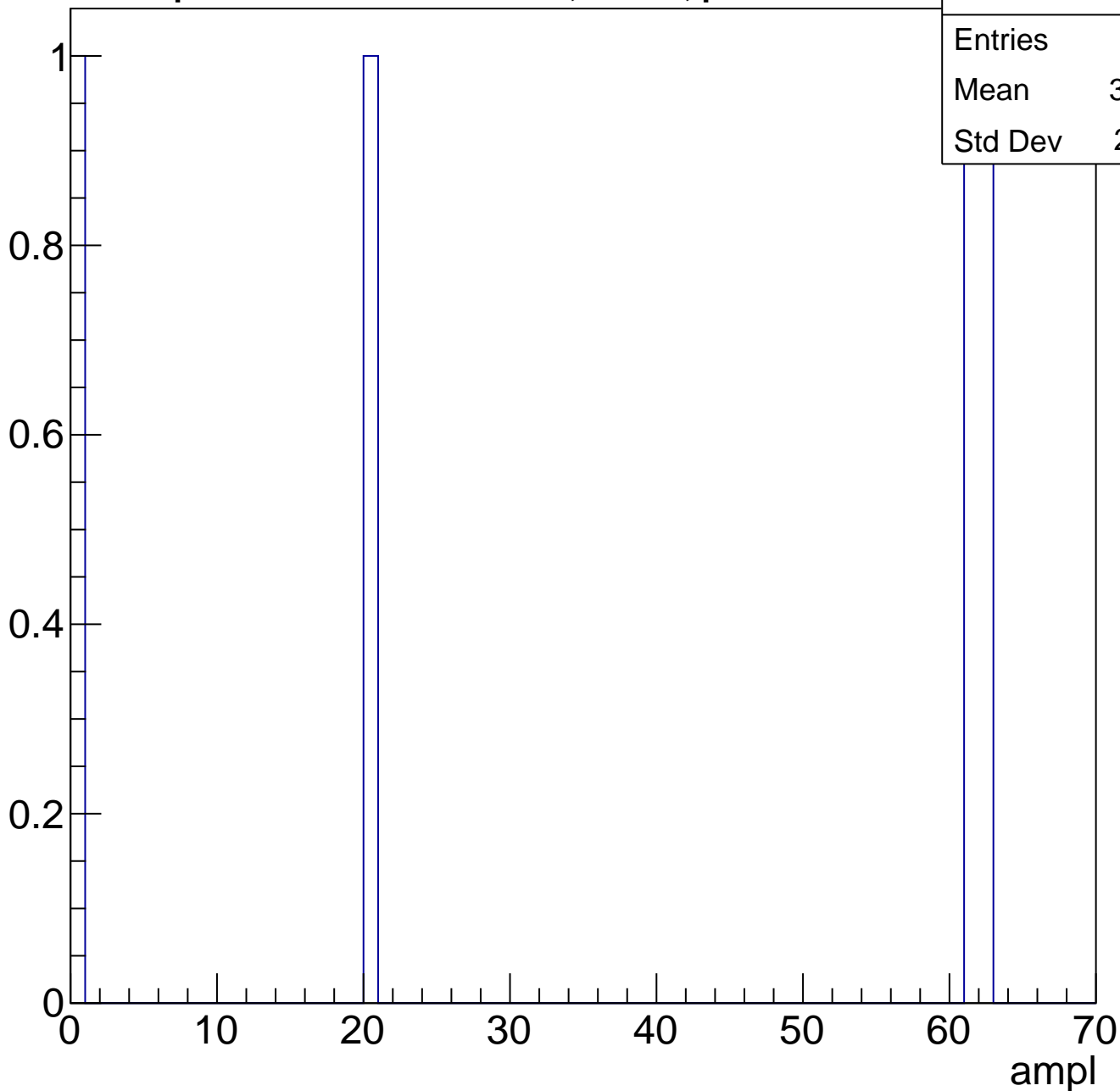




# B1L002S, U17-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	35.75
Std Dev	26.71

# B1L002S, U17-ch124, adc0

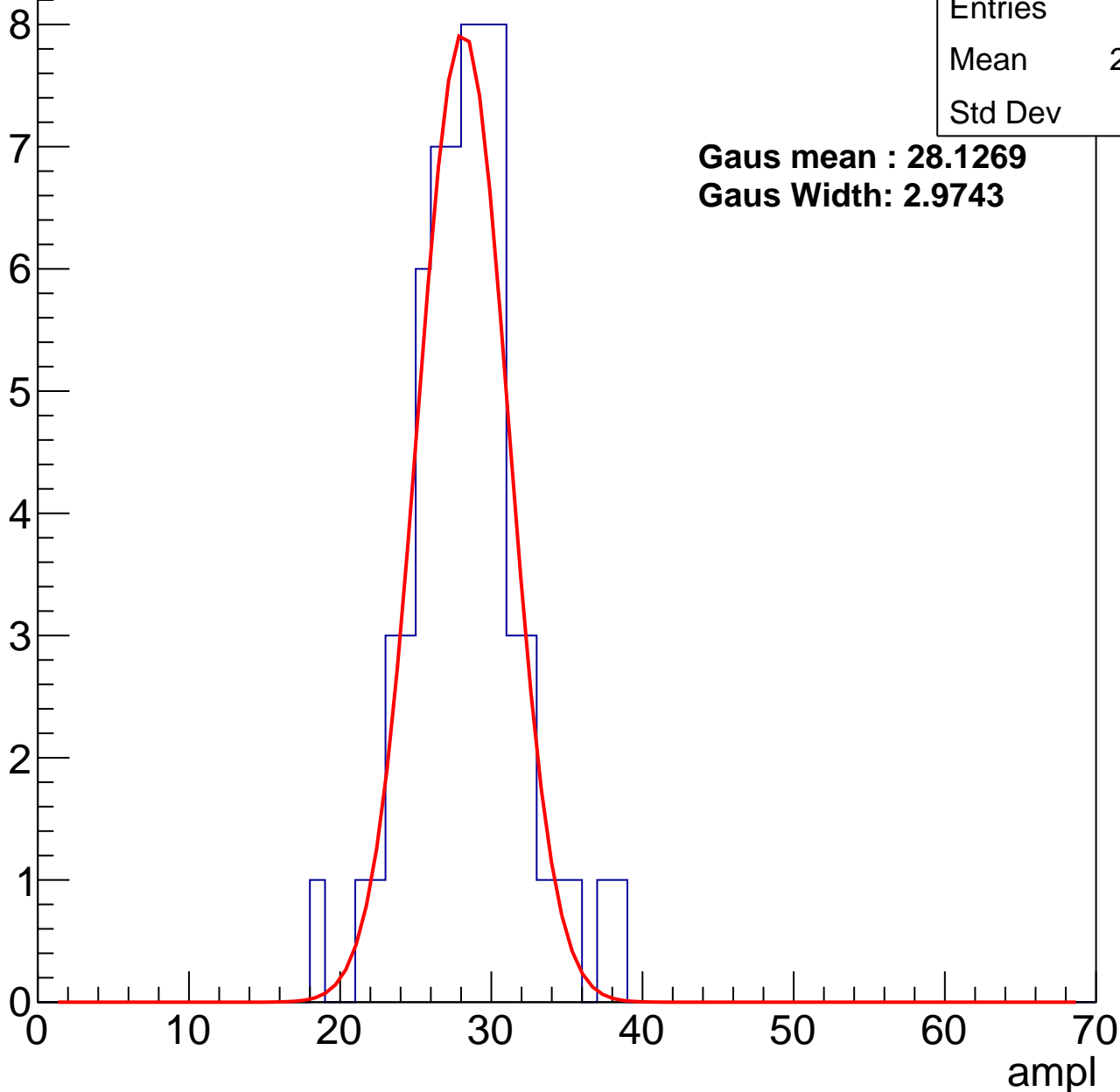
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.89
Std Dev	3.54

**Gaus mean : 28.1269**

**Gaus Width: 2.9743**



# B1L002S, U17-ch124, adc1

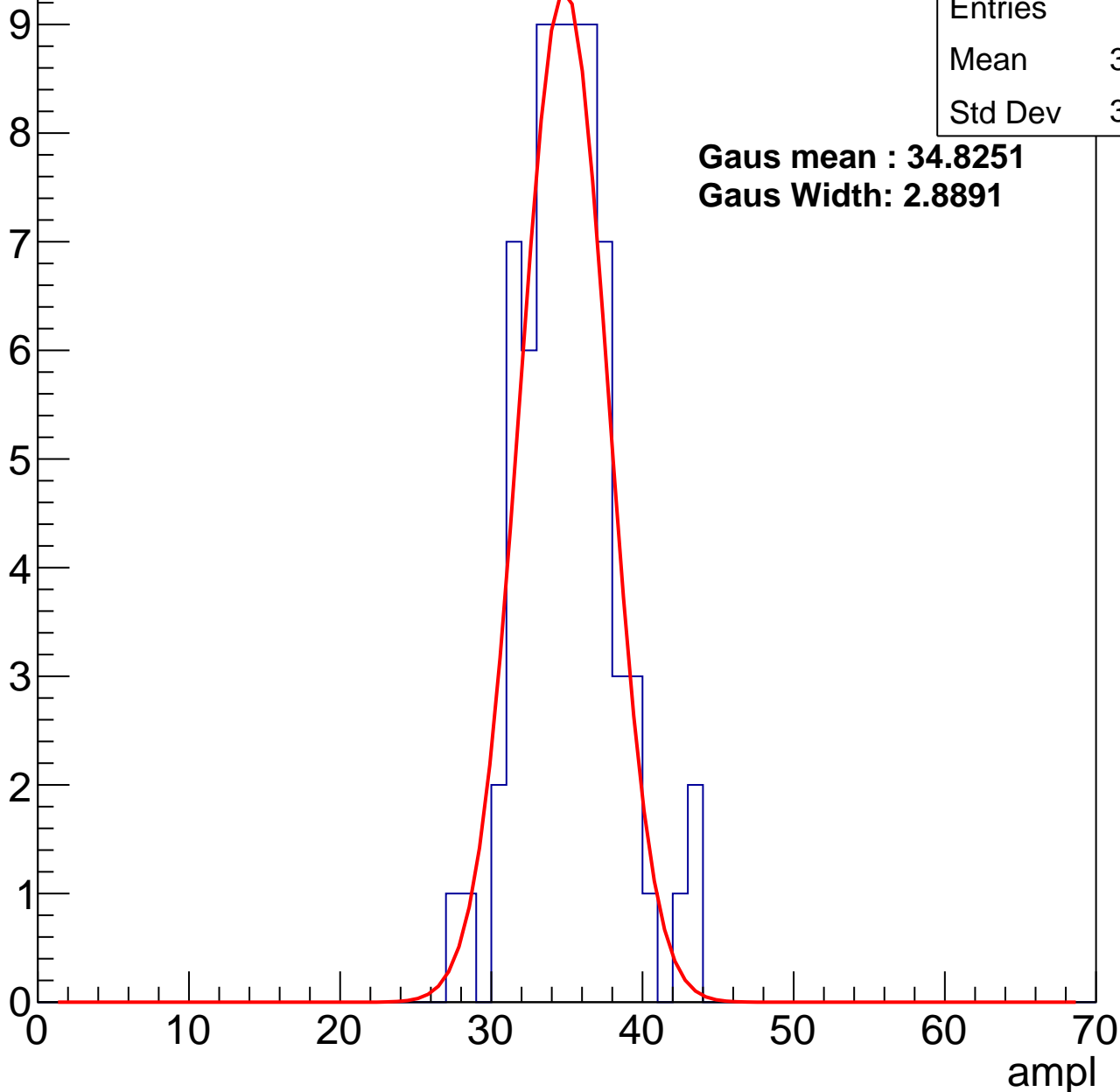
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	34.63
Std Dev	3.113

**Gaus mean : 34.8251**

**Gaus Width: 2.8891**



# B1L002S, U17-ch124, adc2

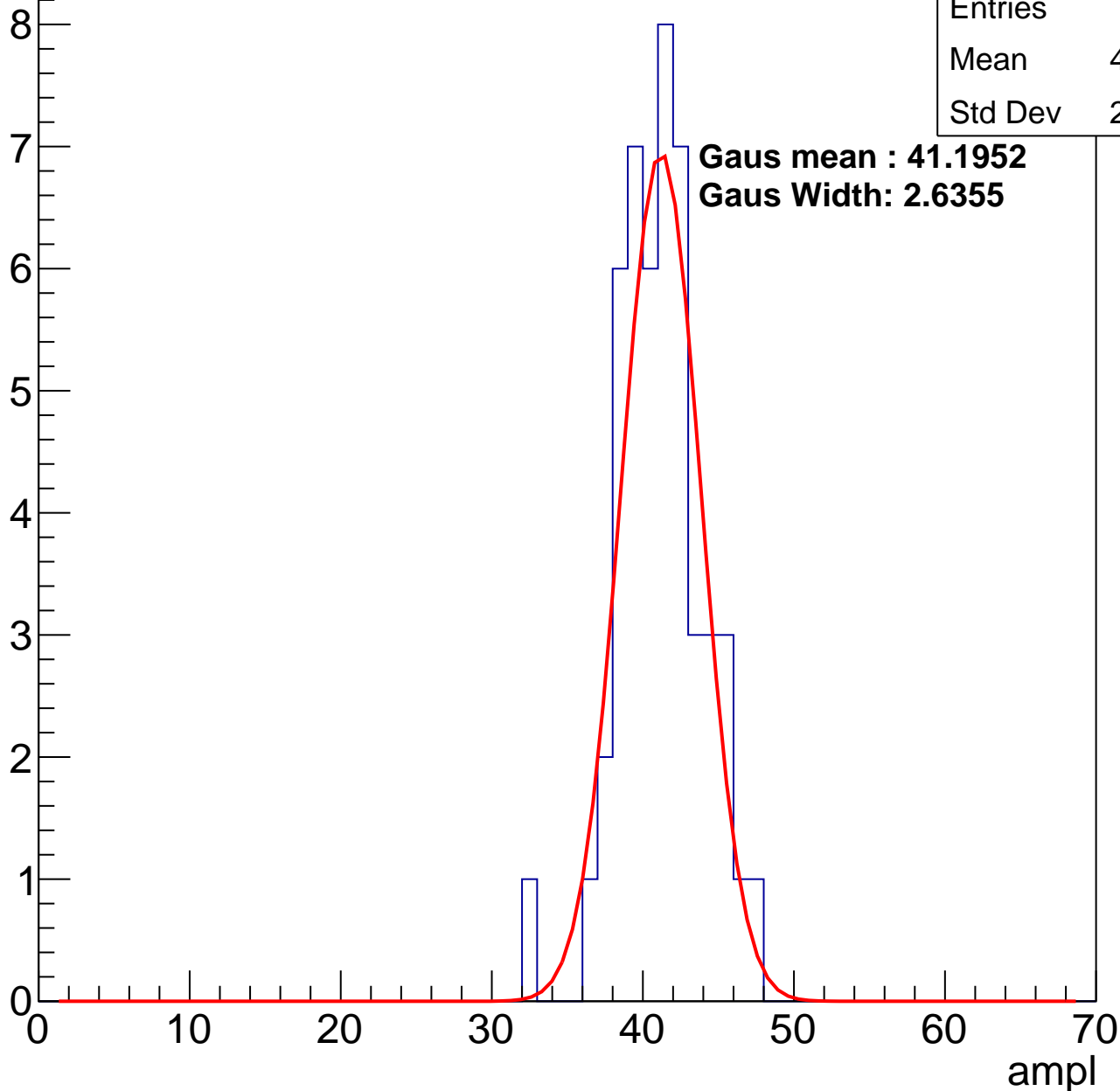
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.69
Std Dev	2.772

**Gaus mean : 41.1952**

**Gaus Width: 2.6355**

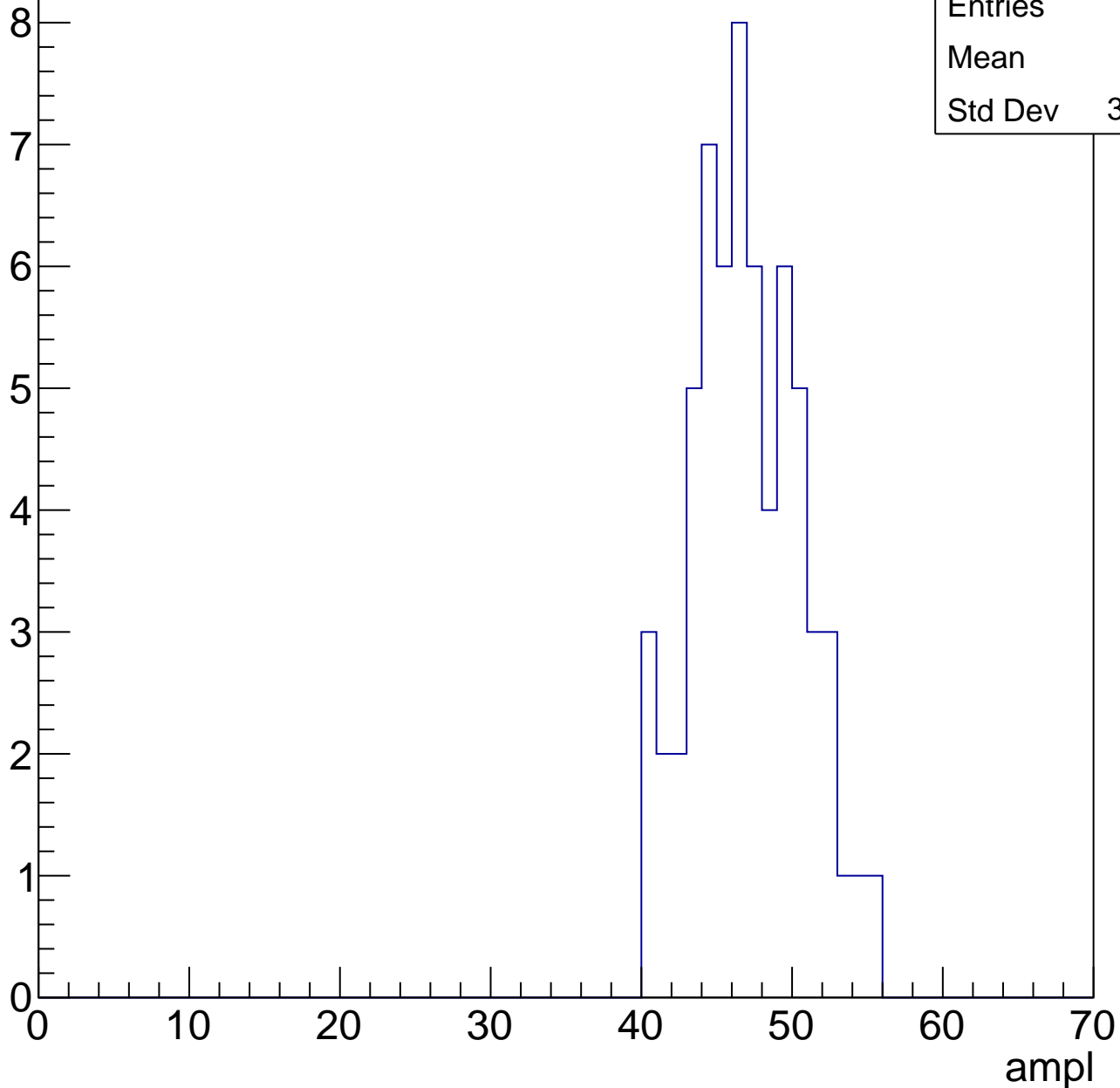


# B1L002S, U17-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.6
Std Dev	3.517

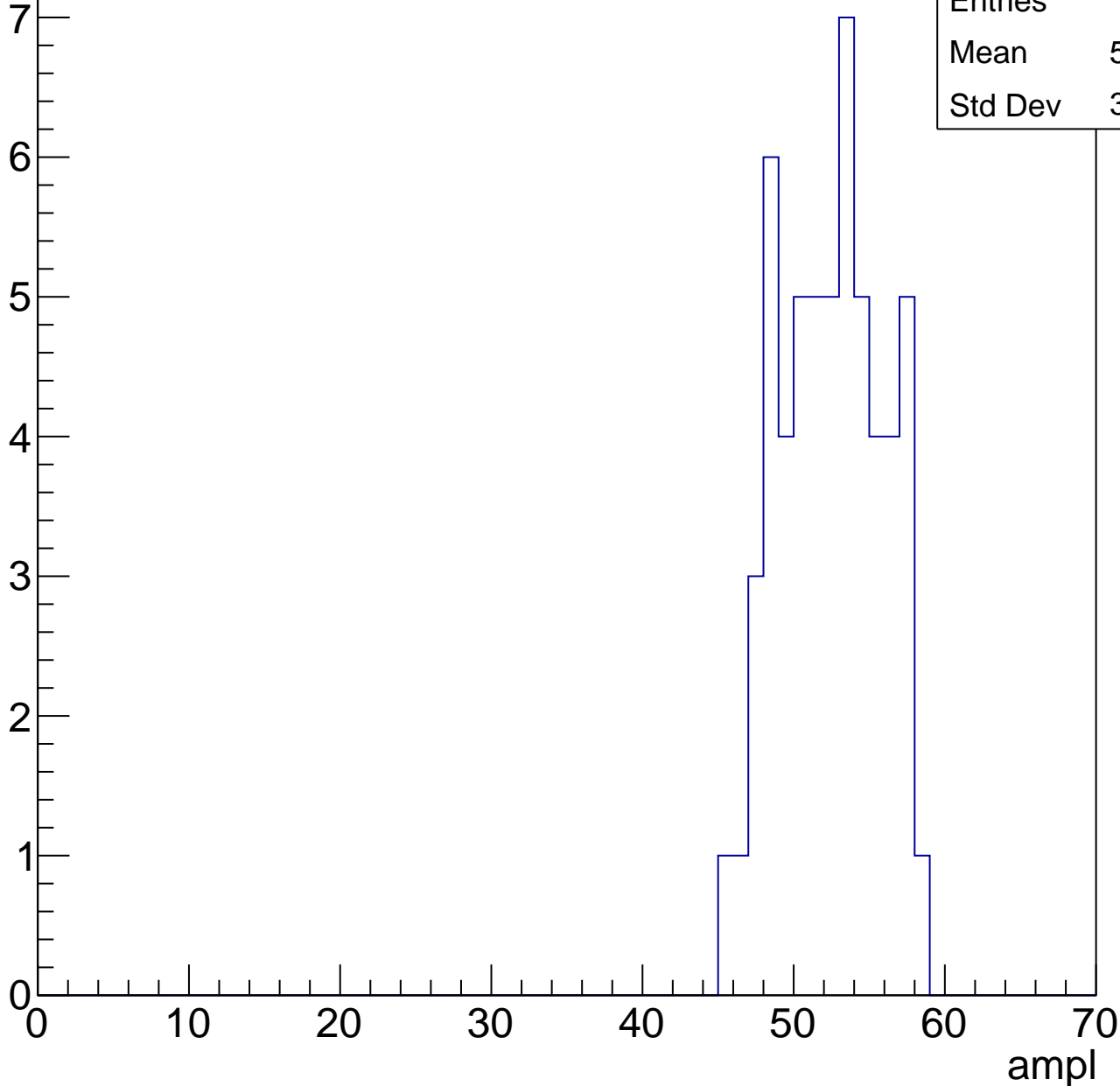


# B1L002S, U17-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	51.95
Std Dev	3.286

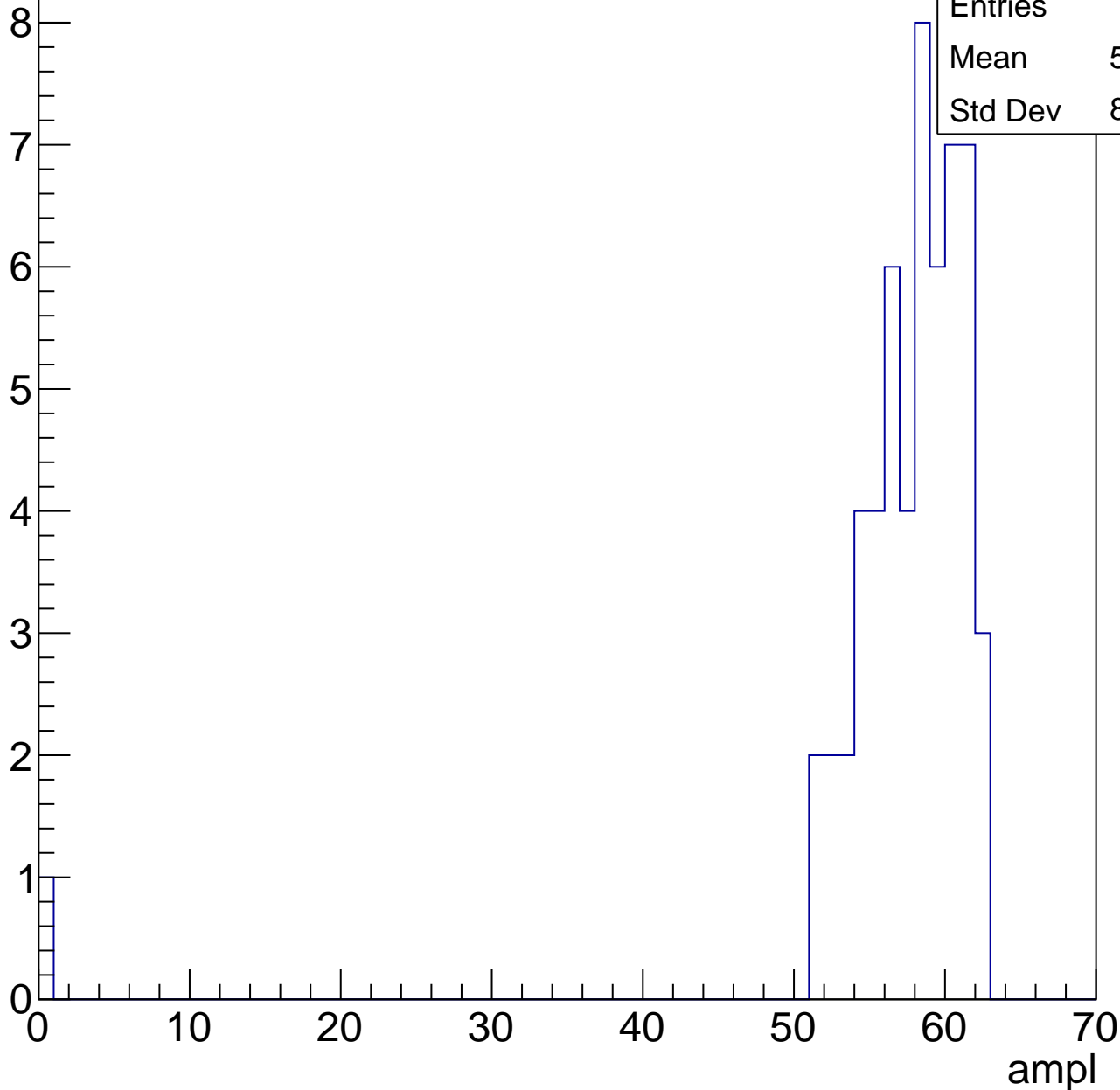


# B1L002S, U17-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.48
Std Dev	8.159

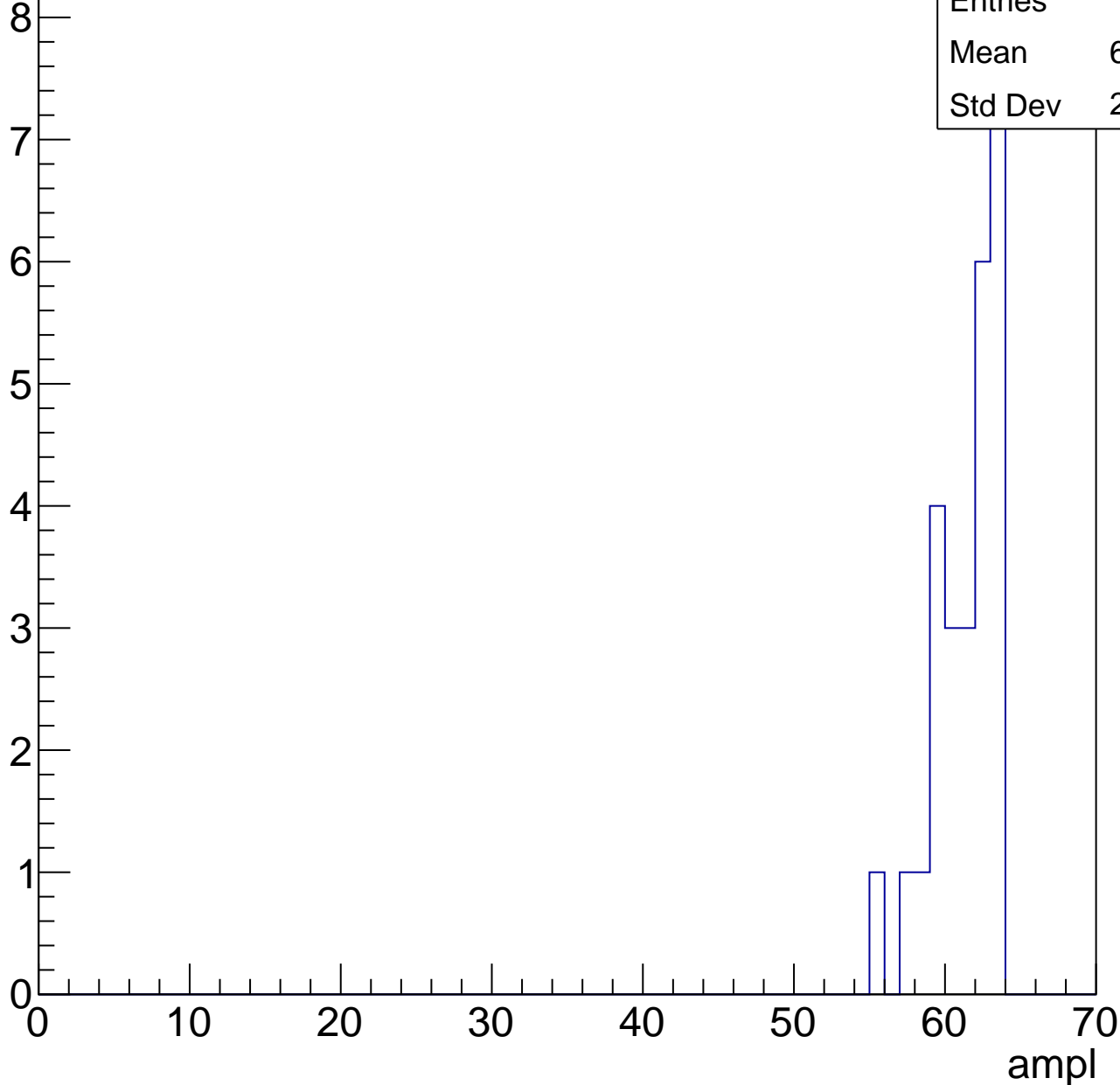


# B1L002S, U17-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.93
Std Dev	2.089

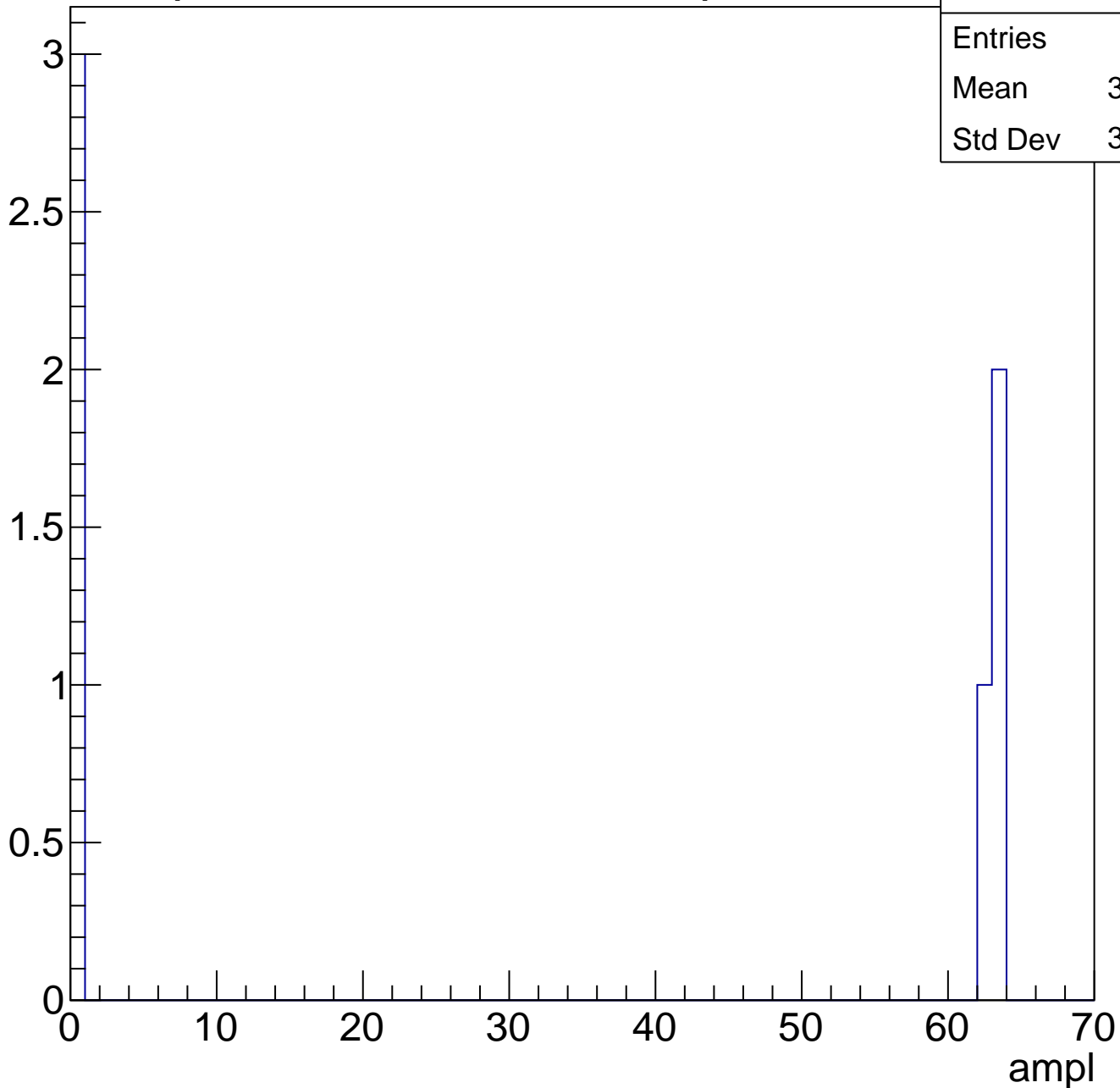




# B1L002S, U17-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch125, adc0

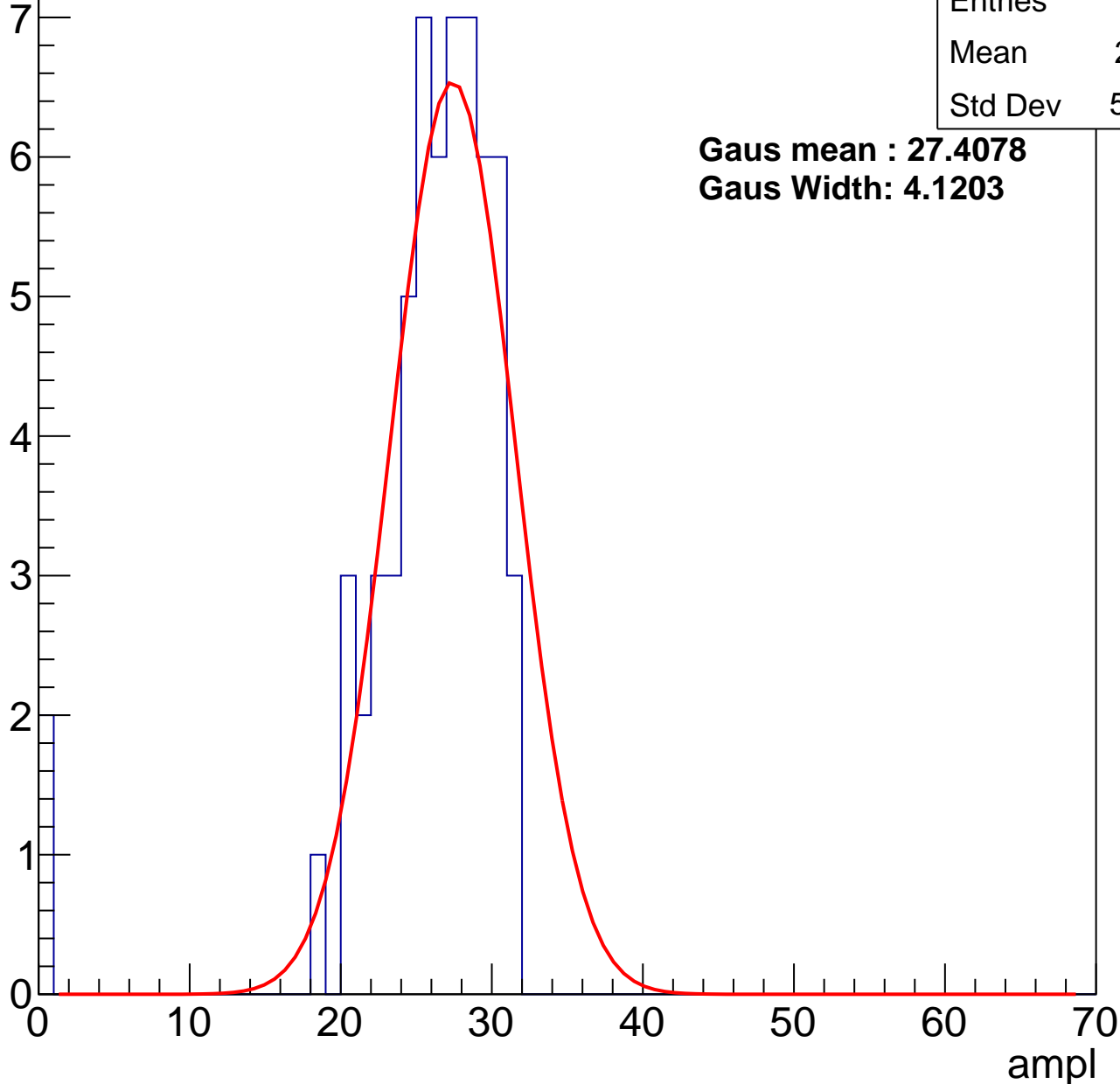
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	25.21
Std Dev	5.578

**Gaus mean : 27.4078**

**Gaus Width: 4.1203**



# B1L002S, U17-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	32.32
Std Dev	3.276

**Gaus mean : 33.3625**

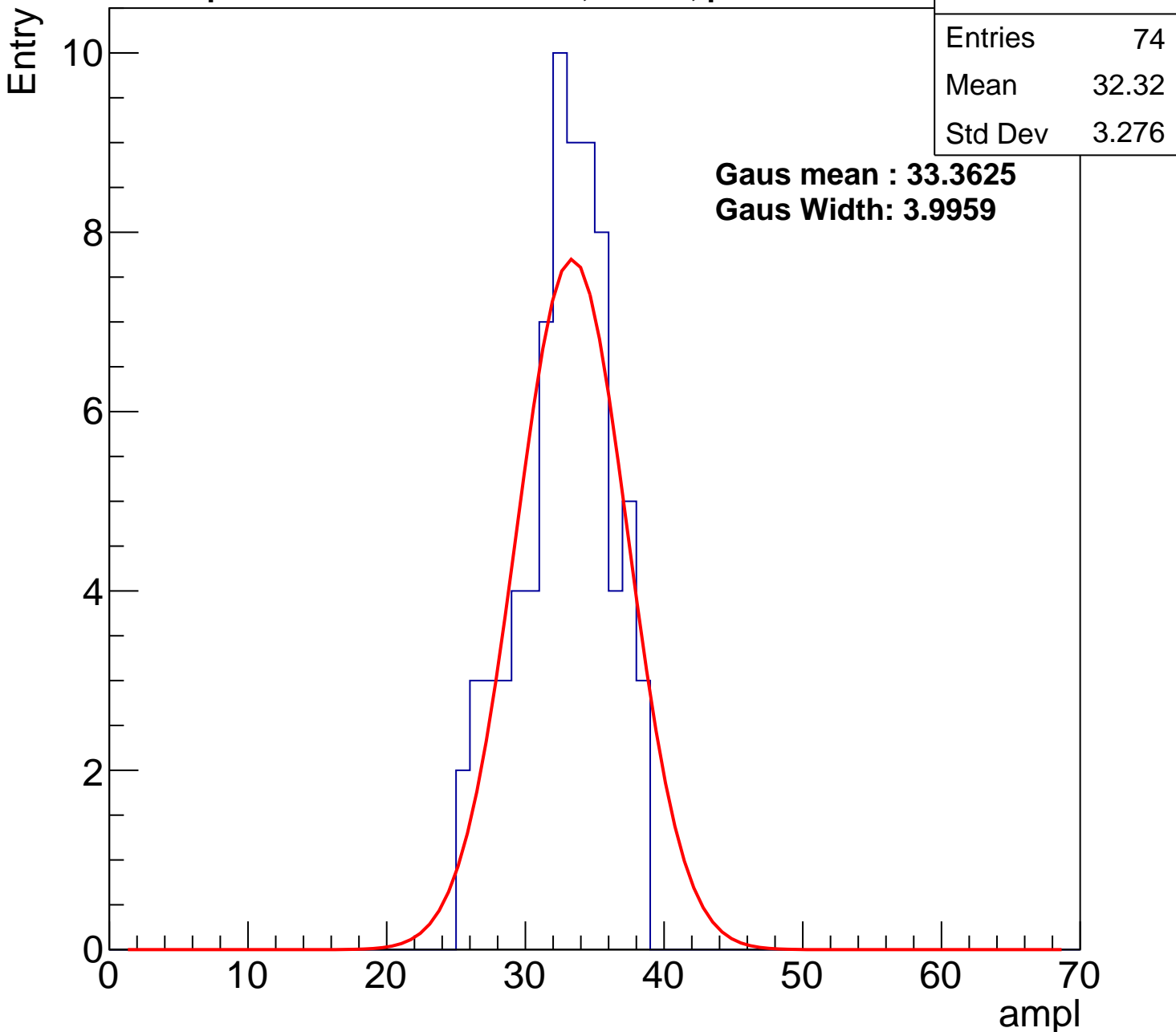
**Gaus Width: 3.9959**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U17-ch125, adc2

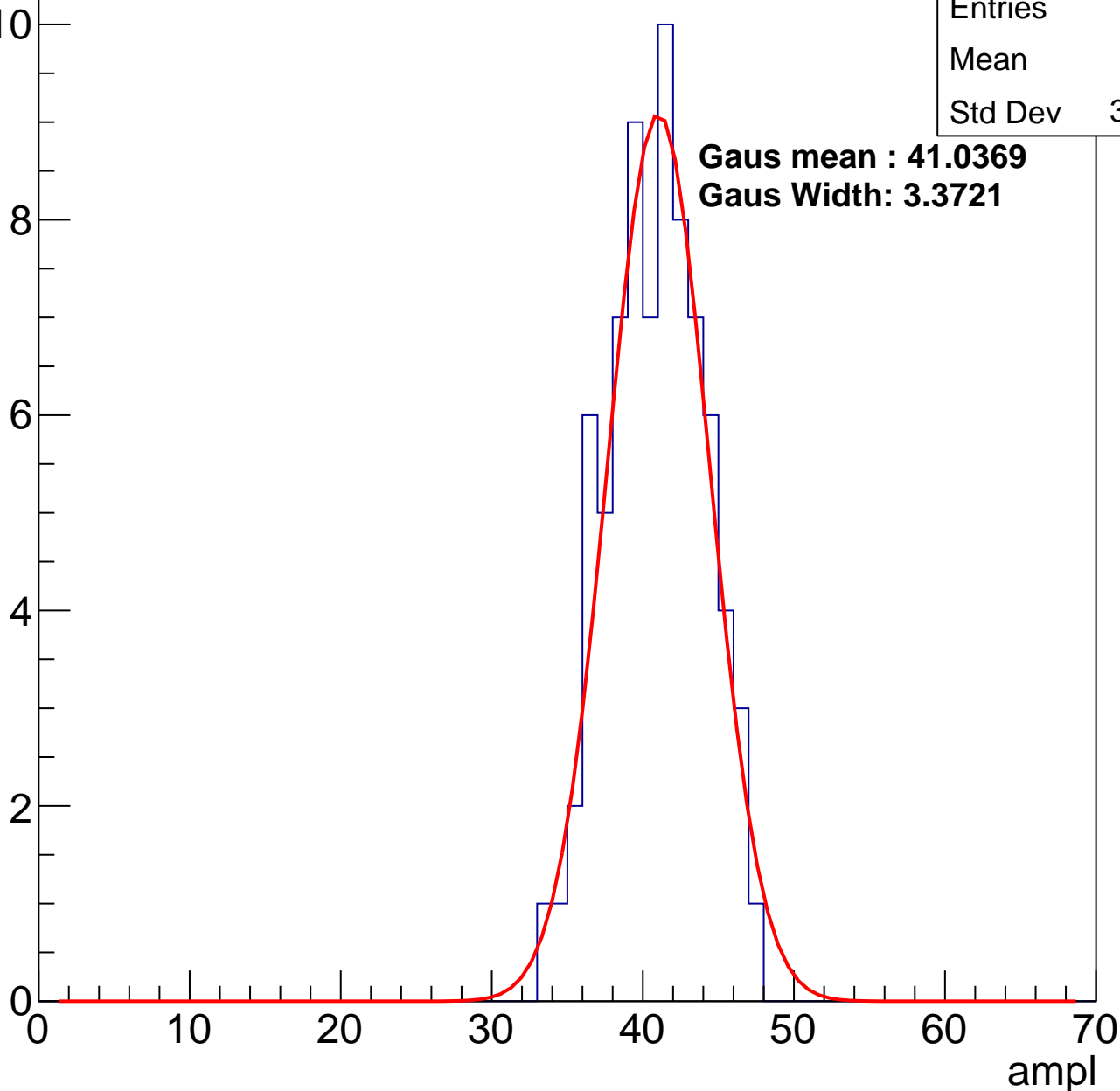
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	40.4
Std Dev	3.139

**Gaus mean : 41.0369**

**Gaus Width: 3.3721**

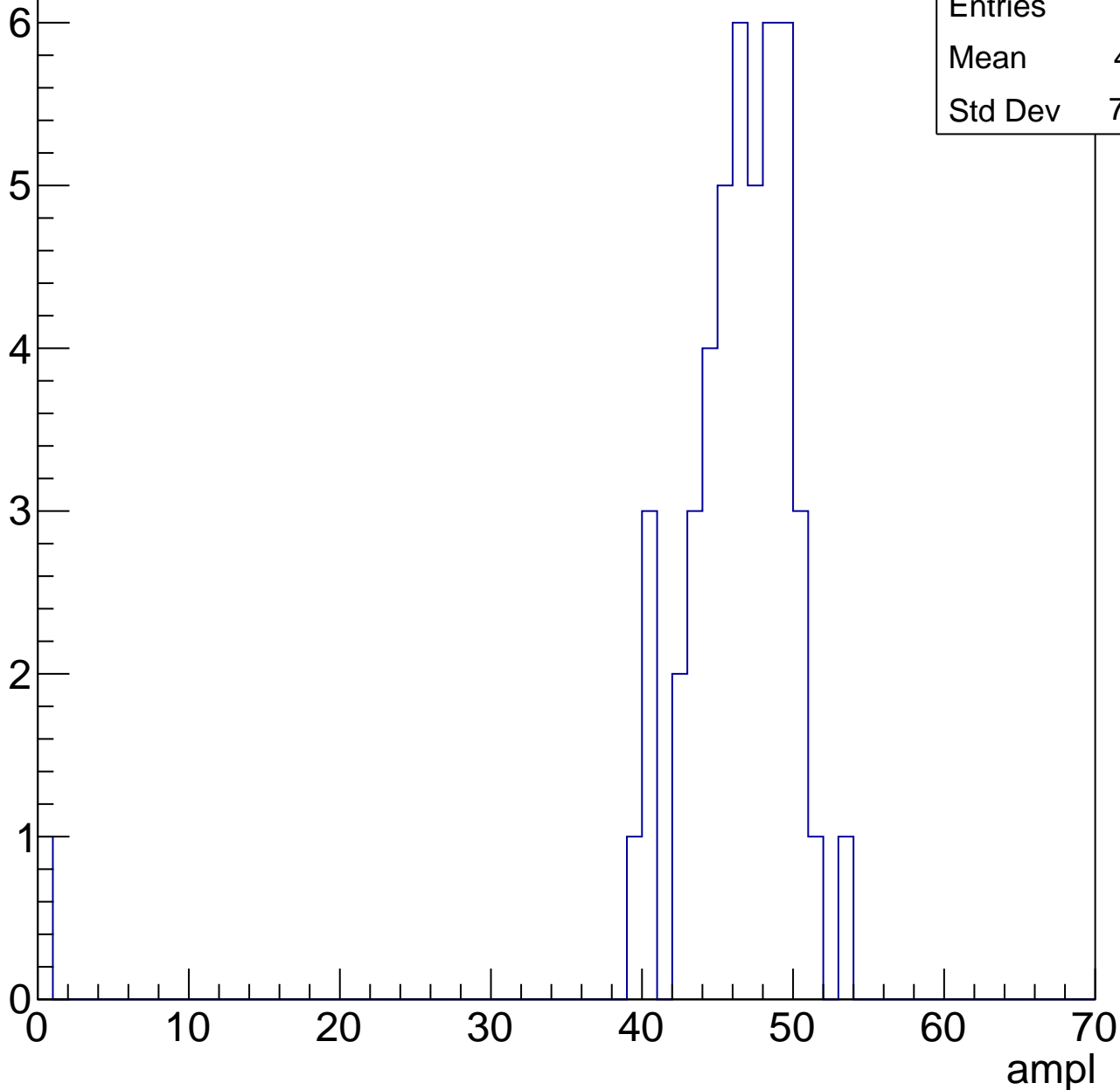


# B1L002S, U17-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	45.11
Std Dev	7.323

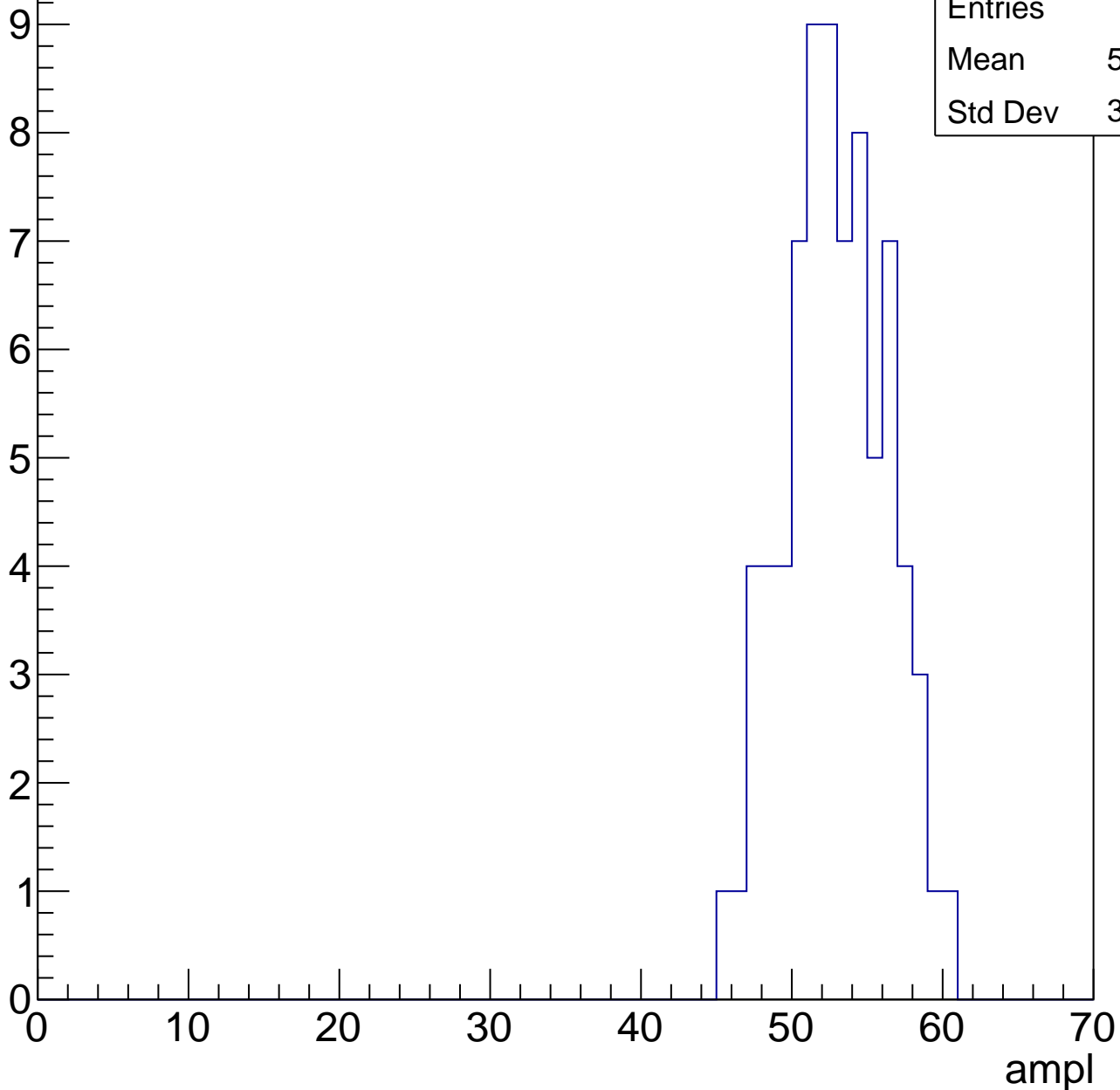


# B1L002S, U17-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	52.47
Std Dev	3.312

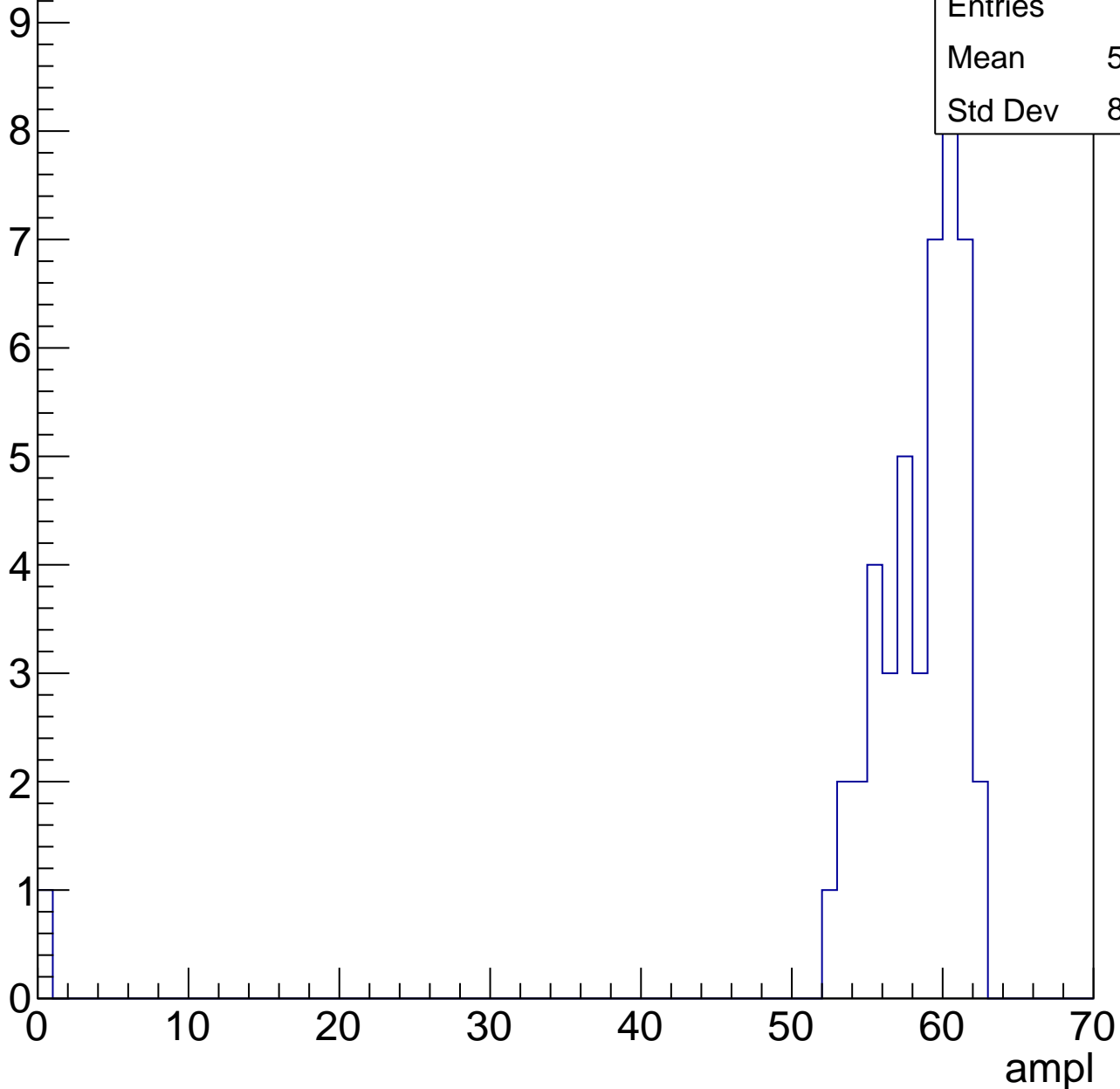


# B1L002S, U17-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	56.89
Std Dev	8.867

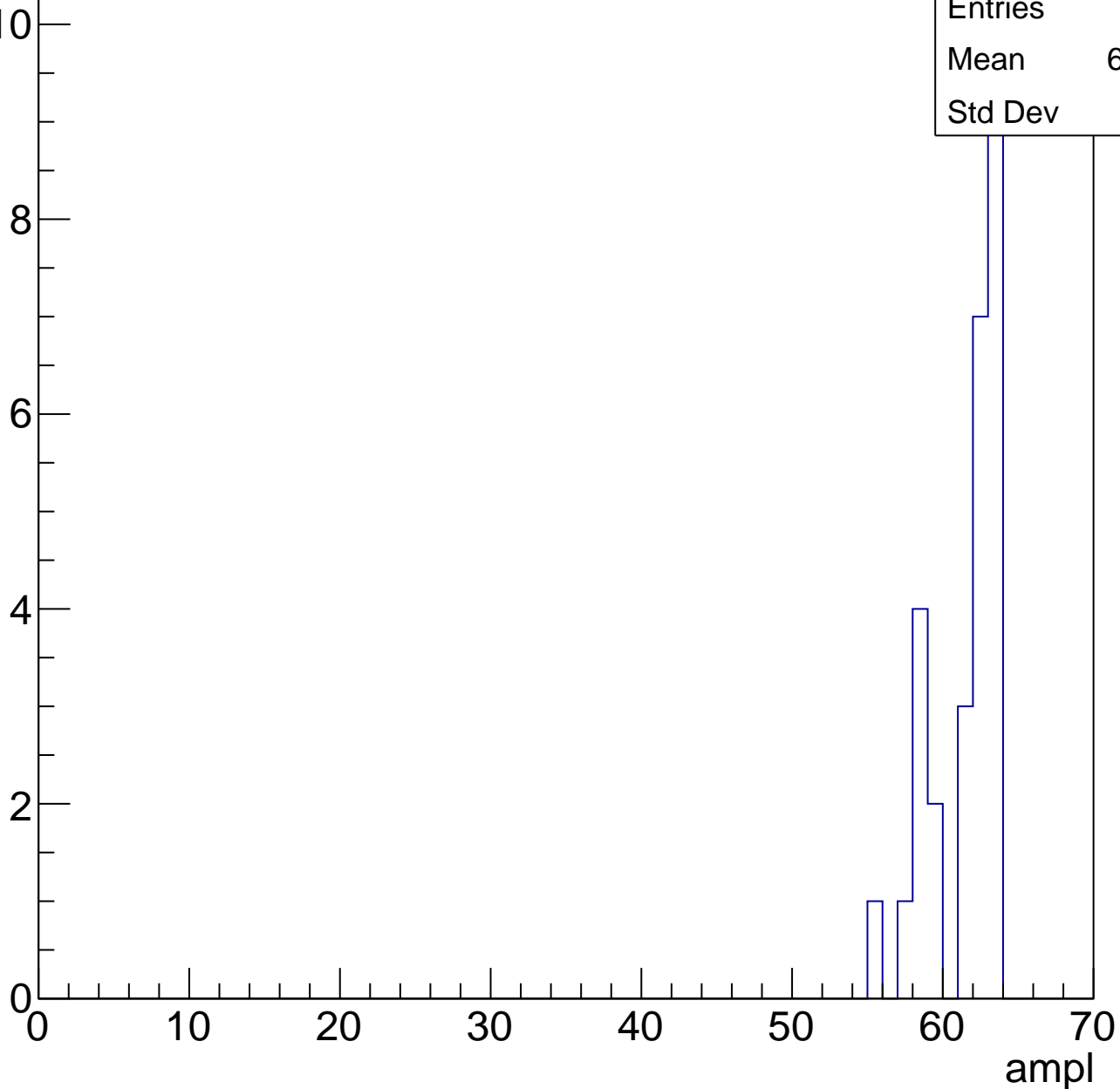


# B1L002S, U17-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	61.04
Std Dev	2.26





# B1L002S, U17-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U17-ch126, adc0

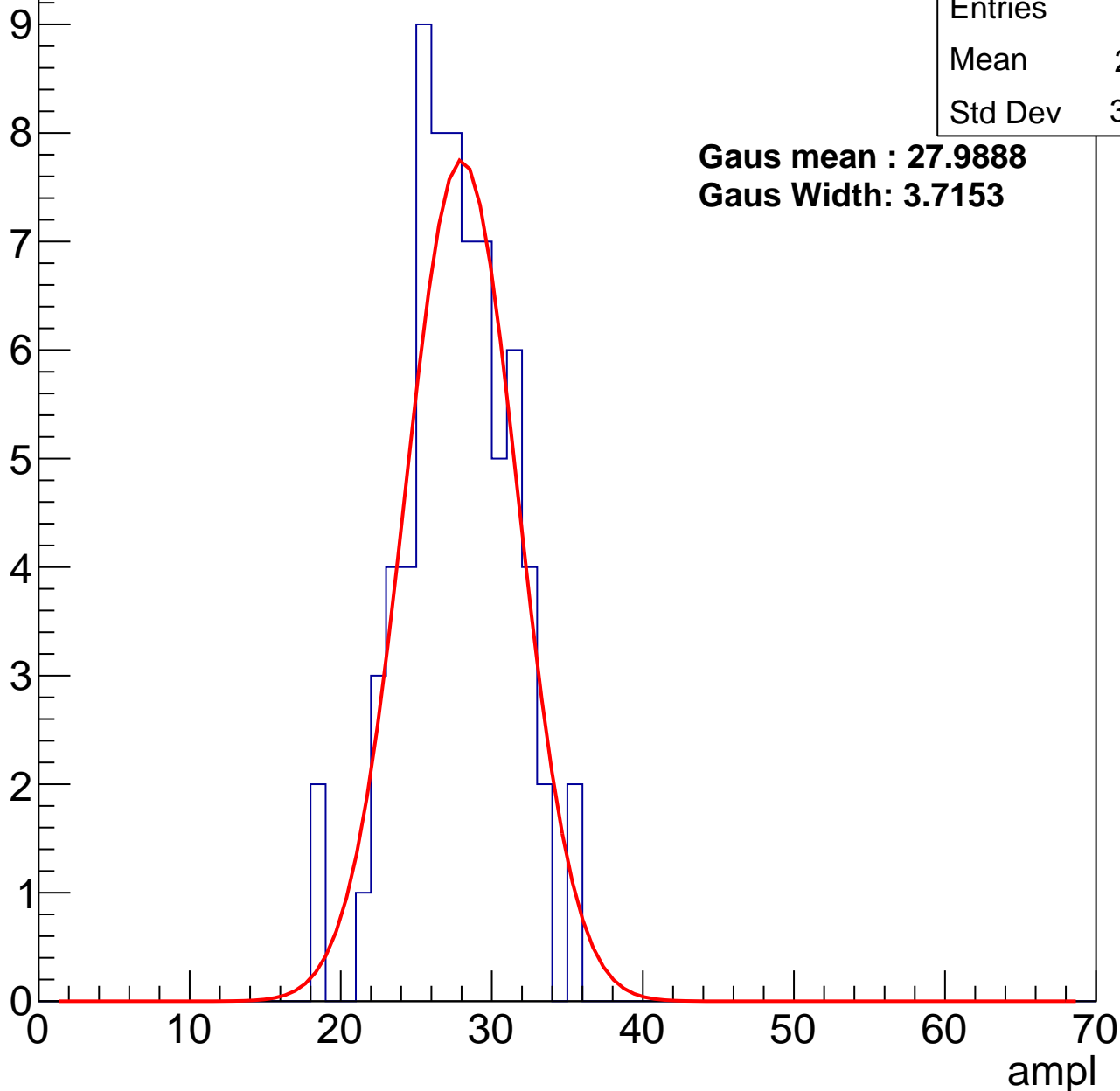
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.21
Std Dev	3.516

**Gaus mean : 27.9888**

**Gaus Width: 3.7153**



# B1L002S, U17-ch126, adc1

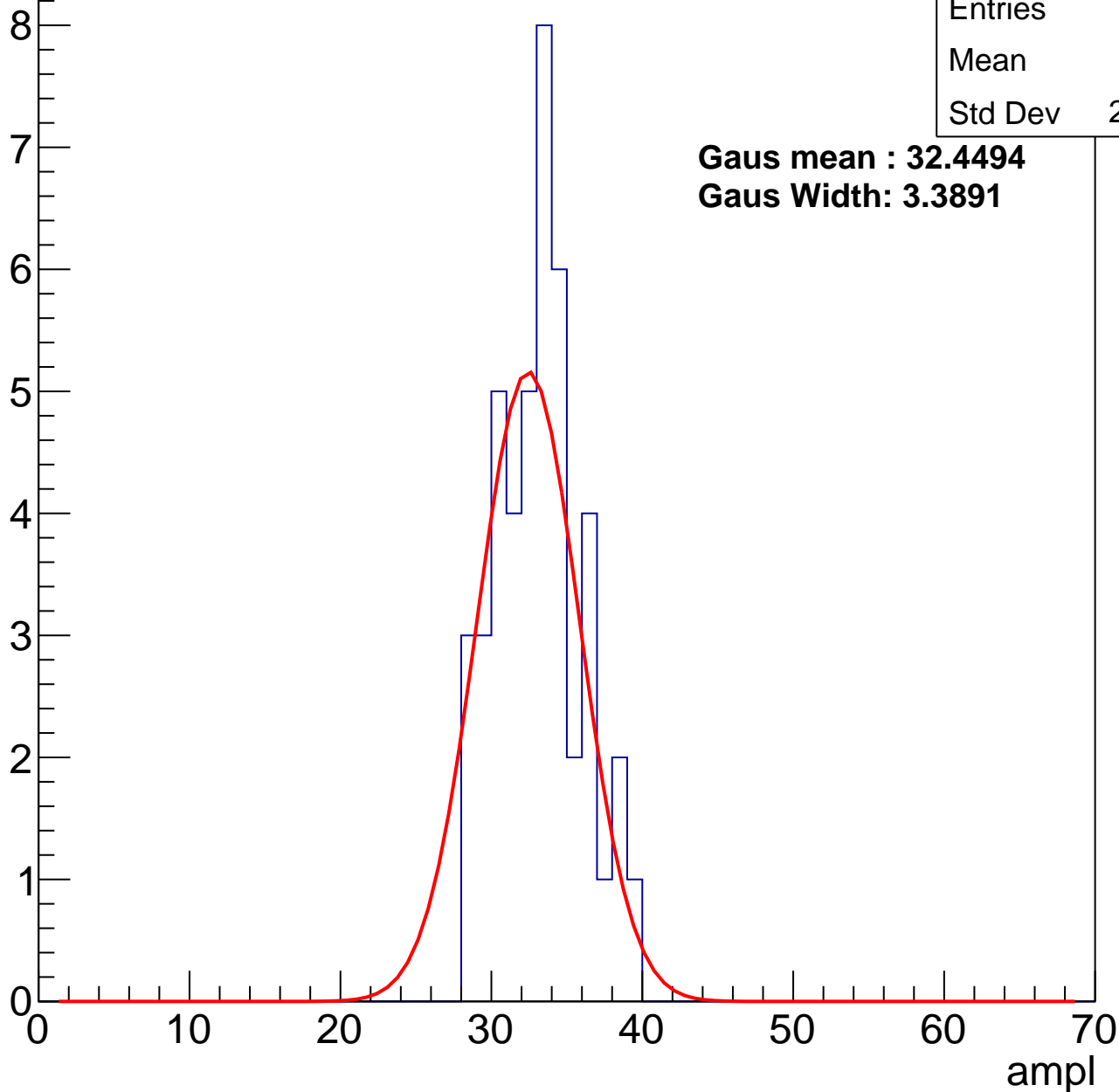
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	32.7
Std Dev	2.768

**Gaus mean : 32.4494**

**Gaus Width: 3.3891**



# B1L002S, U17-ch126, adc2

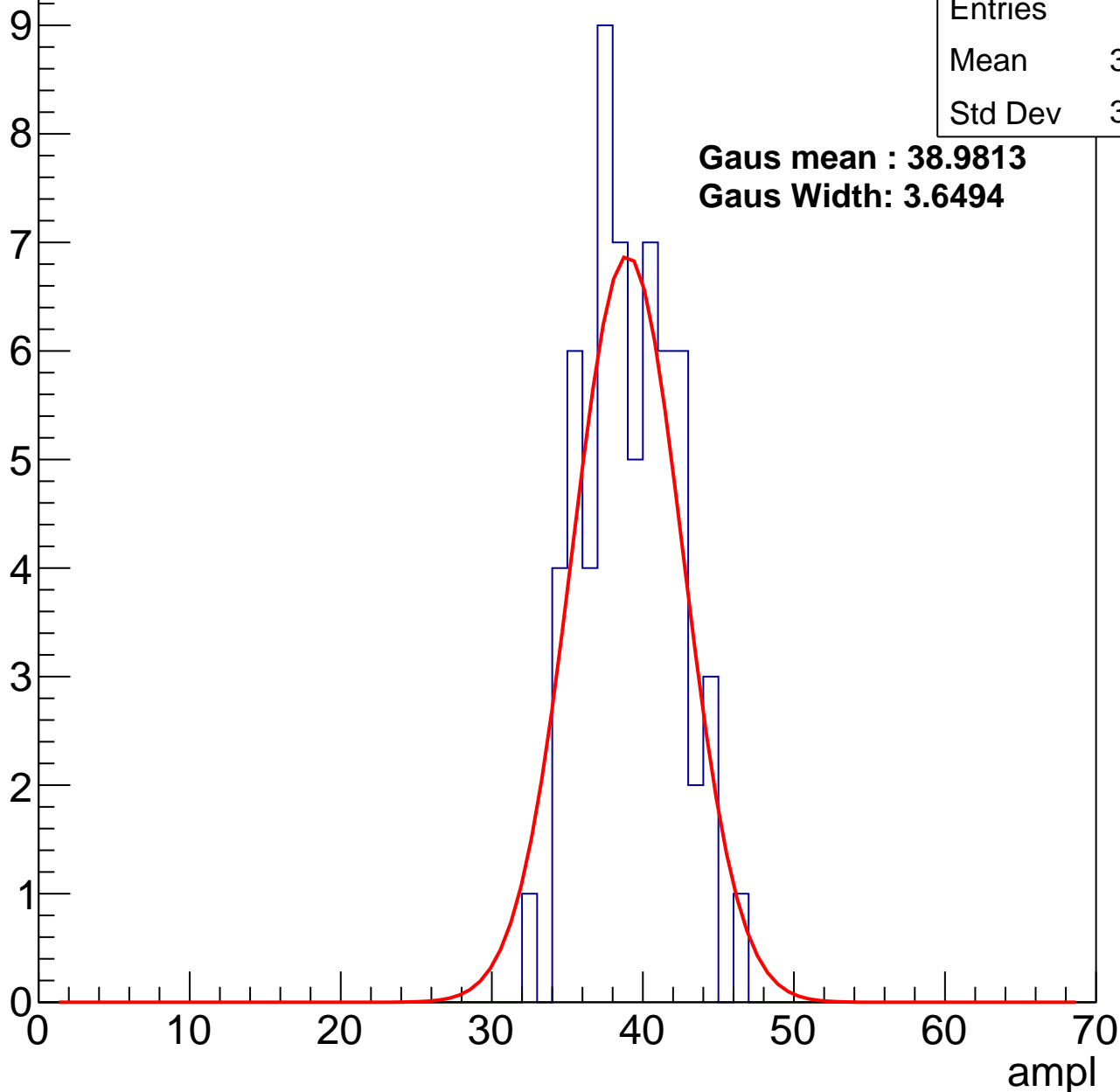
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	38.66
Std Dev	3.024

**Gaus mean : 38.9813**

**Gaus Width: 3.6494**

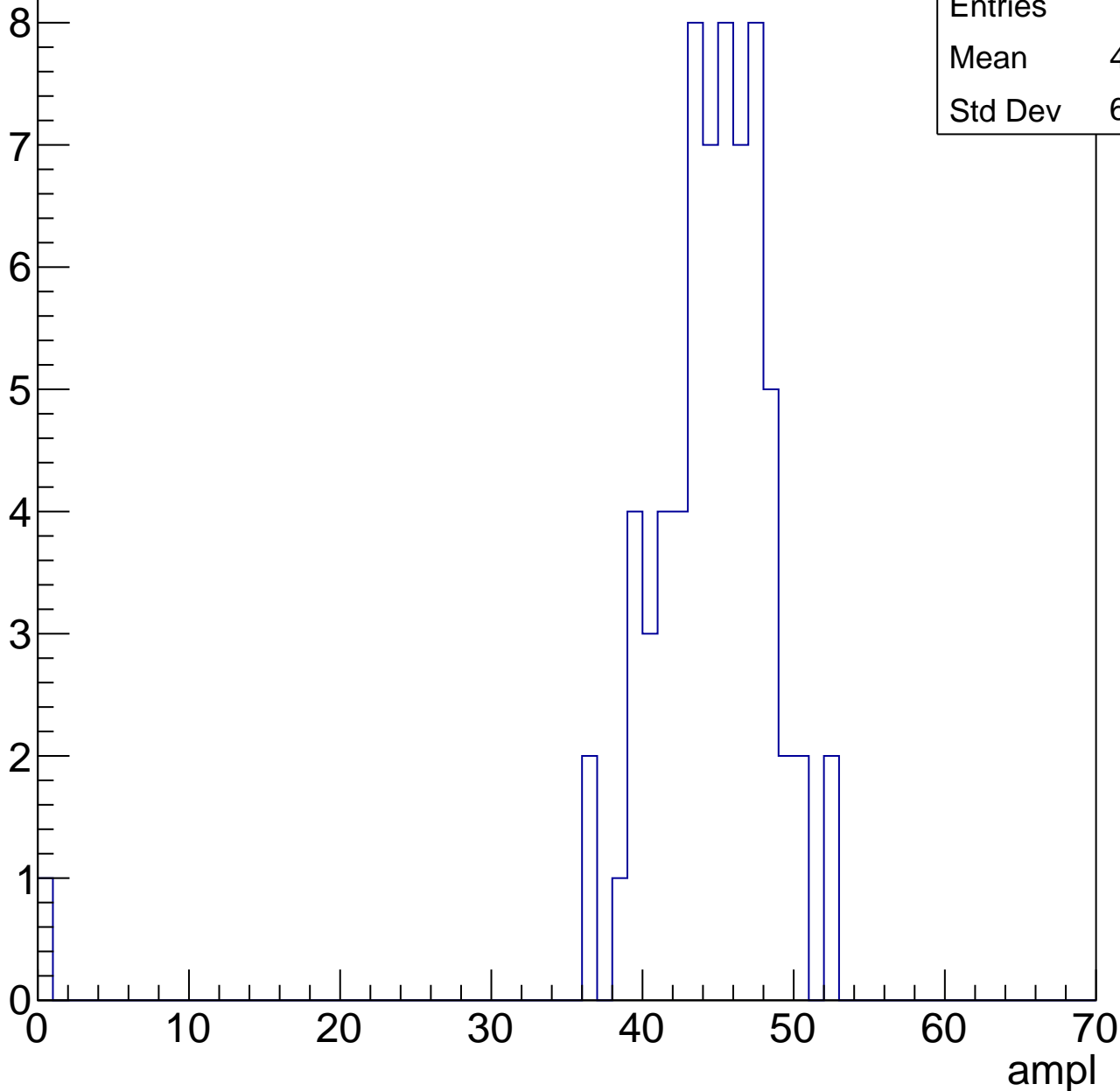


# B1L002S, U17-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	43.68
Std Dev	6.346

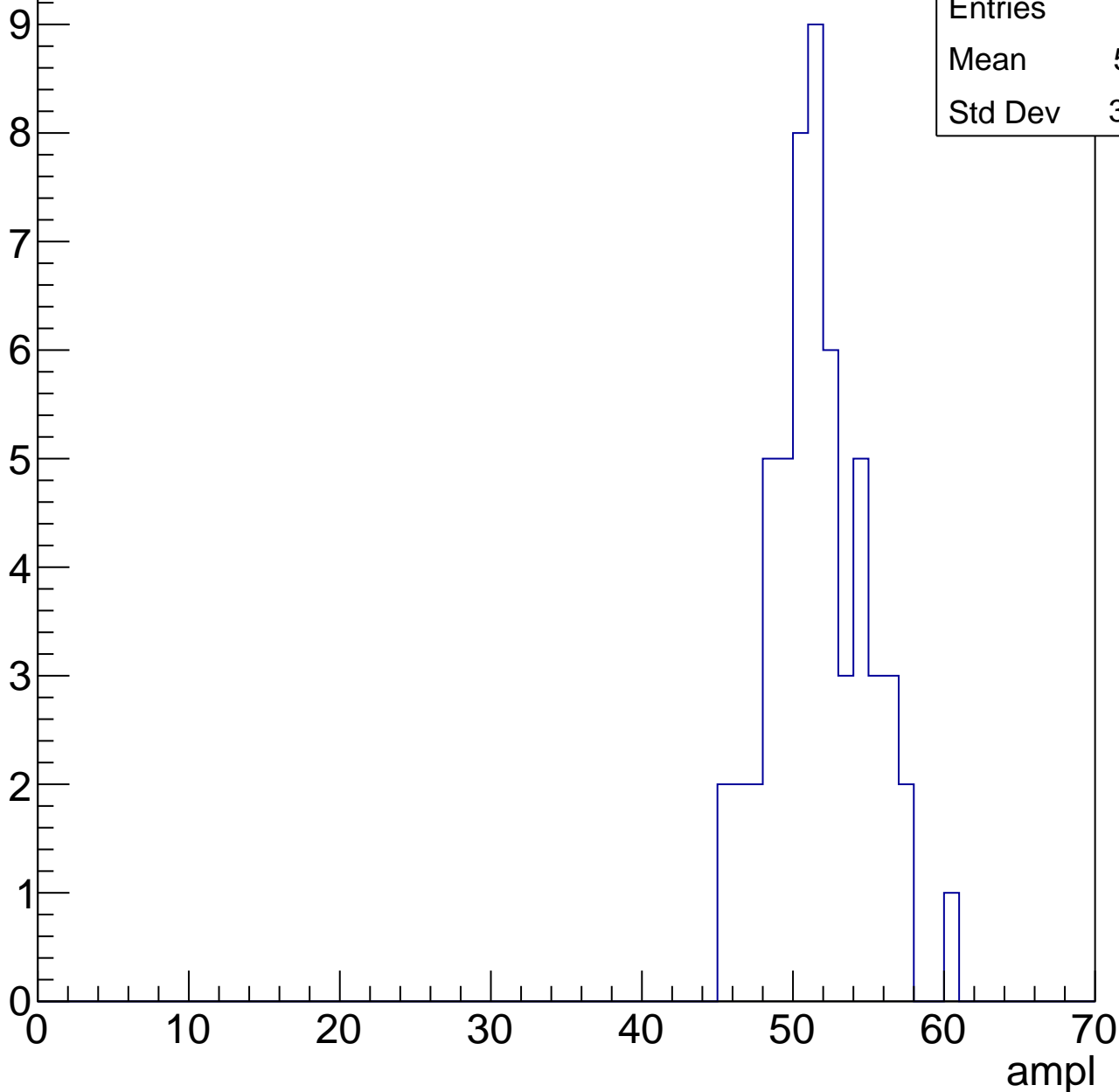


# B1L002S, U17-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	51.21
Std Dev	3.172

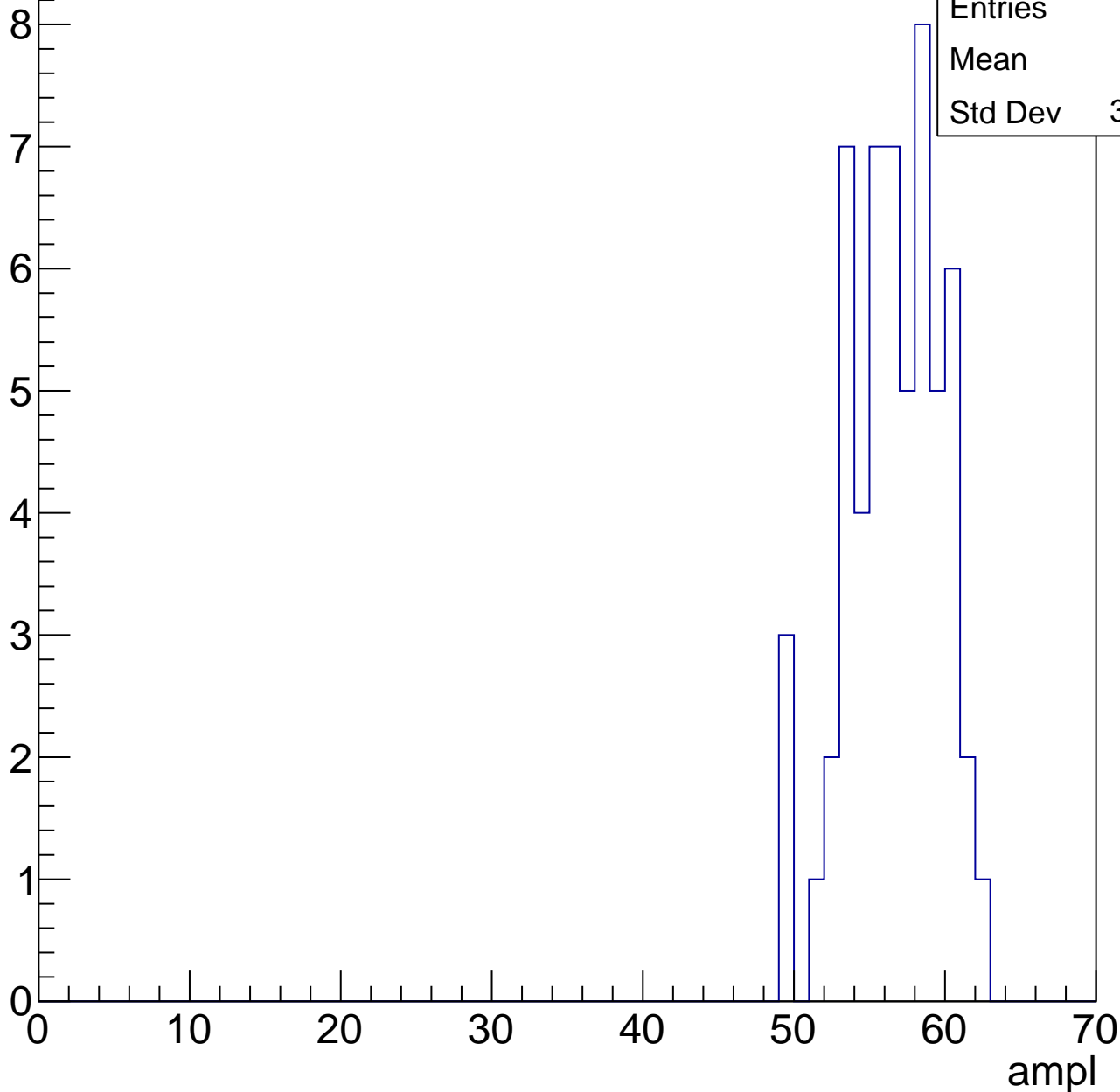


# B1L002S, U17-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.1
Std Dev	3.089

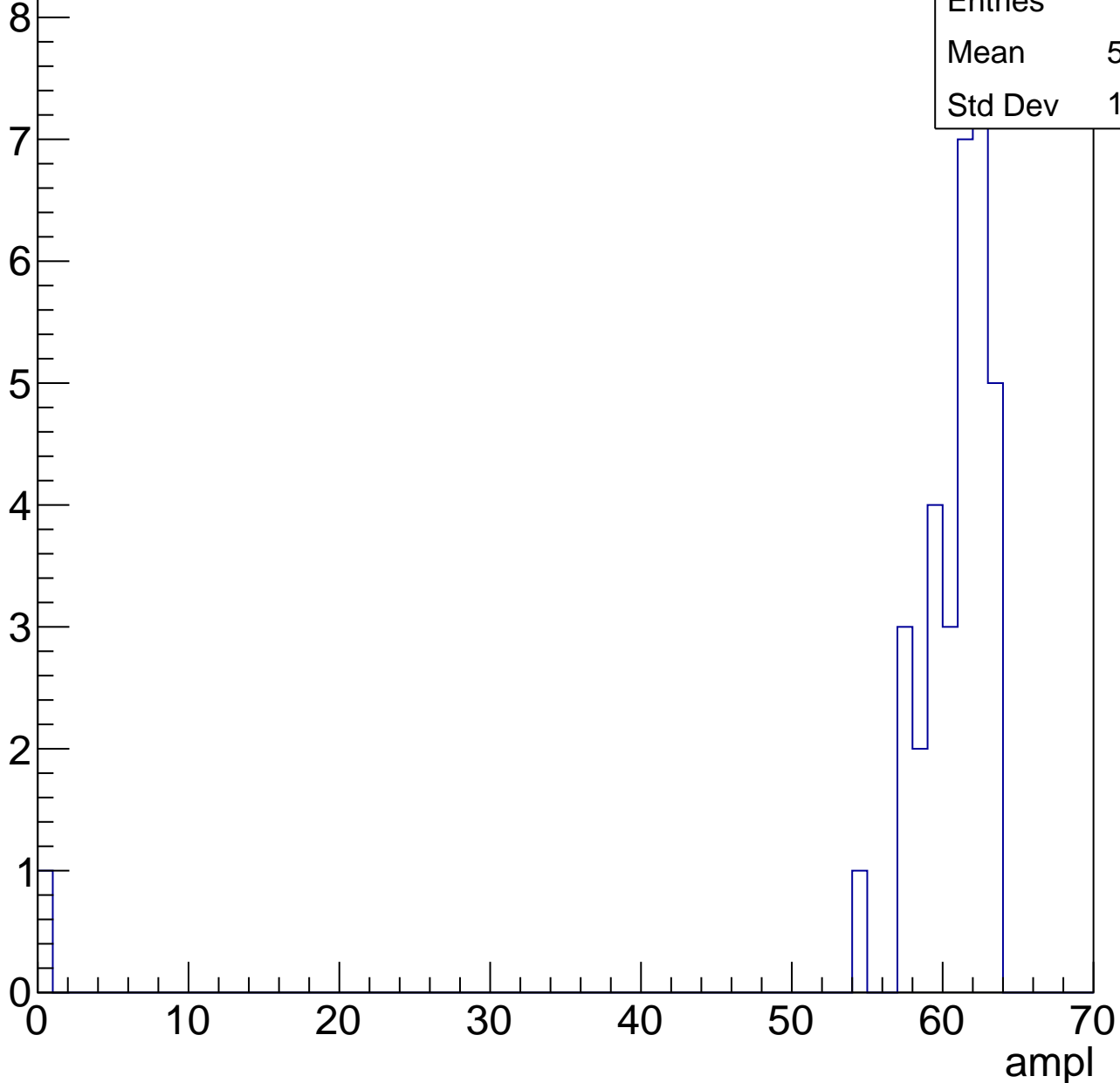


# B1L002S, U17-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	58.68
Std Dev	10.43



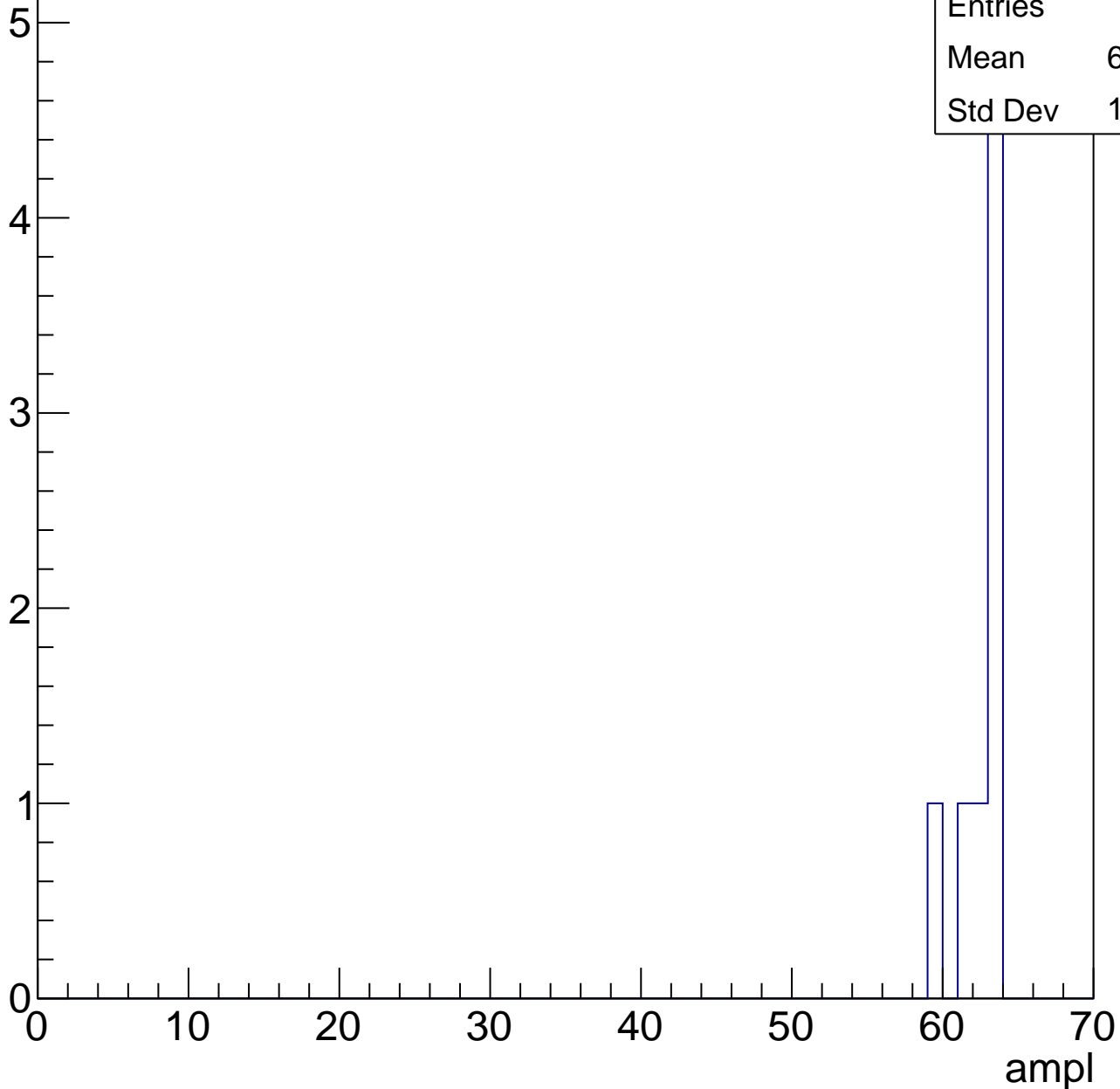


# B1L002S, U17-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	8
Mean	62.12
Std Dev	1.364



# B1L002S, U17-ch127, adc0

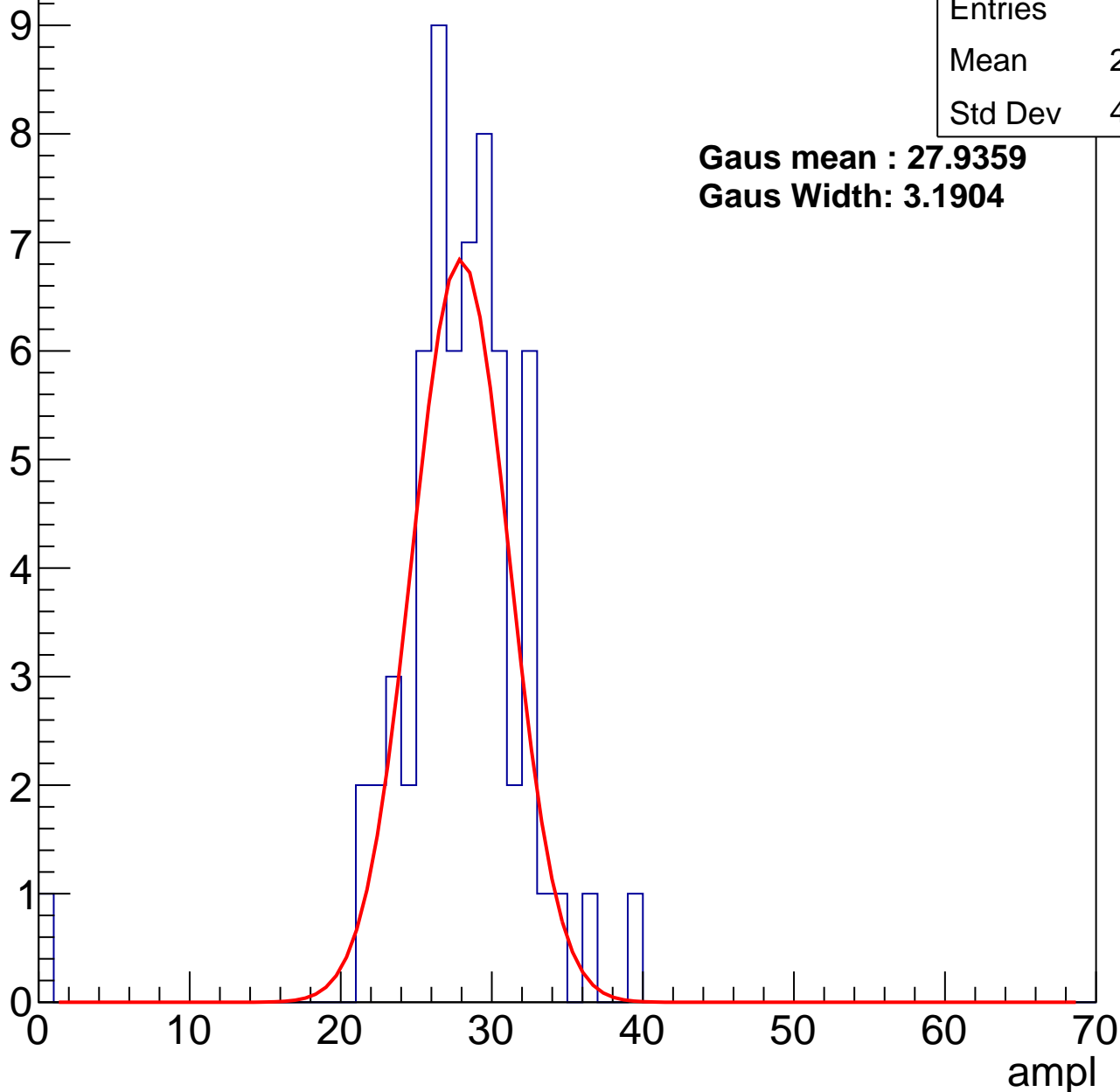
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.39
Std Dev	4.882

**Gaus mean : 27.9359**

**Gaus Width: 3.1904**



# B1L002S, U17-ch127, adc1

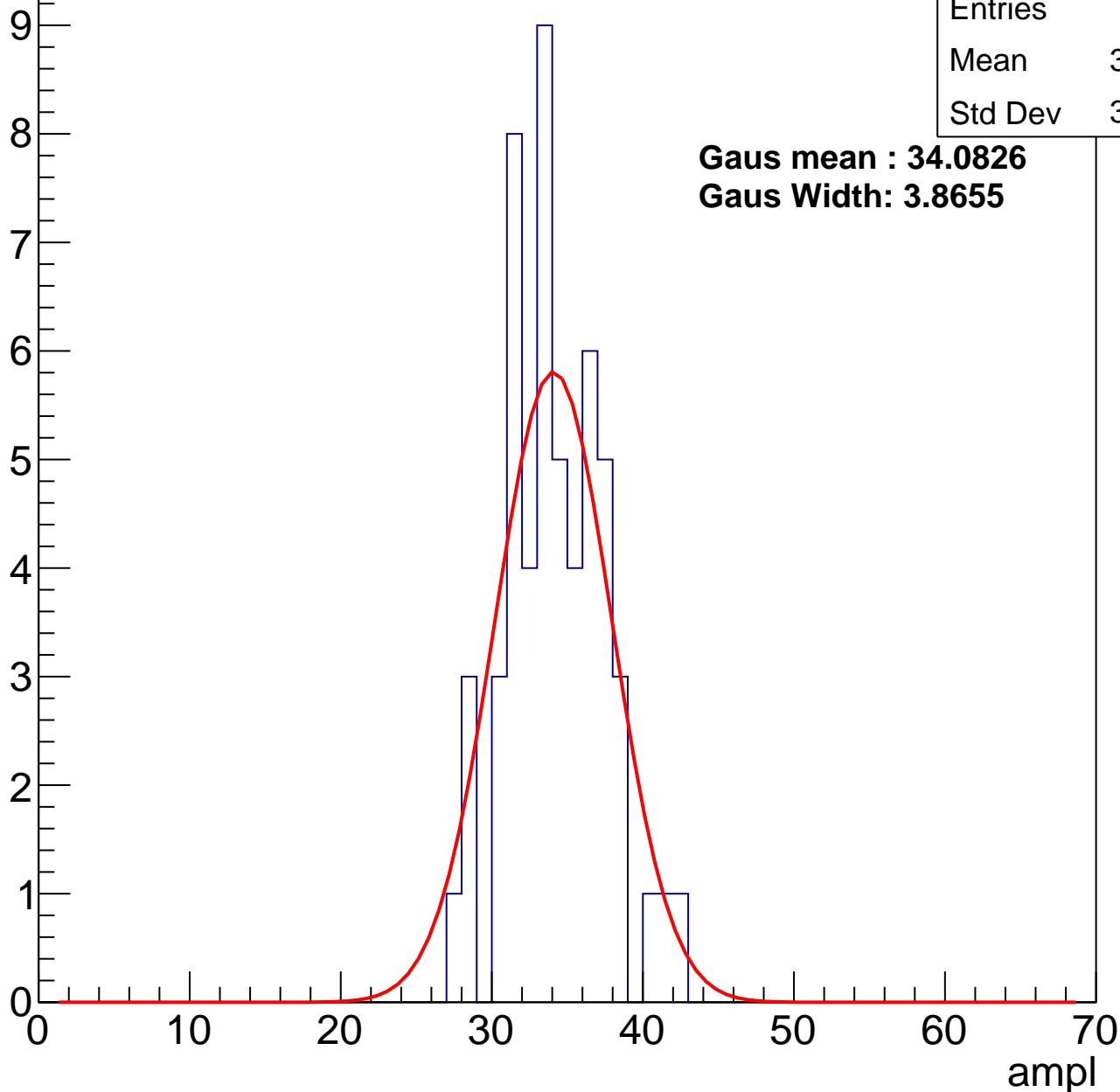
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	33.74
Std Dev	3.244

**Gaus mean : 34.0826**

**Gaus Width: 3.8655**



# B1L002S, U17-ch127, adc2

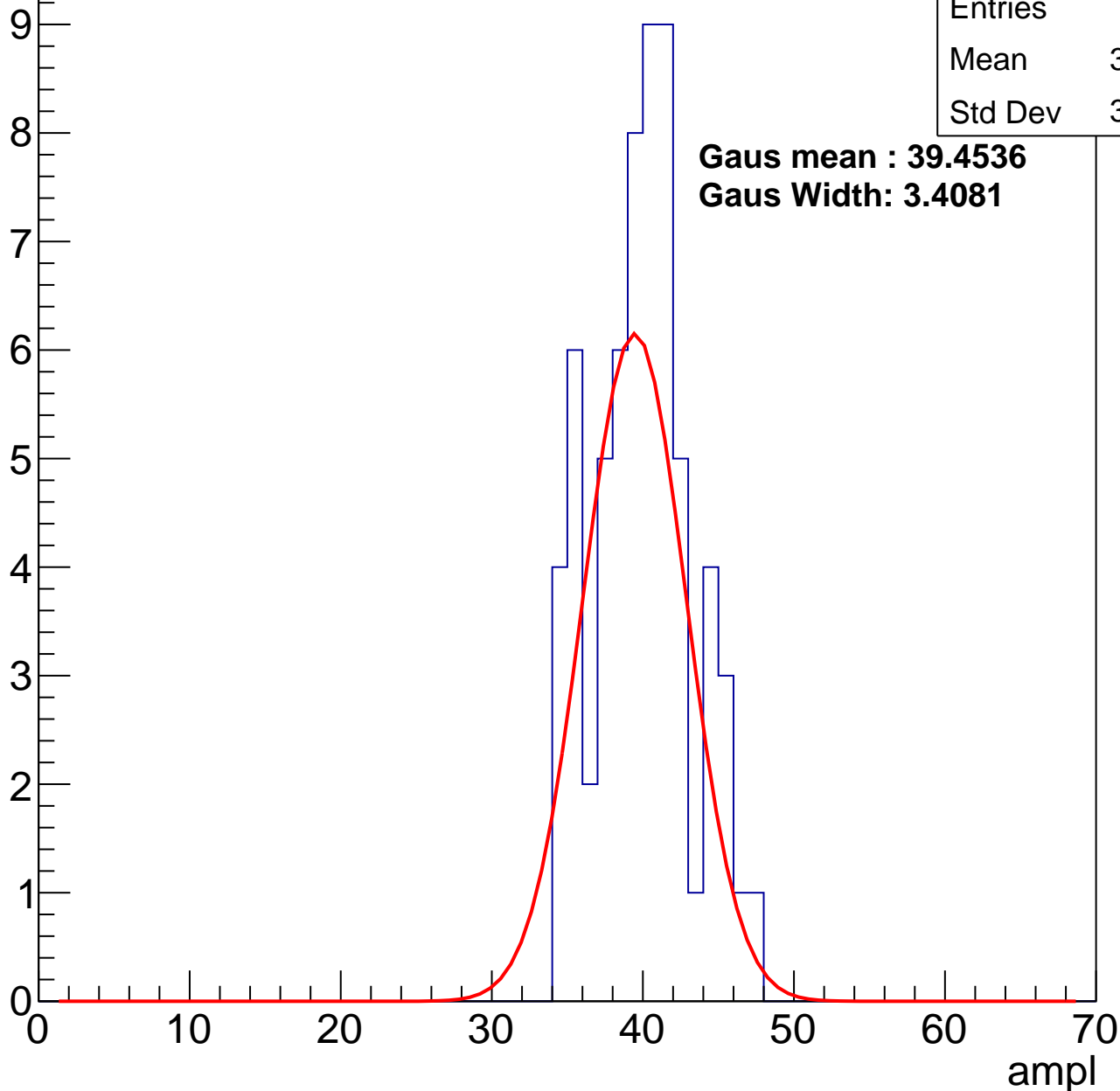
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	39.52
Std Dev	3.187

**Gaus mean : 39.4536**

**Gaus Width: 3.4081**

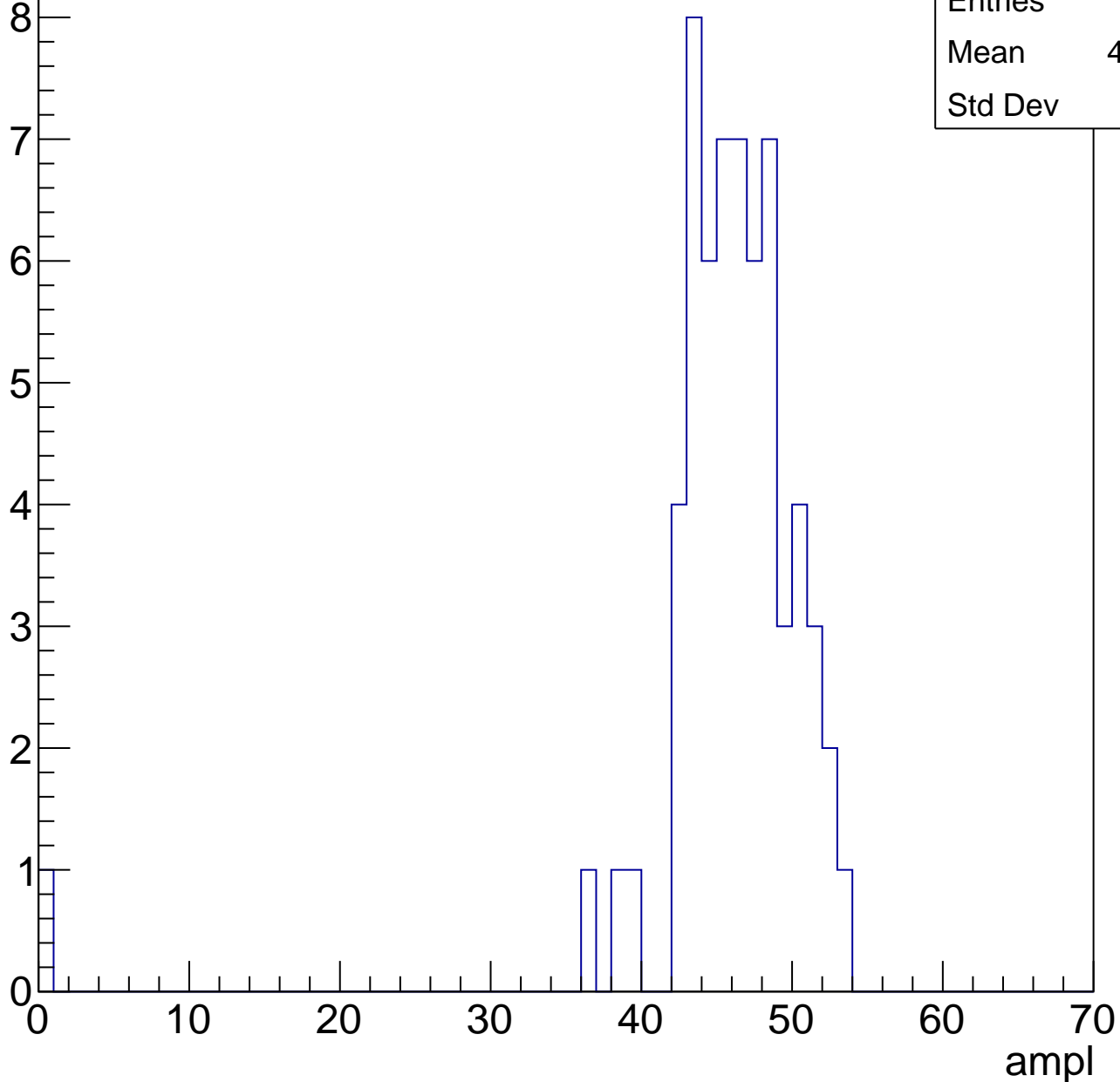


# B1L002S, U17-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	45.18
Std Dev	6.69

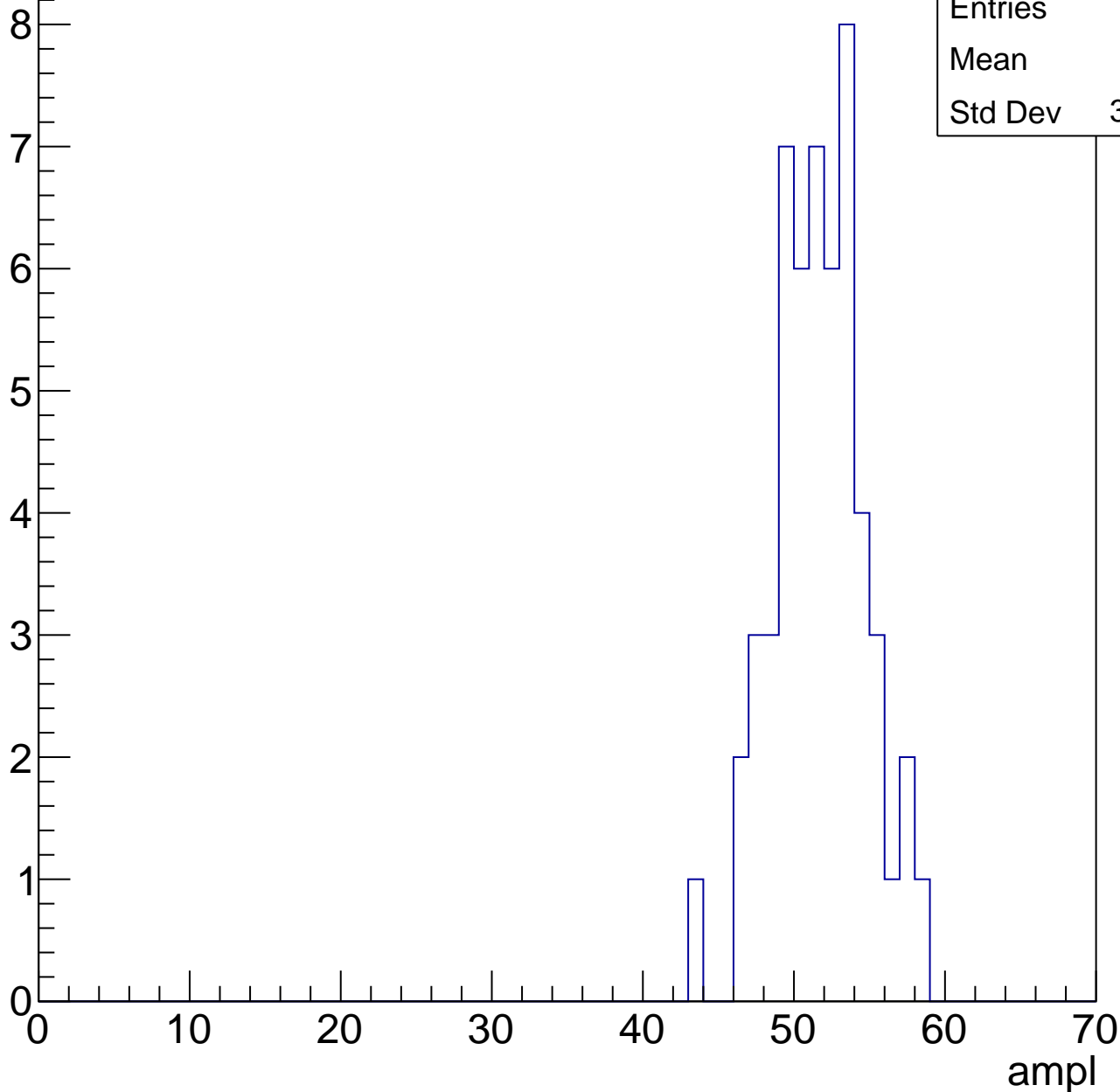


# B1L002S, U17-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.2
Std Dev	3.009

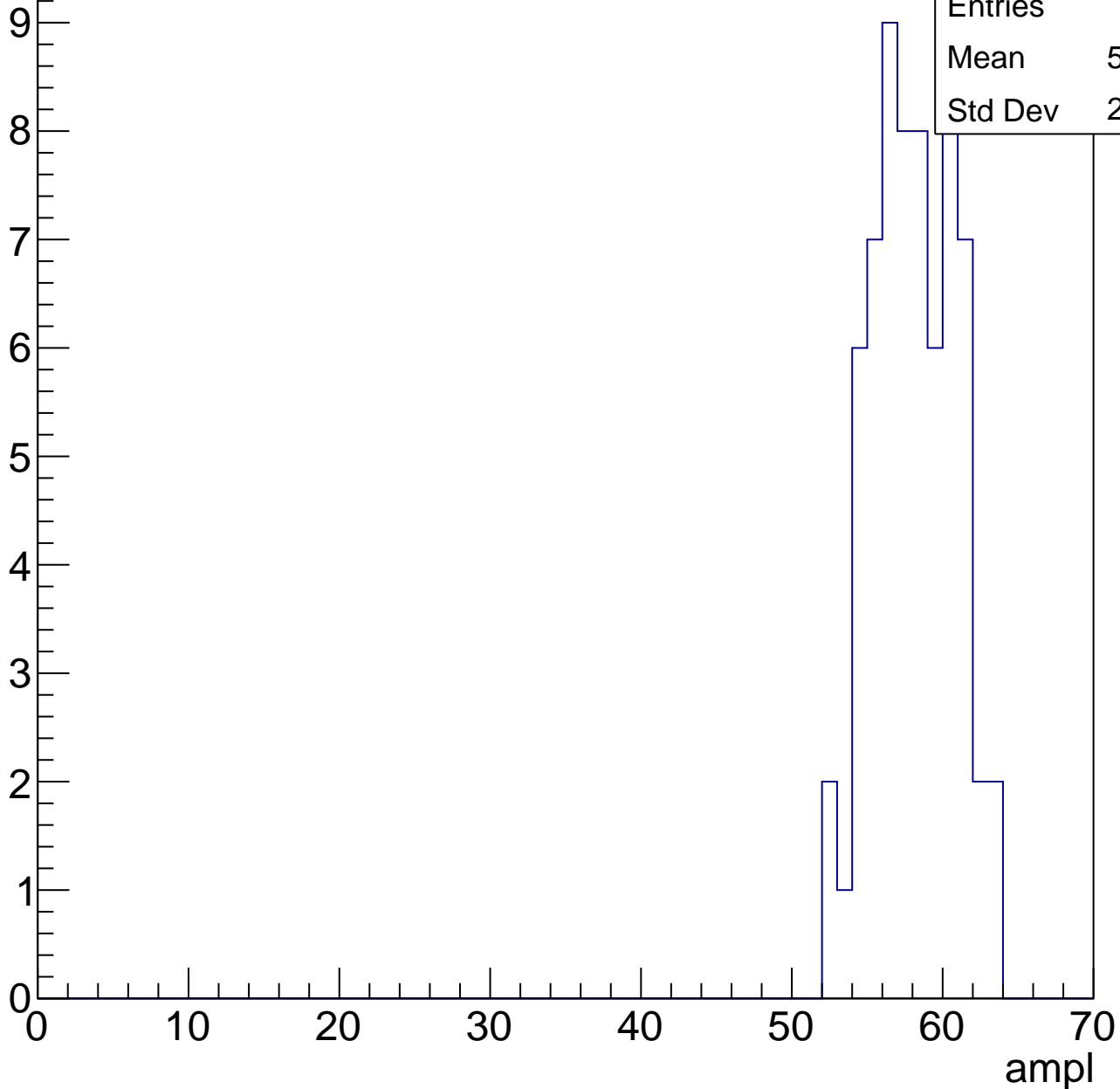


# B1L002S, U17-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.59
Std Dev	2.674

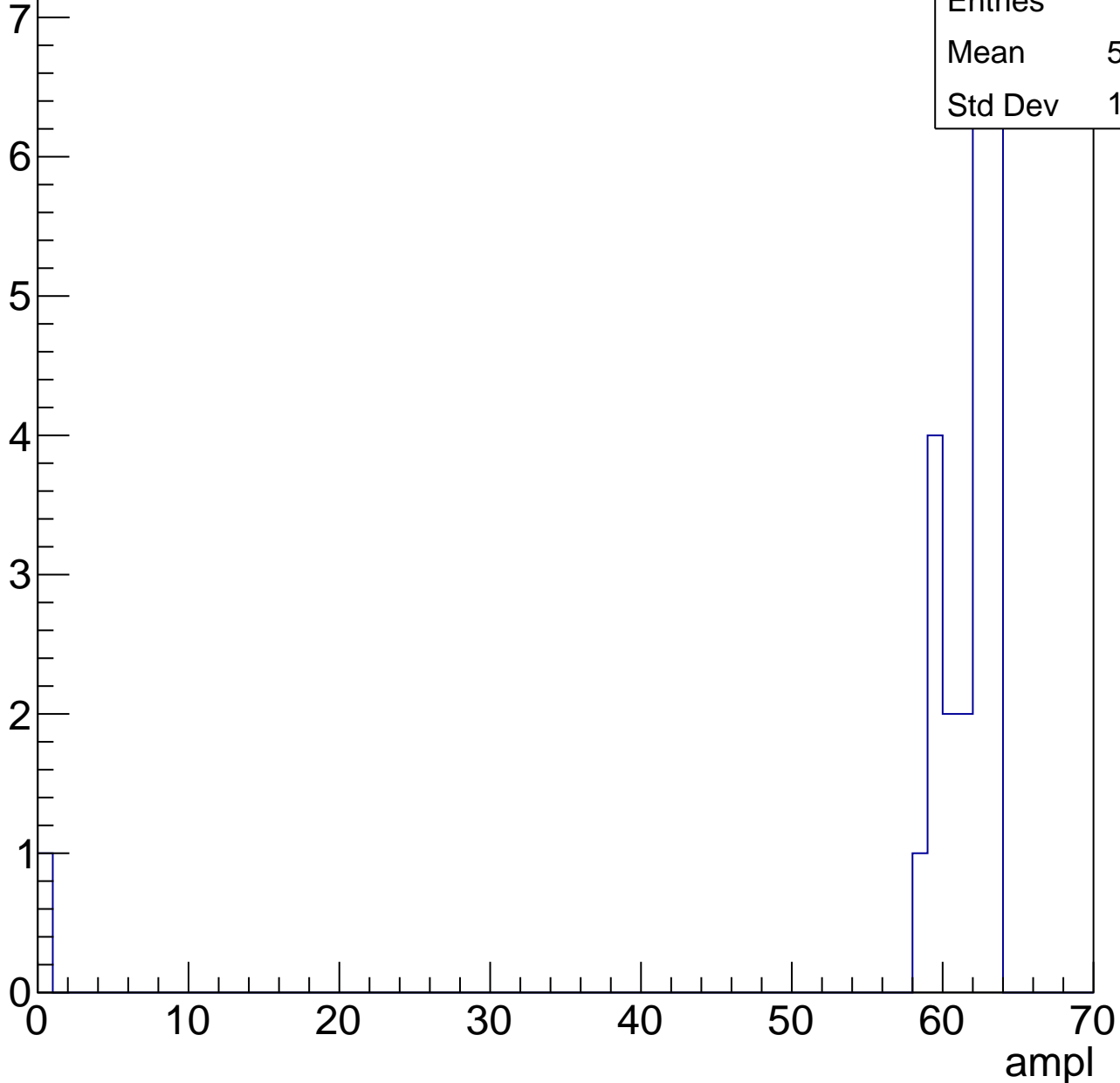


# B1L002S, U17-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.79
Std Dev	12.36





# B1L002S, U17-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

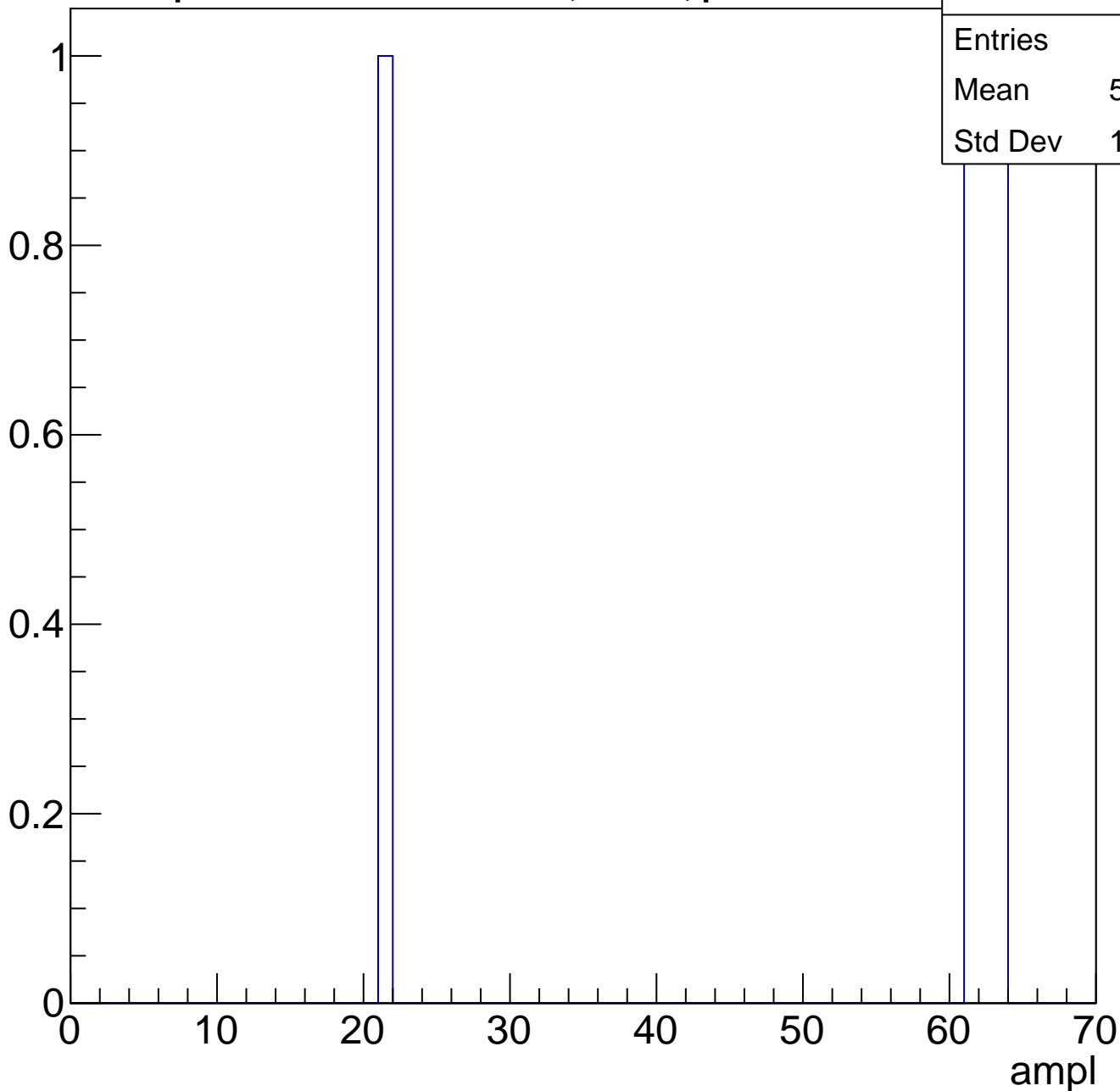
Entries	4
Mean	51.75
Std Dev	17.77

ampl

# B1L002S, U17-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	51.75
Std Dev	17.77