

B1L100S, U22-ch0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.93
Std Dev	11.73

Turn on : 25.6750

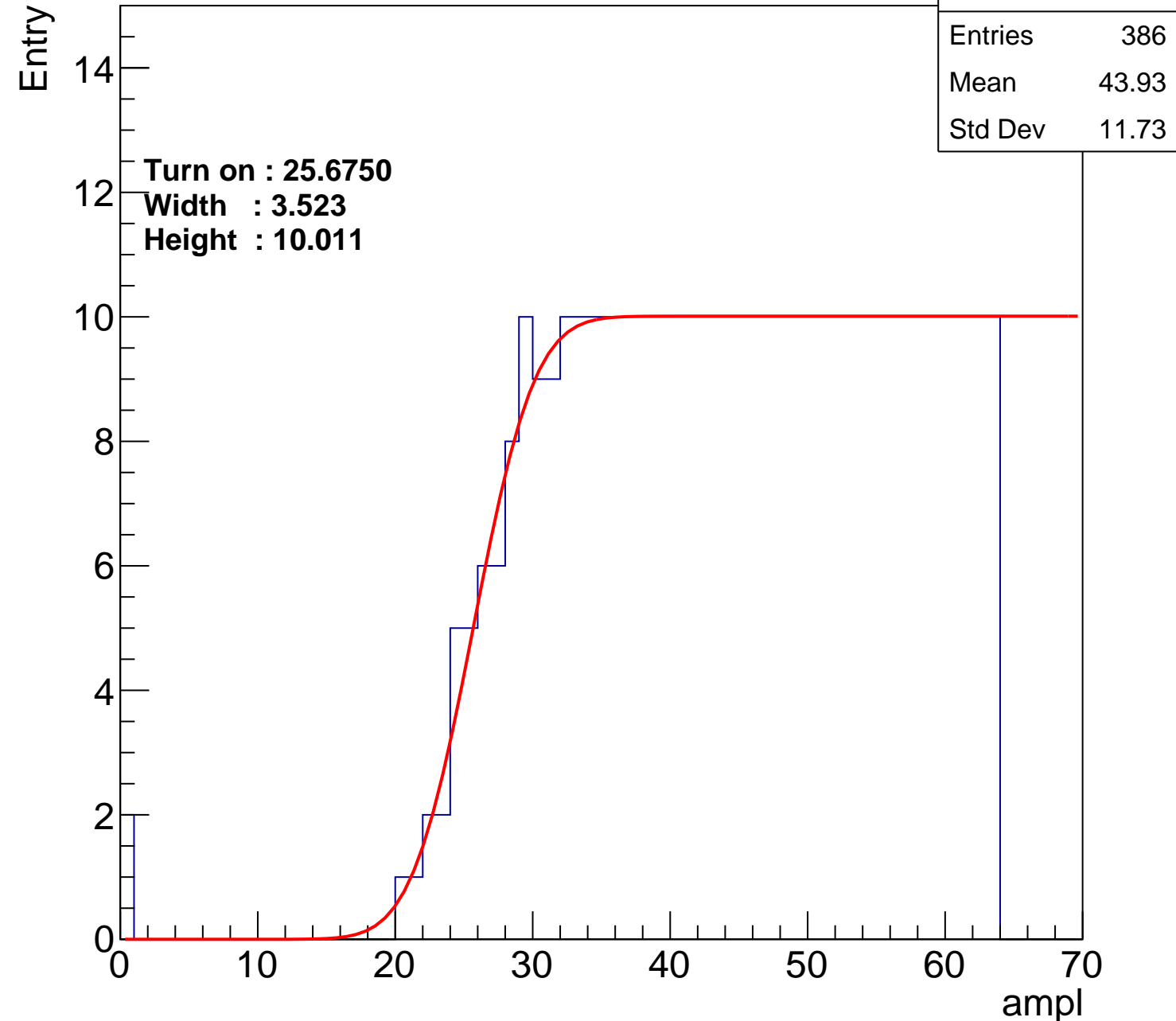
Width : 3.523

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.2035

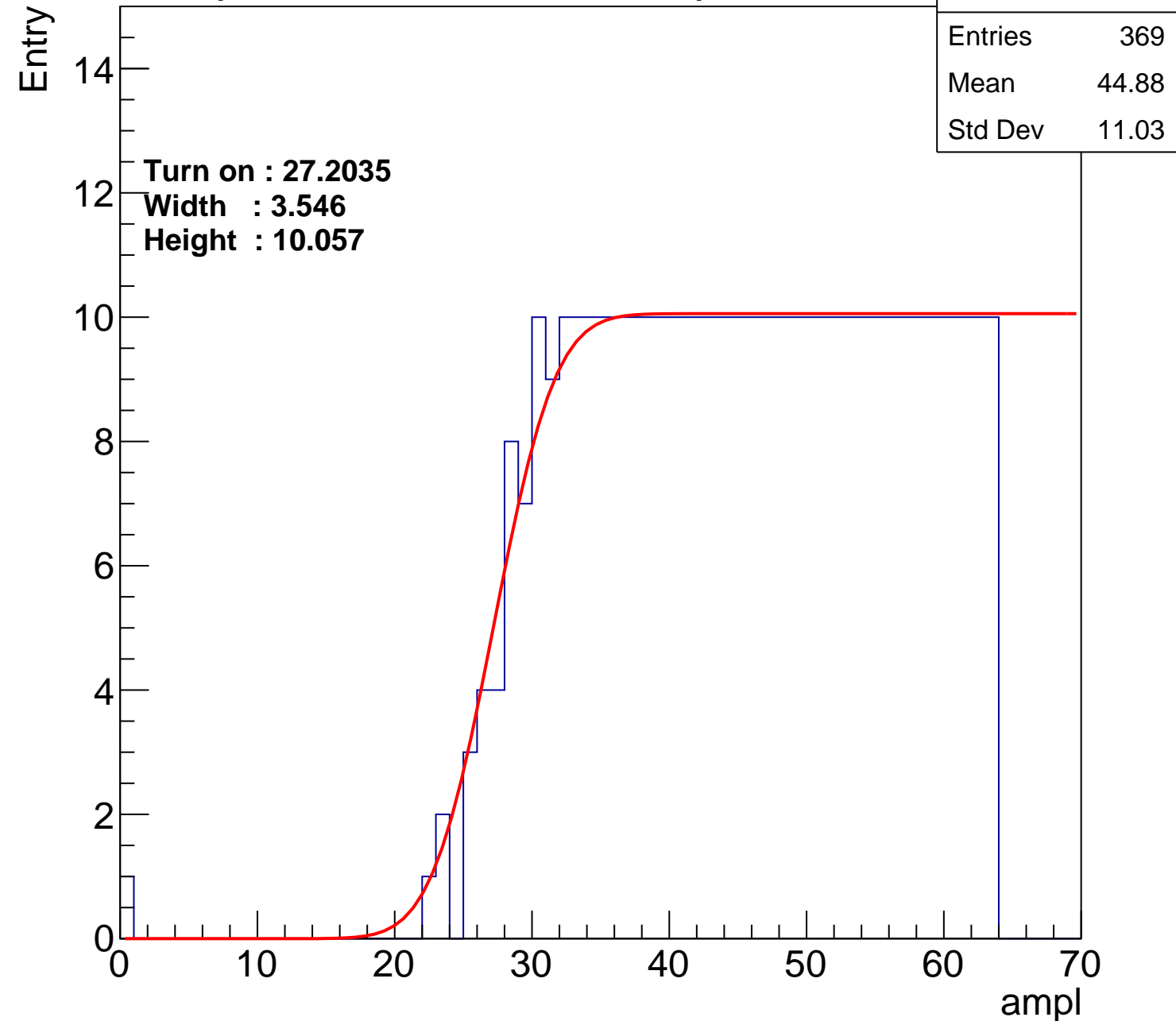
Width : 3.546

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.13
Std Dev	11.79

Turn on : 26.7719

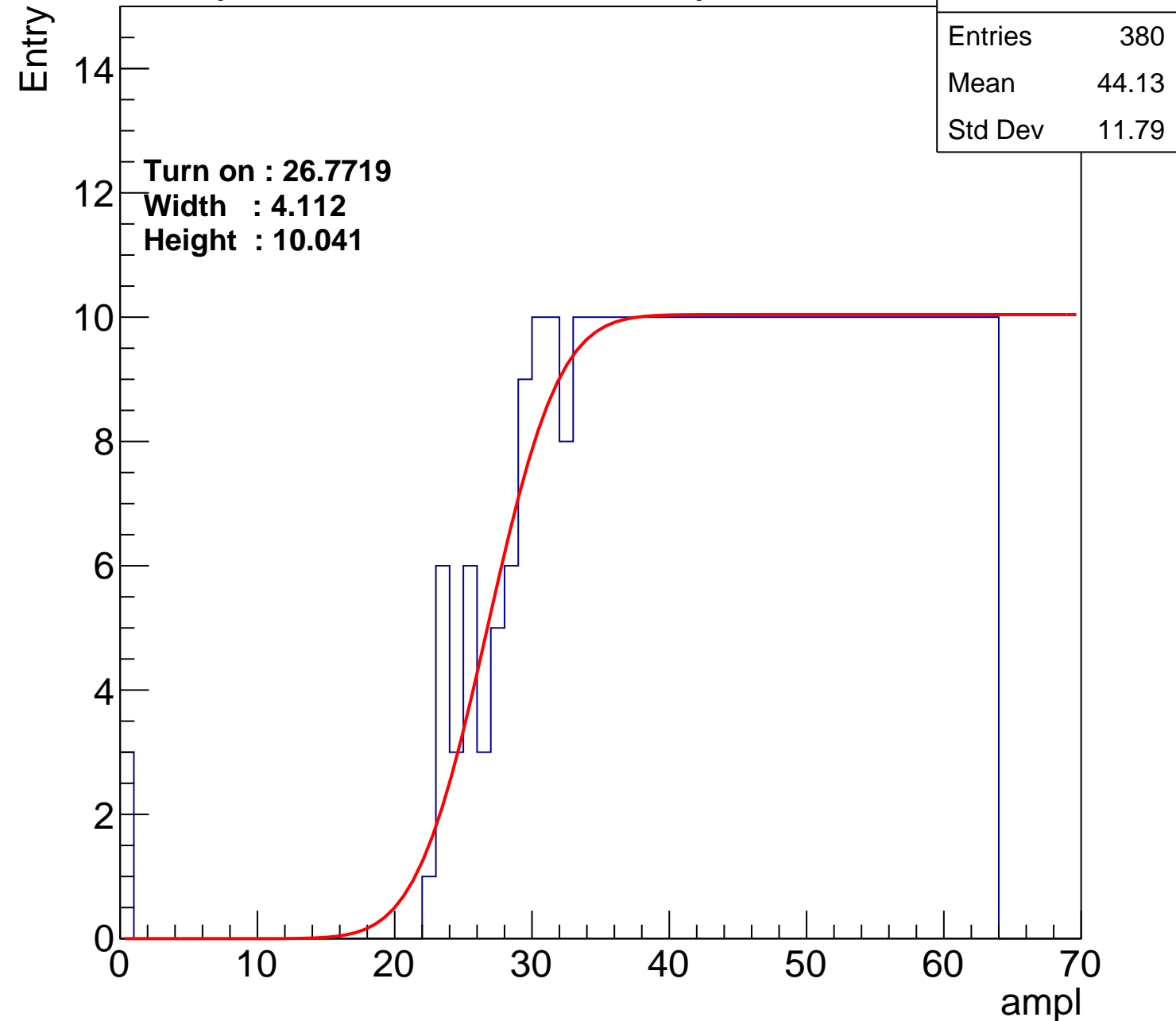
Width : 4.112

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch3

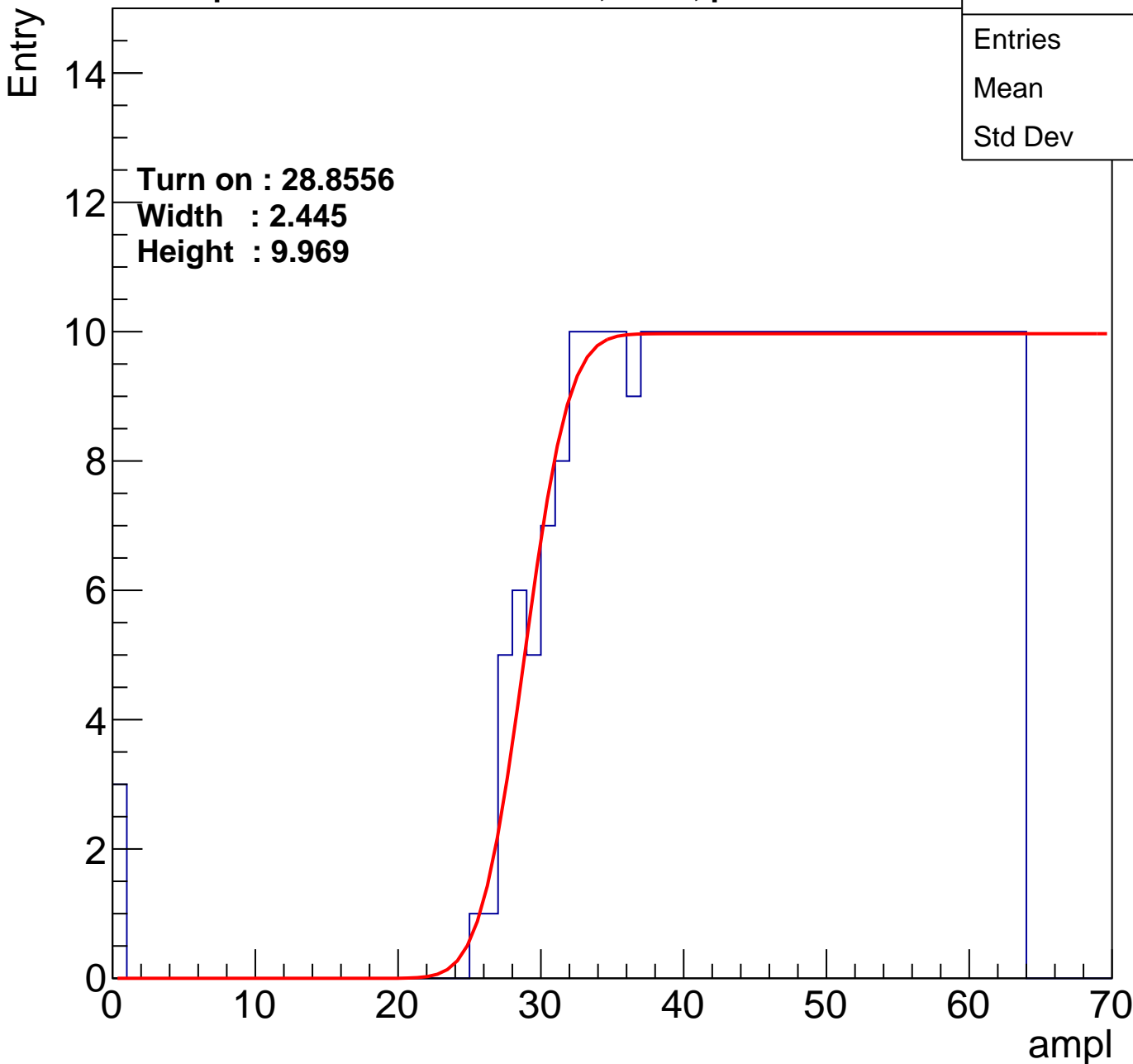
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 28.8556

Width : 2.445

Height : 9.969

Entries	355
Mean	45.41
Std Dev	11.1



B1L100S, U22-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.63
Std Dev	11.2

Turn on : 26.9242

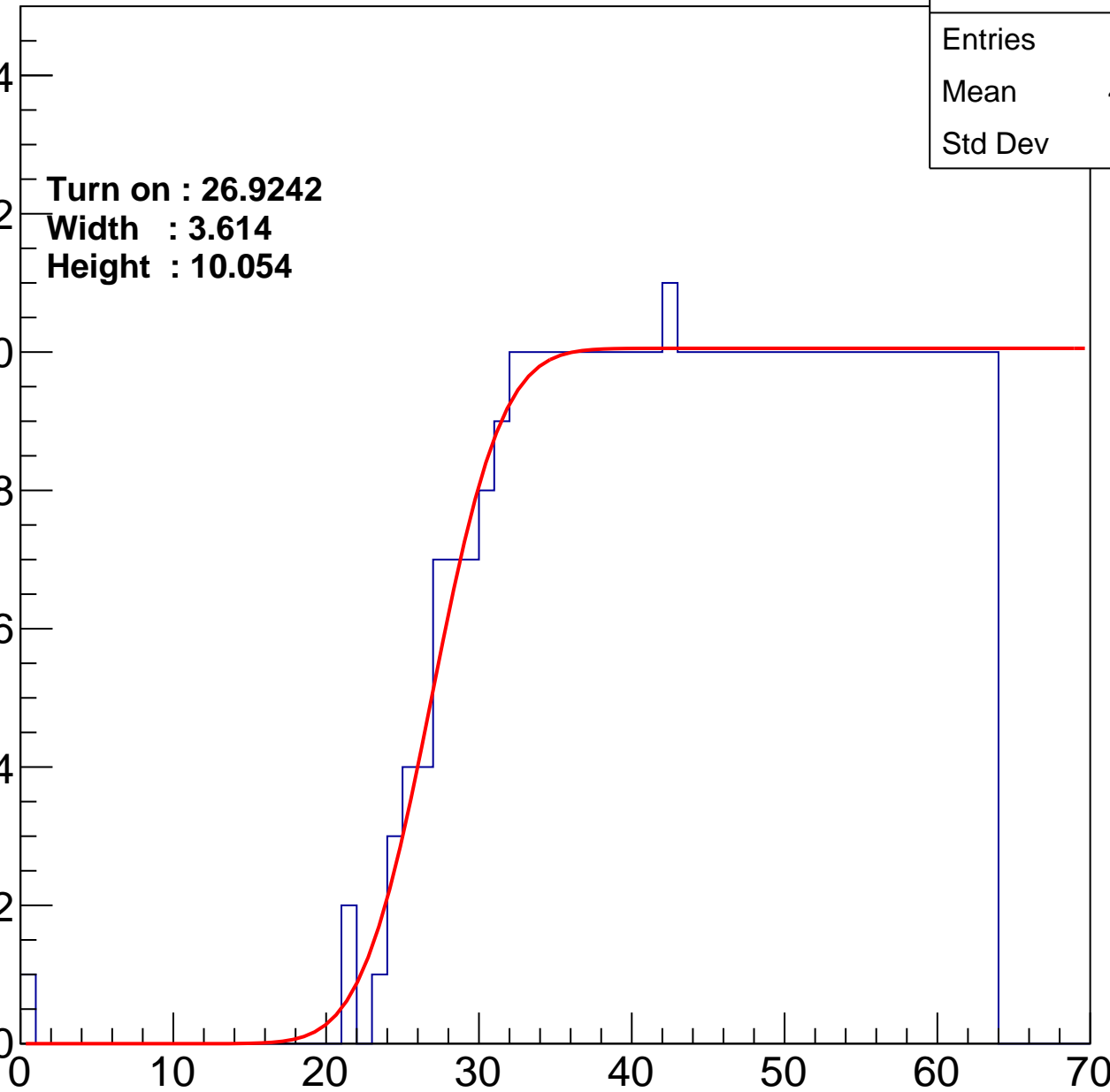
Width : 3.614

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch5

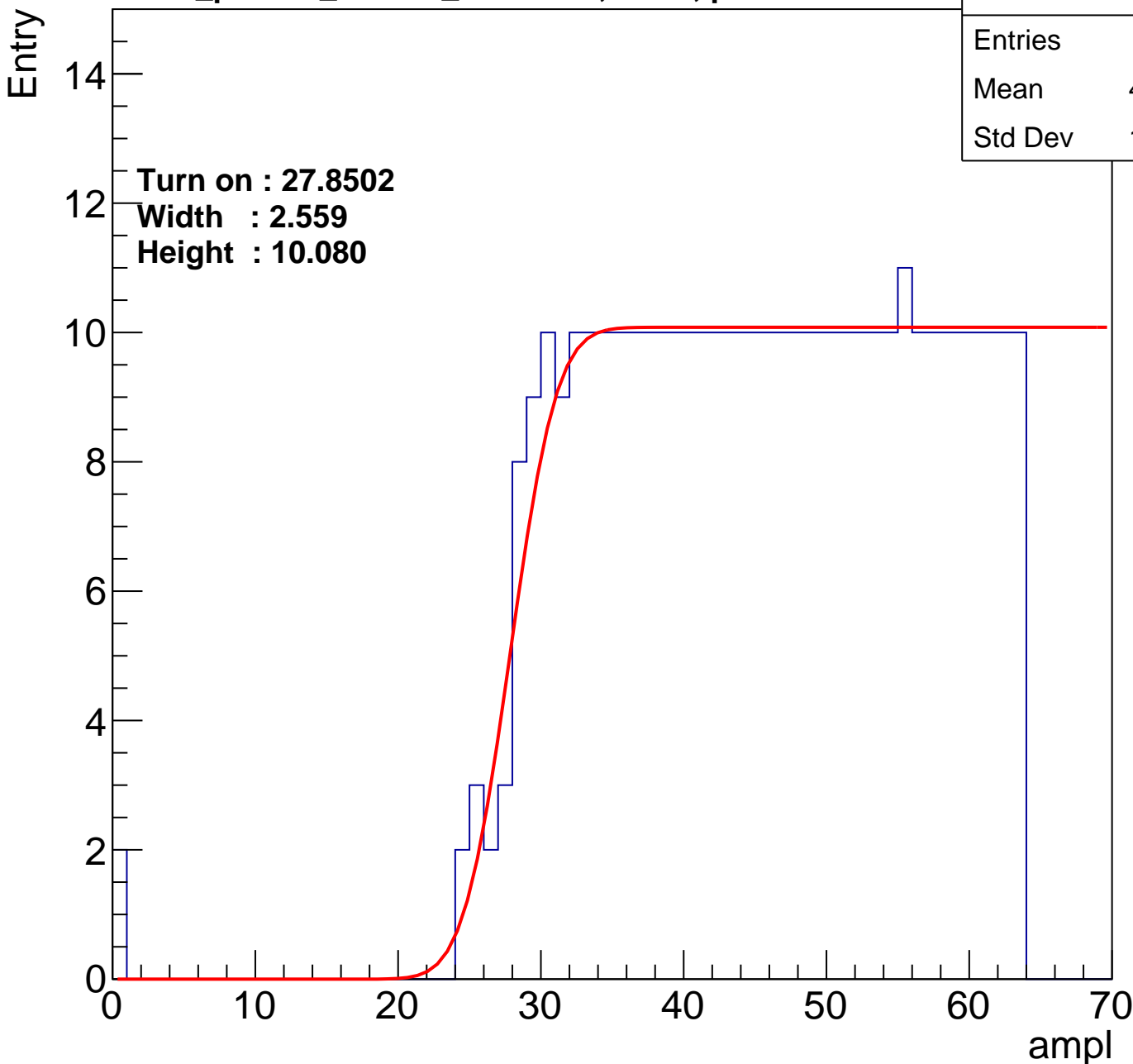
calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.92
Std Dev	11.14

Turn on : 27.8502

Width : 2.559

Height : 10.080



B1L100S, U22-ch6

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.29
Std Dev	10.96

Turn on : 28.5138

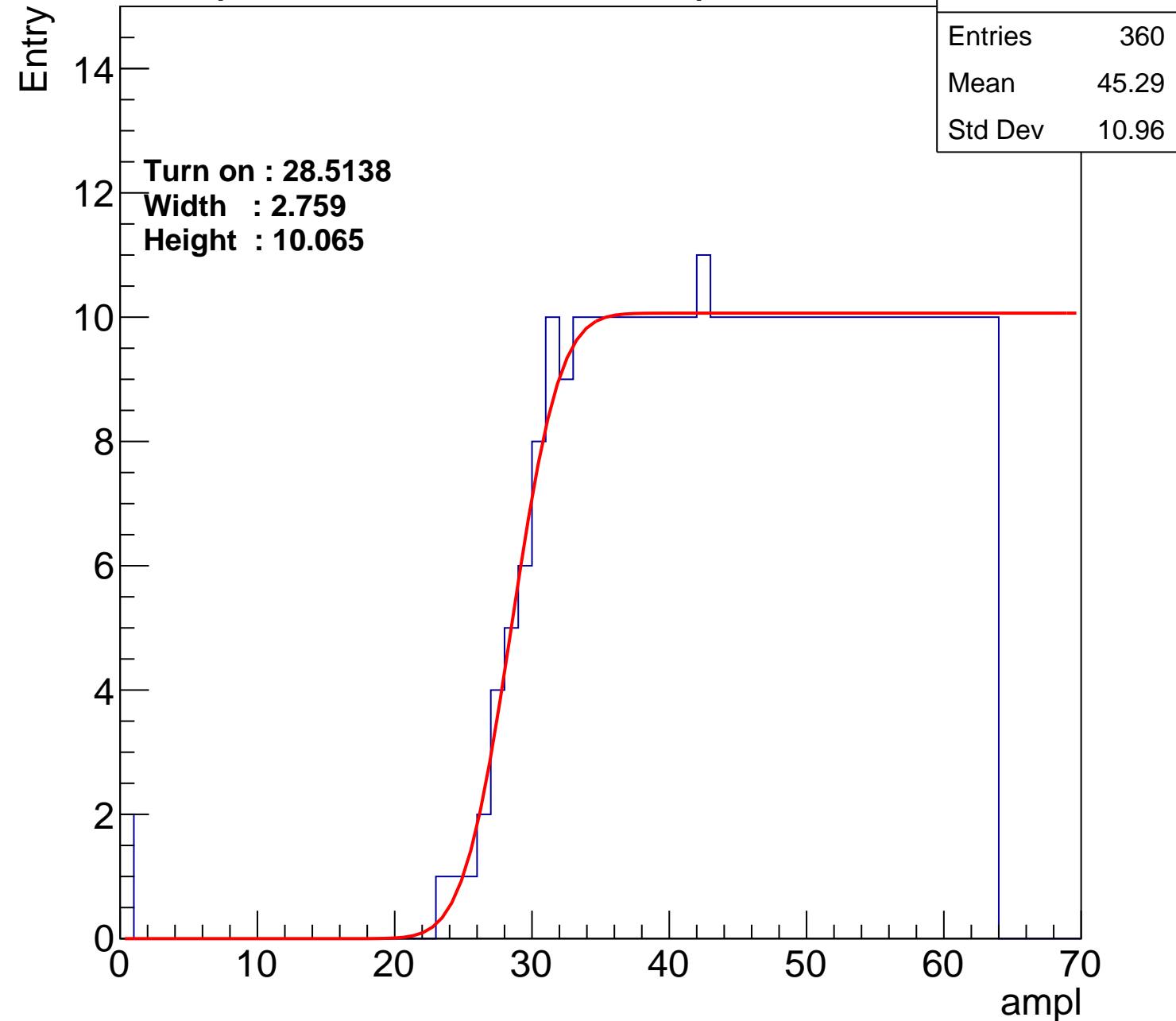
Width : 2.759

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch7

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.27
Std Dev	10.77

Turn on : 27.8324

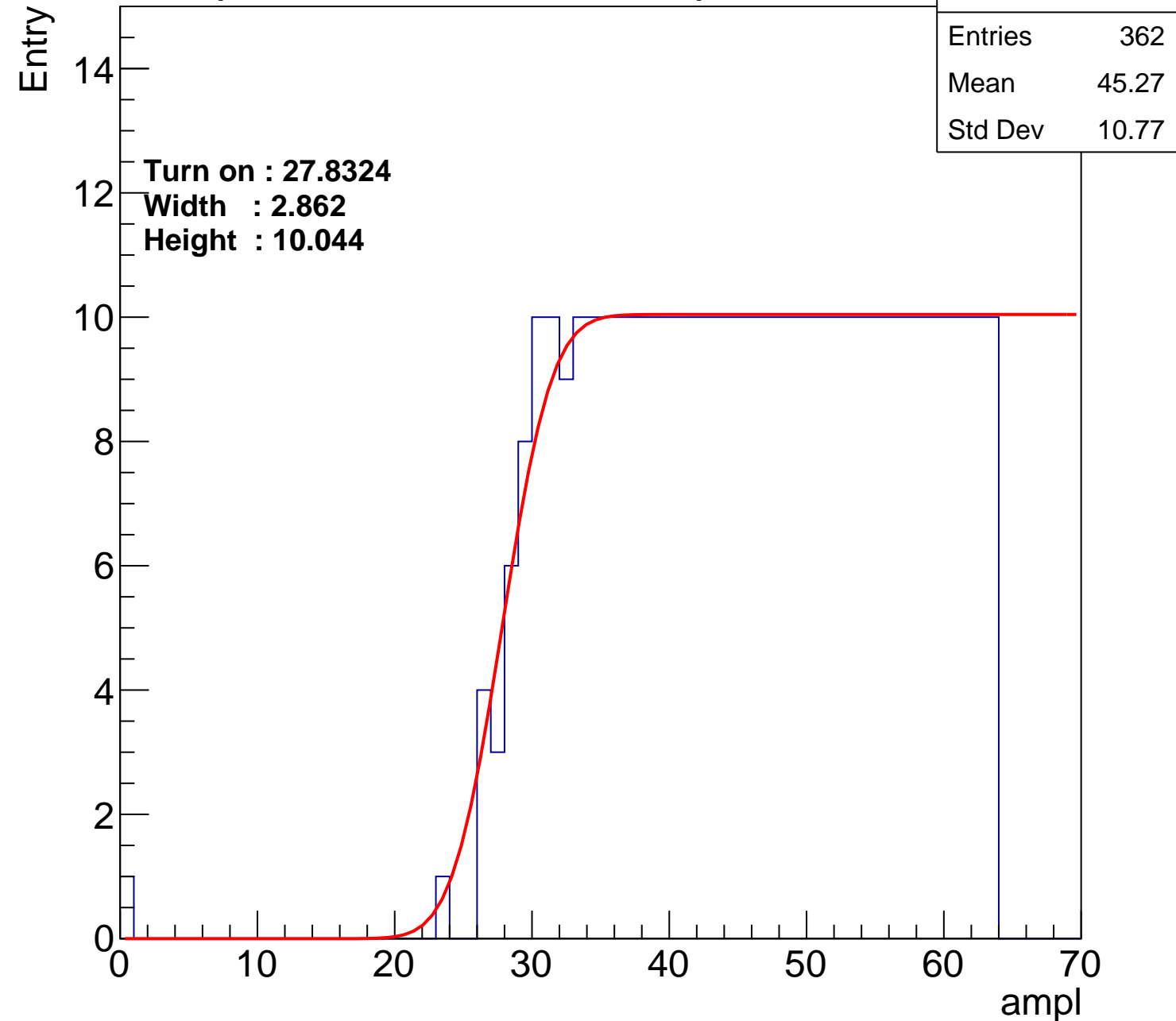
Width : 2.862

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch8

calib_packv5_042523_0143.root, FC#4, port A2

Entries	357
Mean	45.36
Std Dev	11.08

Turn on : 28.6733

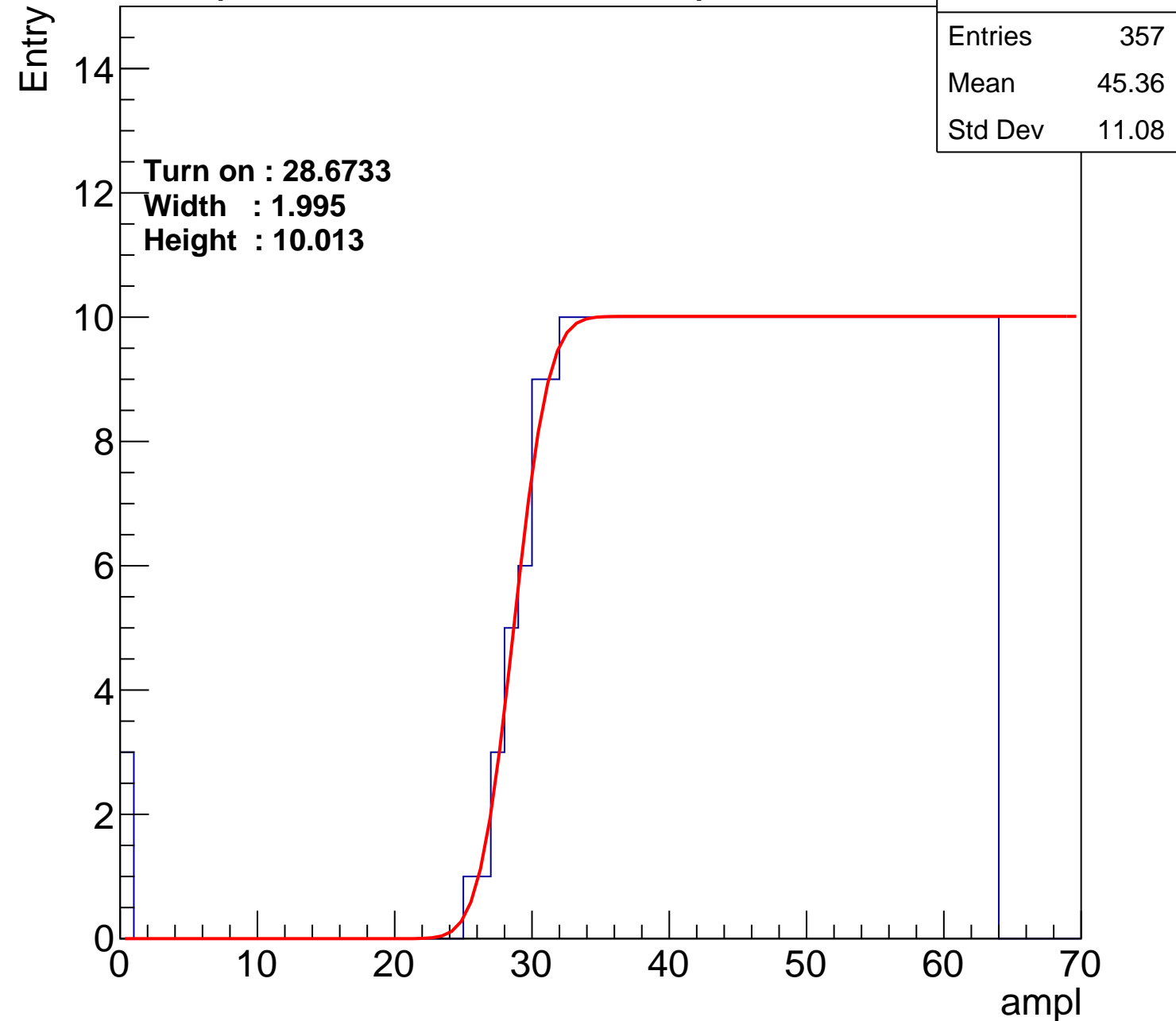
Width : 1.995

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch9

calib_packv5_042523_0143.root, FC#4, port A2

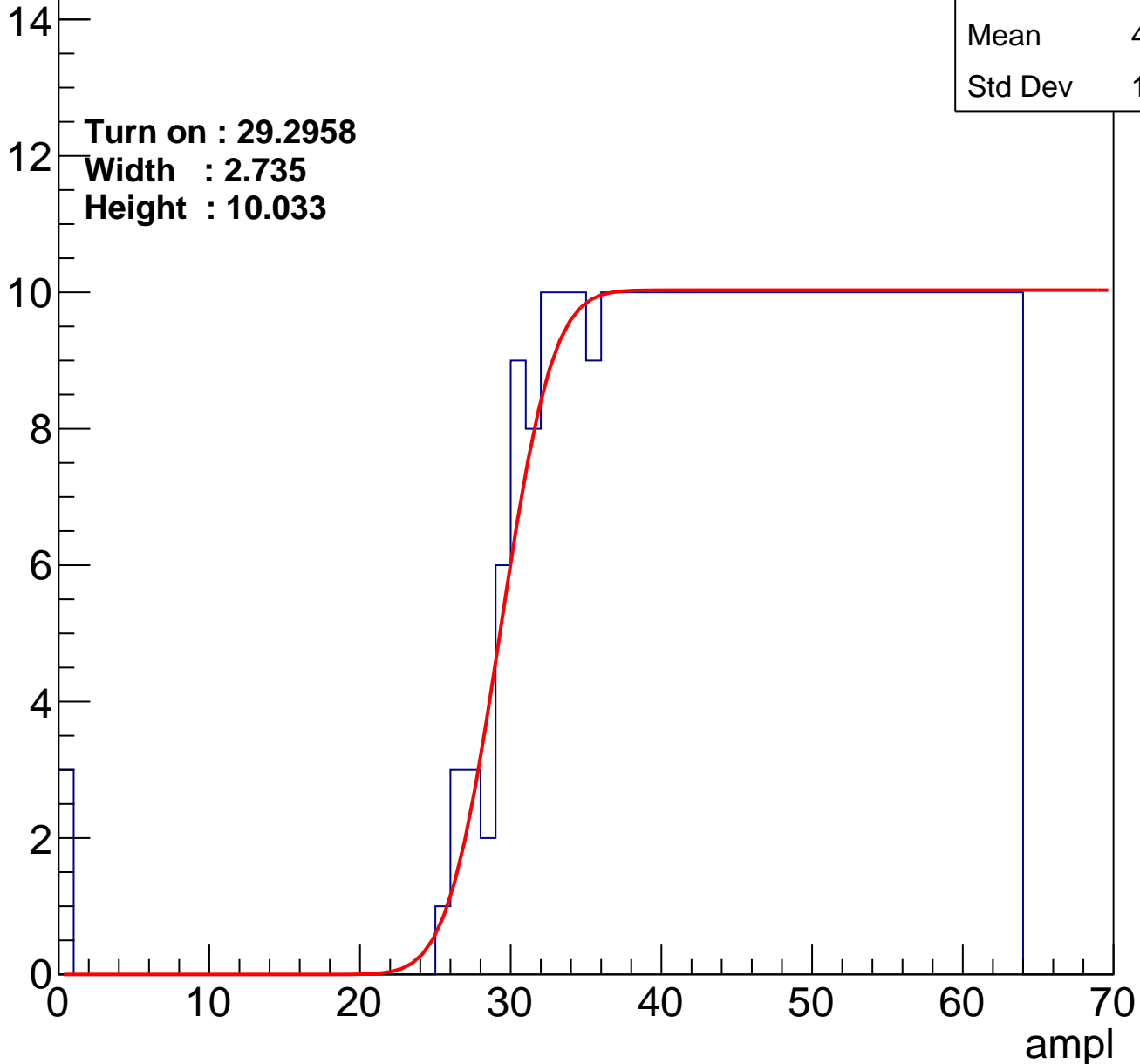
Entries	354
Mean	45.47
Std Dev	11.06

Turn on : 29.2958

Width : 2.735

Height : 10.033

Entry



B1L100S, U22-ch10

calib_packv5_042523_0143.root, FC#4, port A2

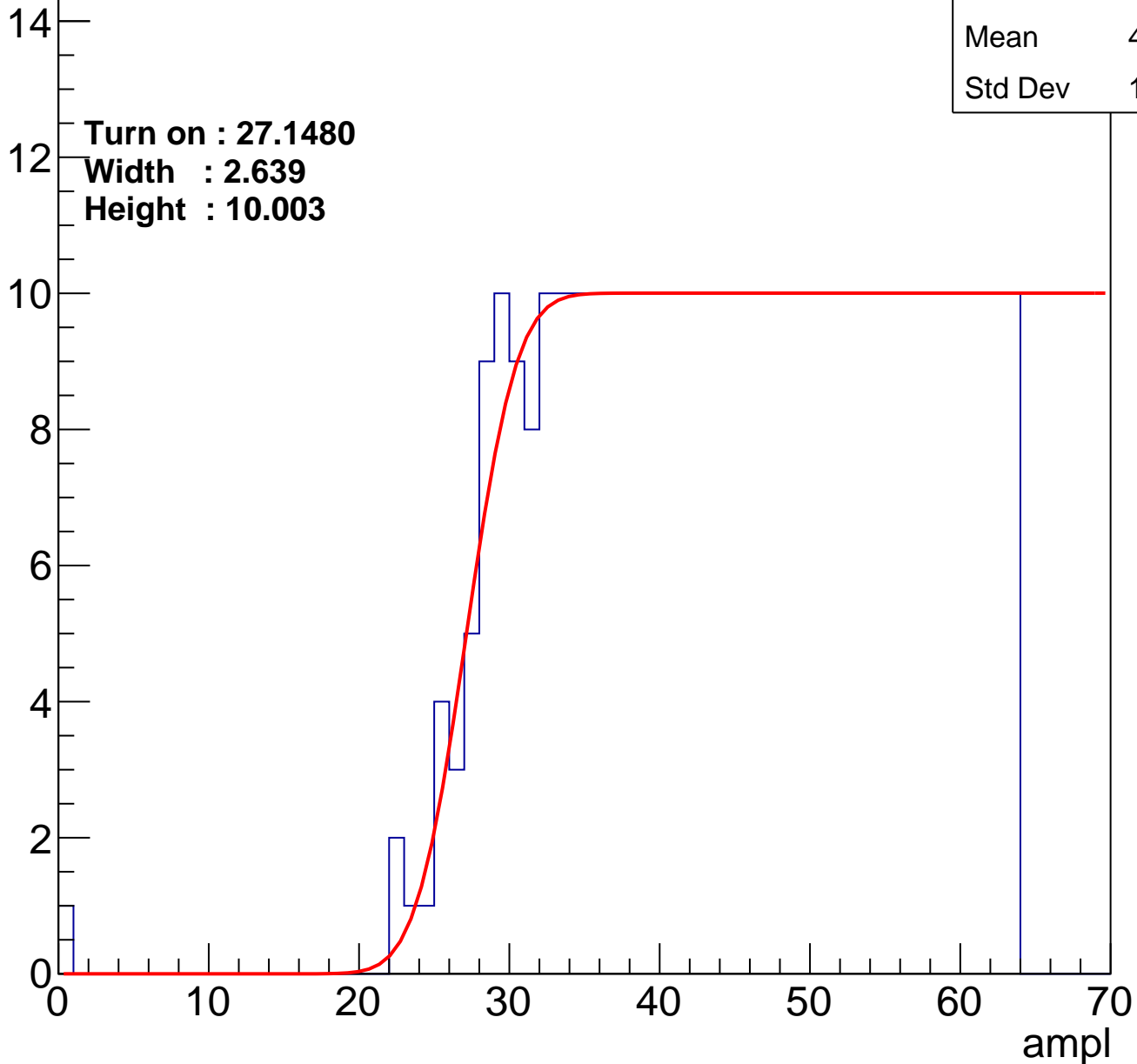
Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 27.1480

Width : 2.639

Height : 10.003

Entry



B1L100S, U22-ch11

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.18
Std Dev	11.66

Turn on : 25.2484

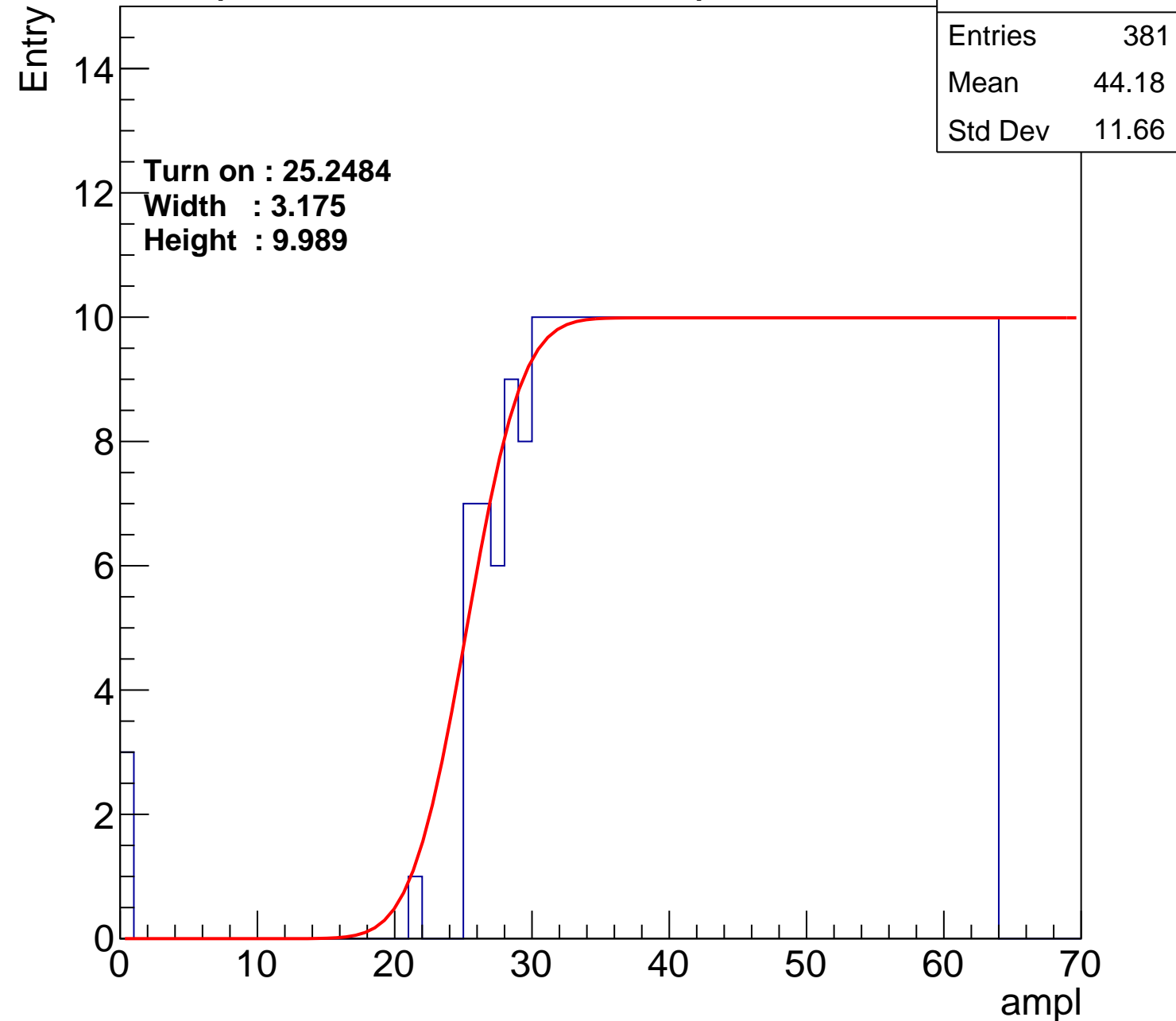
Width : 3.175

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch12

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.74
Std Dev	11.89

Turn on : 25.5212

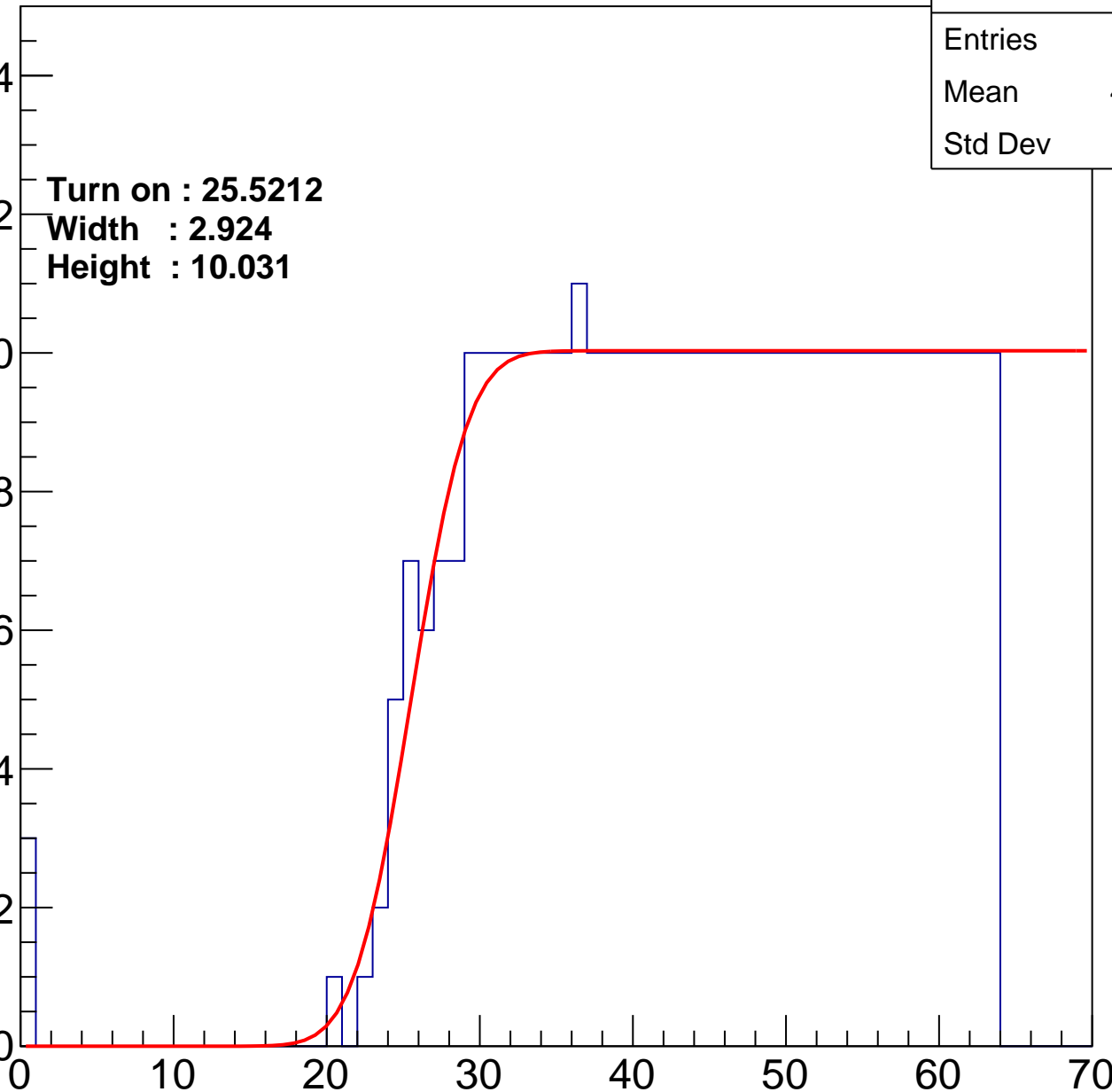
Width : 2.924

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch13

calib_packv5_042523_0143.root, FC#4, port A2

Entries	338
Mean	46.36
Std Dev	10.41

Turn on : 30.5935

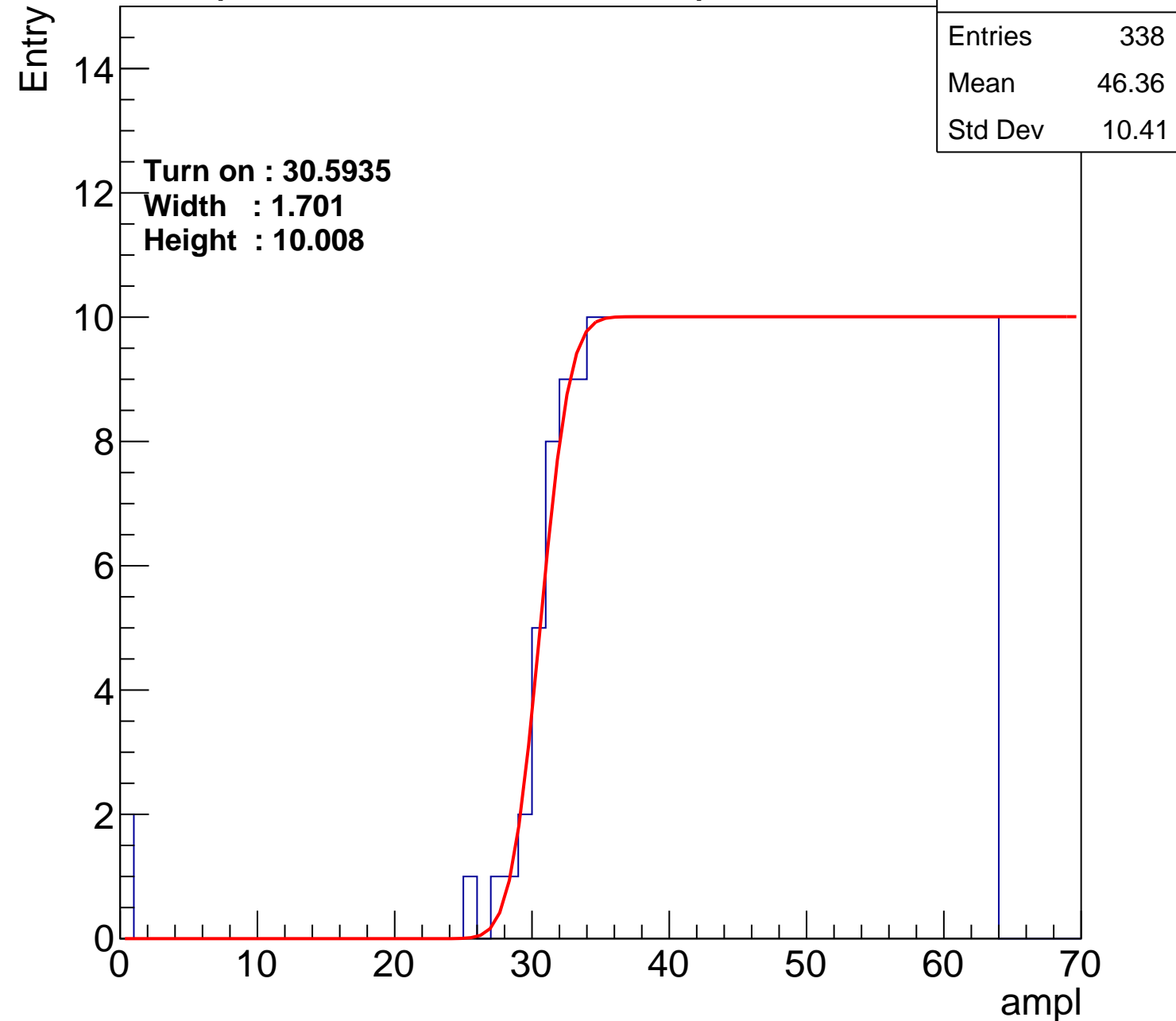
Width : 1.701

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch14

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.23
Std Dev	11.83

Turn on : 26.6724

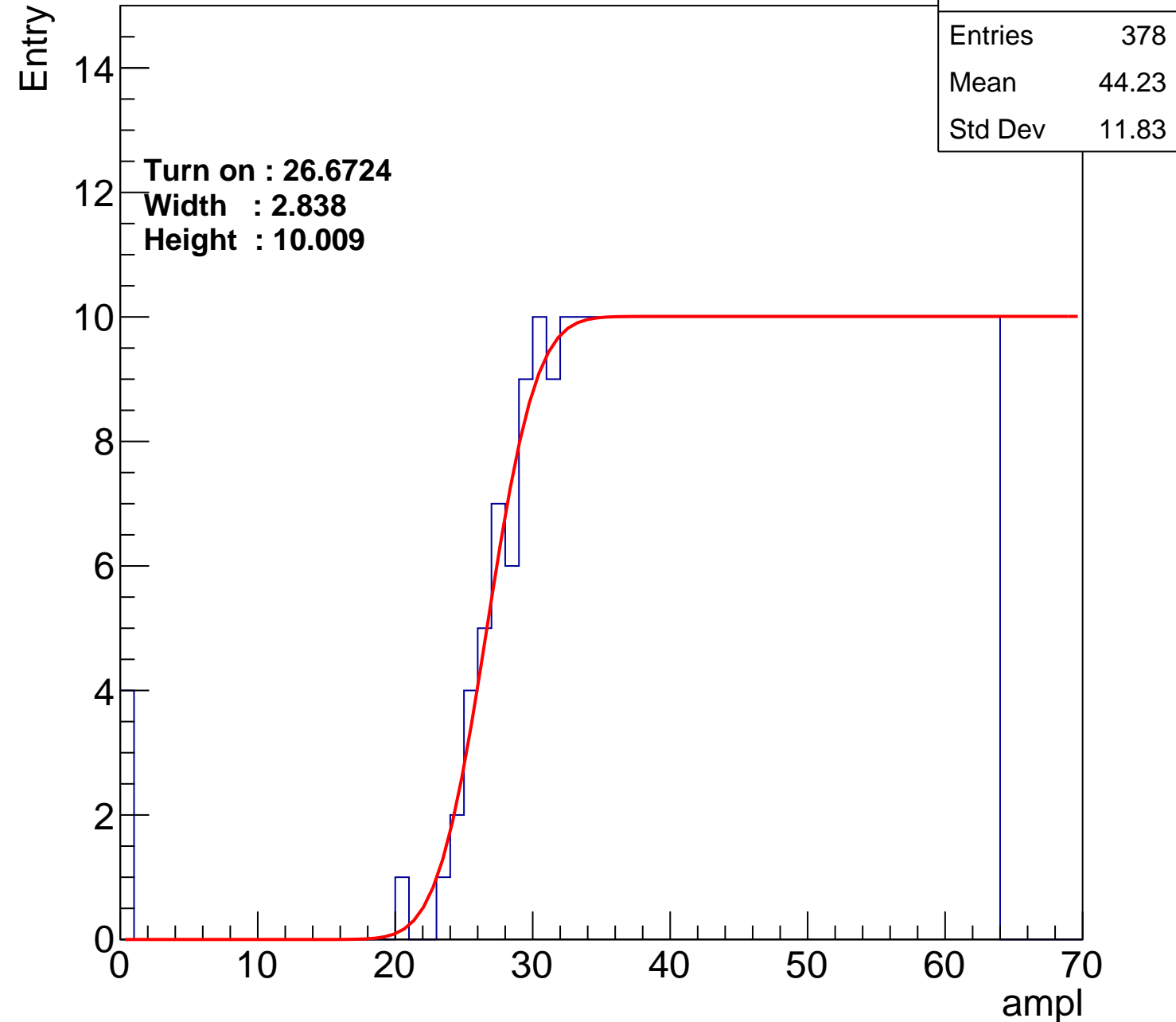
Width : 2.838

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch15

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
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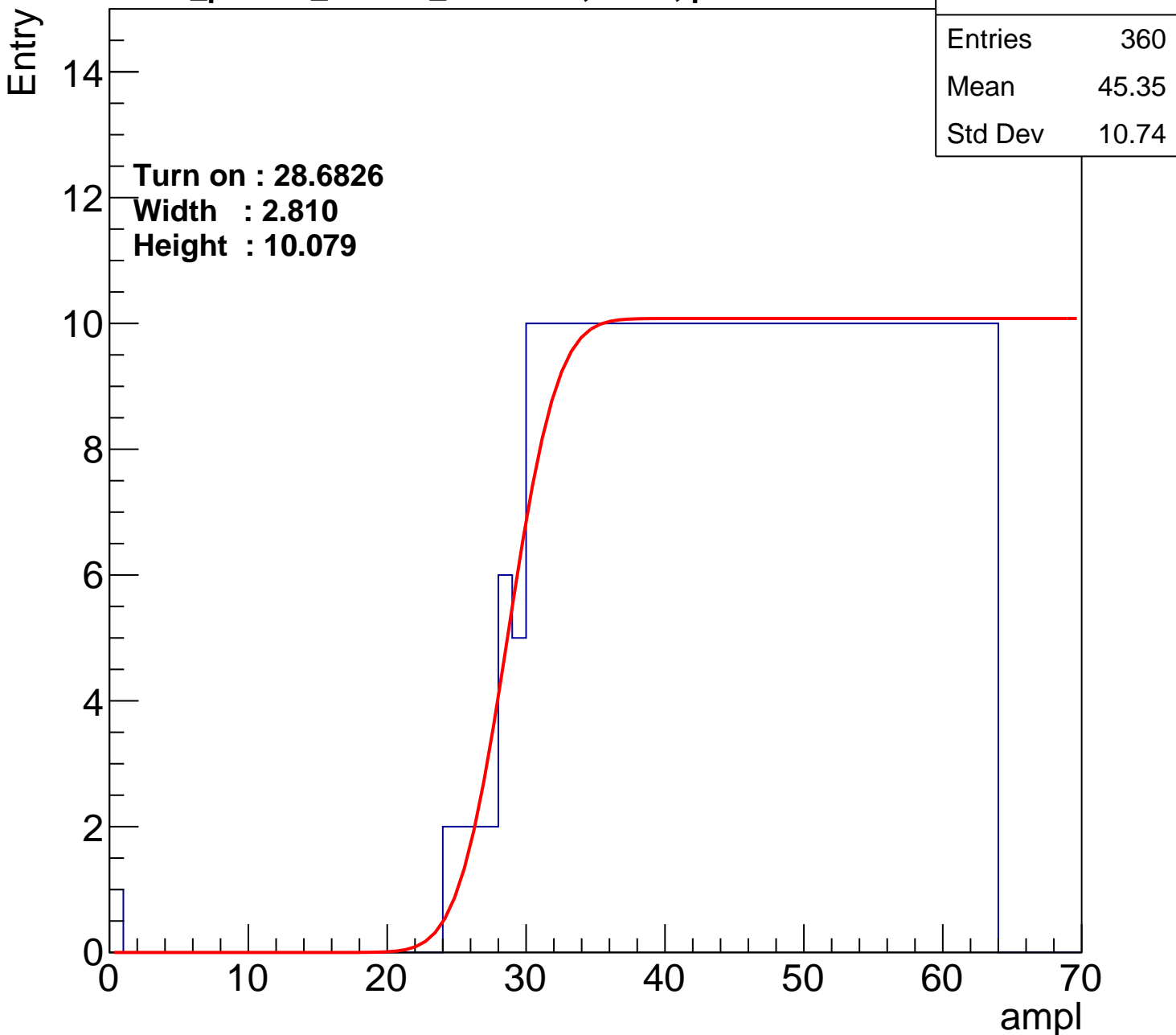
Mean	45.35
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Std Dev	10.74
---------	-------

Turn on : 28.6826

Width : 2.810

Height : 10.079



B1L100S, U22-ch16

calib_packv5_042523_0143.root, FC#4, port A2

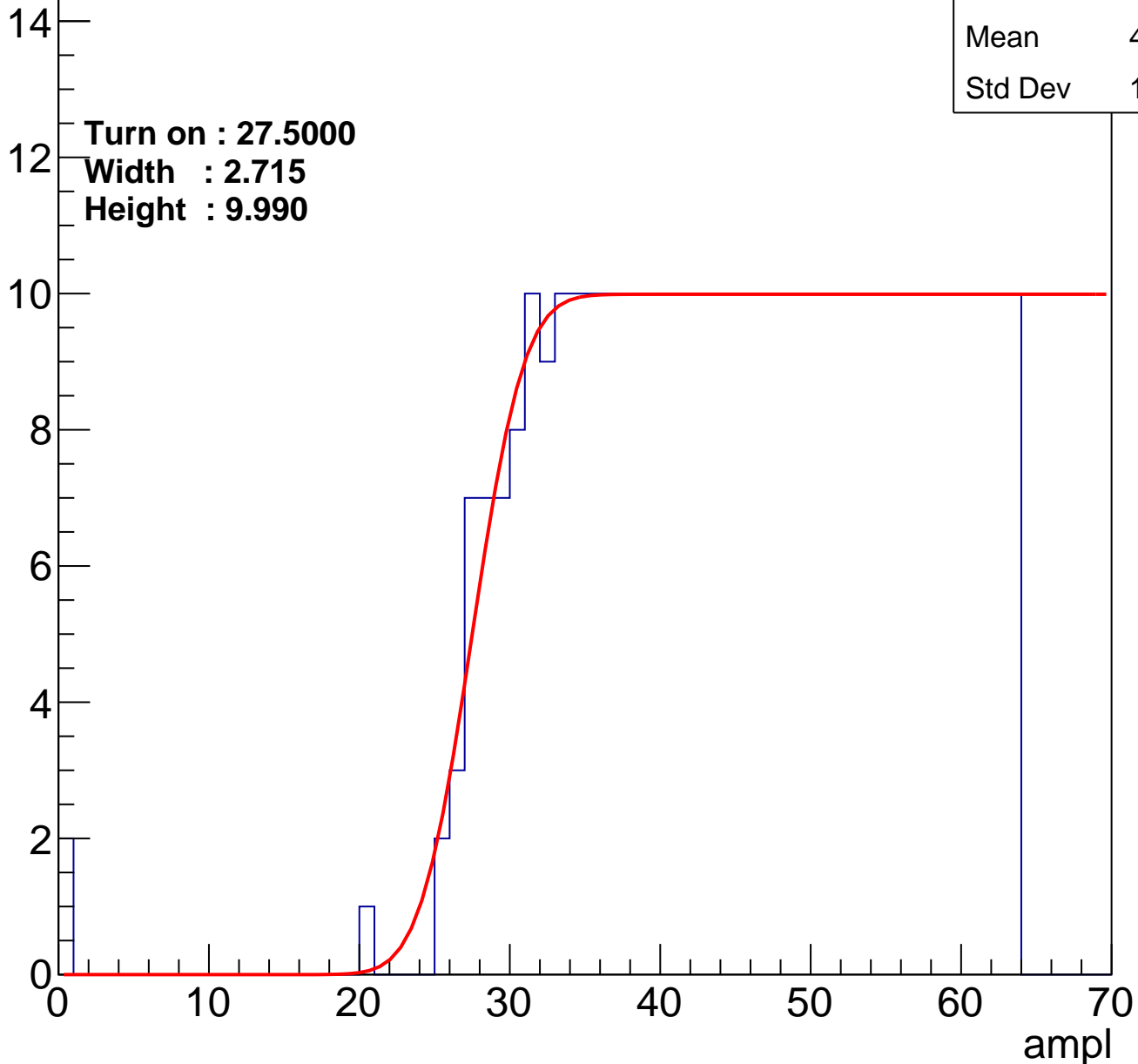
Entries	366
Mean	44.96
Std Dev	11.15

Turn on : 27.5000

Width : 2.715

Height : 9.990

Entry



B1L100S, U22-ch17

calib_packv5_042523_0143.root, FC#4, port A2

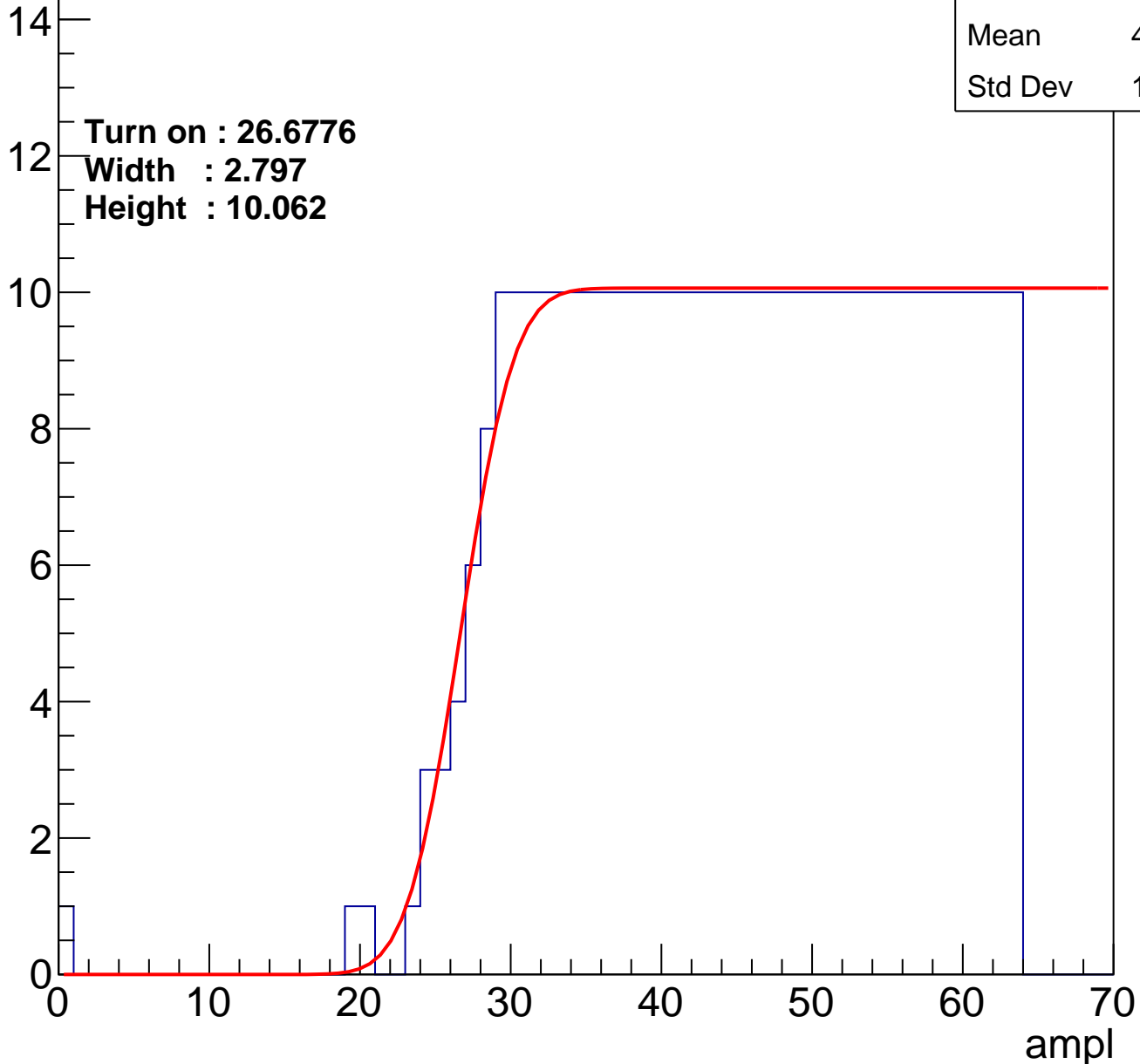
Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.6776

Width : 2.797

Height : 10.062

Entry



B1L100S, U22-ch18

calib_packv5_042523_0143.root, FC#4, port A2

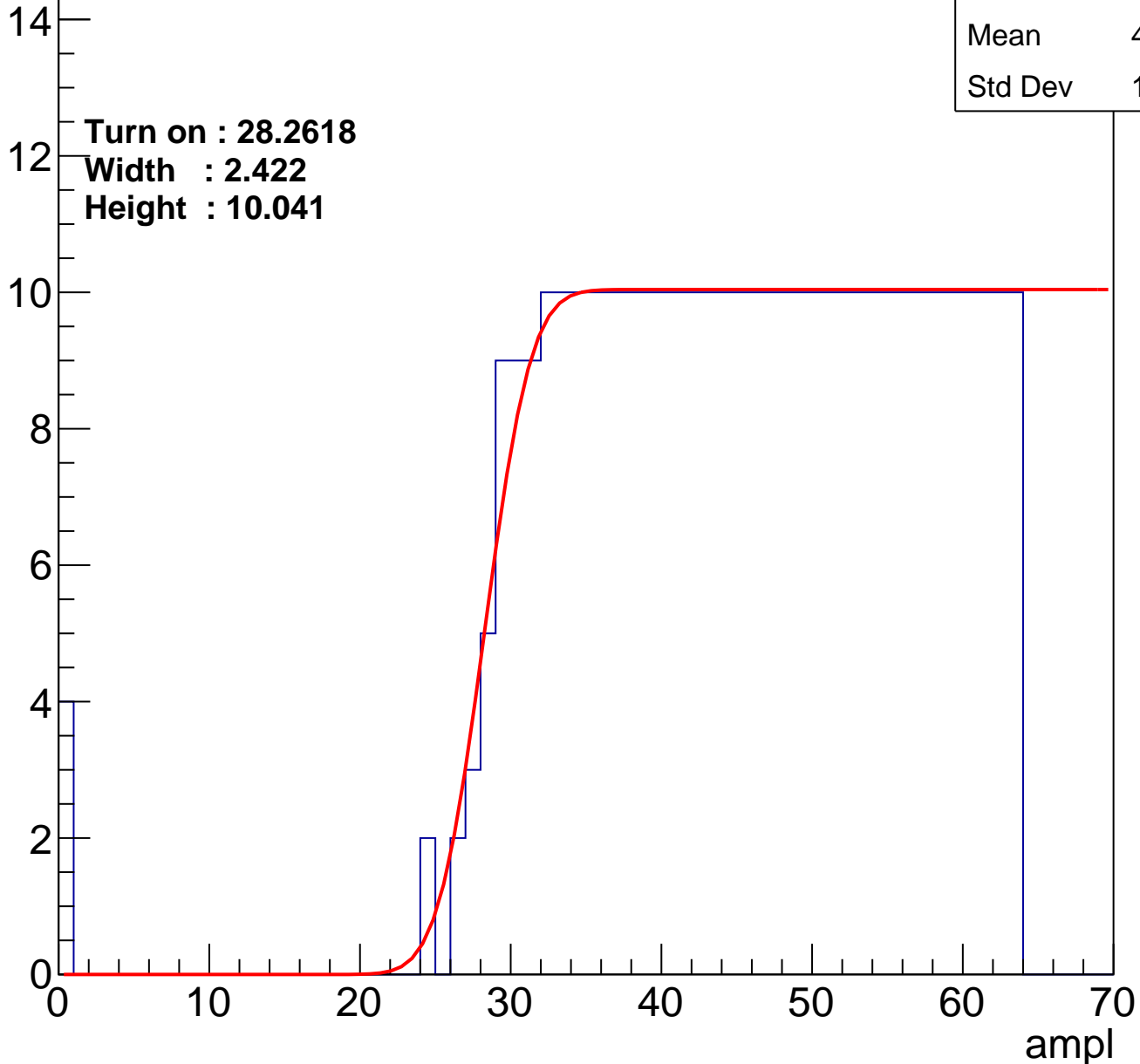
Entries	363
Mean	44.99
Std Dev	11.44

Turn on : 28.2618

Width : 2.422

Height : 10.041

Entry



B1L100S, U22-ch19

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.72
Std Dev	12.44

Turn on : 26.1080

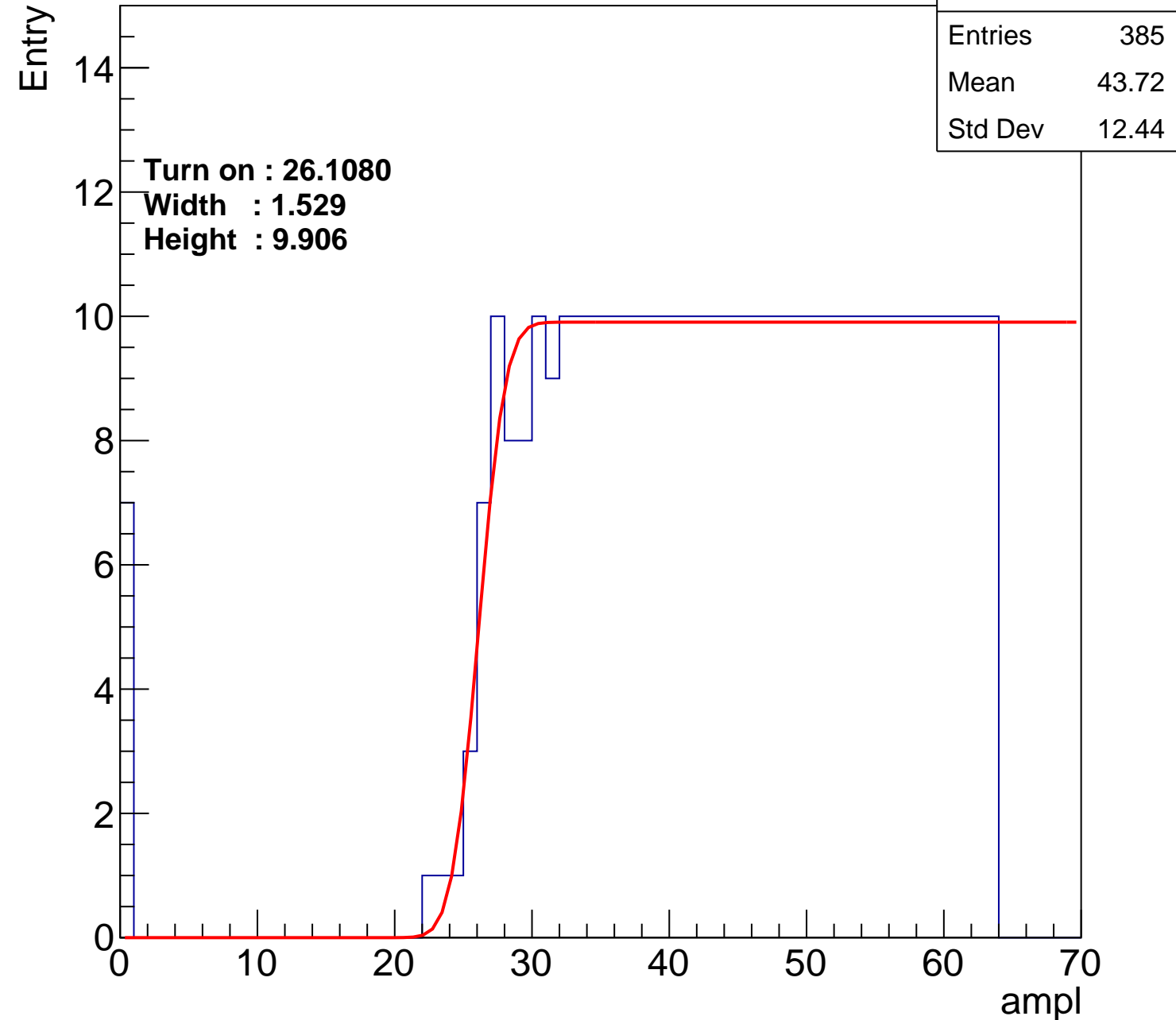
Width : 1.529

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.46
Std Dev	11.68

Turn on : 26.9408

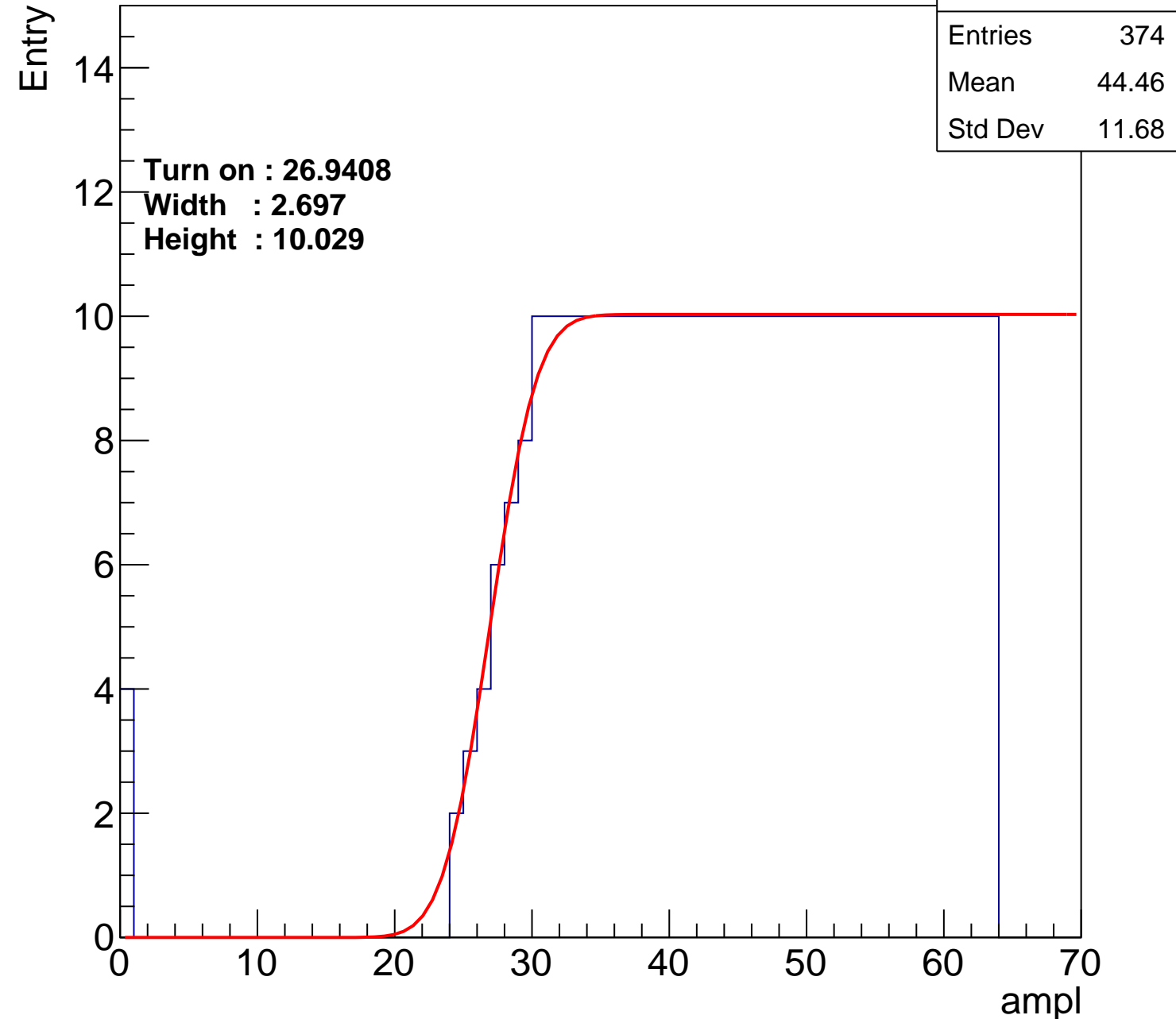
Width : 2.697

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch21

calib_packv5_042523_0143.root, FC#4, port A2

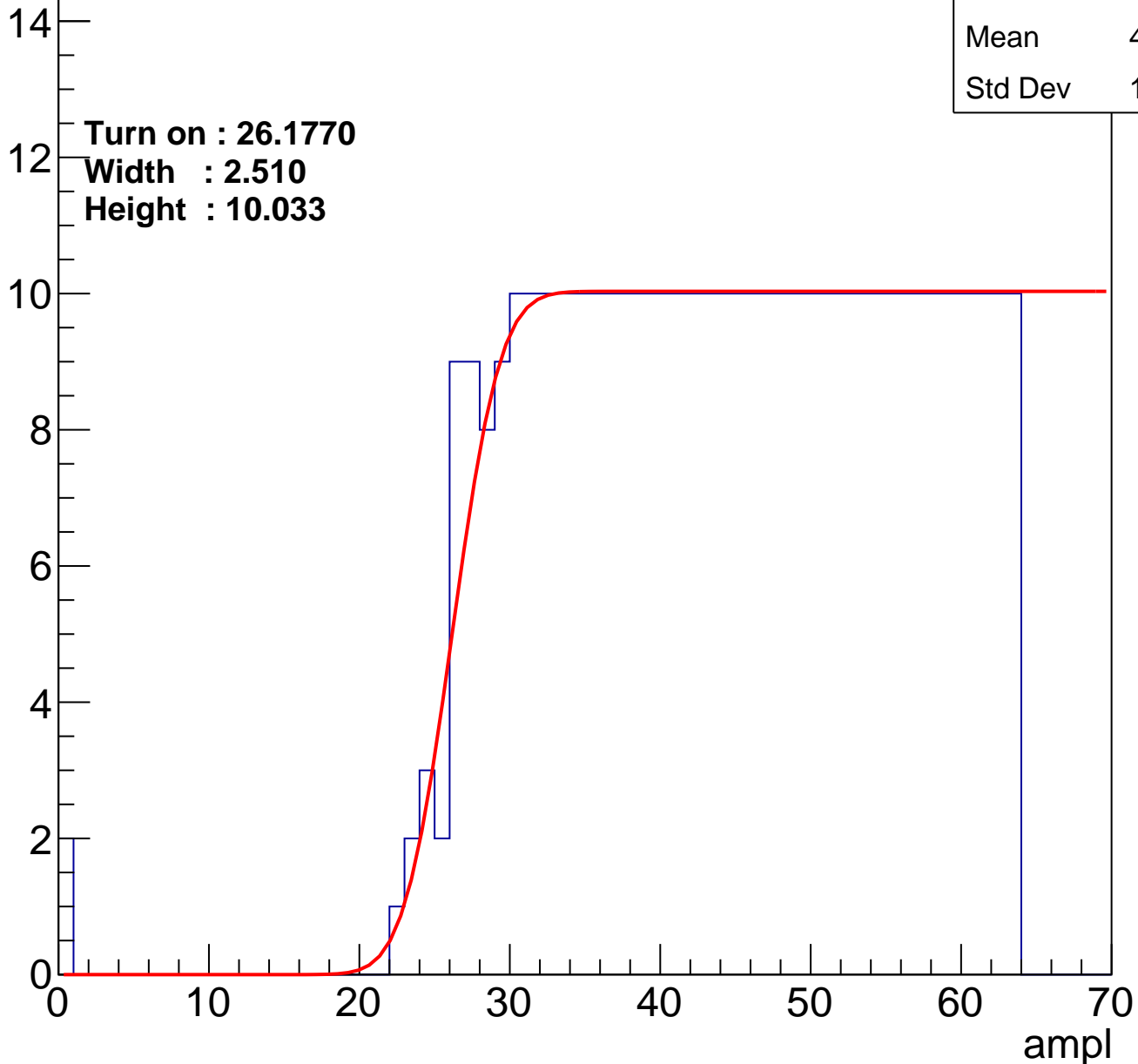
Entries	385
Mean	44.06
Std Dev	11.58

Turn on : 26.1770

Width : 2.510

Height : 10.033

Entry



B1L100S, U22-ch22

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.13
Std Dev	11.09

Turn on : 29.1529

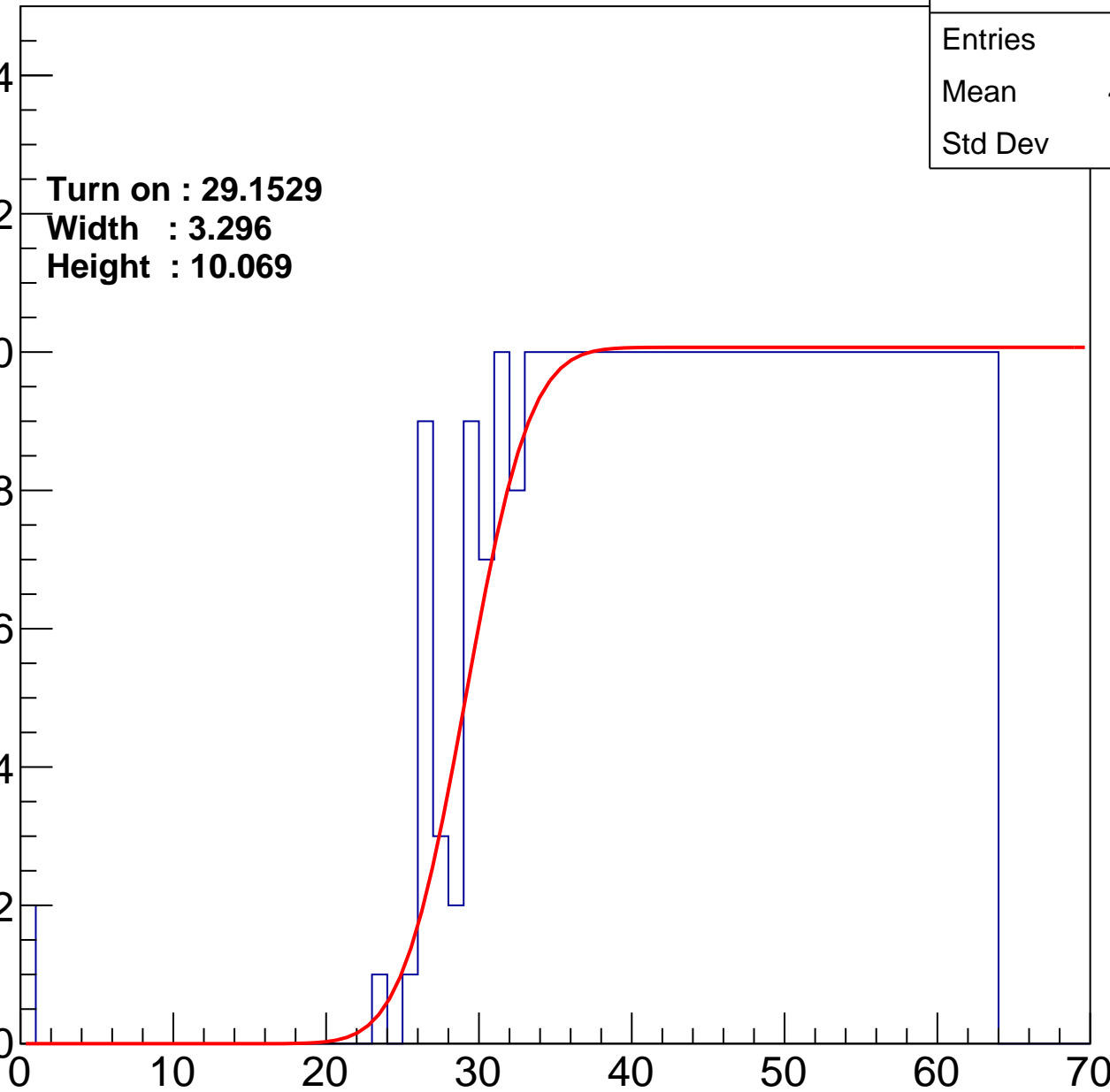
Width : 3.296

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.79
Std Dev	11.12

Turn on : 27.7722

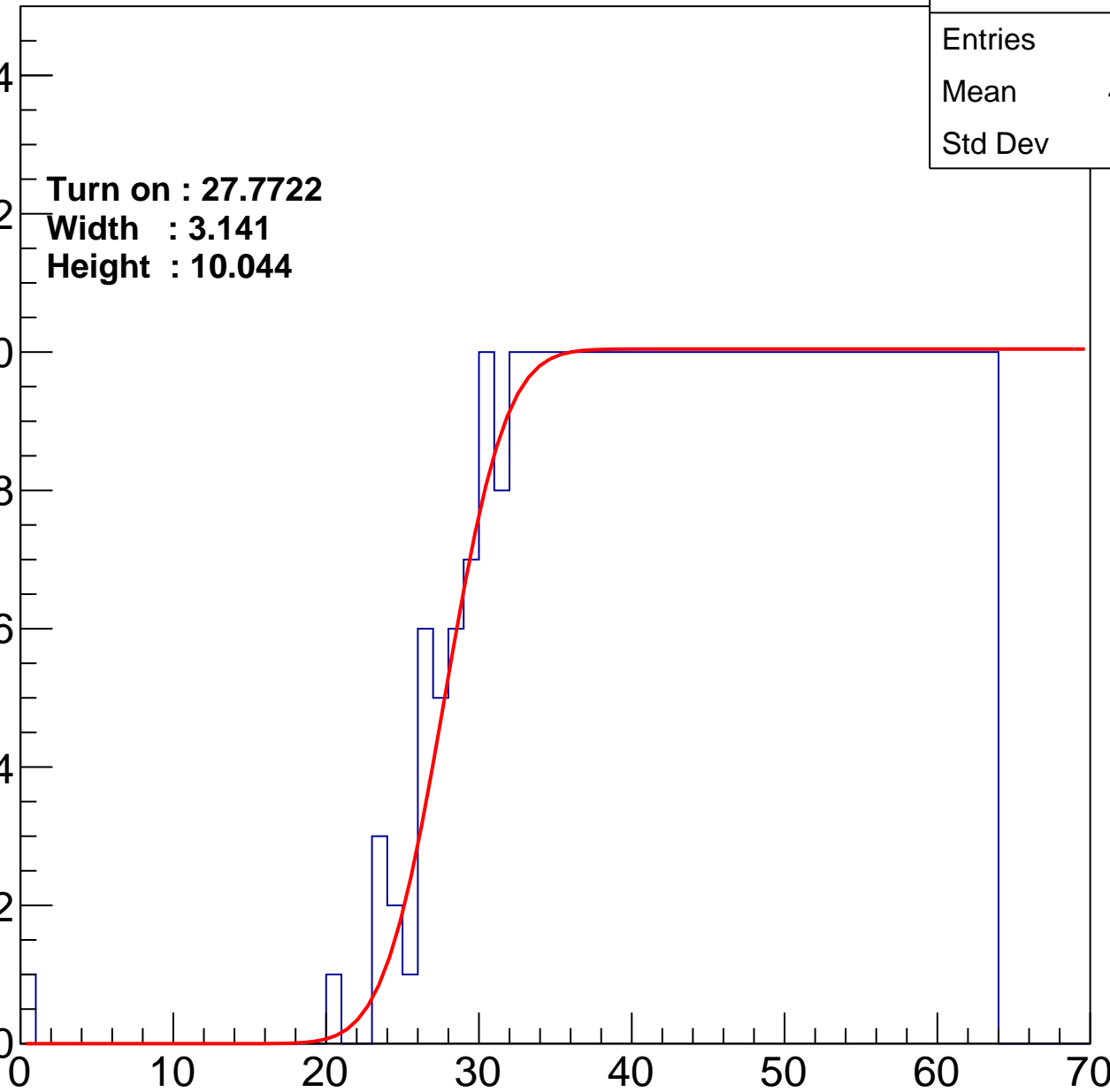
Width : 3.141

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch24

calib_packv5_042523_0143.root, FC#4, port A2

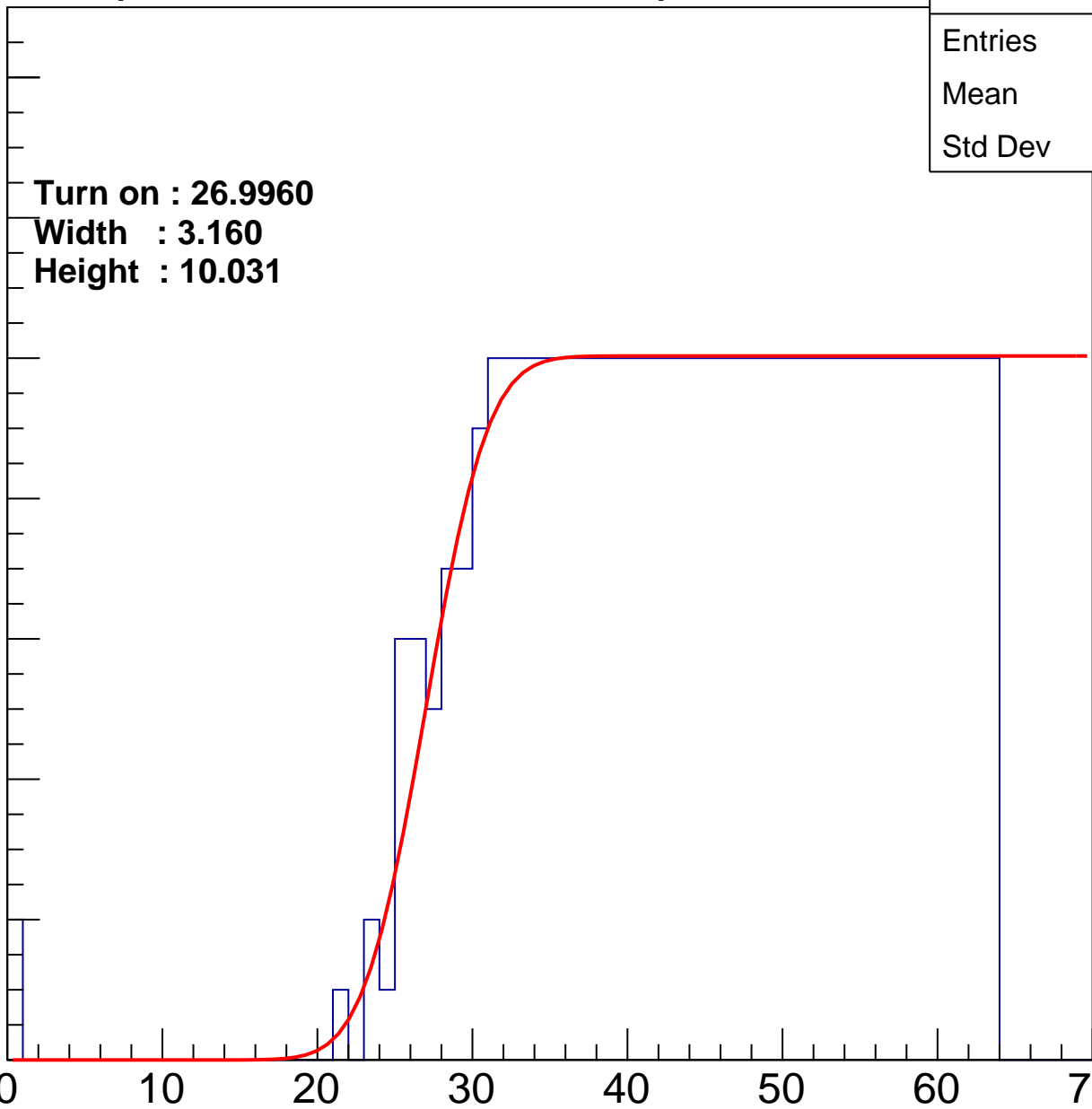
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9960
Width : 3.160
Height : 10.031

Entries	376
Mean	44.44
Std Dev	11.44

ampl



B1L100S, U22-ch25

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 25.7236

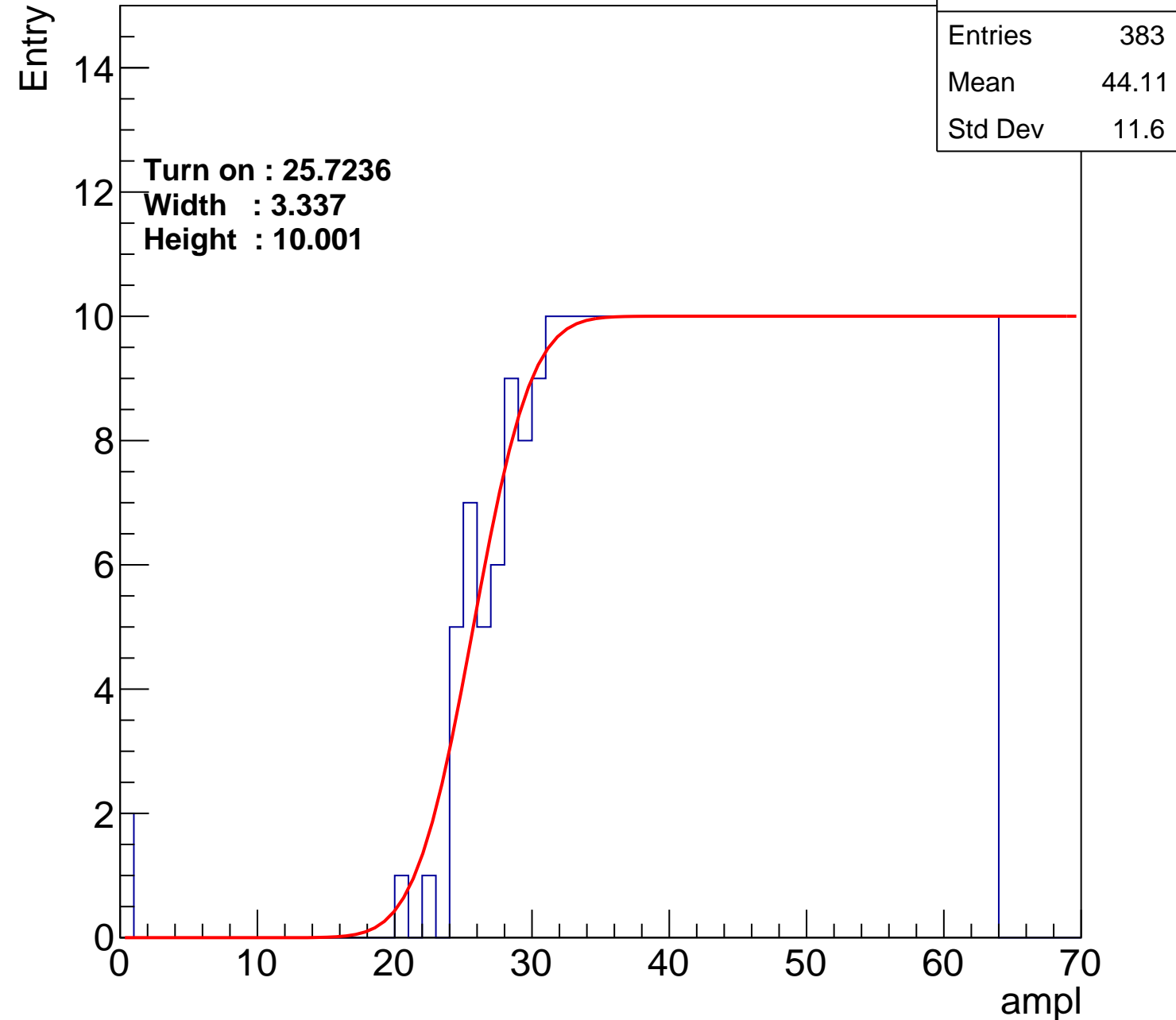
Width : 3.337

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.5976

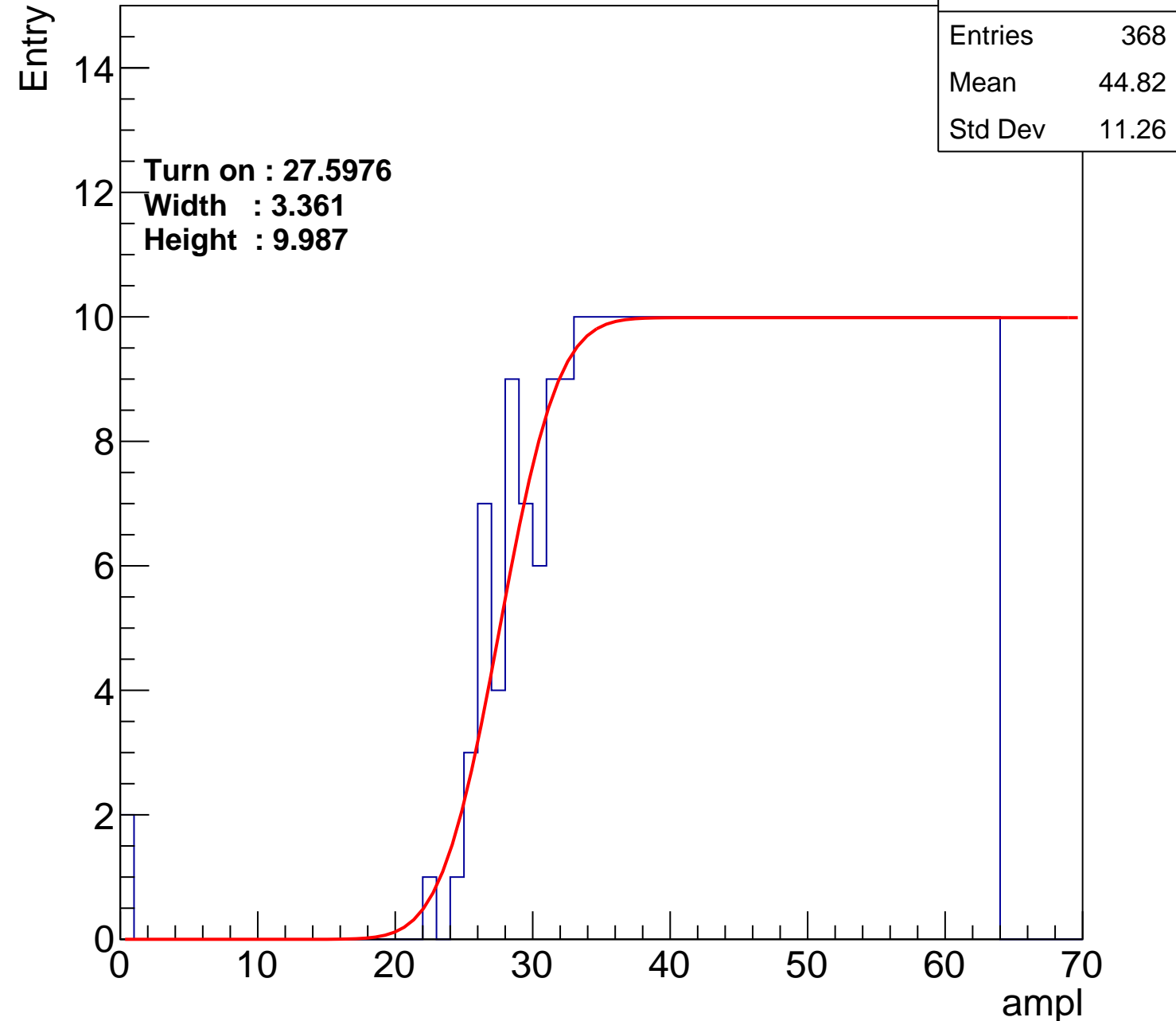
Width : 3.361

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.48
Std Dev	12.2

Turn on : 25.2155

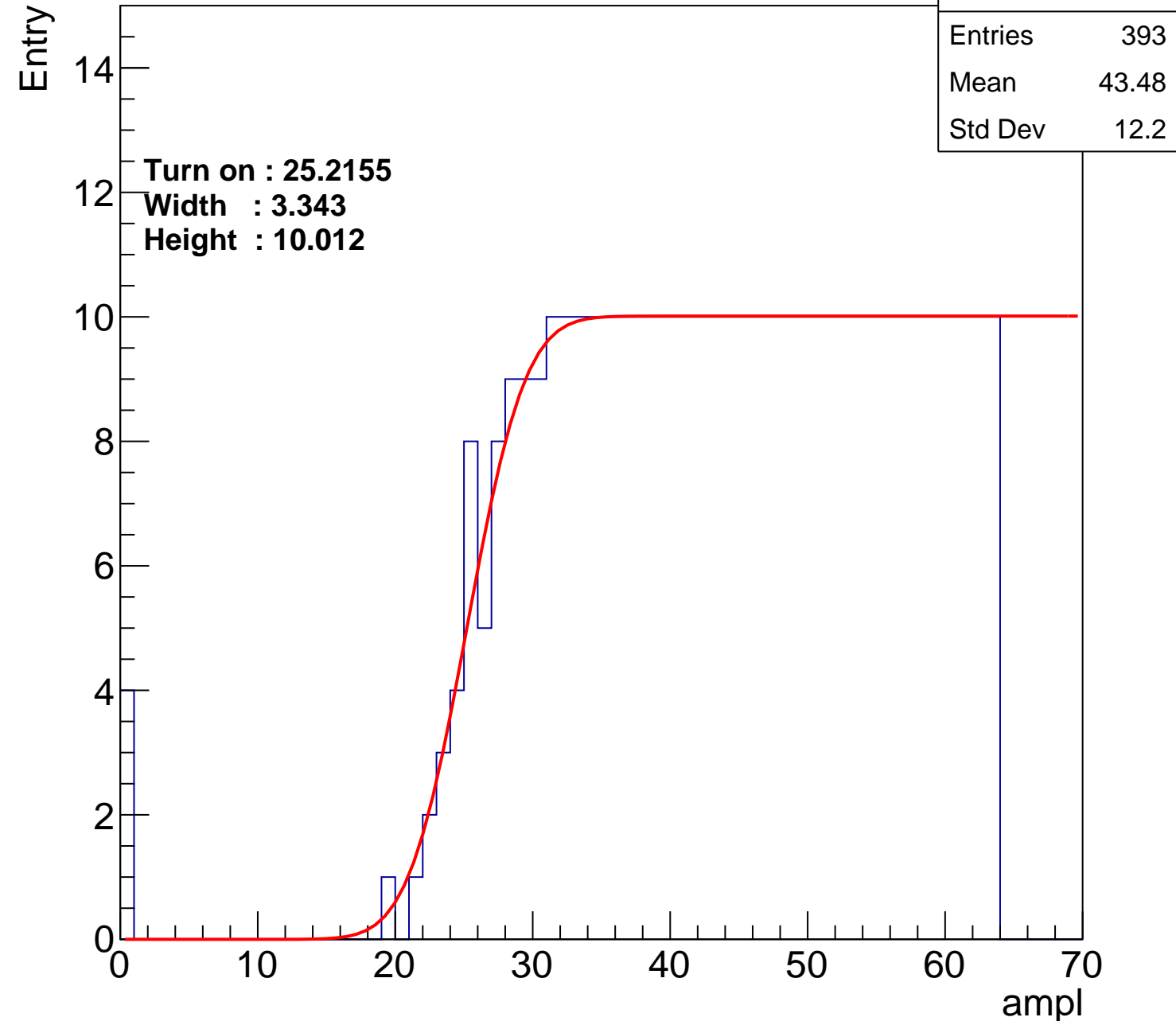
Width : 3.343

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch28

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.33
Std Dev	11.47

Turn on : 26.8129

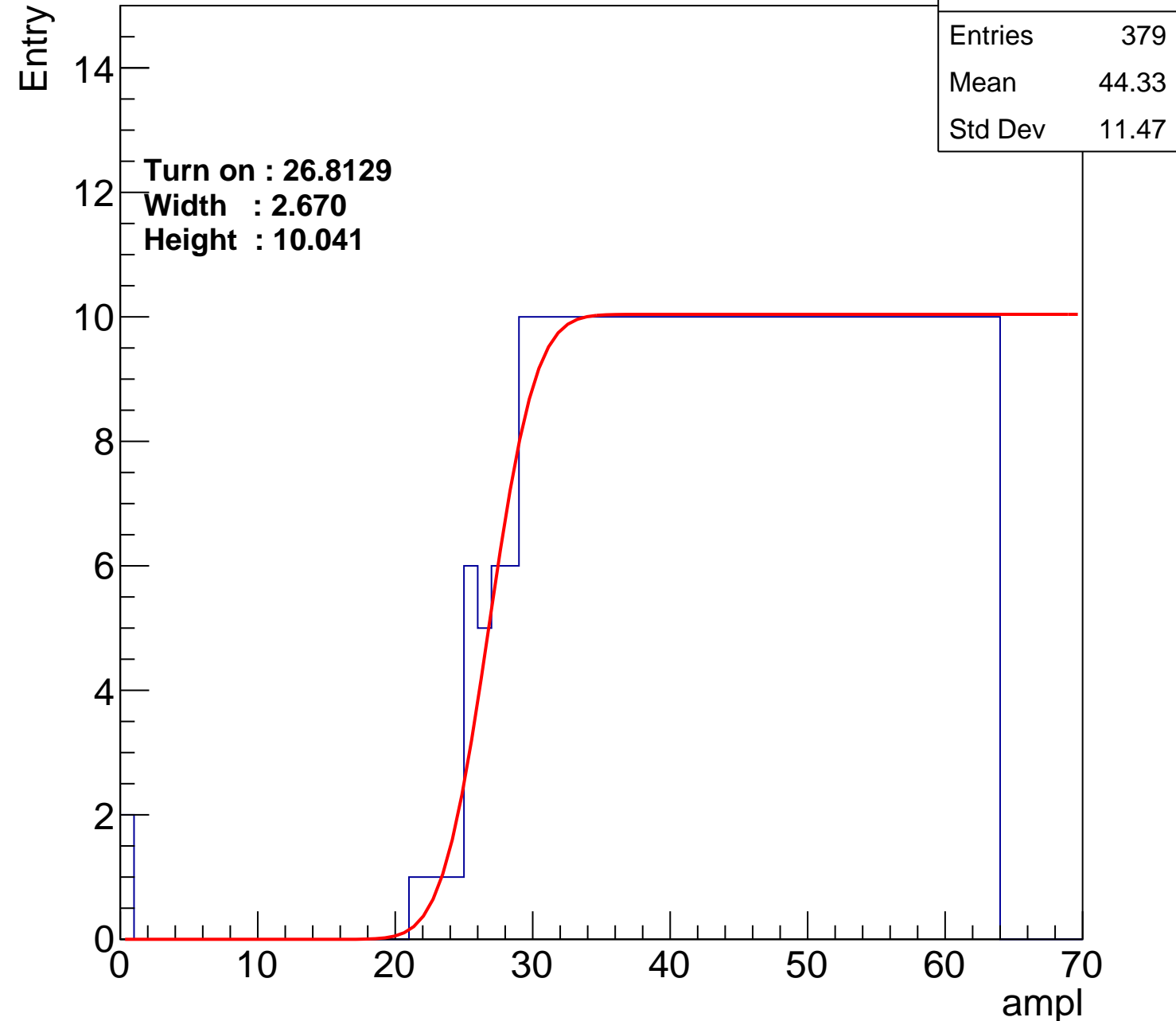
Width : 2.670

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.1762

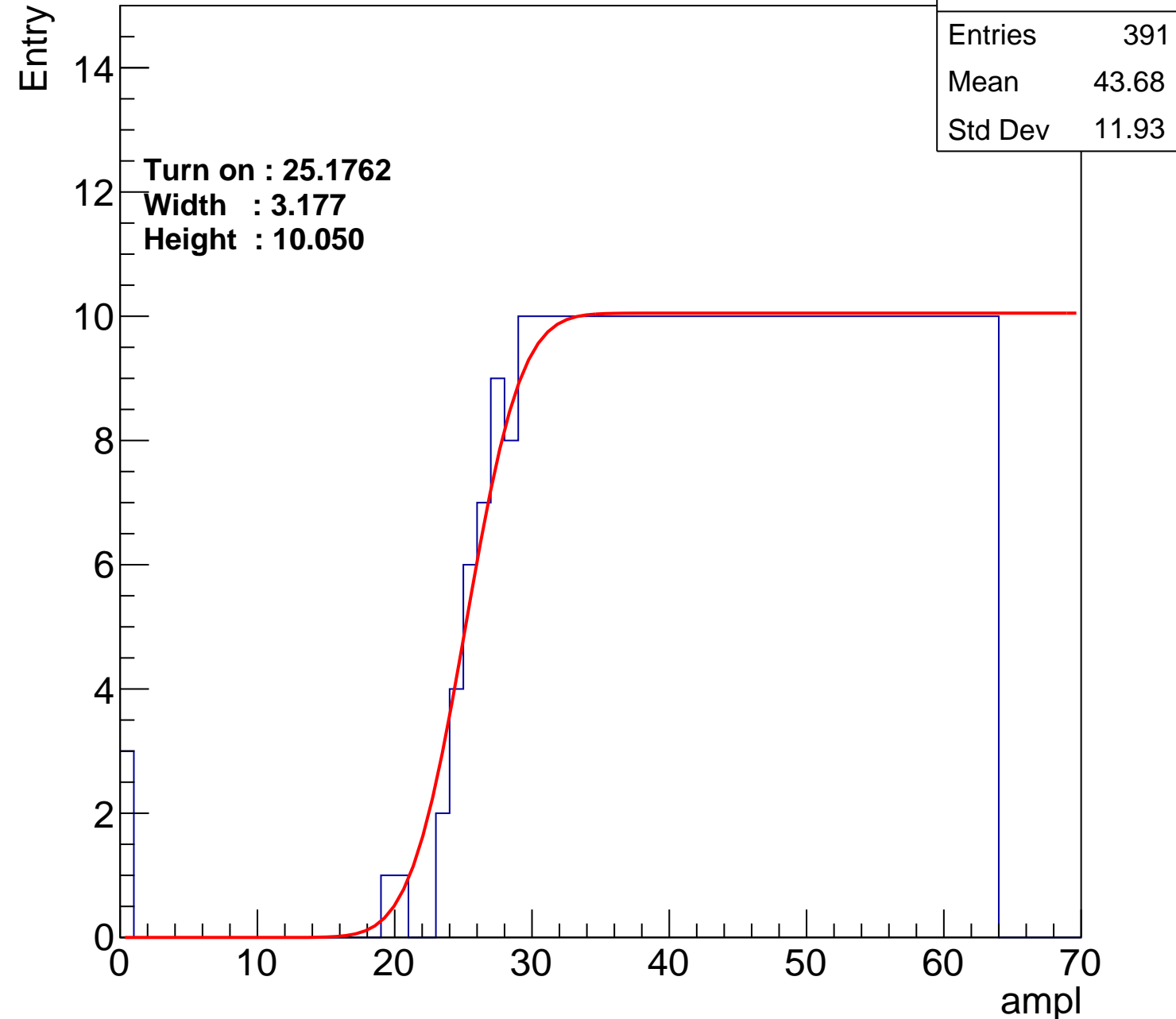
Width : 3.177

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch30

calib_packv5_042523_0143.root, FC#4, port A2

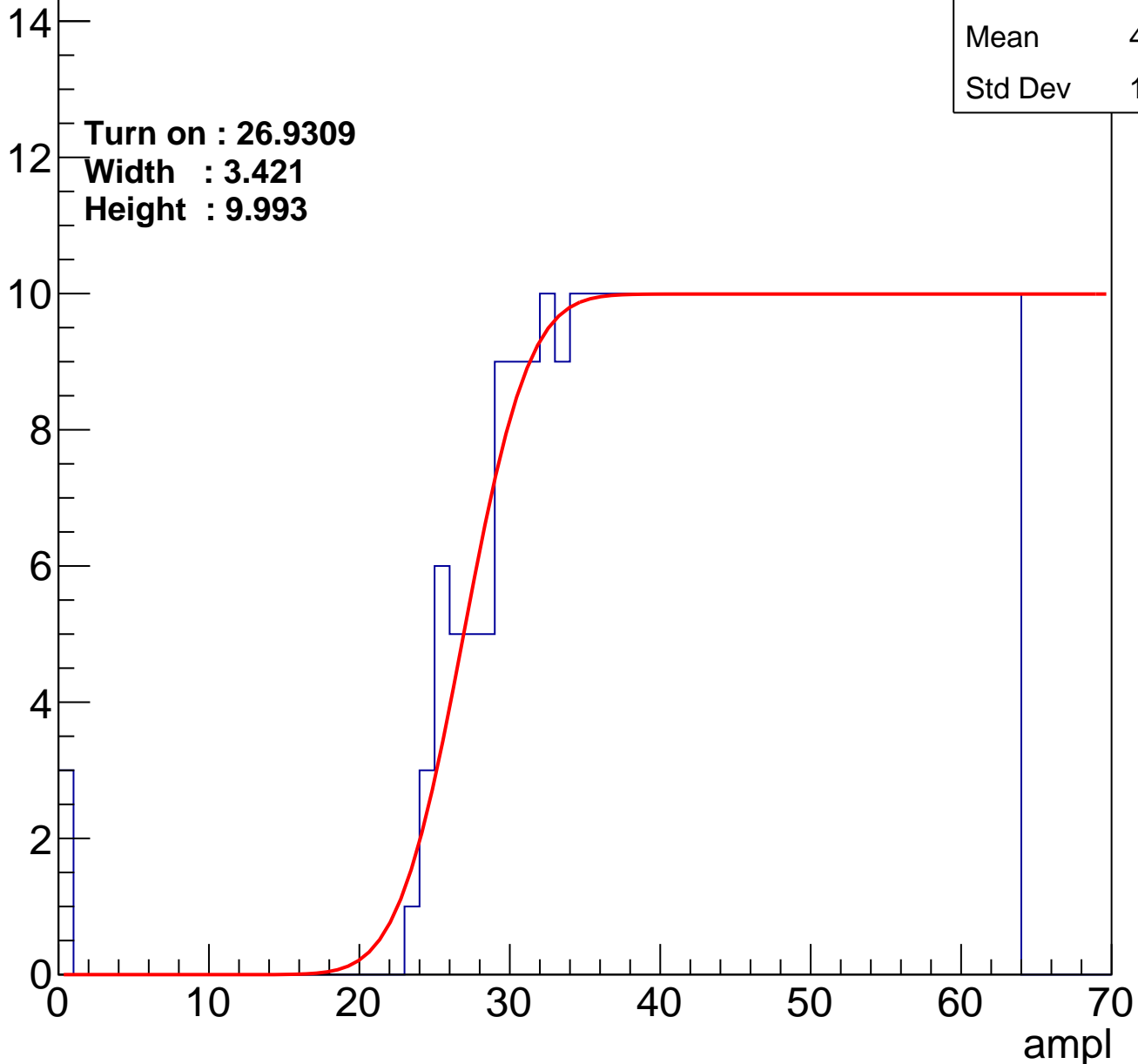
Entries	374
Mean	44.46
Std Dev	11.59

Turn on : 26.9309

Width : 3.421

Height : 9.993

Entry



B1L100S, U22-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.8577

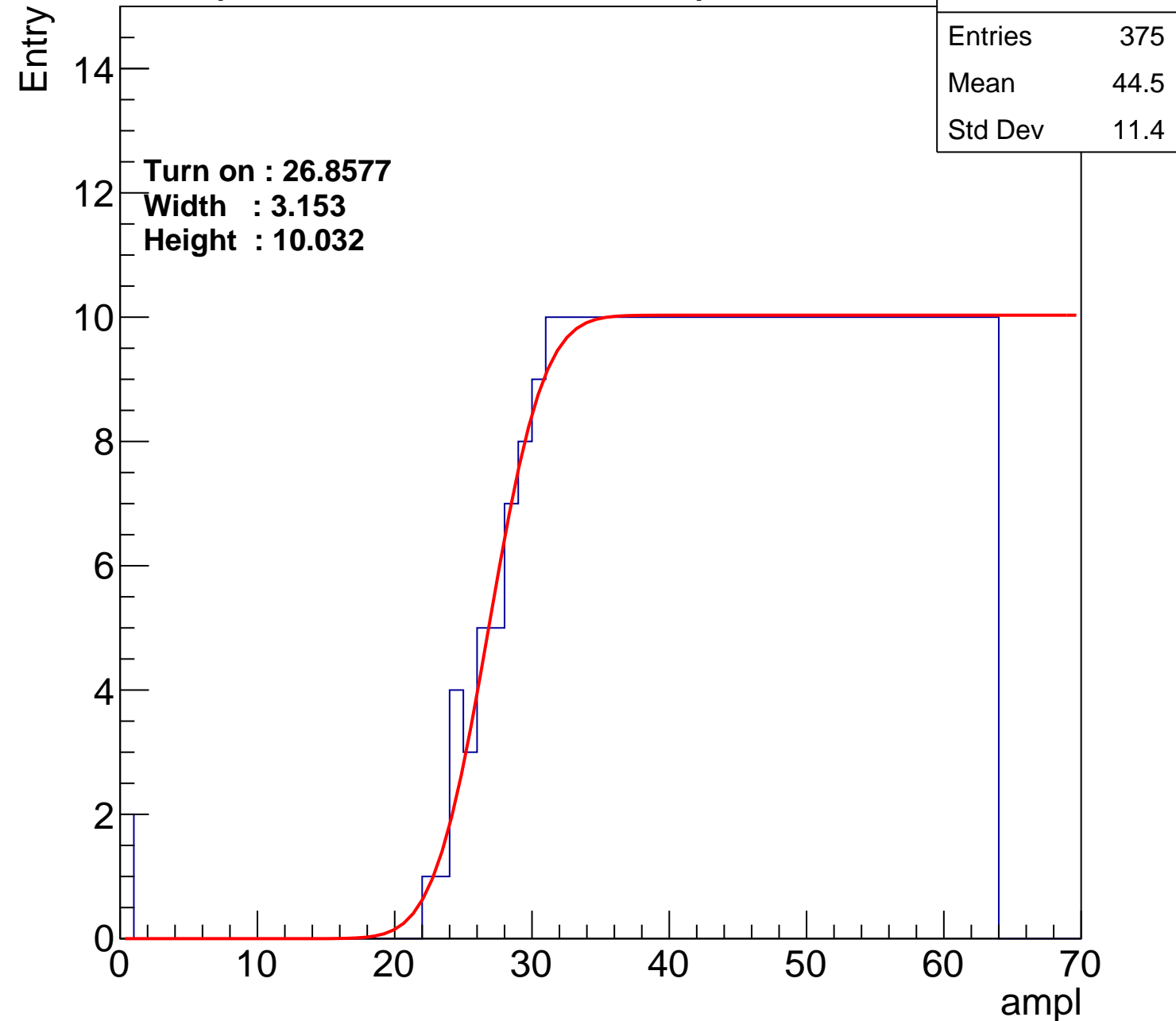
Width : 3.153

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch32

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.7953

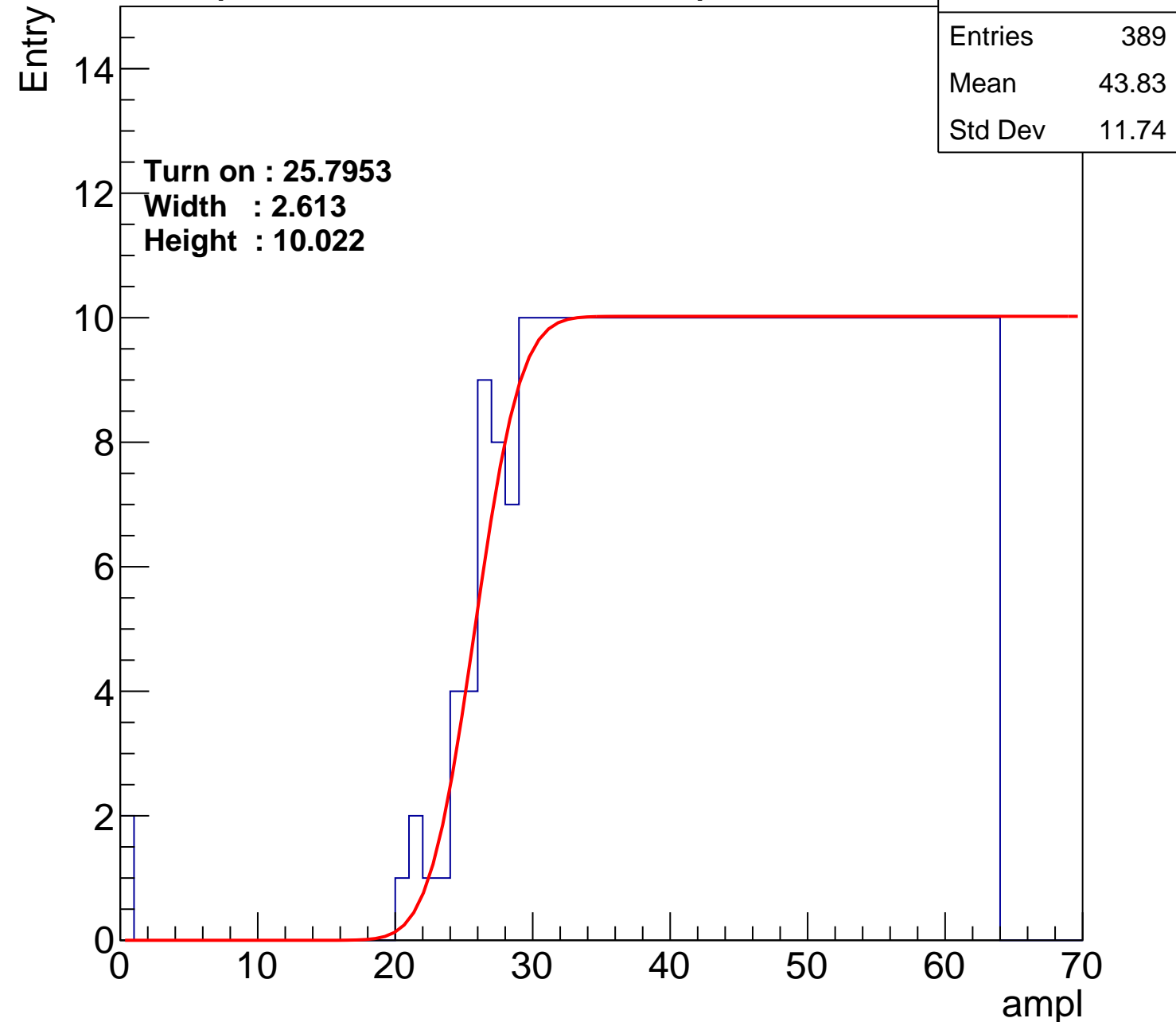
Width : 2.613

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.07
Std Dev	11.91

Turn on : 26.8107

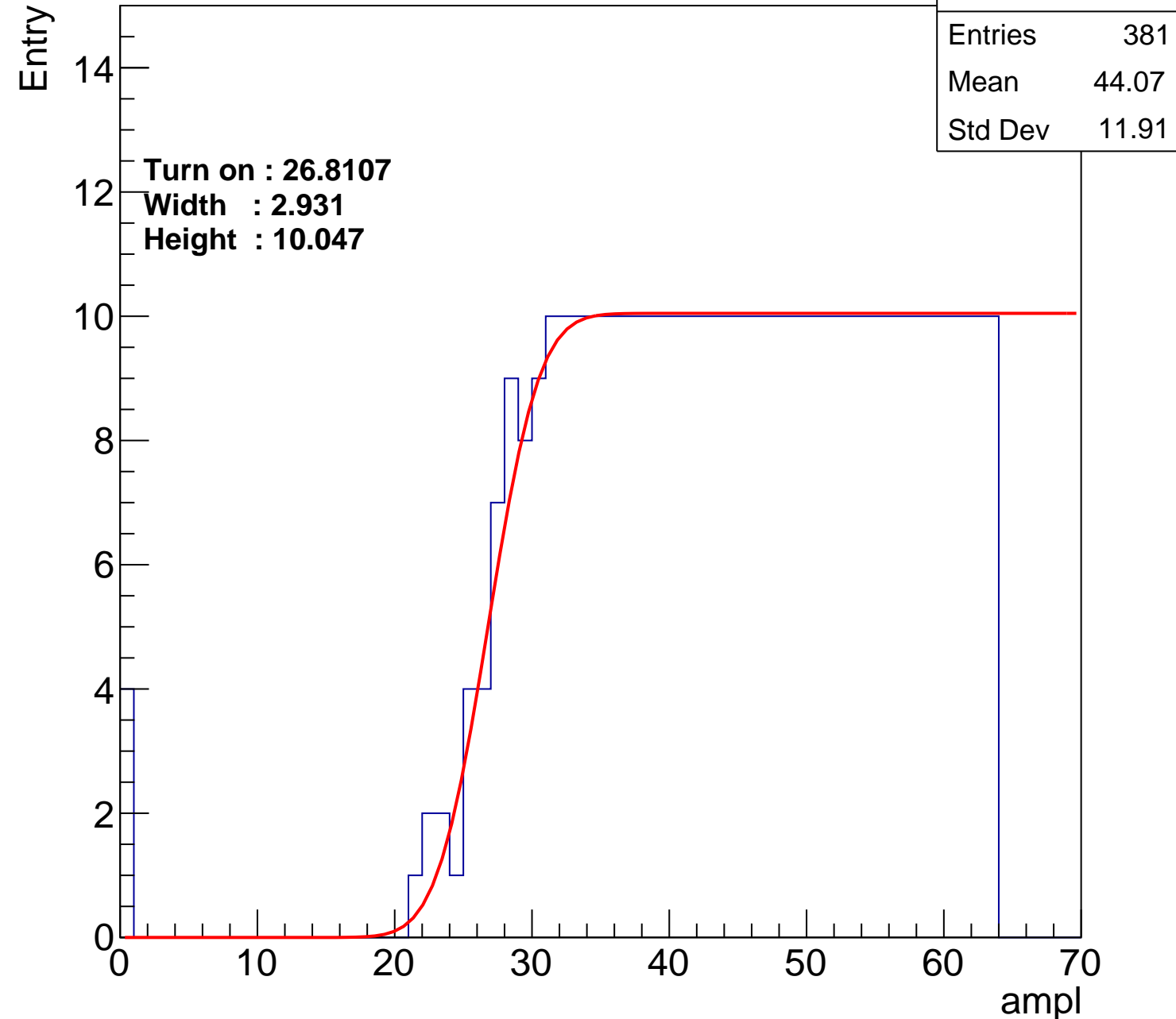
Width : 2.931

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch34

calib_packv5_042523_0143.root, FC#4, port A2

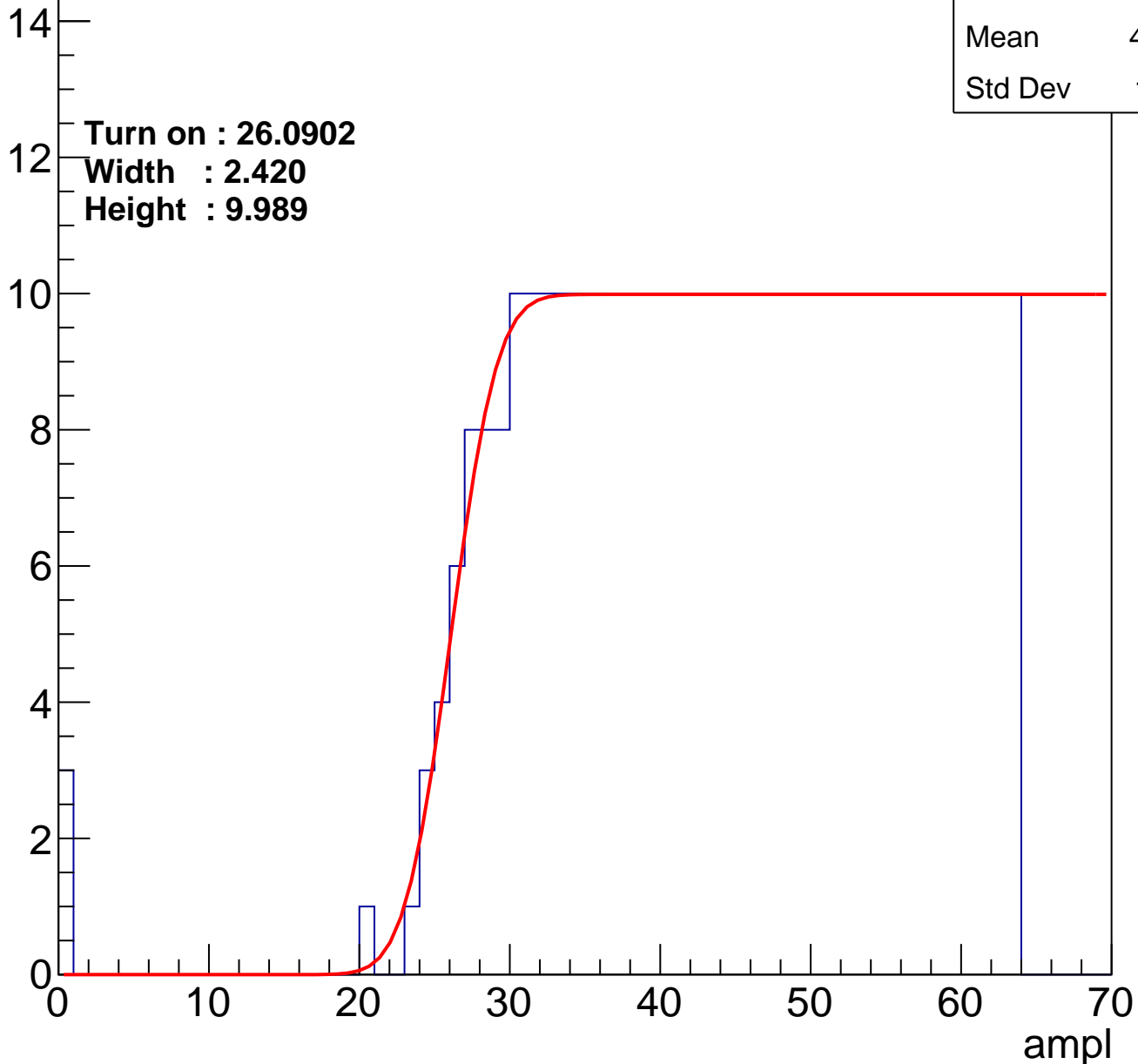
Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.0902

Width : 2.420

Height : 9.989

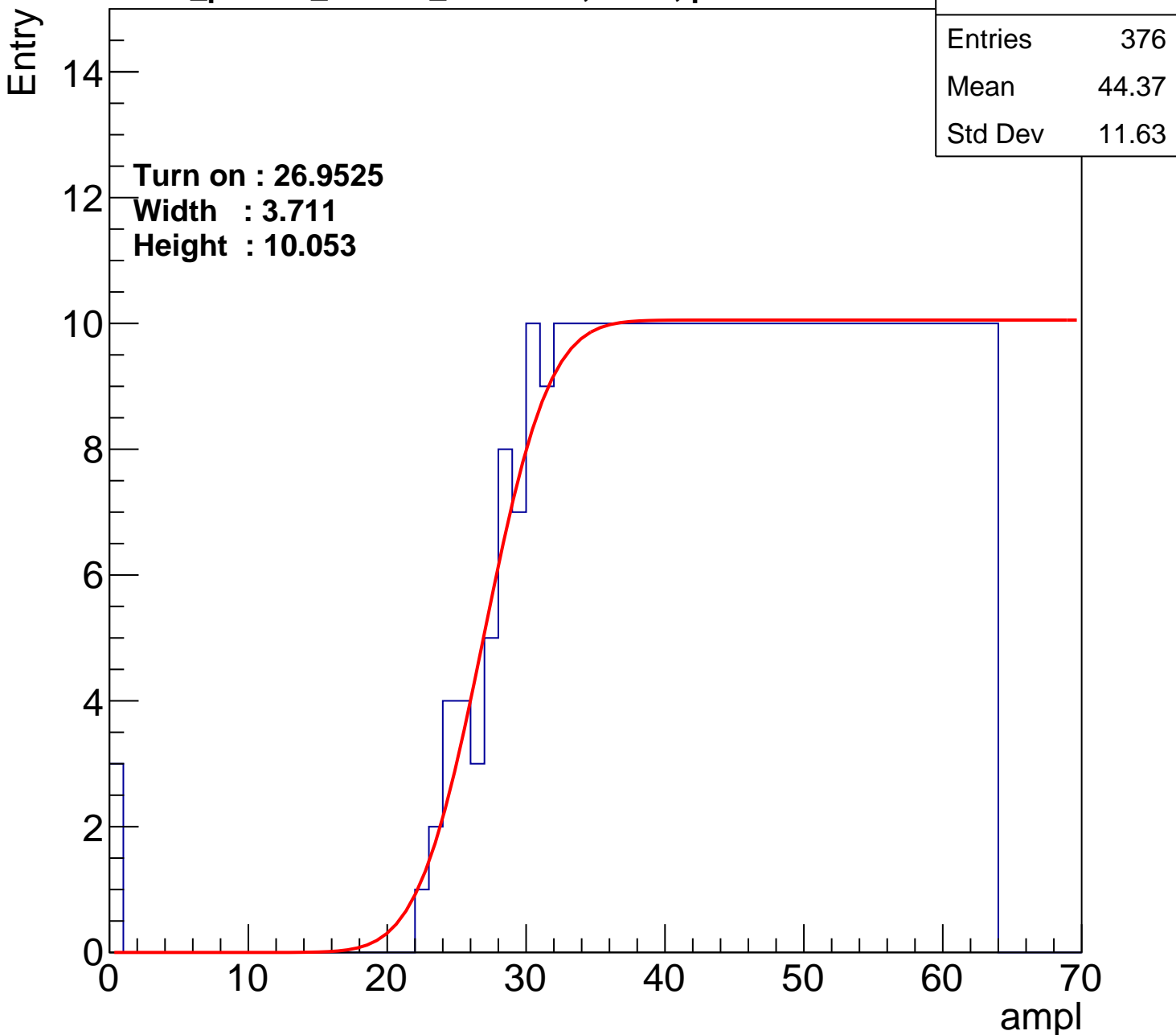
Entry



calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 26.9525
Width : 3.711
Height : 10.053



B1L100S, U22-ch36

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.3828

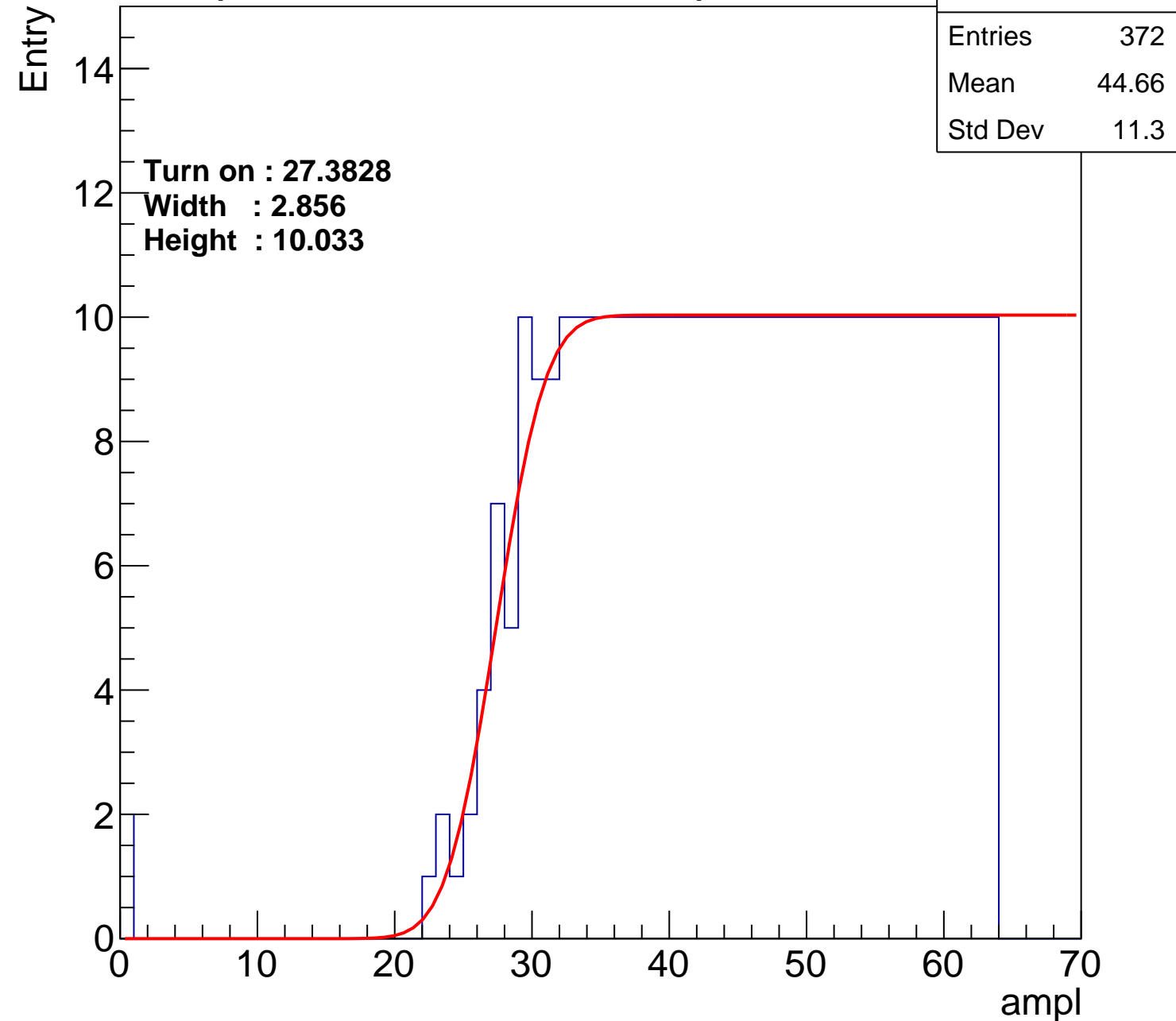
Width : 2.856

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch37

calib_packv5_042523_0143.root, FC#4, port A2

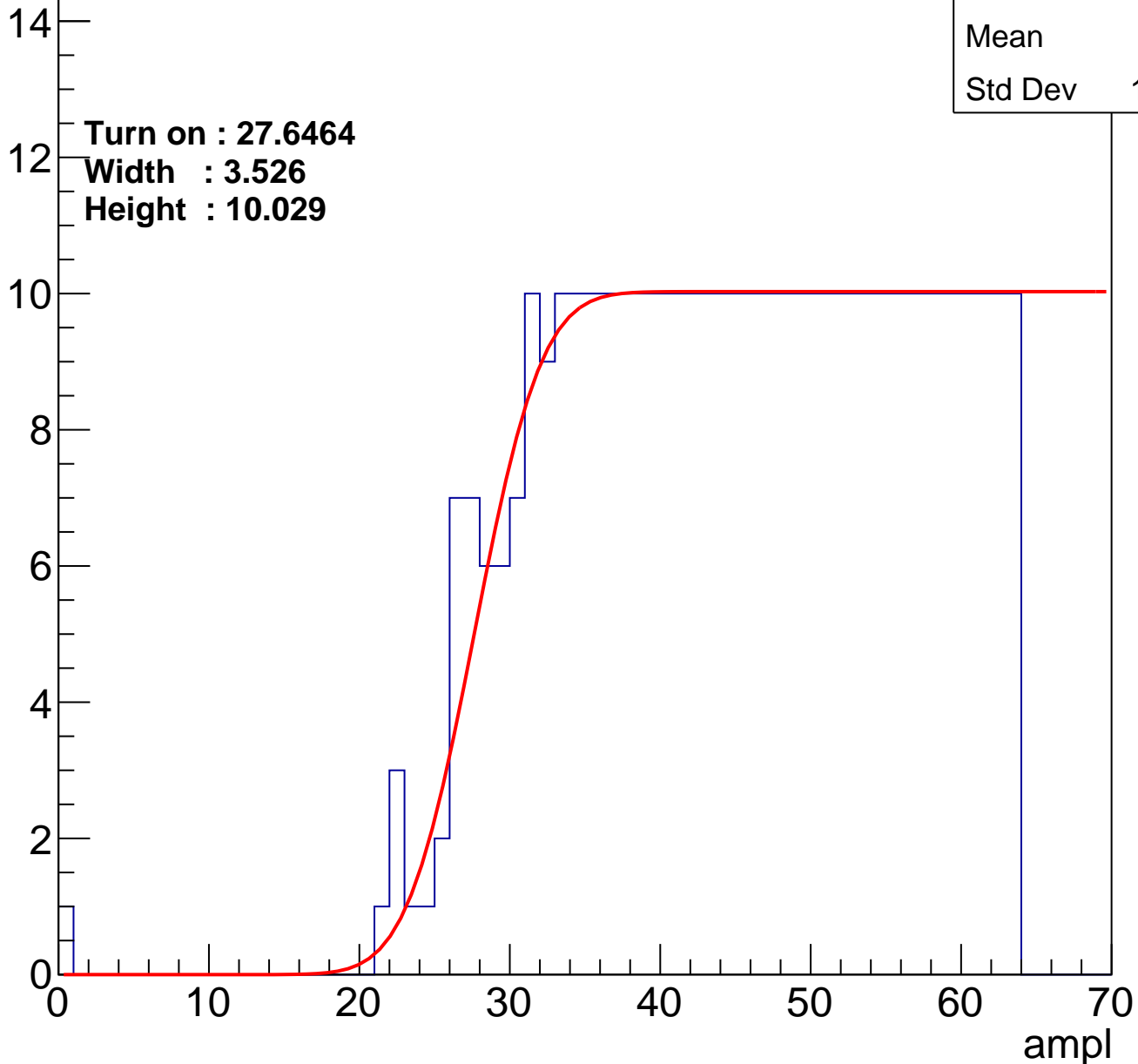
Entries	371
Mean	44.7
Std Dev	11.21

Turn on : 27.6464

Width : 3.526

Height : 10.029

Entry



B1L100S, U22-ch38

calib_packv5_042523_0143.root, FC#4, port A2

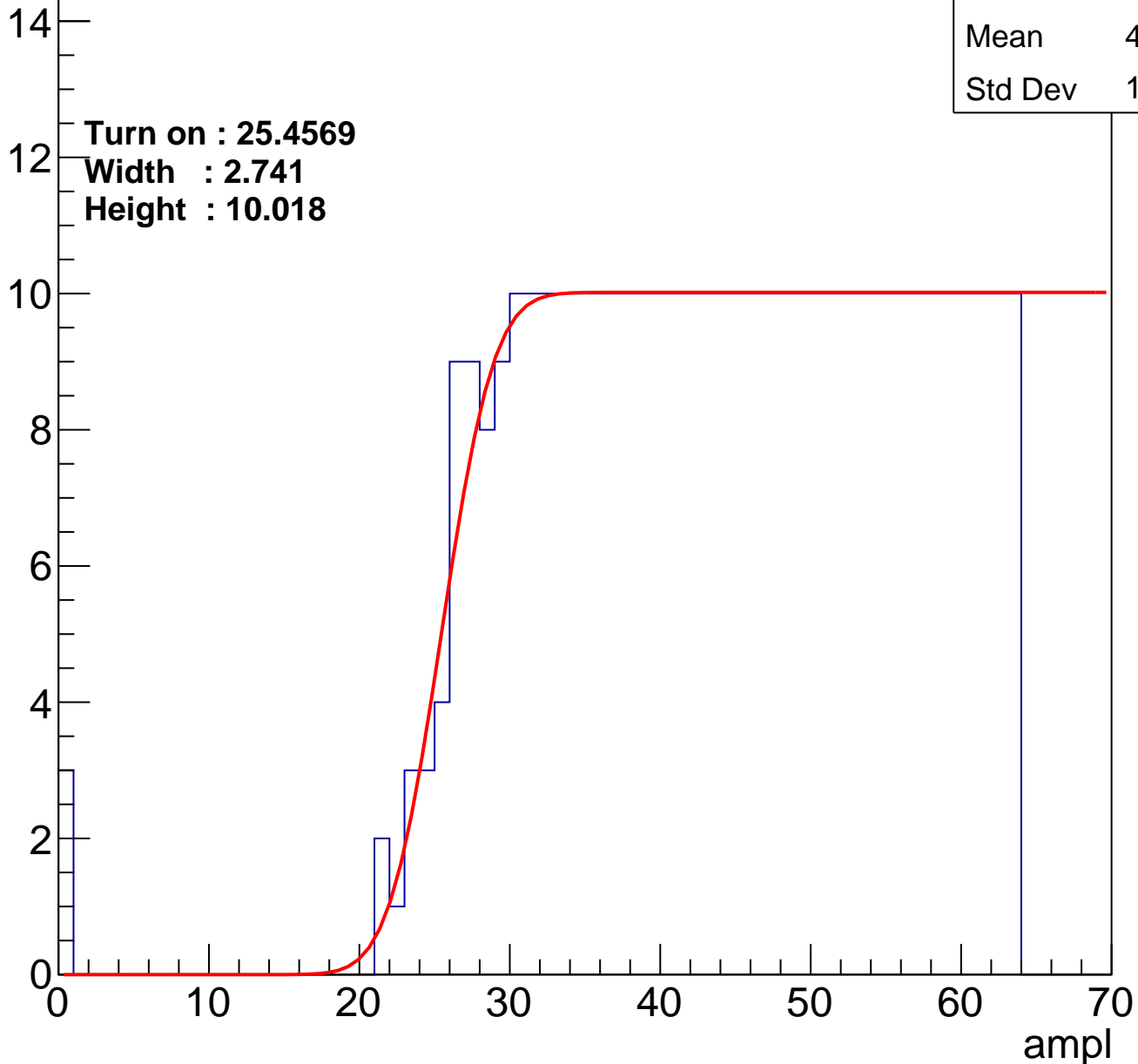
Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.4569

Width : 2.741

Height : 10.018

Entry



B1L100S, U22-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.63
Std Dev	11.22

Turn on : 27.0901

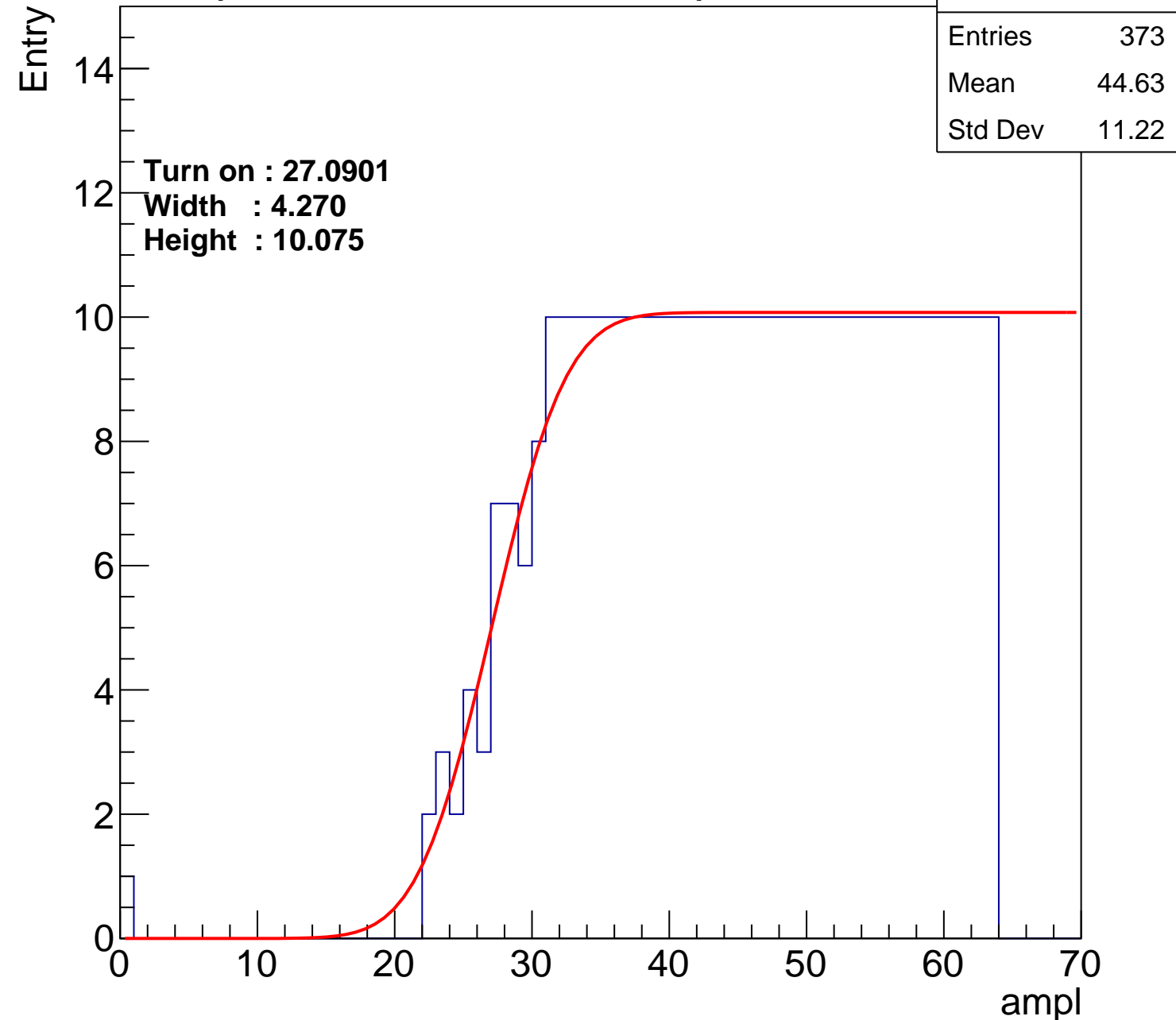
Width : 4.270

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch40

calib_packv5_042523_0143.root, FC#4, port A2

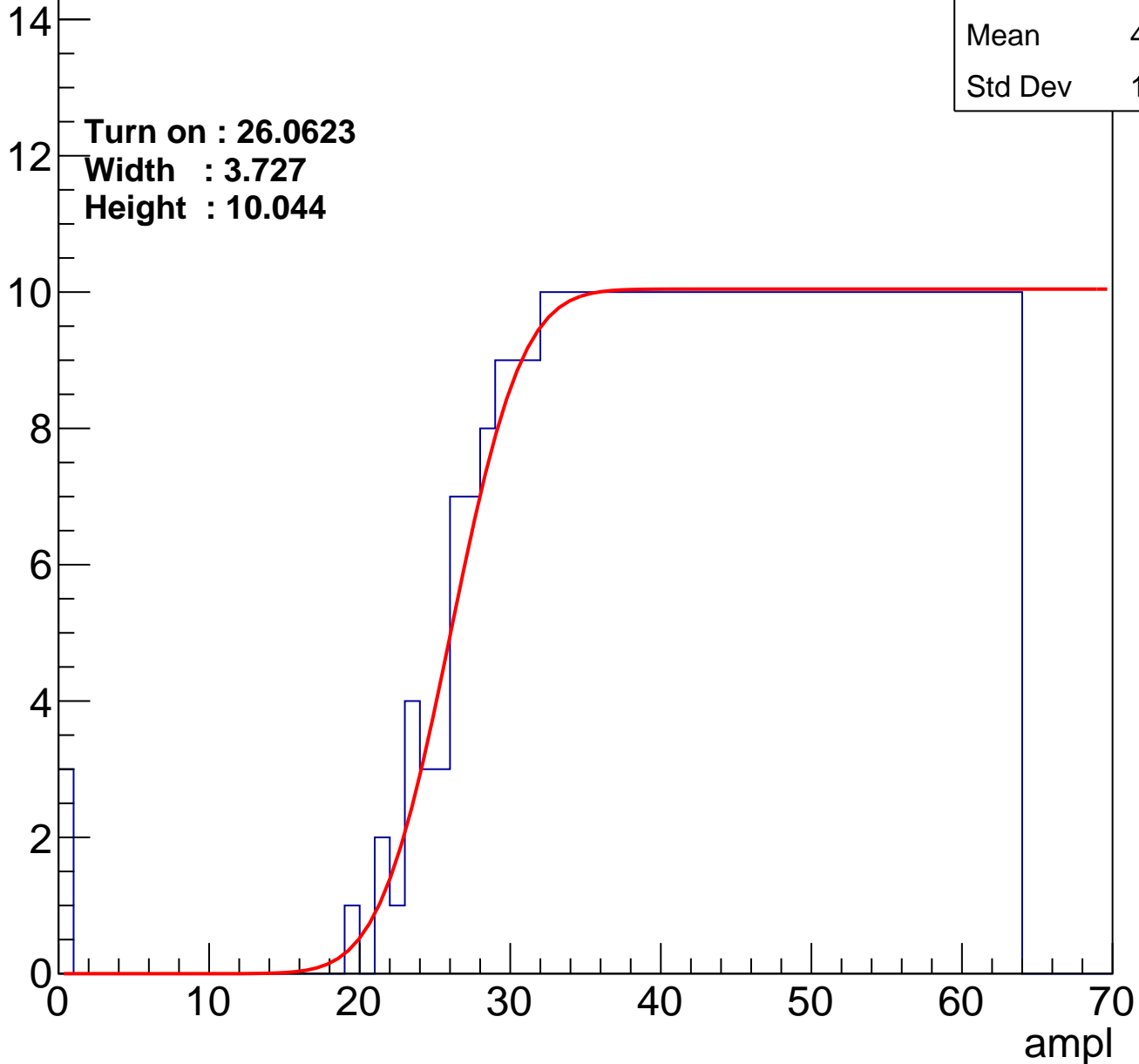
Entries	386
Mean	43.85
Std Dev	11.92

Turn on : 26.0623

Width : 3.727

Height : 10.044

Entry



B1L100S, U22-ch41

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.21
Std Dev	11.21

Turn on : 28.2889

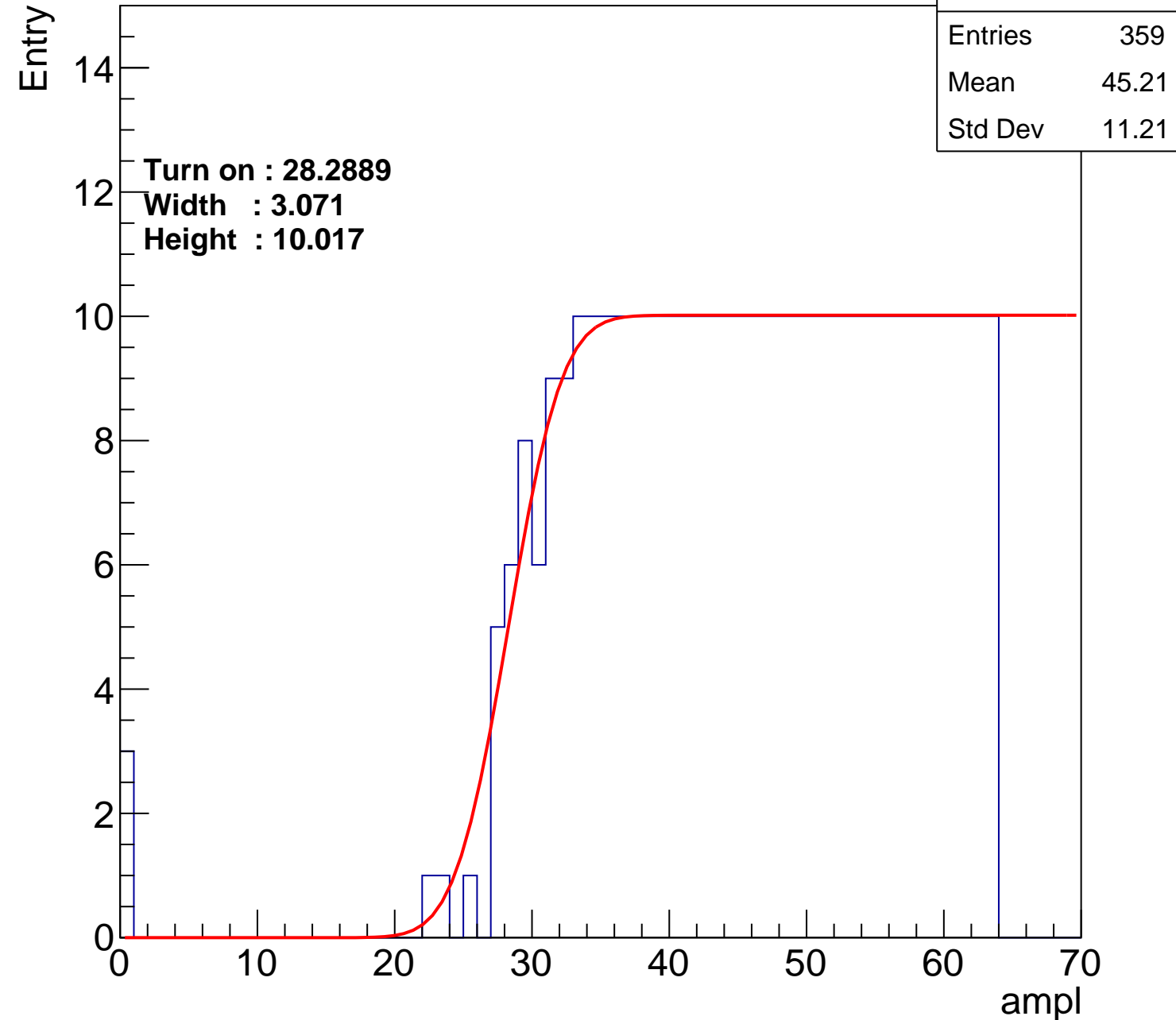
Width : 3.071

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch42

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.37
Std Dev	11.38

Turn on : 26.7204

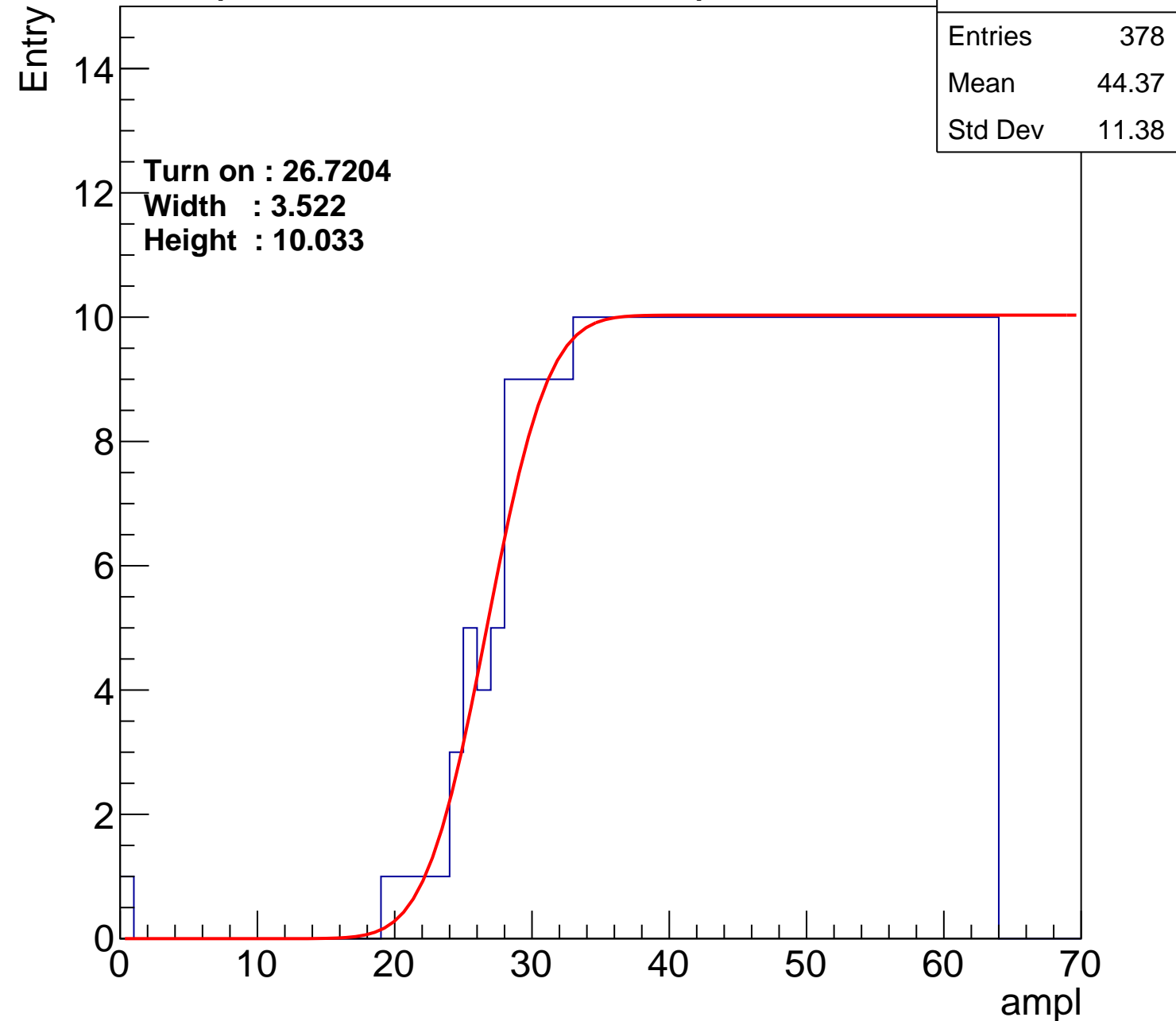
Width : 3.522

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	351
Mean	45.82
Std Dev	10.53

Turn on : 29.1997

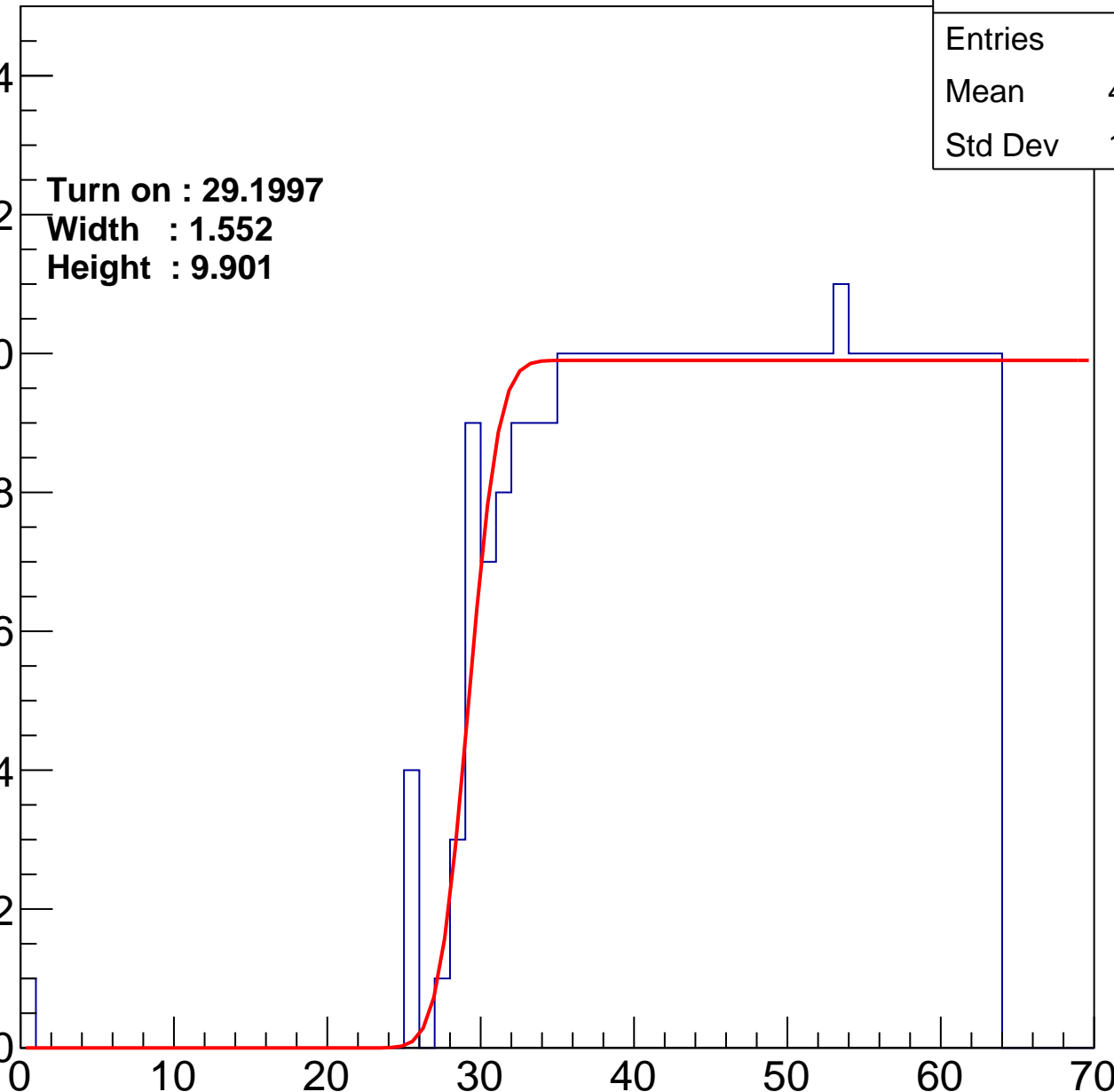
Width : 1.552

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch44

calib_packv5_042523_0143.root, FC#4, port A2

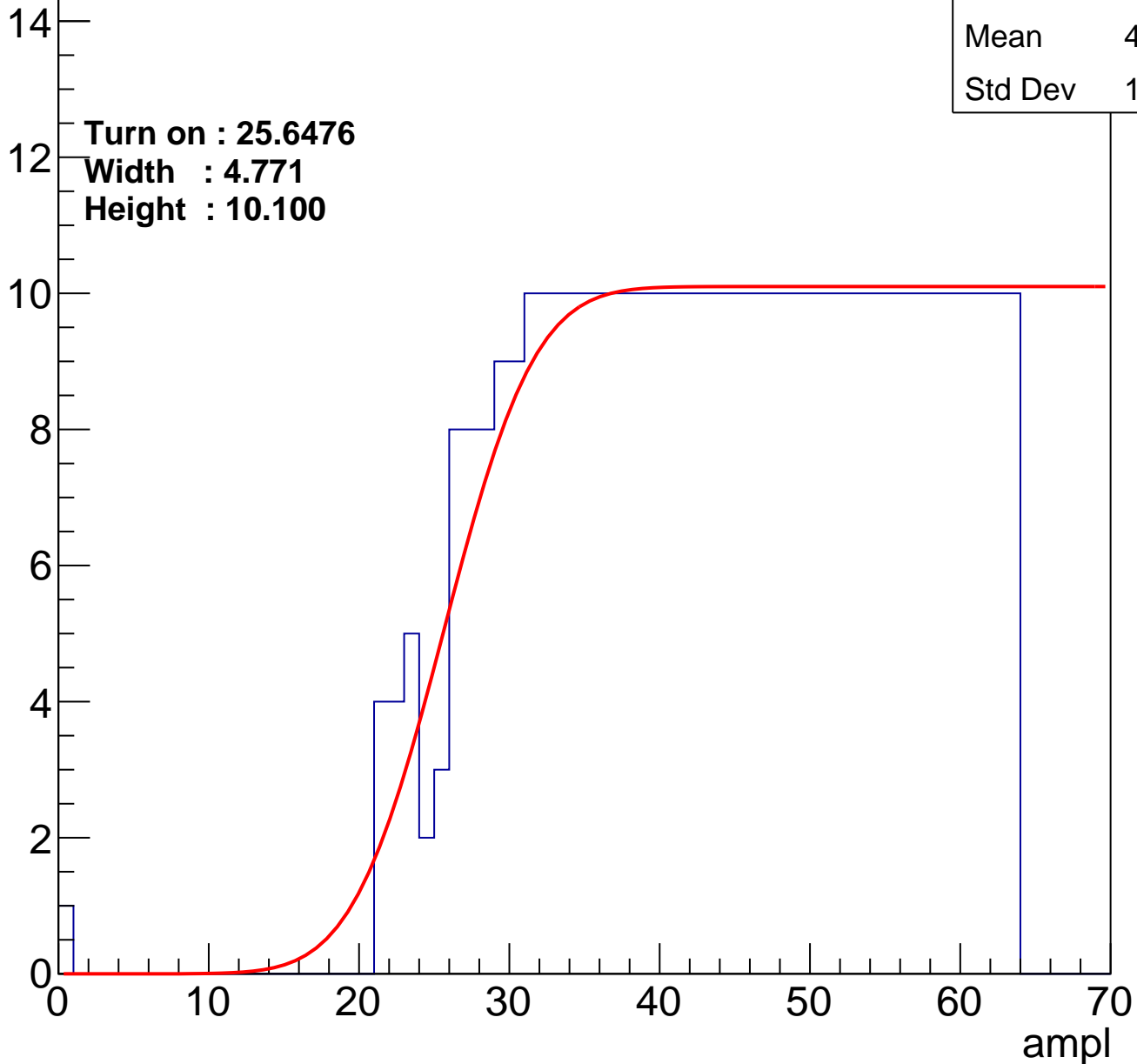
Entries	391
Mean	43.73
Std Dev	11.72

Turn on : 25.6476

Width : 4.771

Height : 10.100

Entry



B1L100S, U22-ch45

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.38
Std Dev	11.67

Turn on : 27.1301

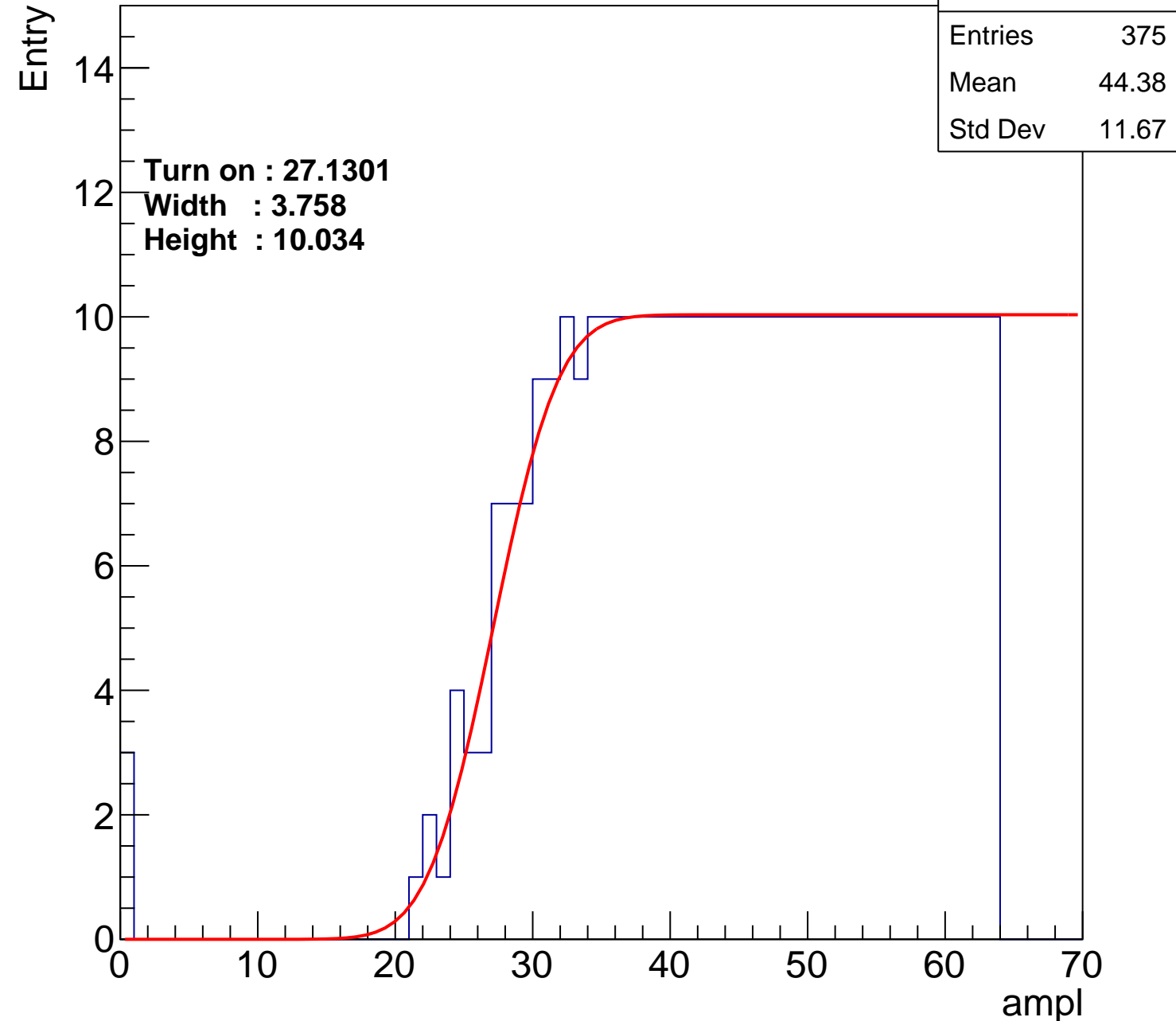
Width : 3.758

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch46

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.86
Std Dev	11.72

Turn on : 25.2021

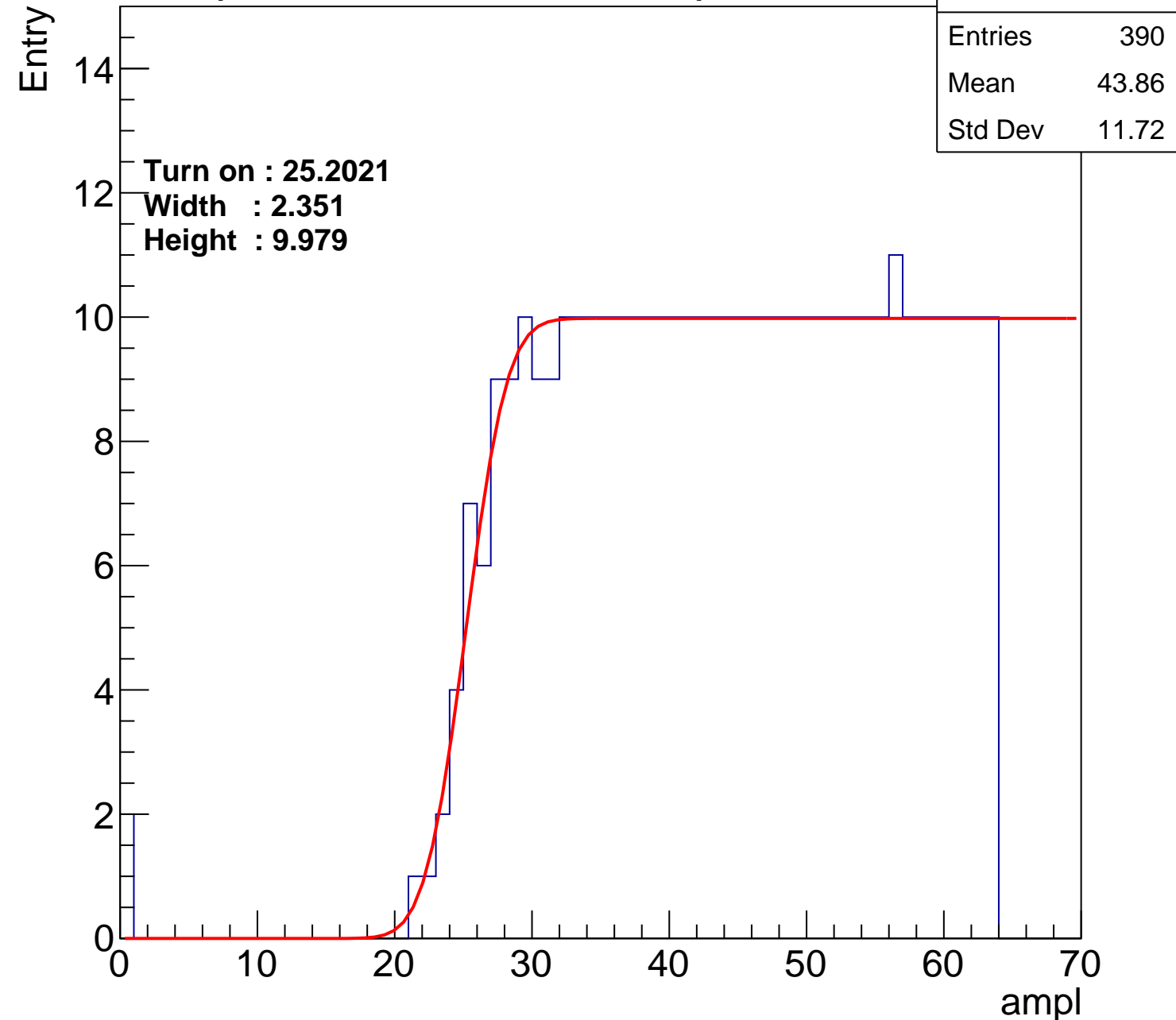
Width : 2.351

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch47

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.63
Std Dev	11.35

Turn on : 27.6713

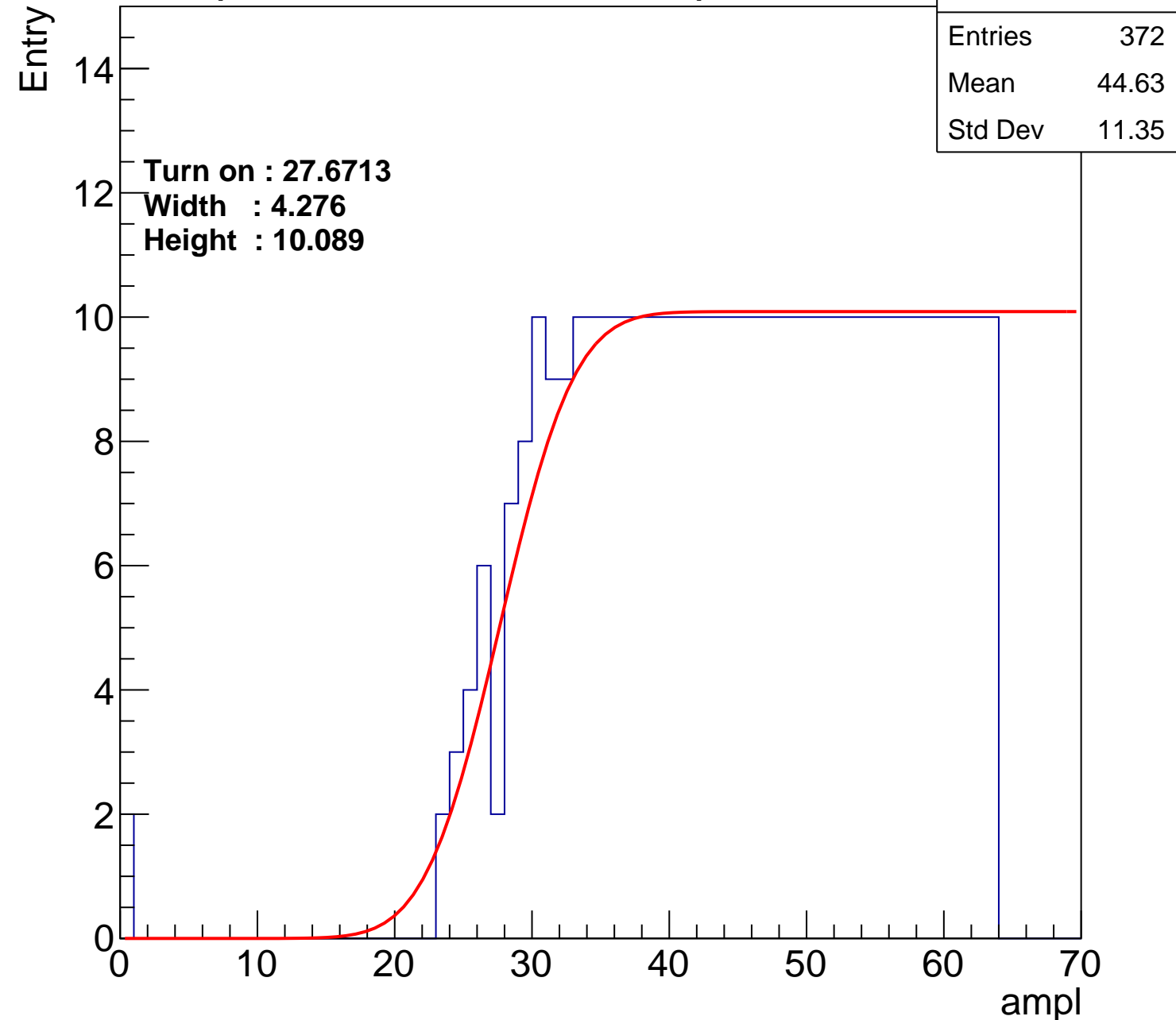
Width : 4.276

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch48

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.63
Std Dev	11.21

Turn on : 26.7550

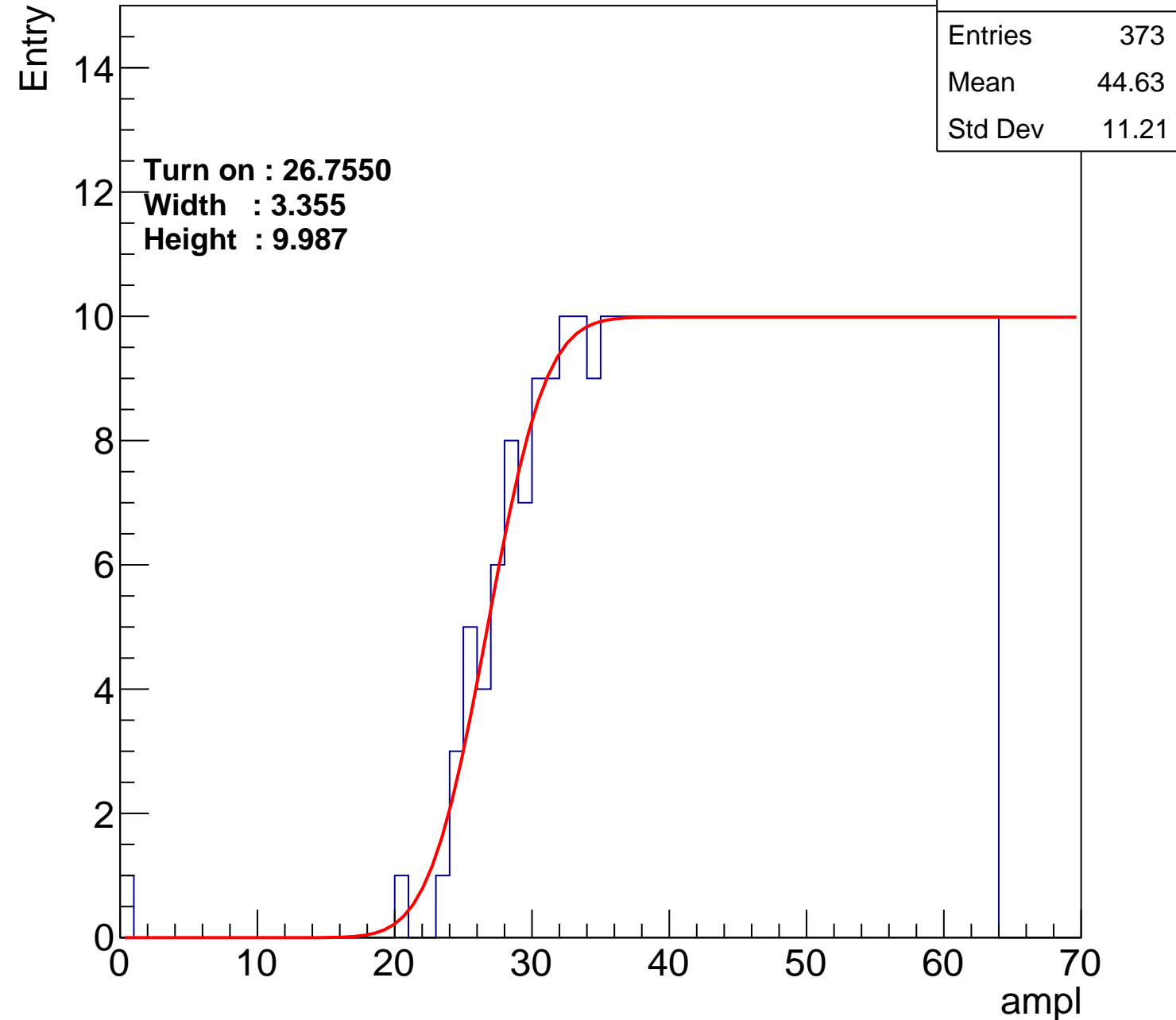
Width : 3.355

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch49

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.84
Std Dev	11.88

Turn on : 25.7682

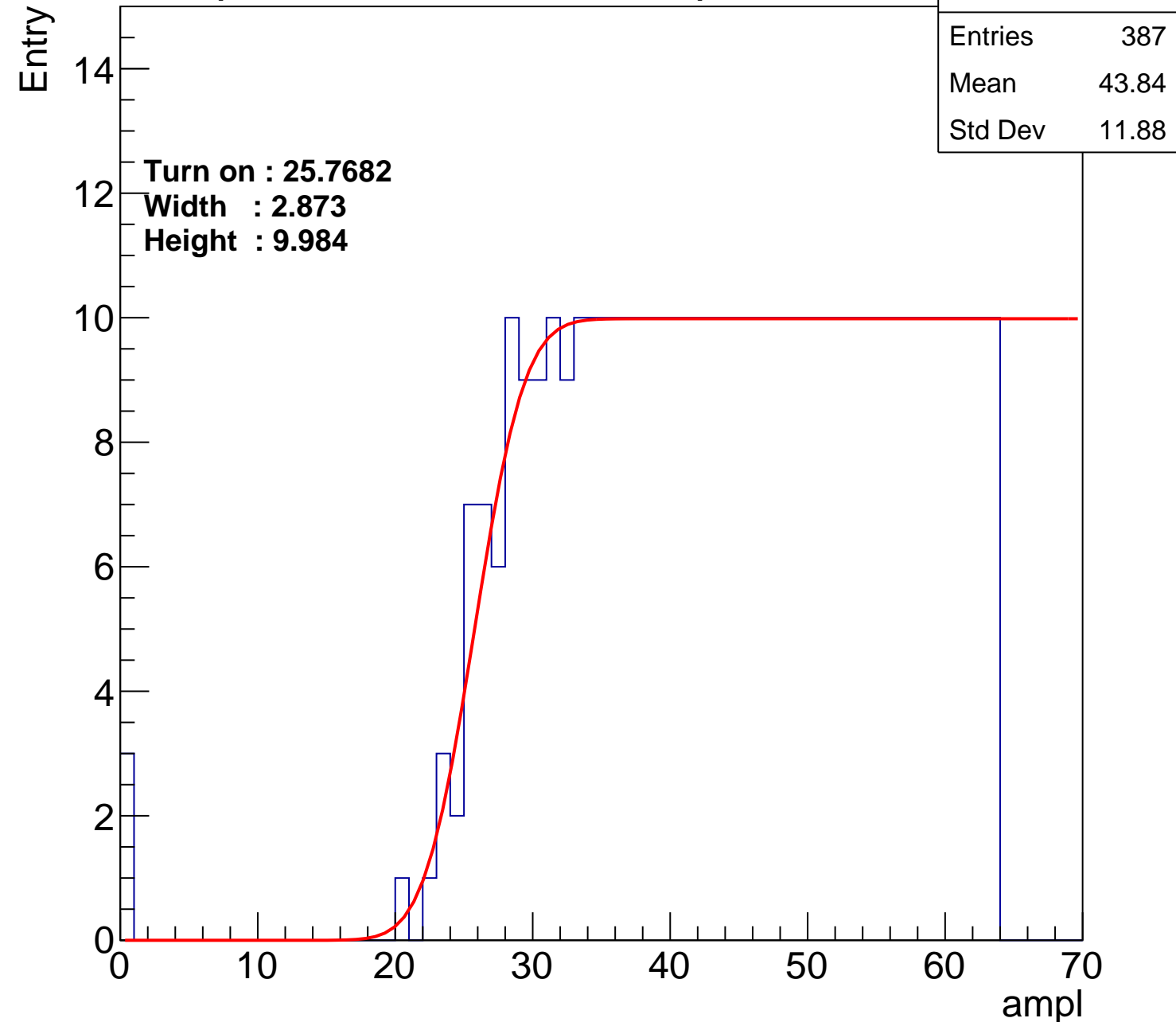
Width : 2.873

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch50

calib_packv5_042523_0143.root, FC#4, port A2

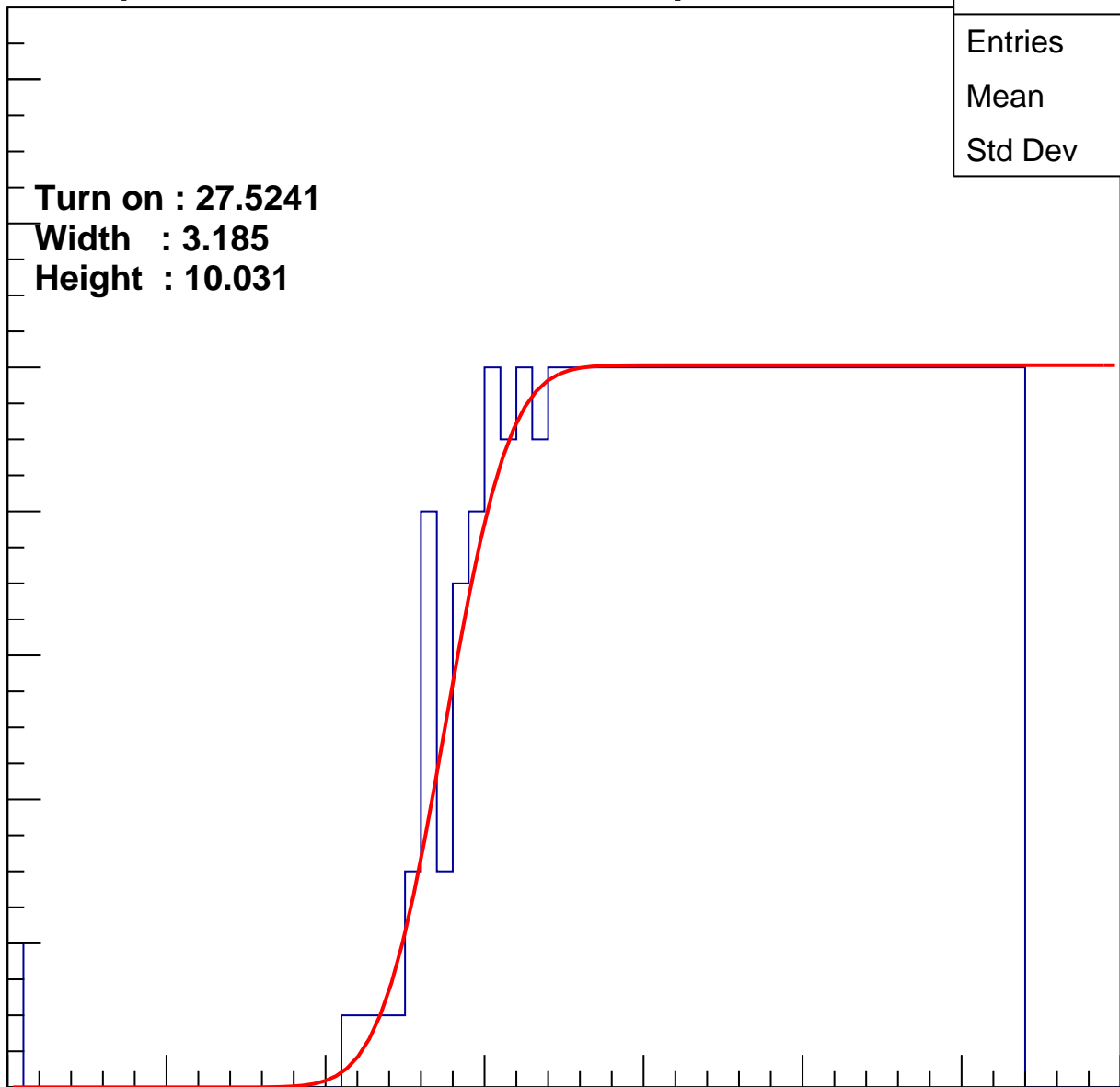
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5241
Width : 3.185
Height : 10.031

Entries	373
Mean	44.58
Std Dev	11.38

ampl



B1L100S, U22-ch51

calib_packv5_042523_0143.root, FC#4, port A2

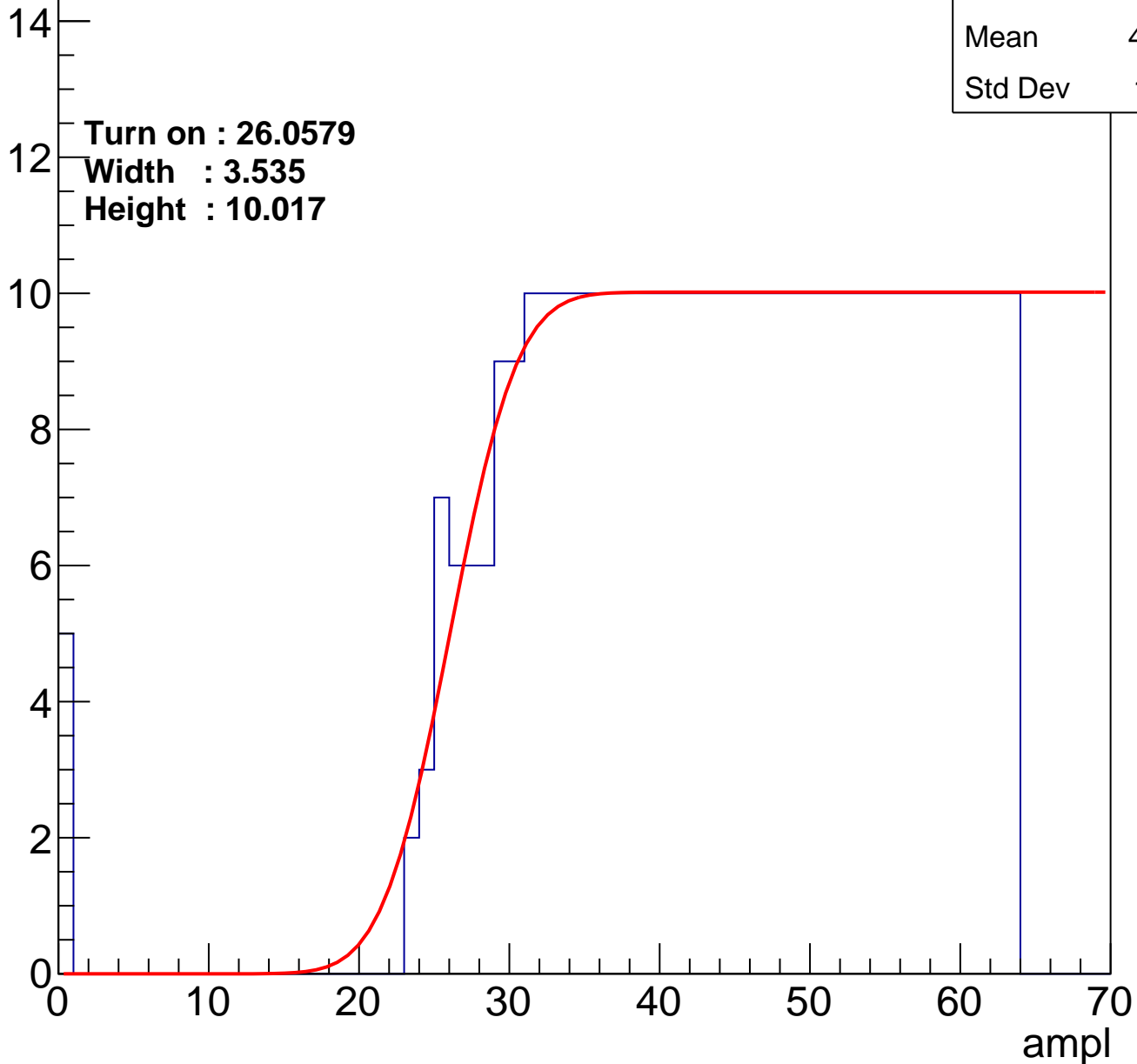
Entries	383
Mean	43.92
Std Dev	12.11

Turn on : 26.0579

Width : 3.535

Height : 10.017

Entry



B1L100S, U22-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.67
Std Dev	11.52

Turn on : 27.6857

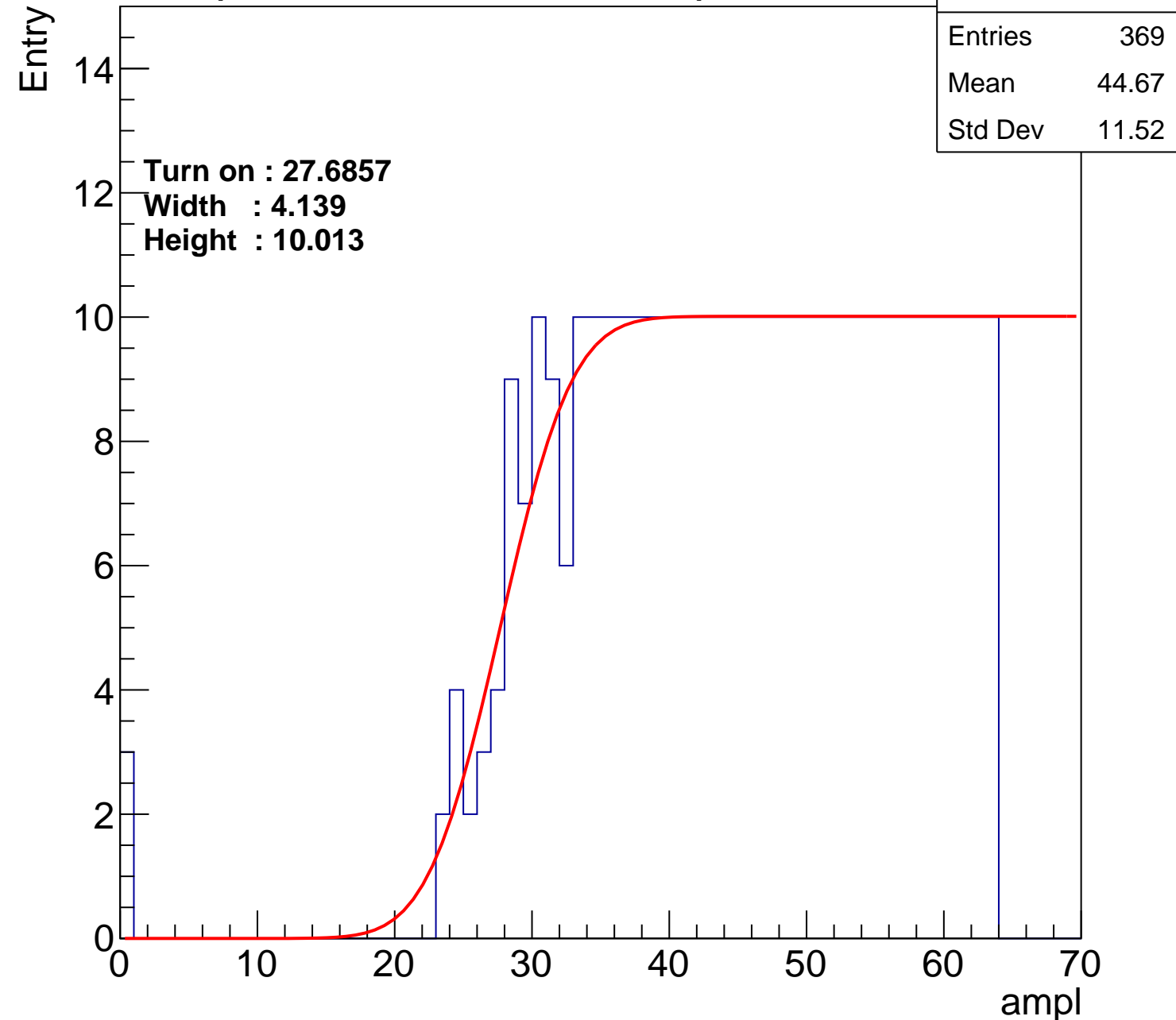
Width : 4.139

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch53

calib_packv5_042523_0143.root, FC#4, port A2

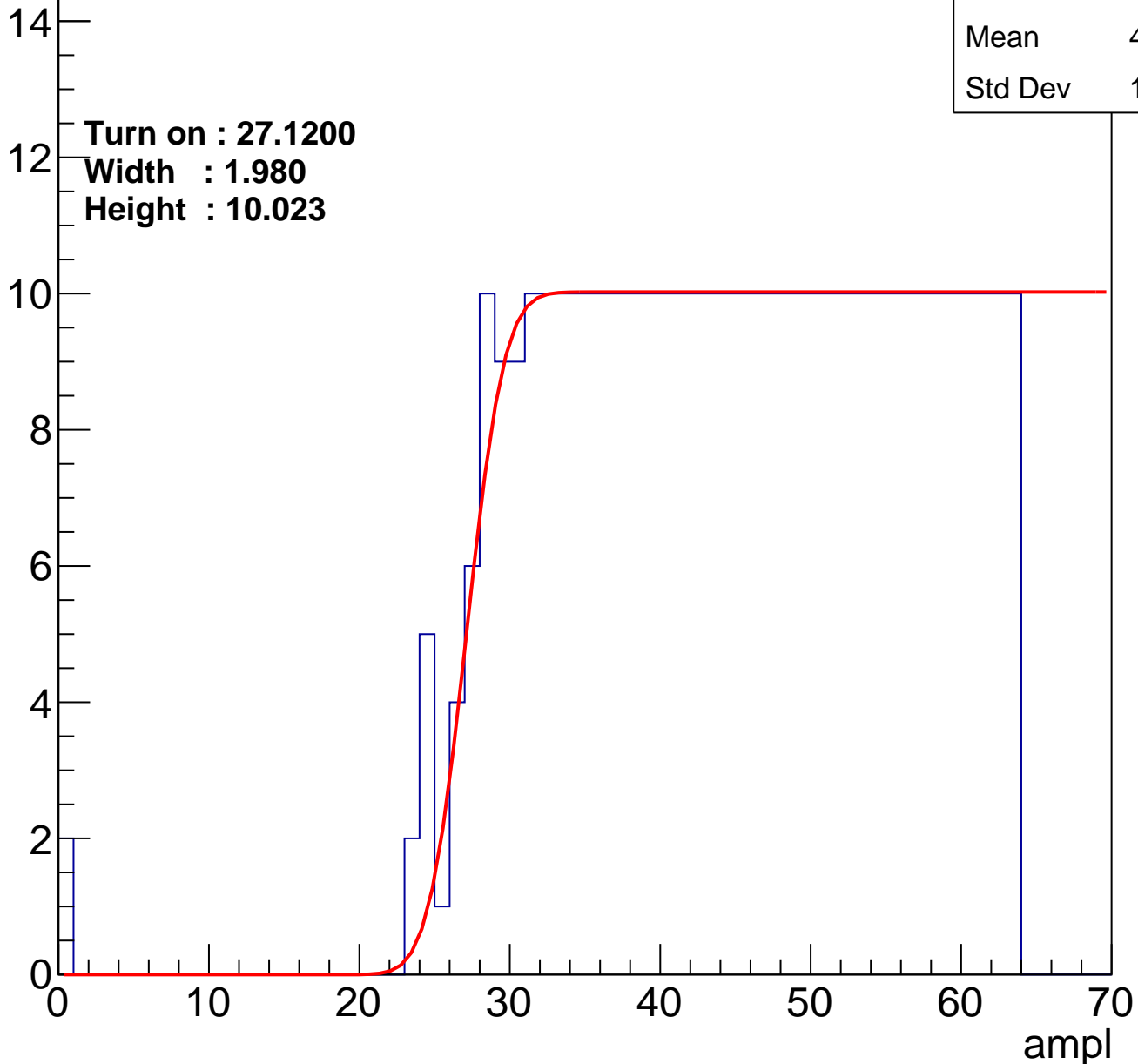
Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 27.1200

Width : 1.980

Height : 10.023

Entry



B1L100S, U22-ch54

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.81
Std Dev	11.02

Turn on : 27.1743

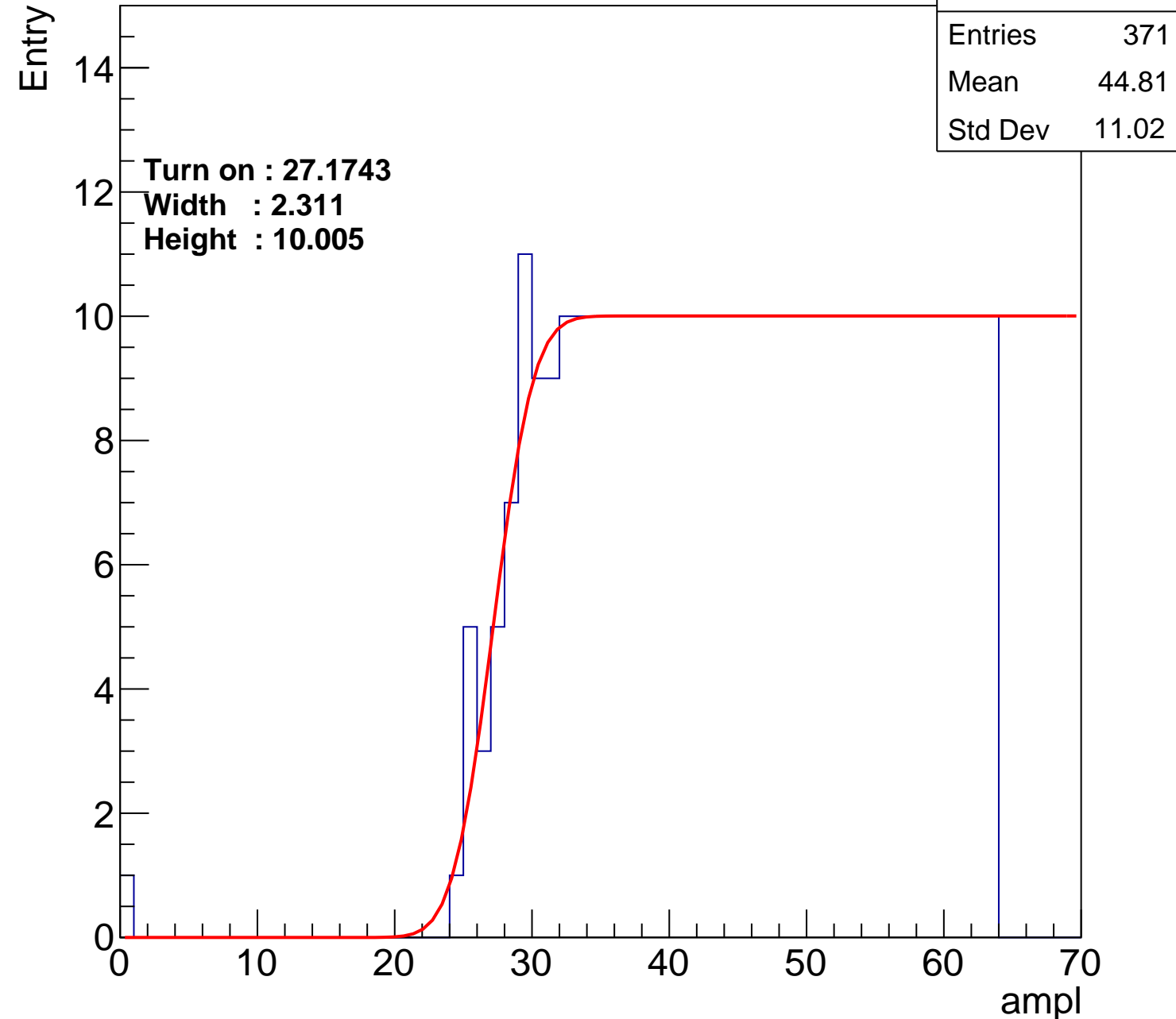
Width : 2.311

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch55

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.4507

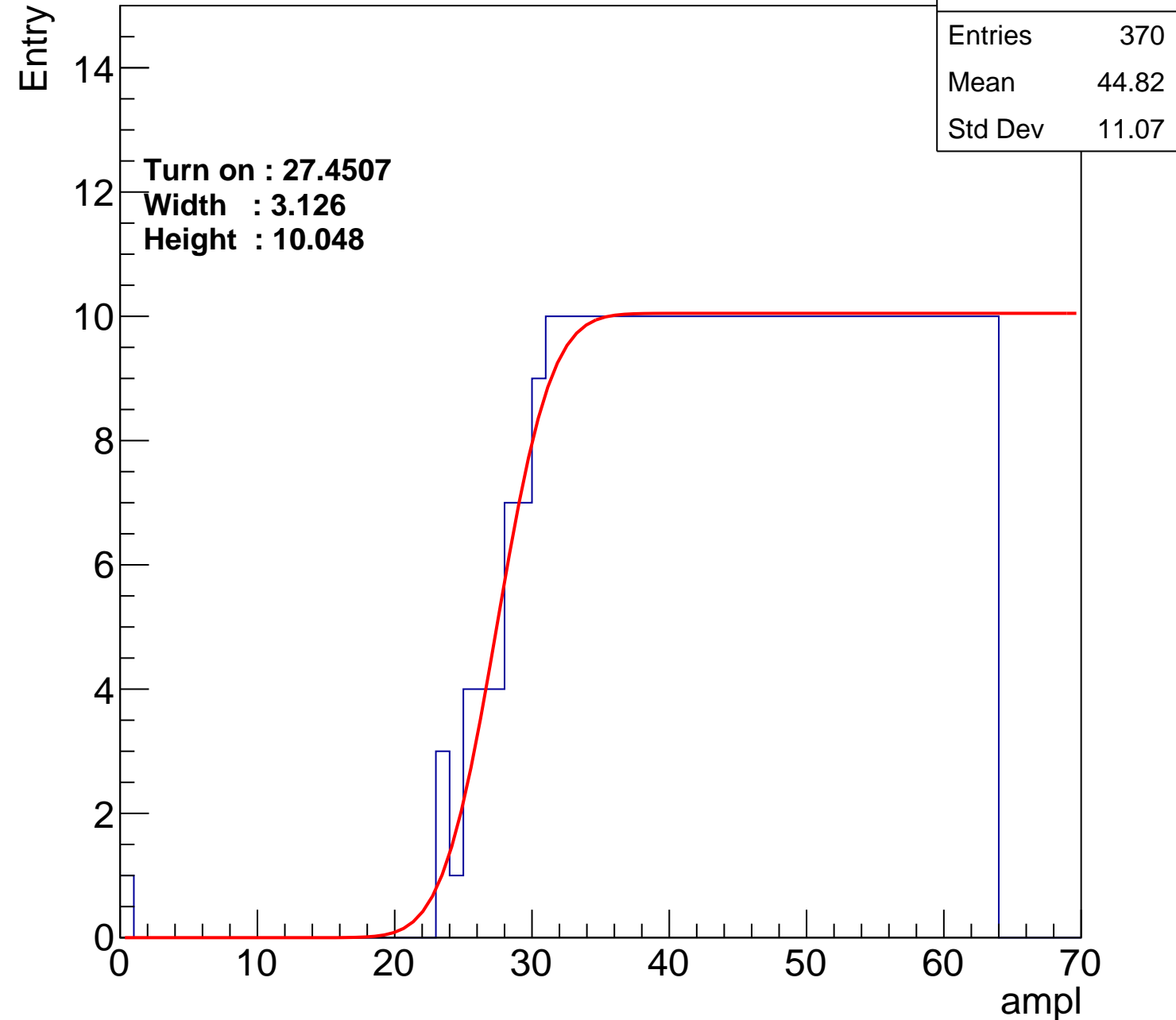
Width : 3.126

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch56

calib_packv5_042523_0143.root, FC#4, port A2

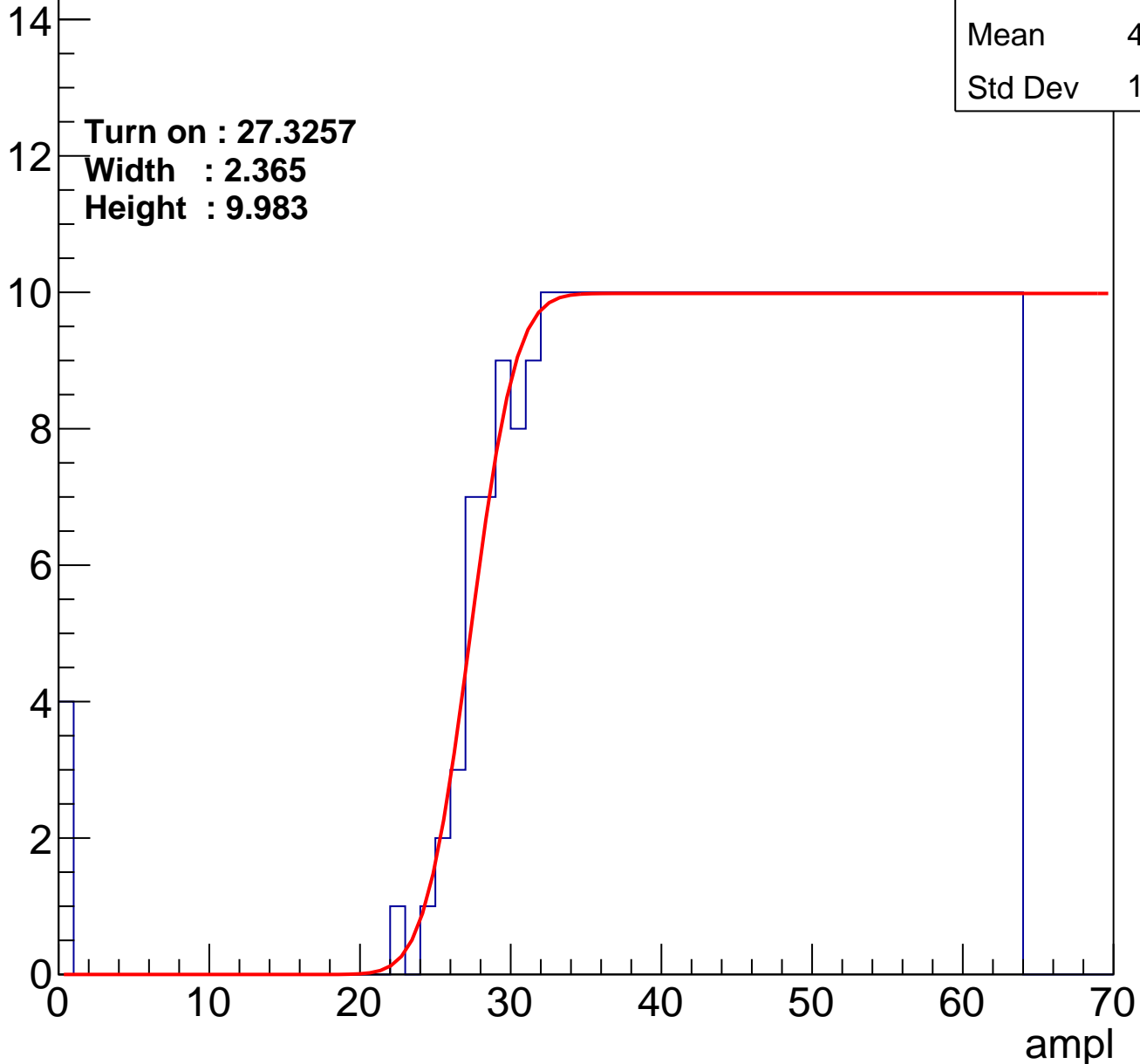
Entries	371
Mean	44.58
Std Dev	11.65

Turn on : 27.3257

Width : 2.365

Height : 9.983

Entry



B1L100S, U22-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	44.88
Std Dev	11.68

Turn on : 28.3596

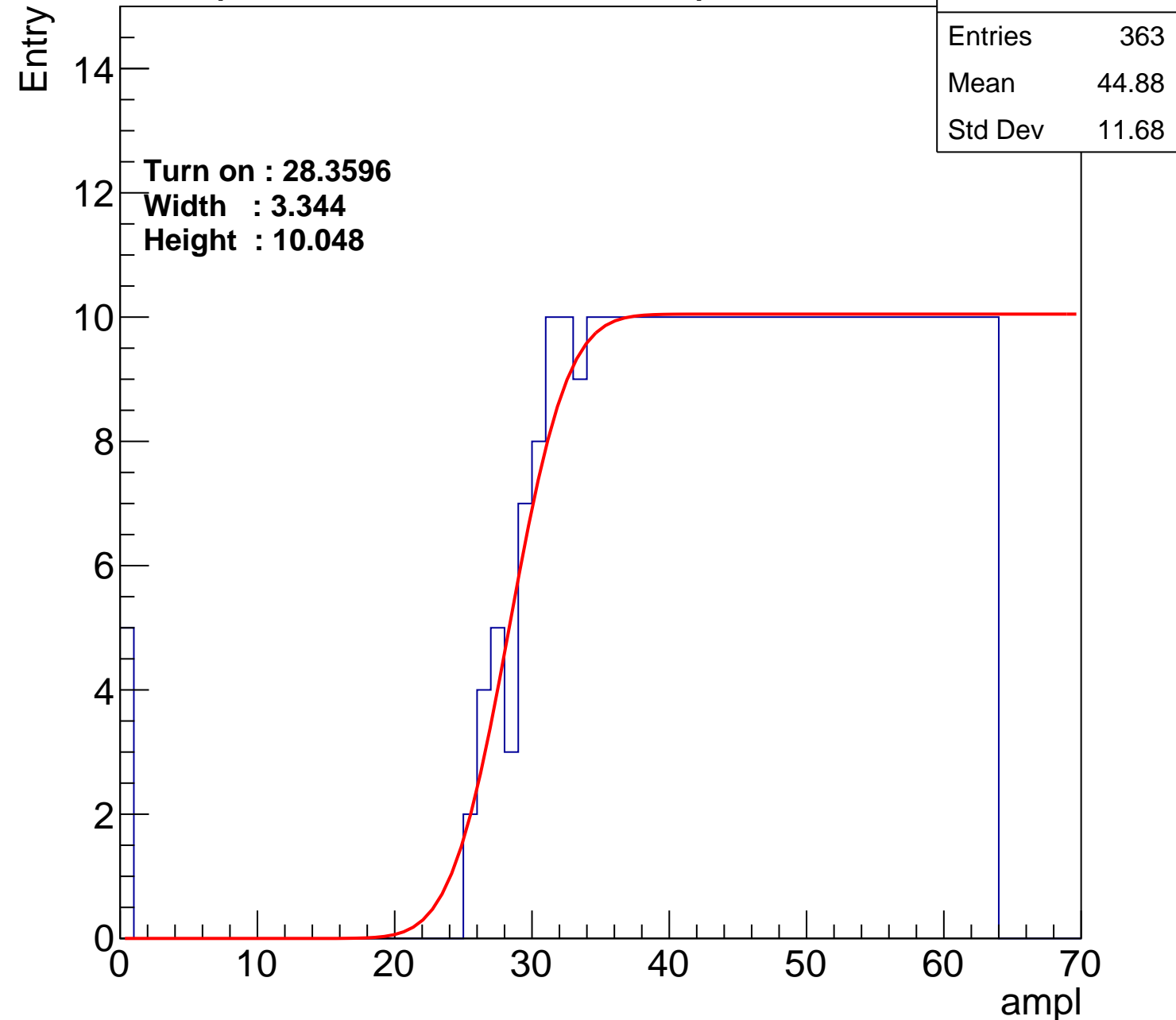
Width : 3.344

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch58

calib_packv5_042523_0143.root, FC#4, port A2

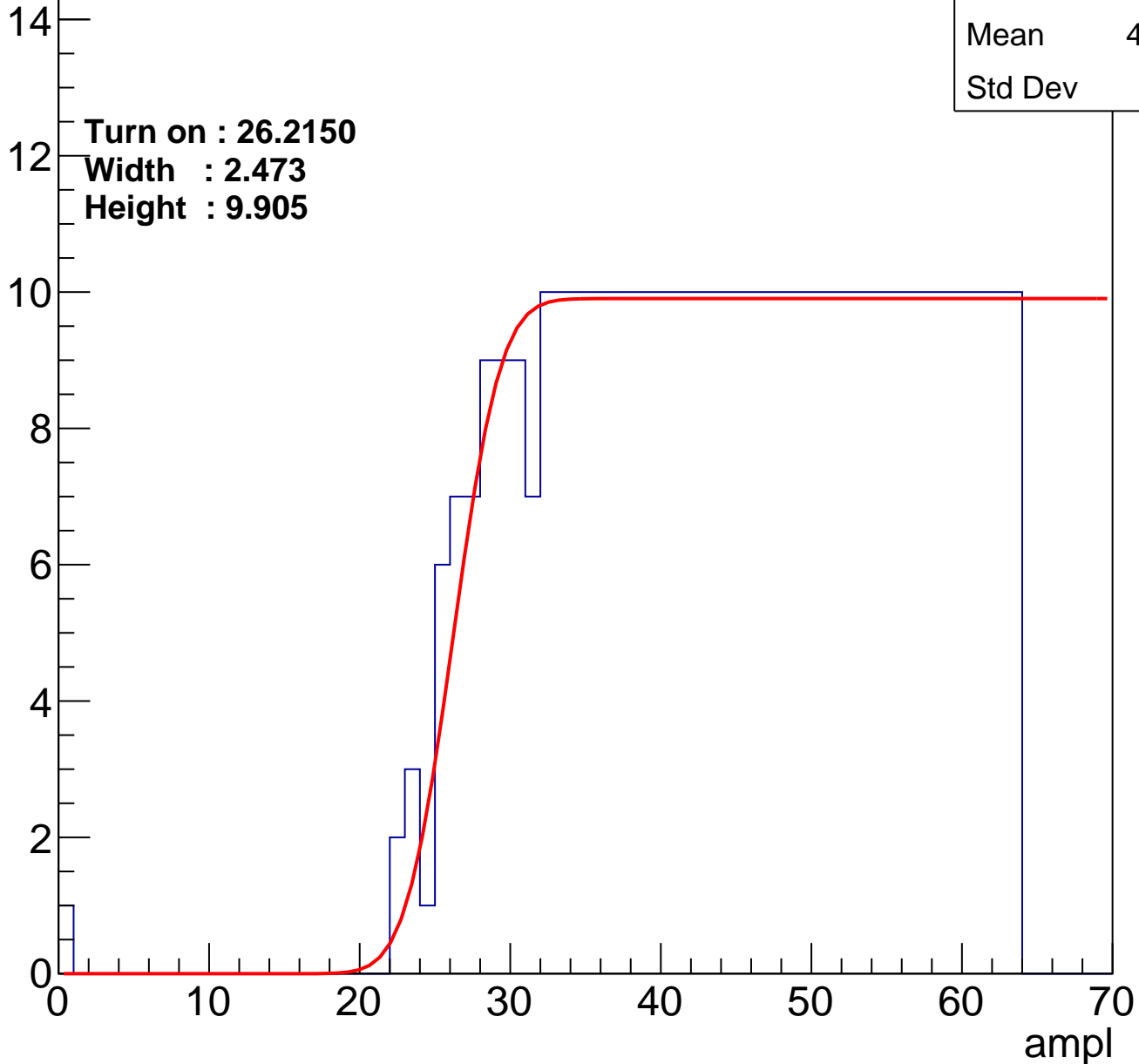
Entries	381
Mean	44.25
Std Dev	11.4

Turn on : 26.2150

Width : 2.473

Height : 9.905

Entry



B1L100S, U22-ch59

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.31
Std Dev	11.33

Turn on : 29.2827

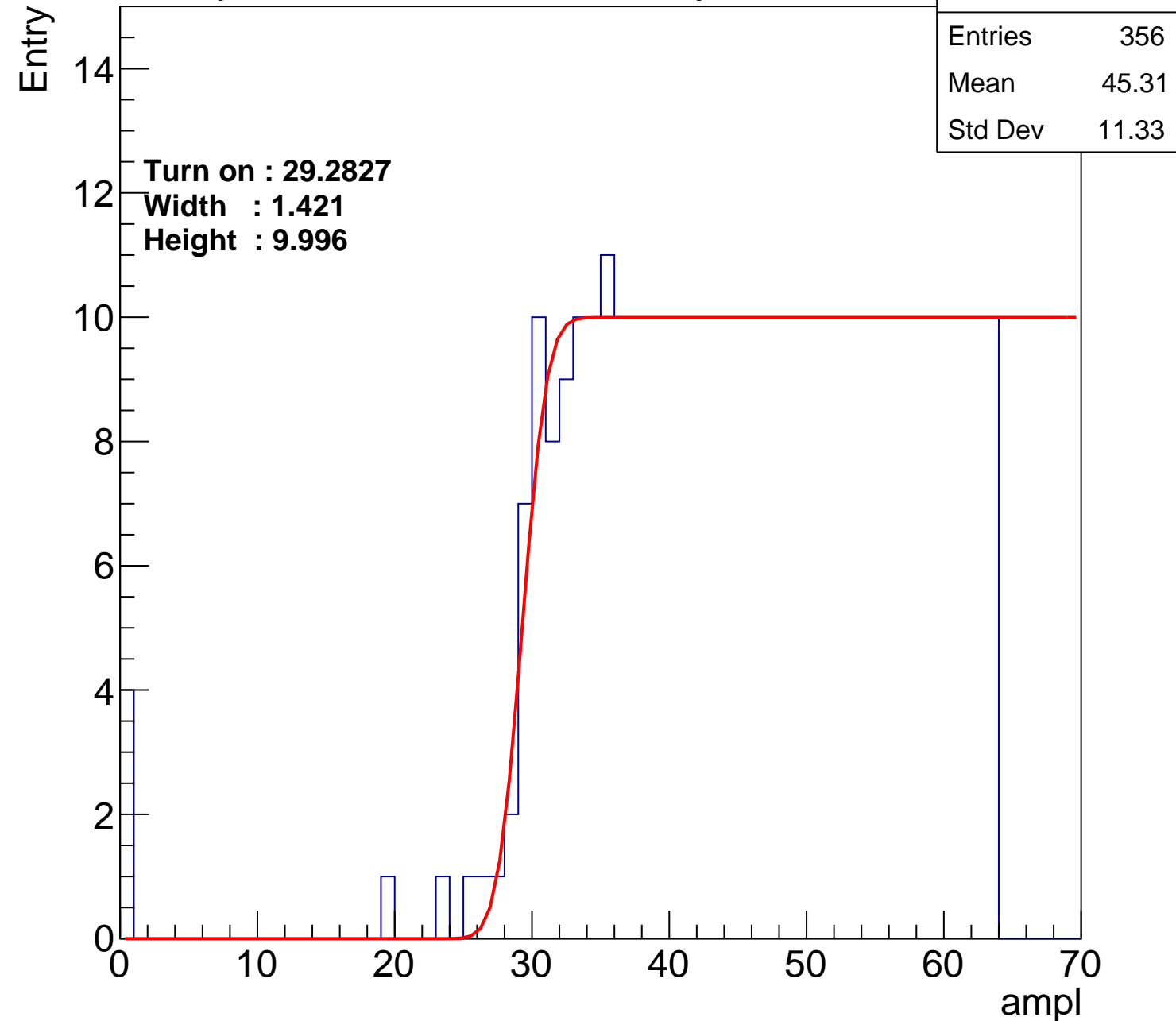
Width : 1.421

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch60

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.09
Std Dev	11.67

Turn on : 25.9266

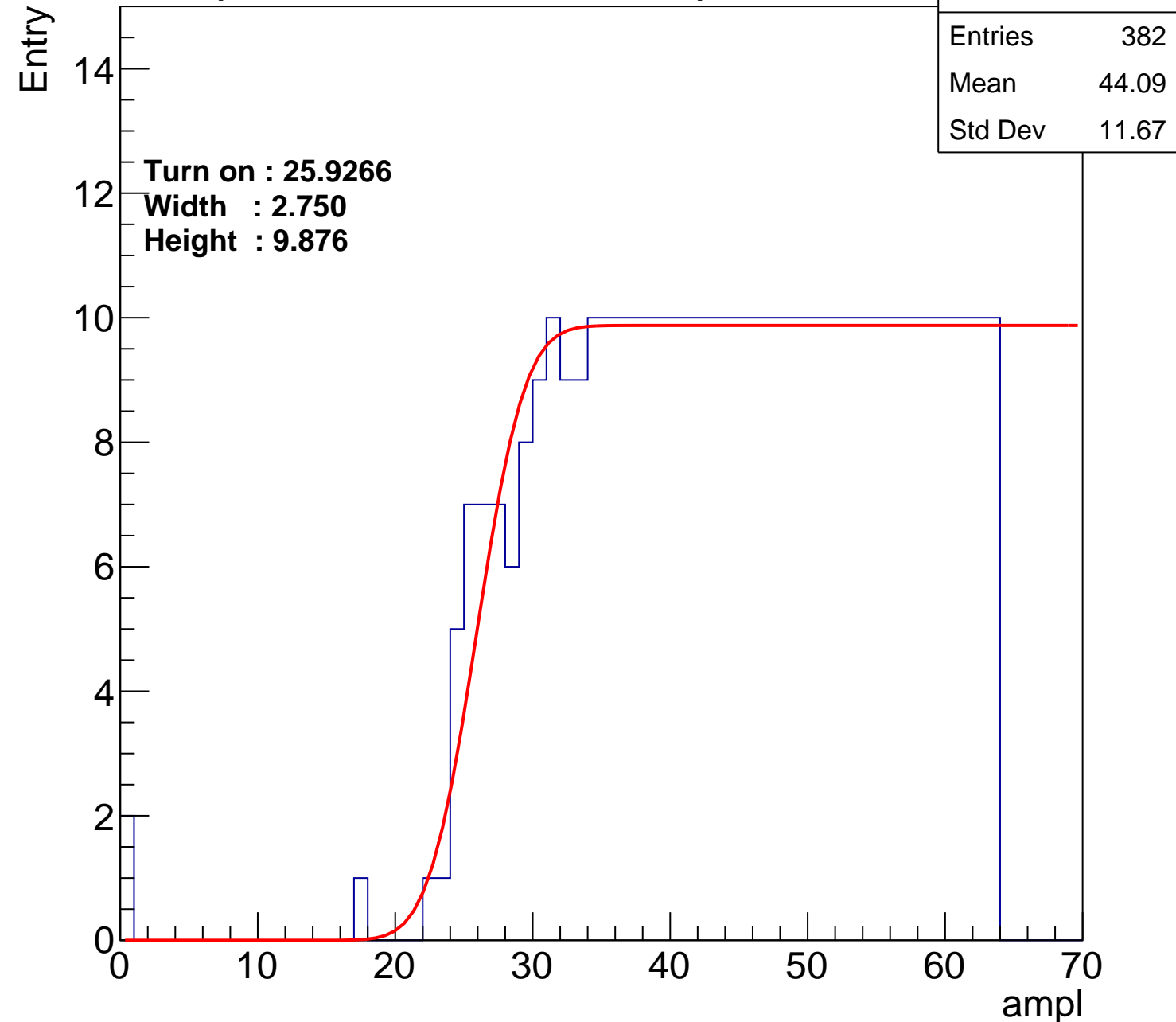
Width : 2.750

Height : 9.876

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 27.0437

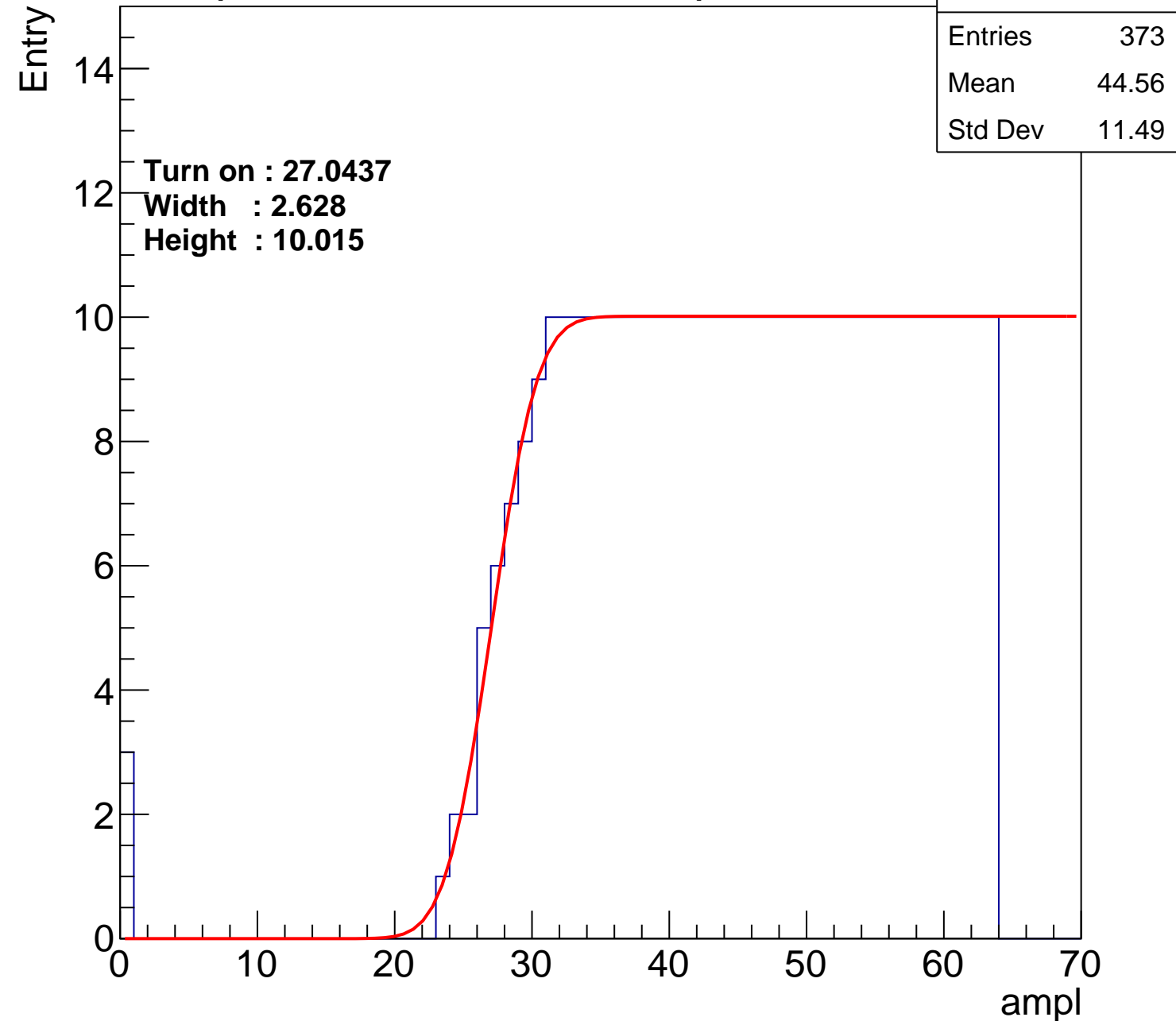
Width : 2.628

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch62

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.5649

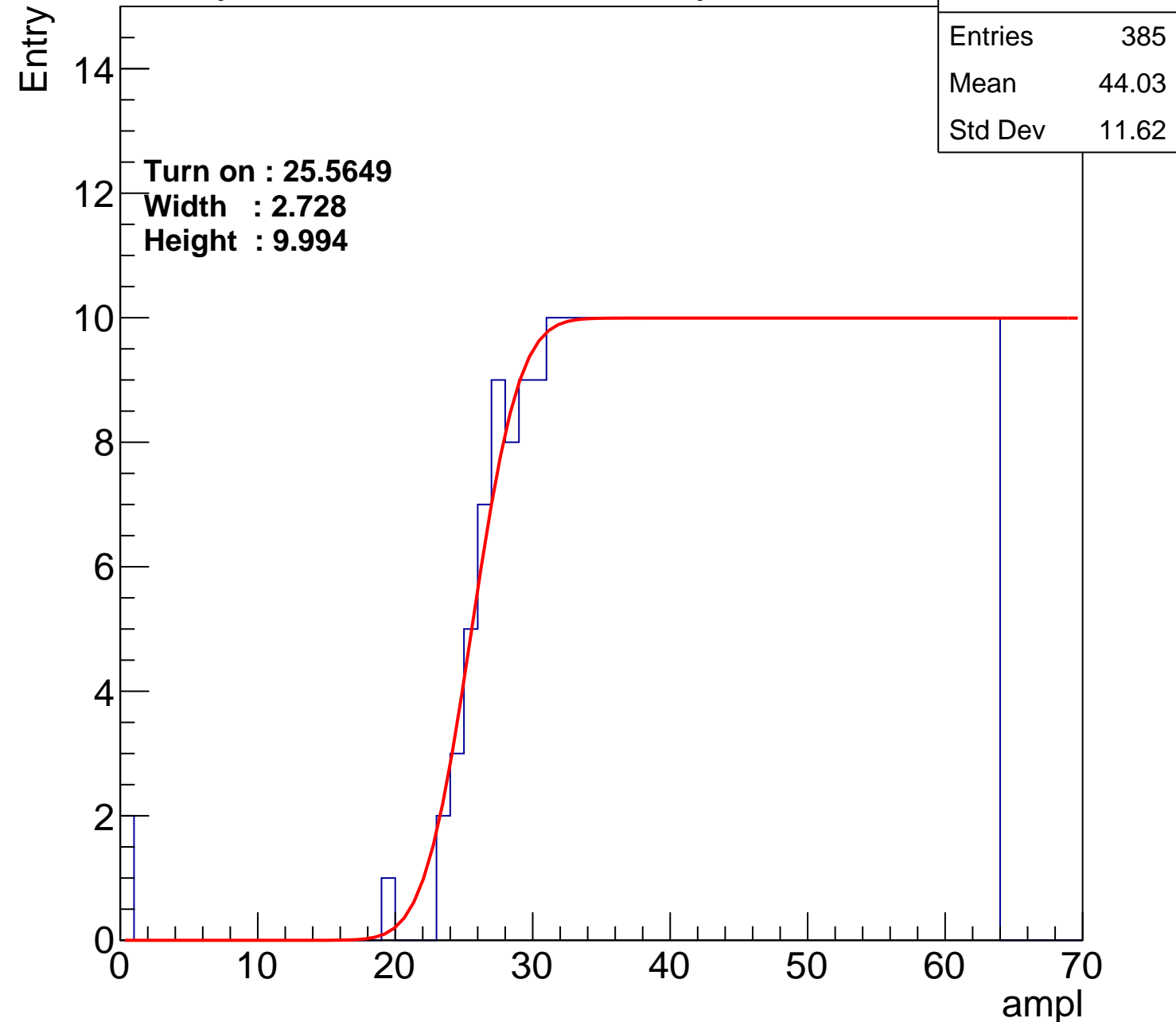
Width : 2.728

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch63

calib_packv5_042523_0143.root, FC#4, port A2

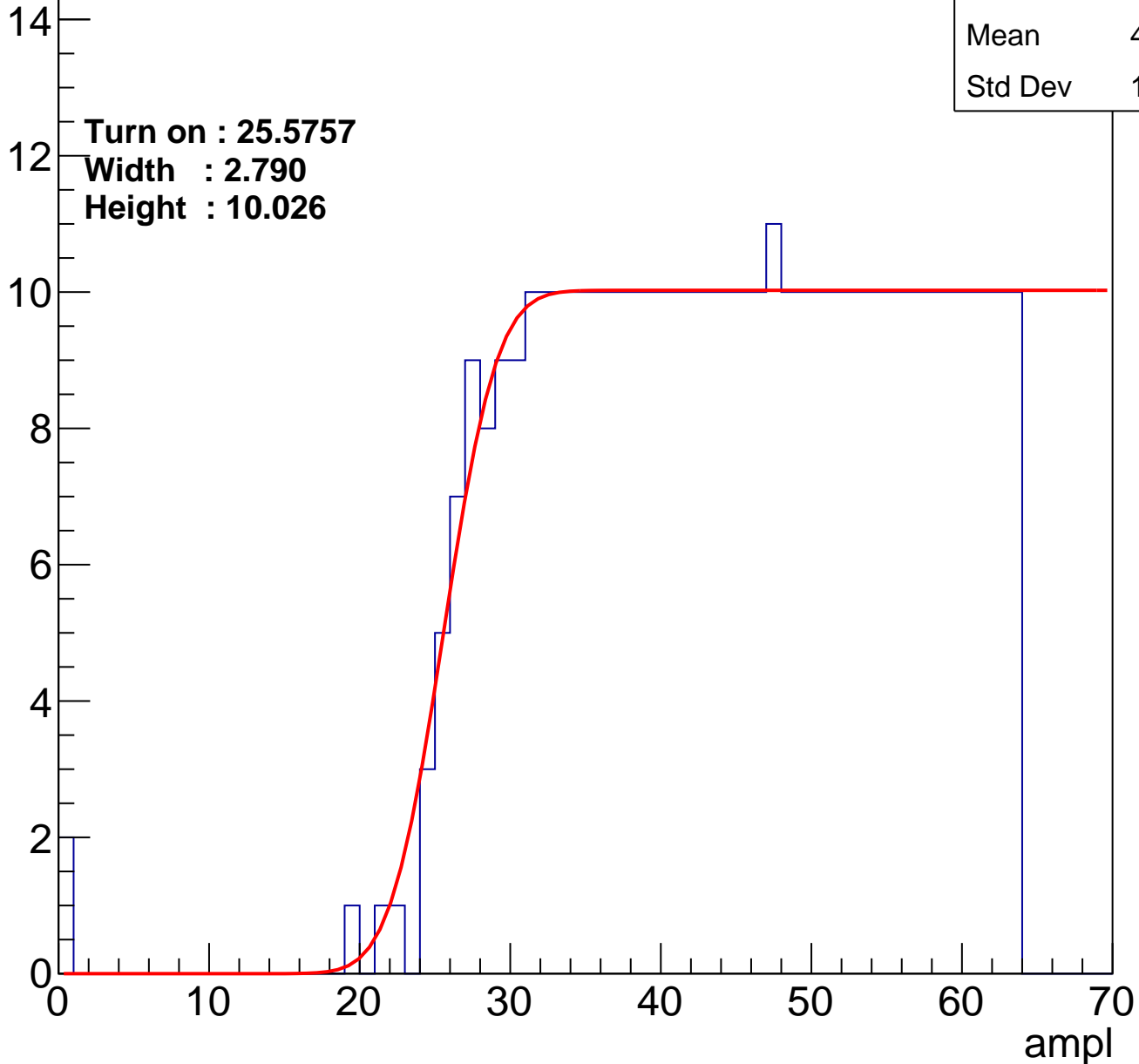
Entries	386
Mean	44.03
Std Dev	11.62

Turn on : 25.5757

Width : 2.790

Height : 10.026

Entry



B1L100S, U22-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.6224

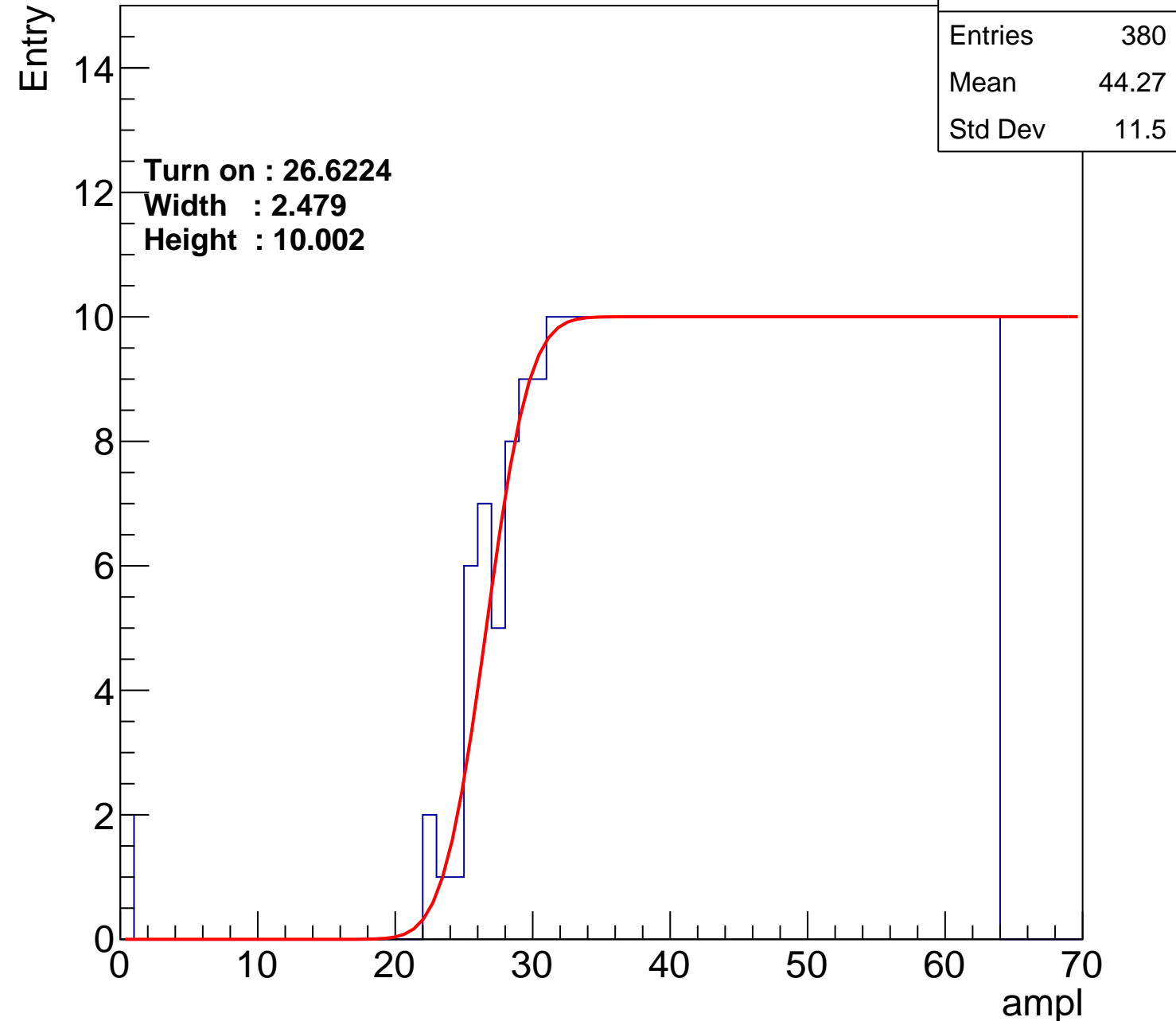
Width : 2.479

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	351
Mean	45.76
Std Dev	10.59

Turn on : 29.5694

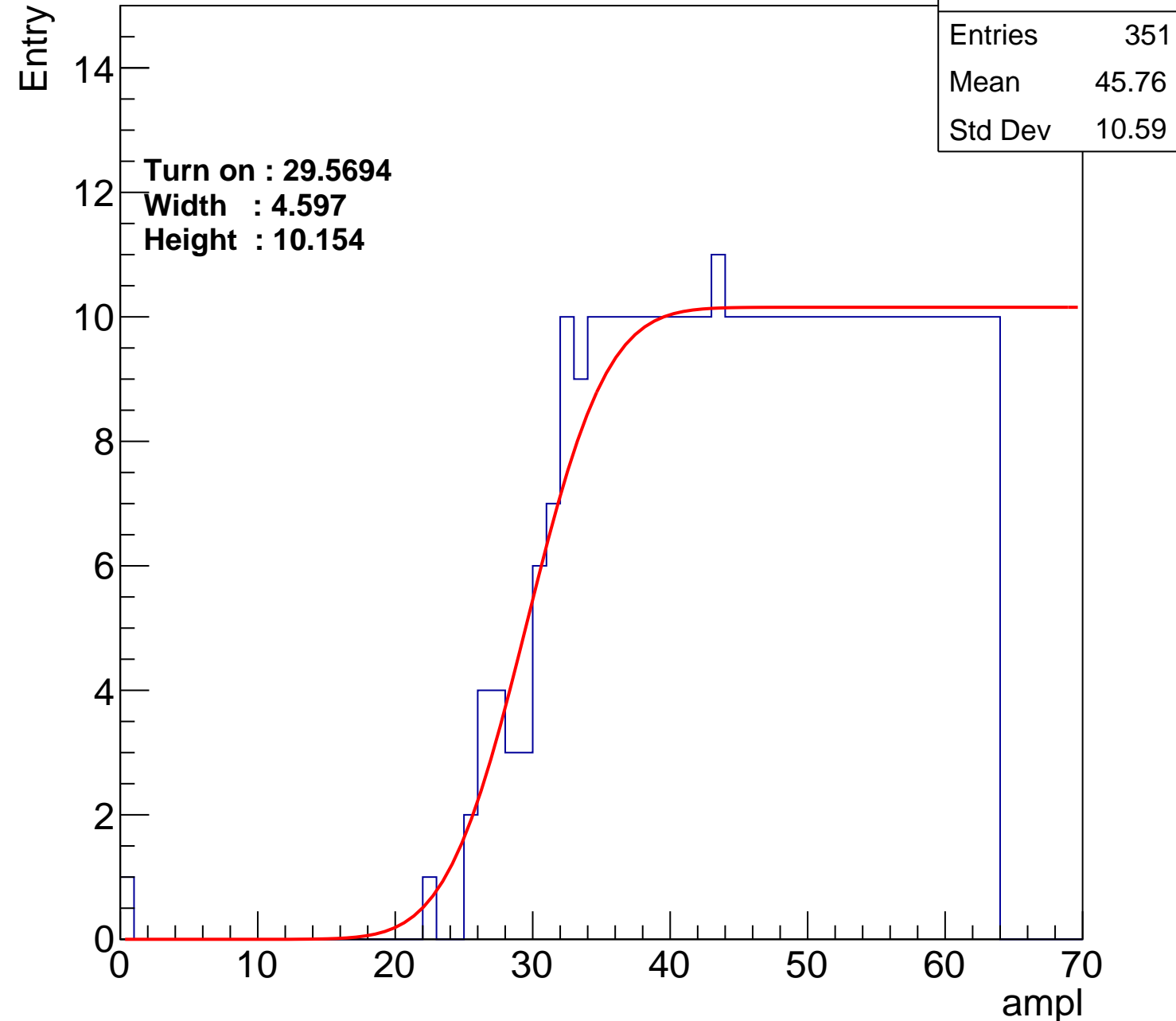
Width : 4.597

Height : 10.154

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch66

calib_packv5_042523_0143.root, FC#4, port A2

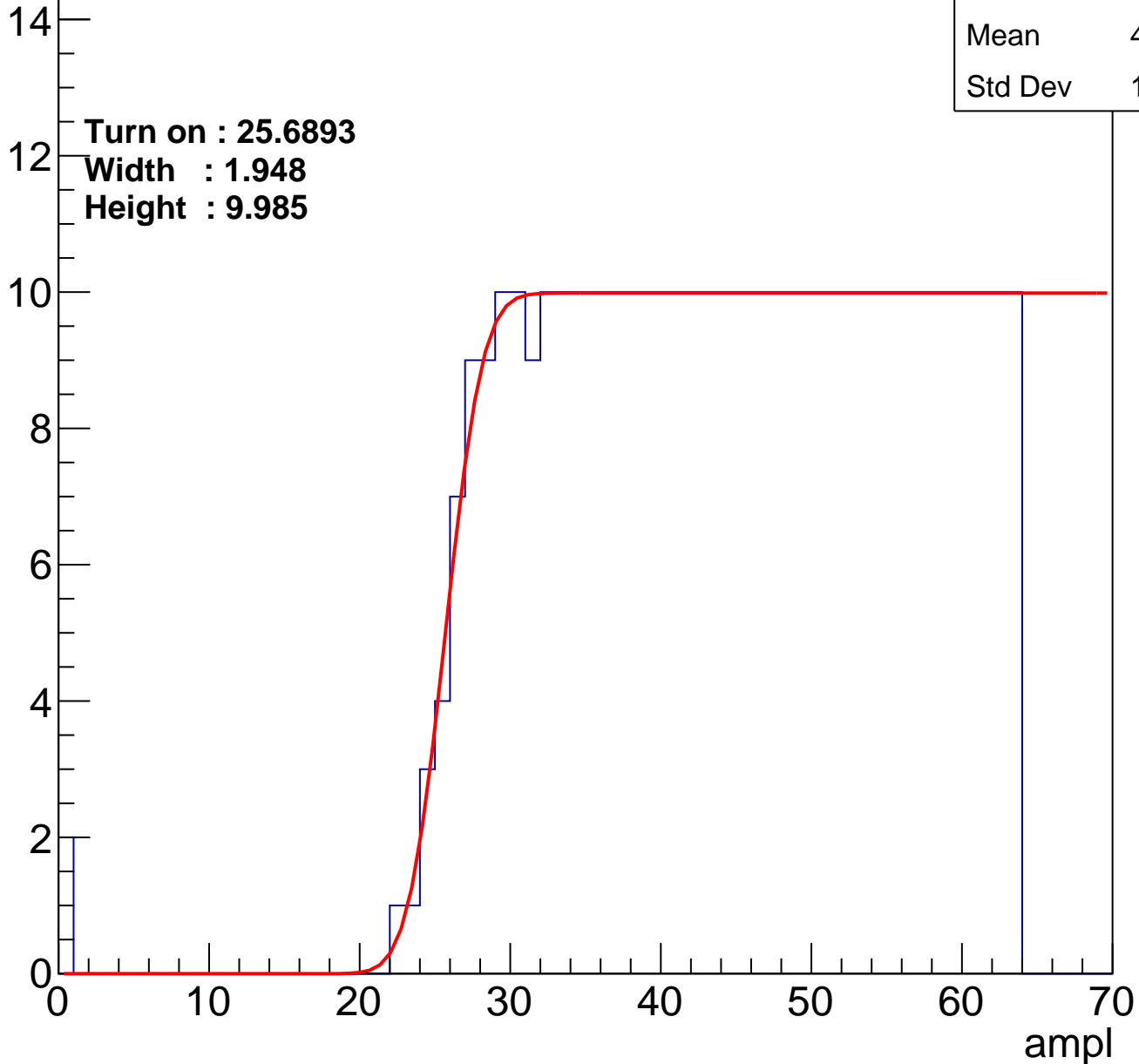
Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.6893

Width : 1.948

Height : 9.985

Entry



B1L100S, U22-ch67

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.36
Std Dev	12.34

Turn on : 25.0981

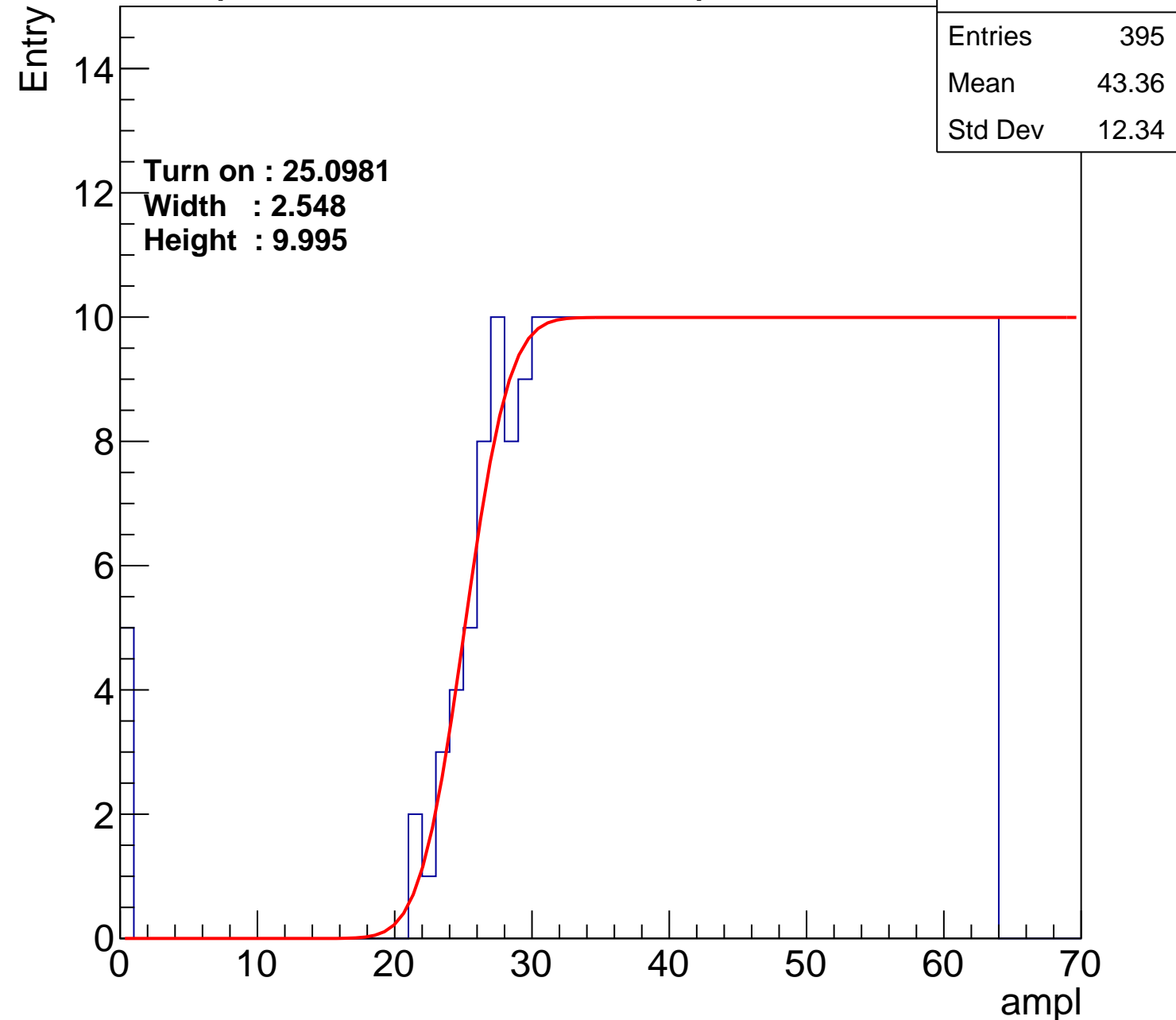
Width : 2.548

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch68

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.23
Std Dev	12.31

Turn on : 27.6533

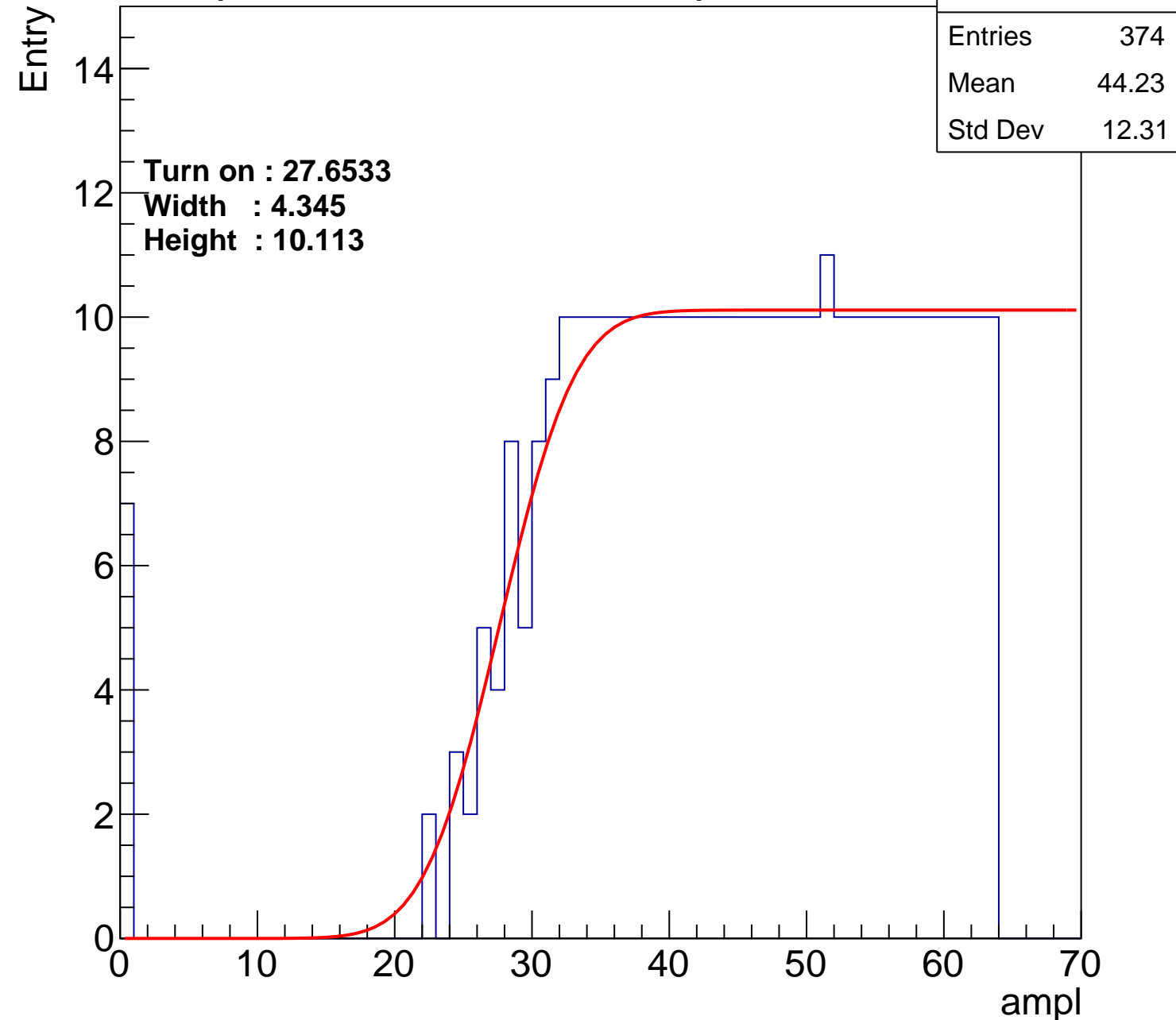
Width : 4.345

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch69

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.6773

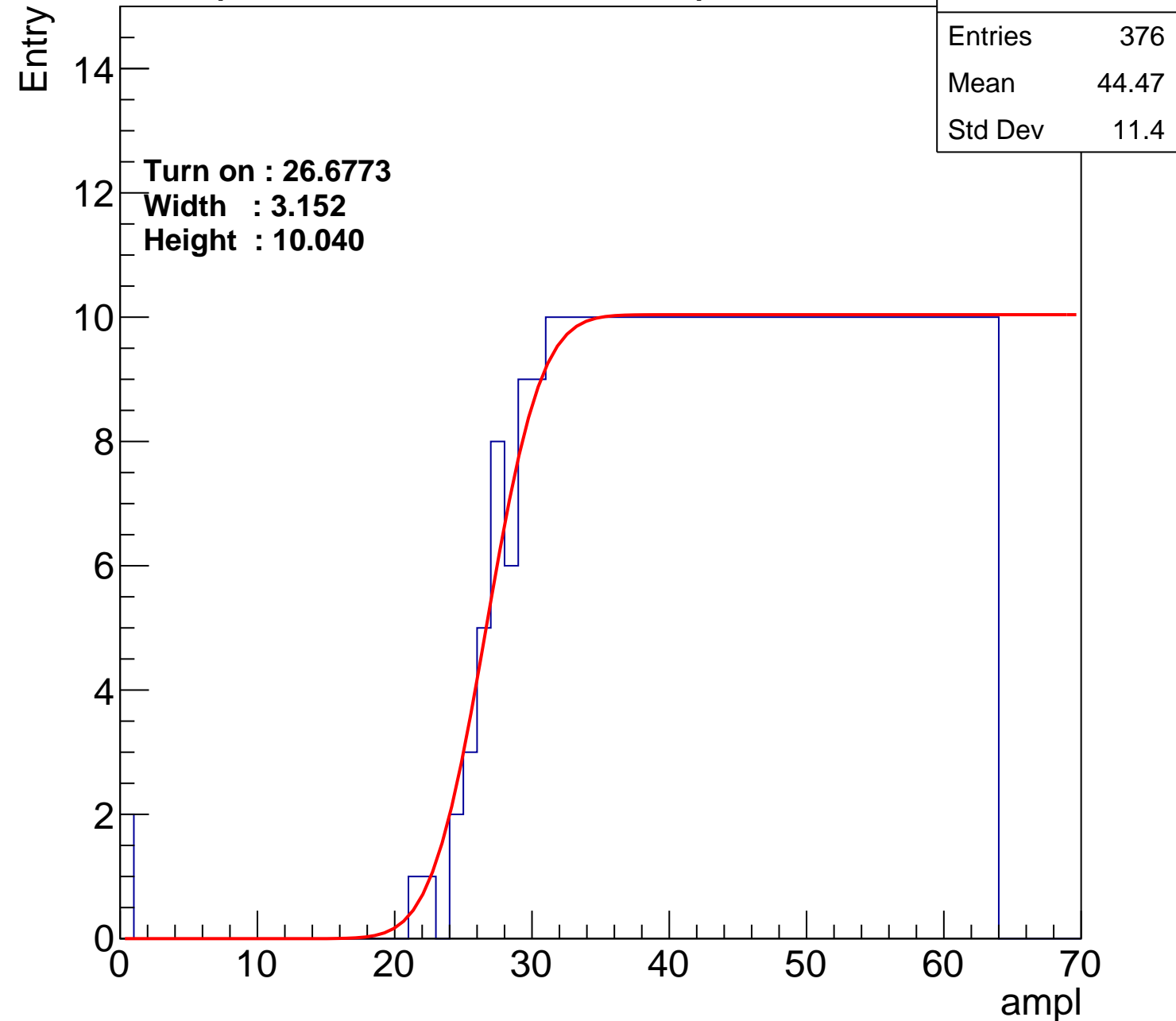
Width : 3.152

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch70

calib_packv5_042523_0143.root, FC#4, port A2

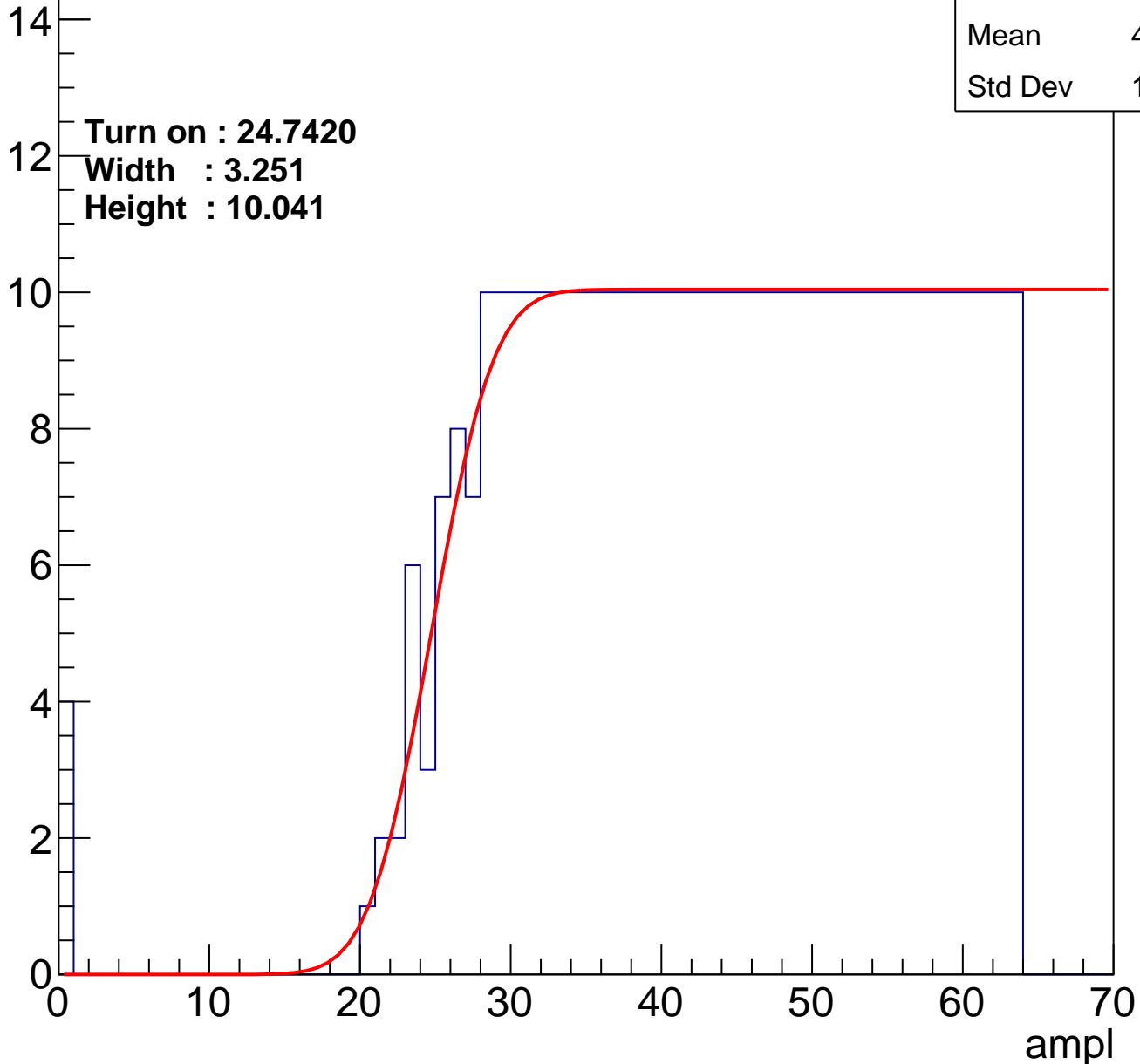
Entries	400
Mean	43.17
Std Dev	12.32

Turn on : 24.7420

Width : 3.251

Height : 10.041

Entry



B1L100S, U22-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.82
Std Dev	11.67

Turn on : 25.6494

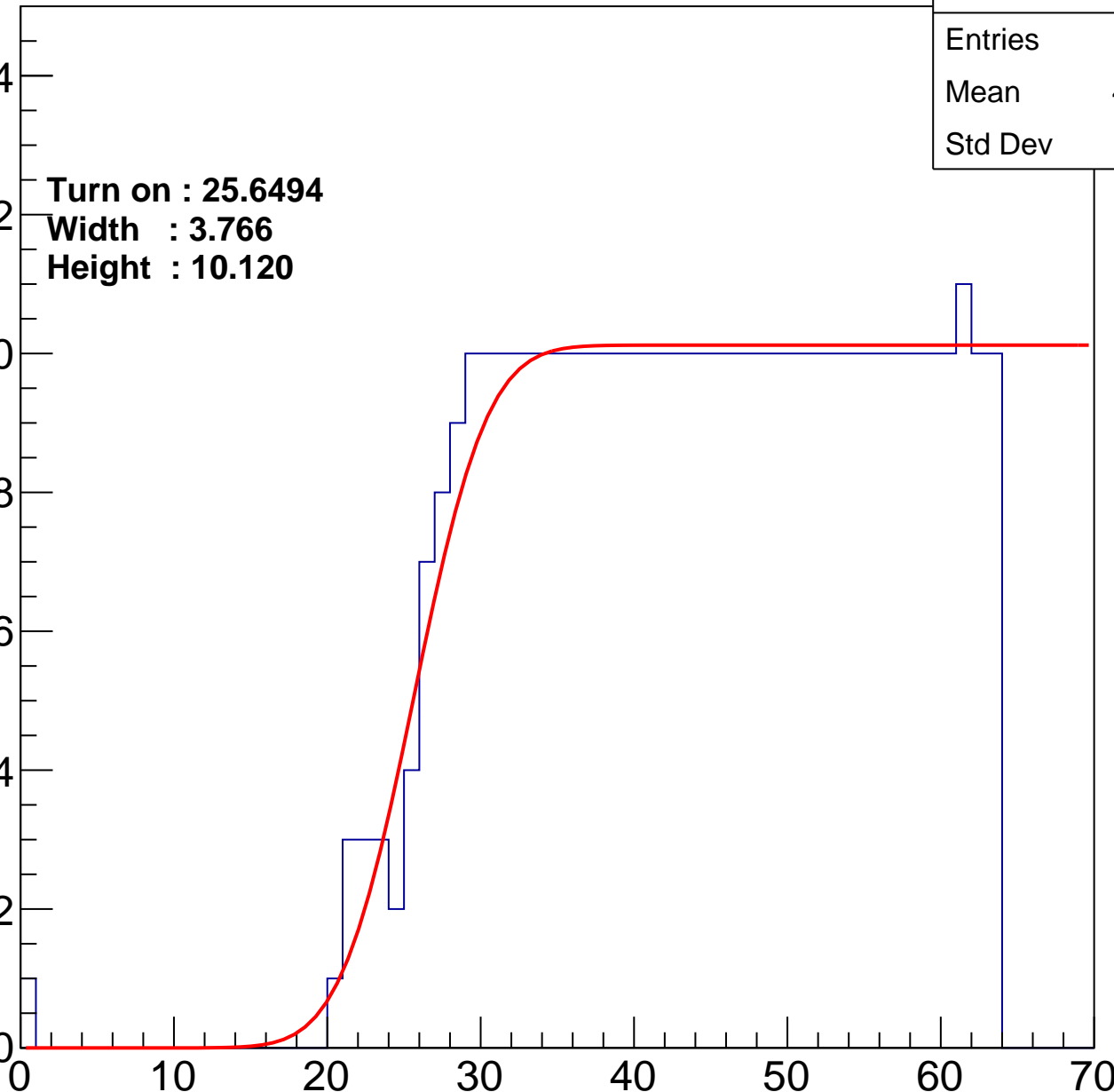
Width : 3.766

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 28.4326
Width : 2.127
Height : 10.025



B1L100S, U22-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.76
Std Dev	11.57

Turn on : 27.8616

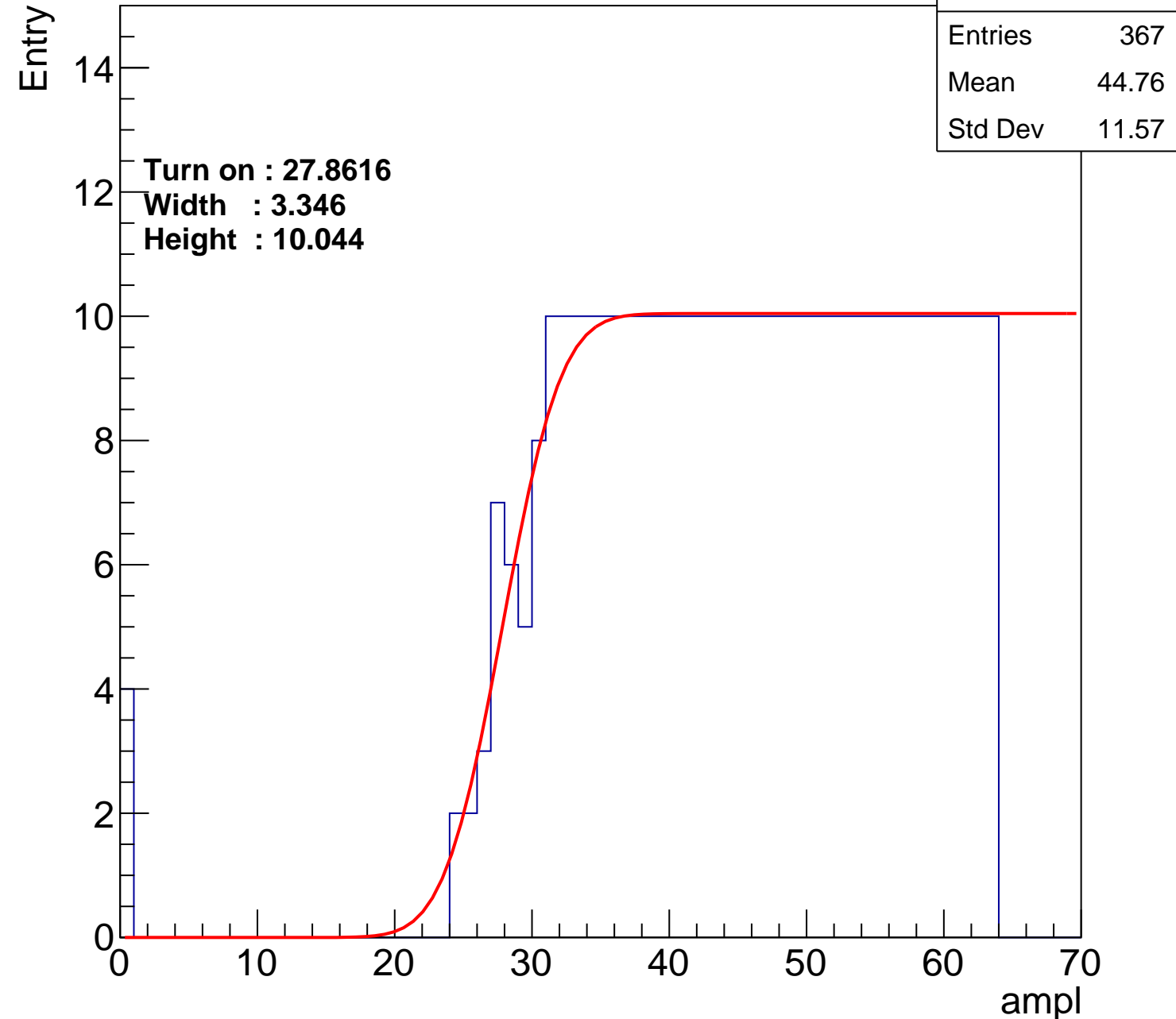
Width : 3.346

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

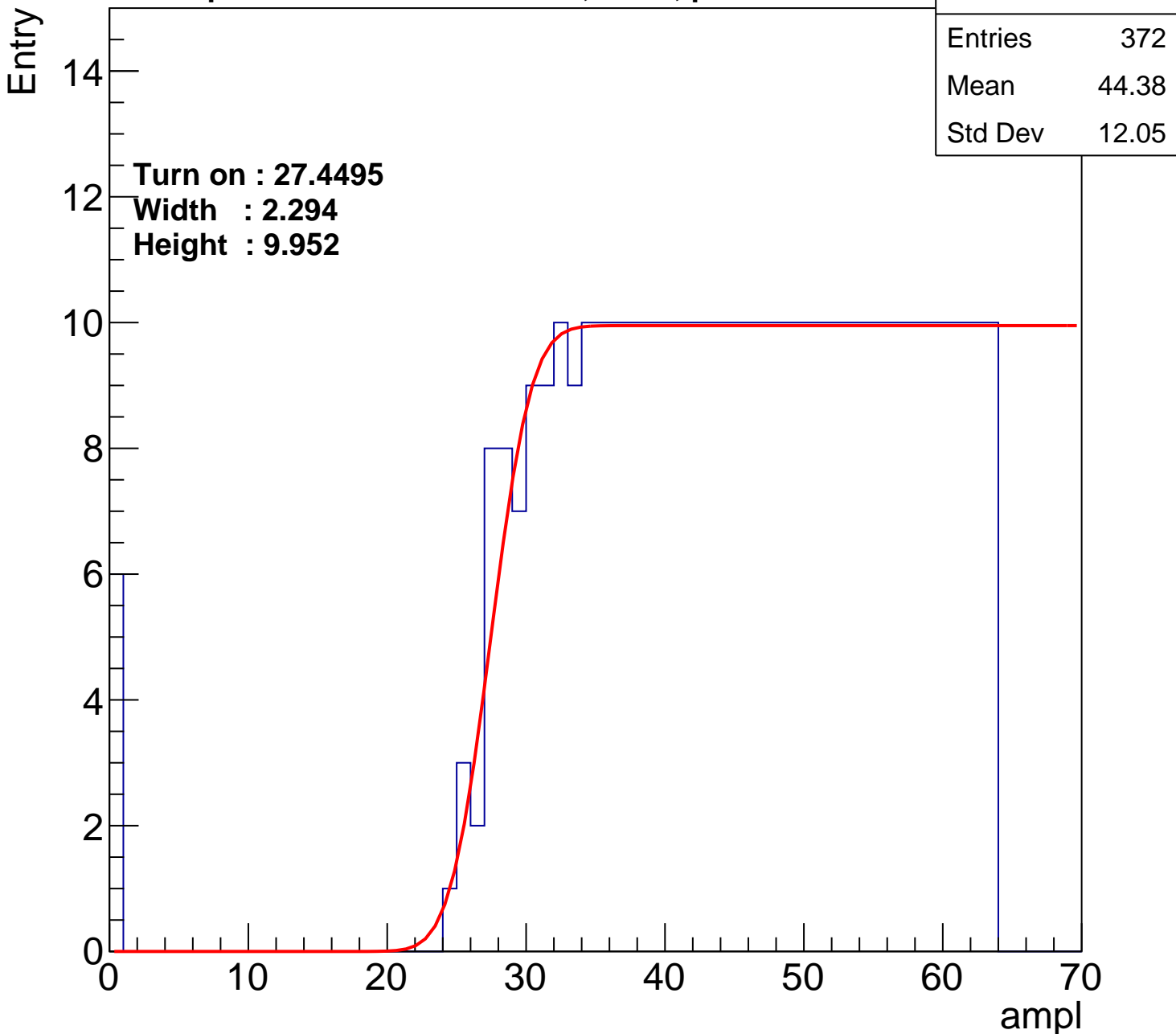
Entries	372
Mean	44.38
Std Dev	12.05

Mean	44.38
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Std Dev	12.05
---------	-------

Width : 2.294

Height : 9.952



B1L100S, U22-ch75

calib_packv5_042523_0143.root, FC#4, port A2

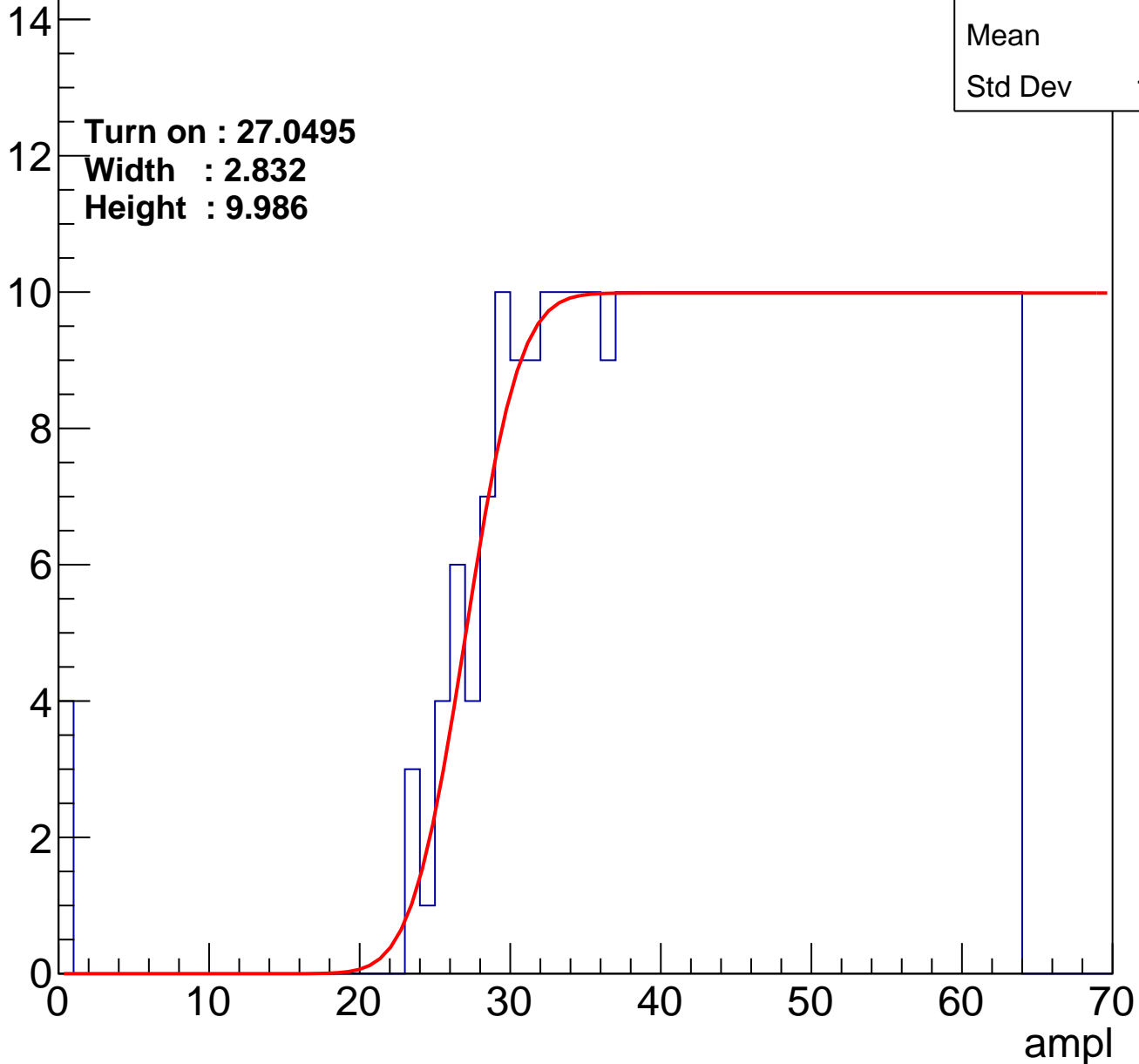
Entries	376
Mean	44.3
Std Dev	11.81

Turn on : 27.0495

Width : 2.832

Height : 9.986

Entry



B1L100S, U22-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.0023

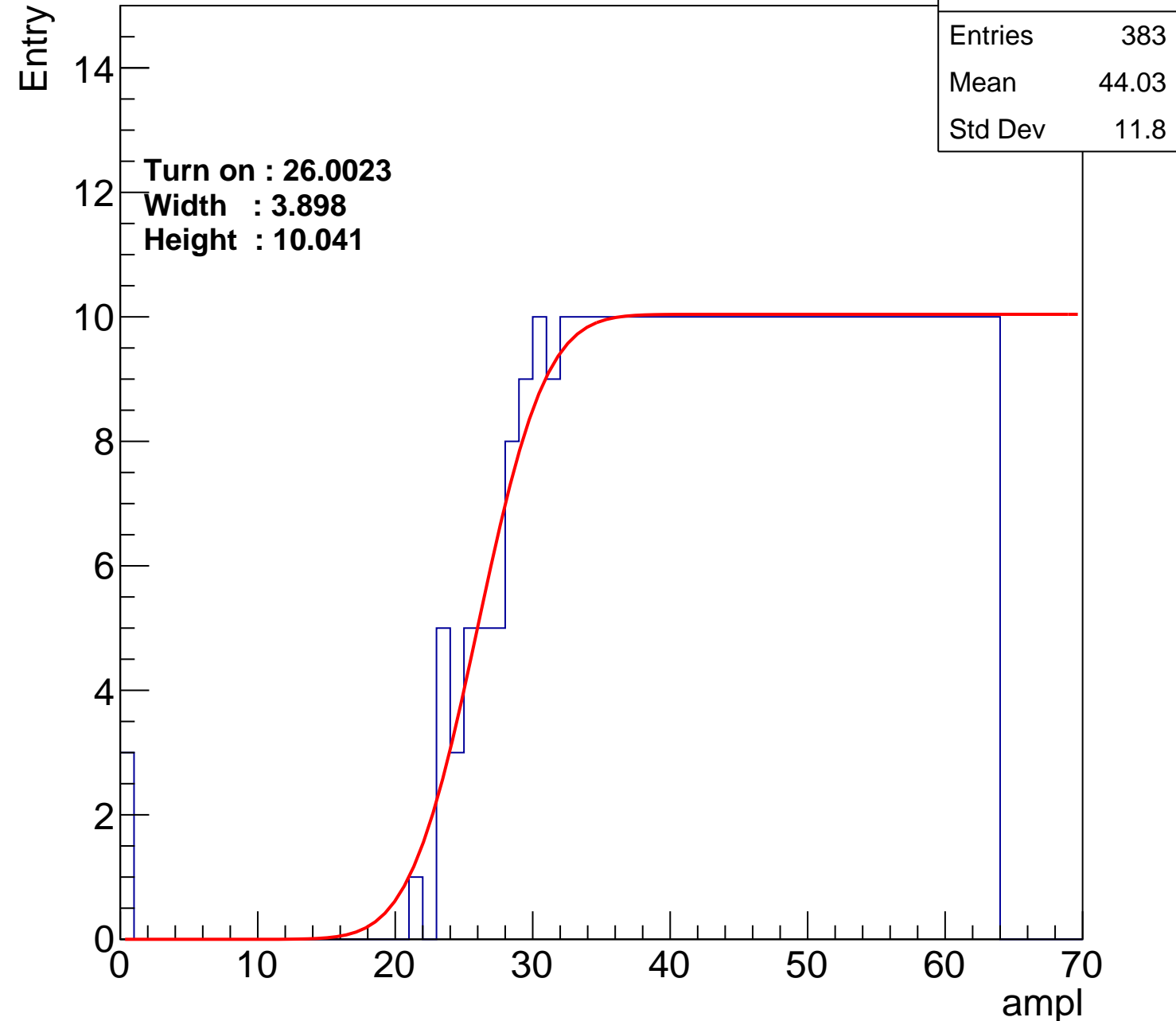
Width : 3.898

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.52
Std Dev	11.68

Turn on : 27.2380

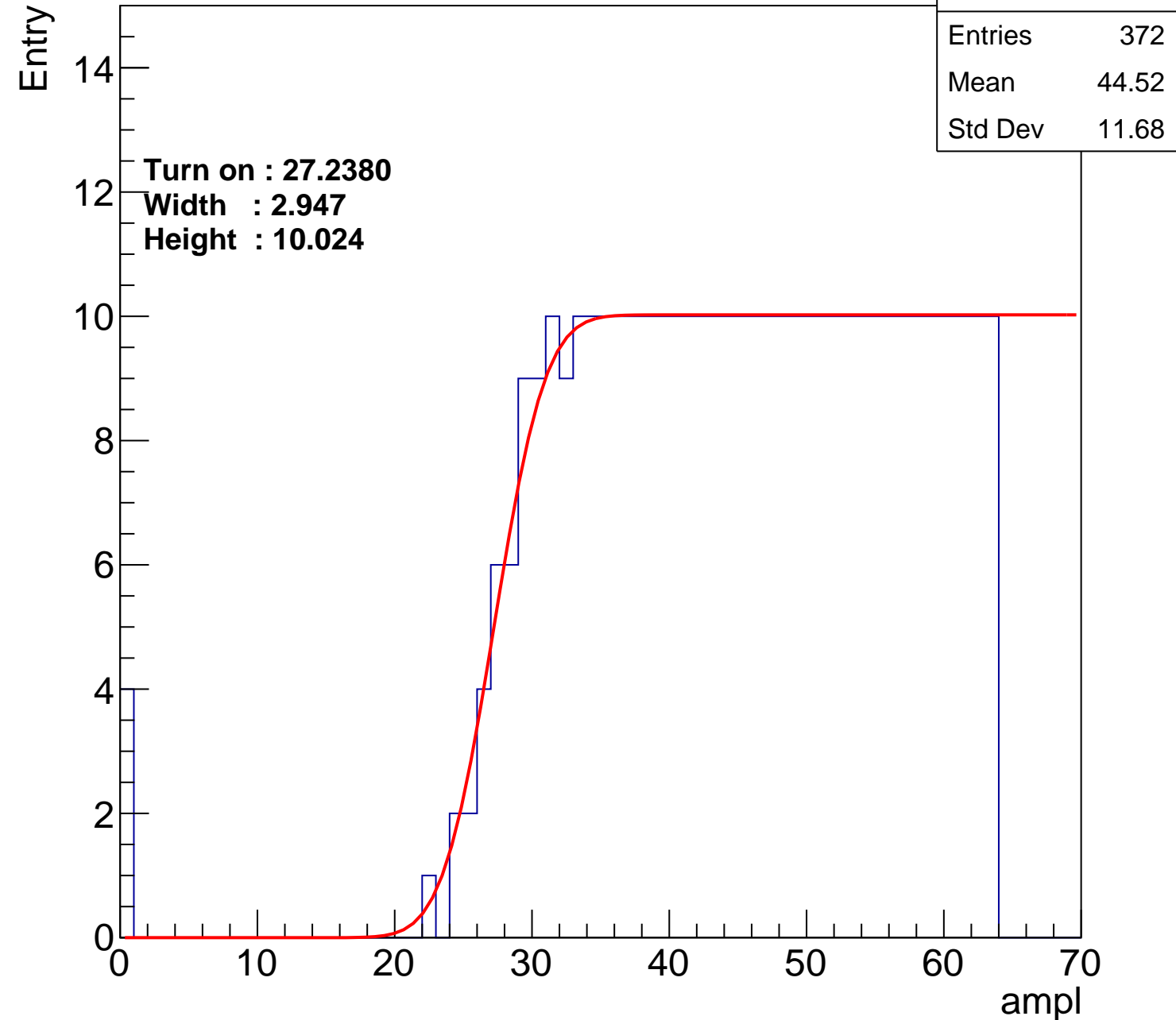
Width : 2.947

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch78

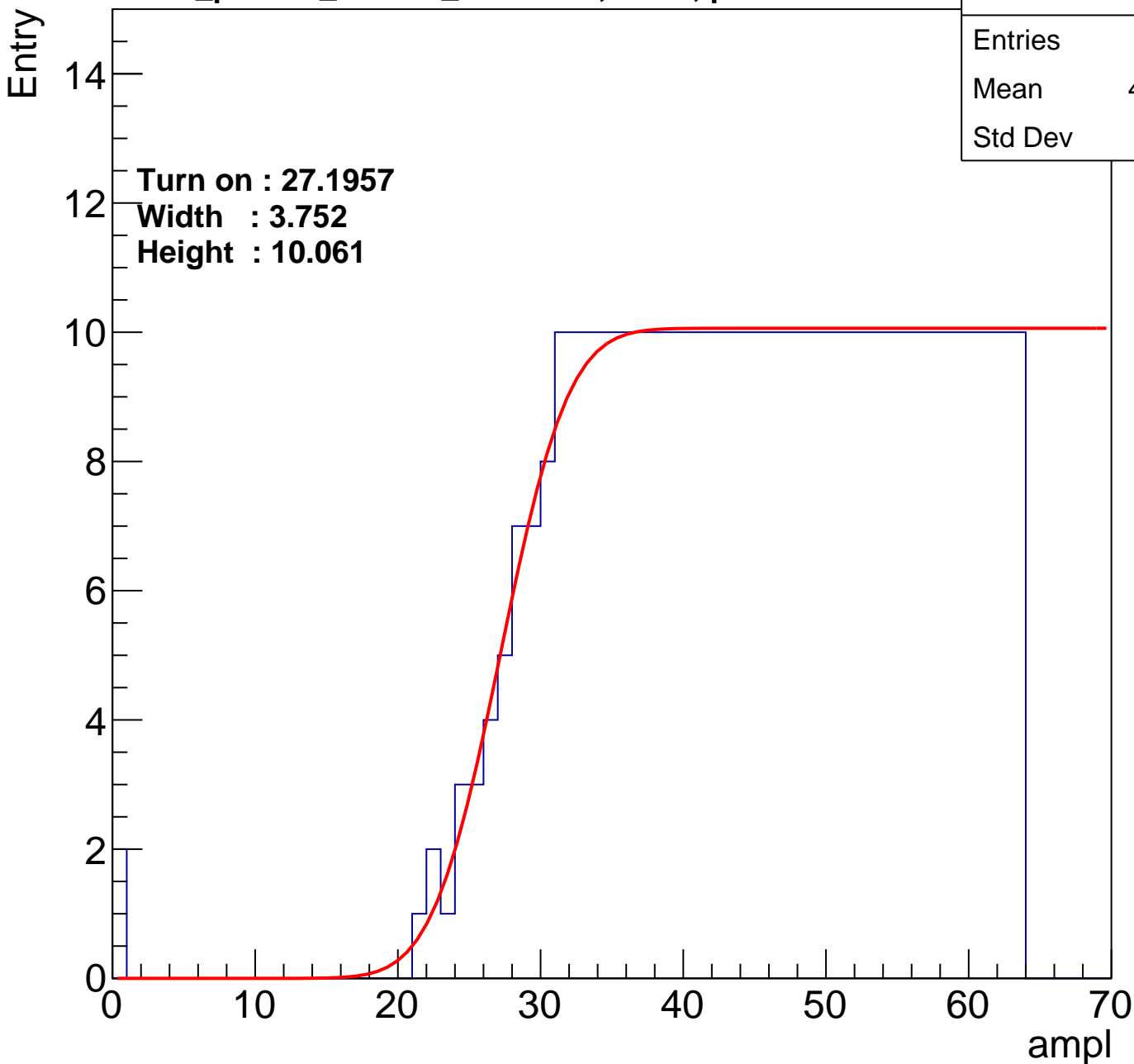
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 27.1957

Width : 3.752

Height : 10.061

Entries	373
Mean	44.57
Std Dev	11.41



B1L100S, U22-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.3260

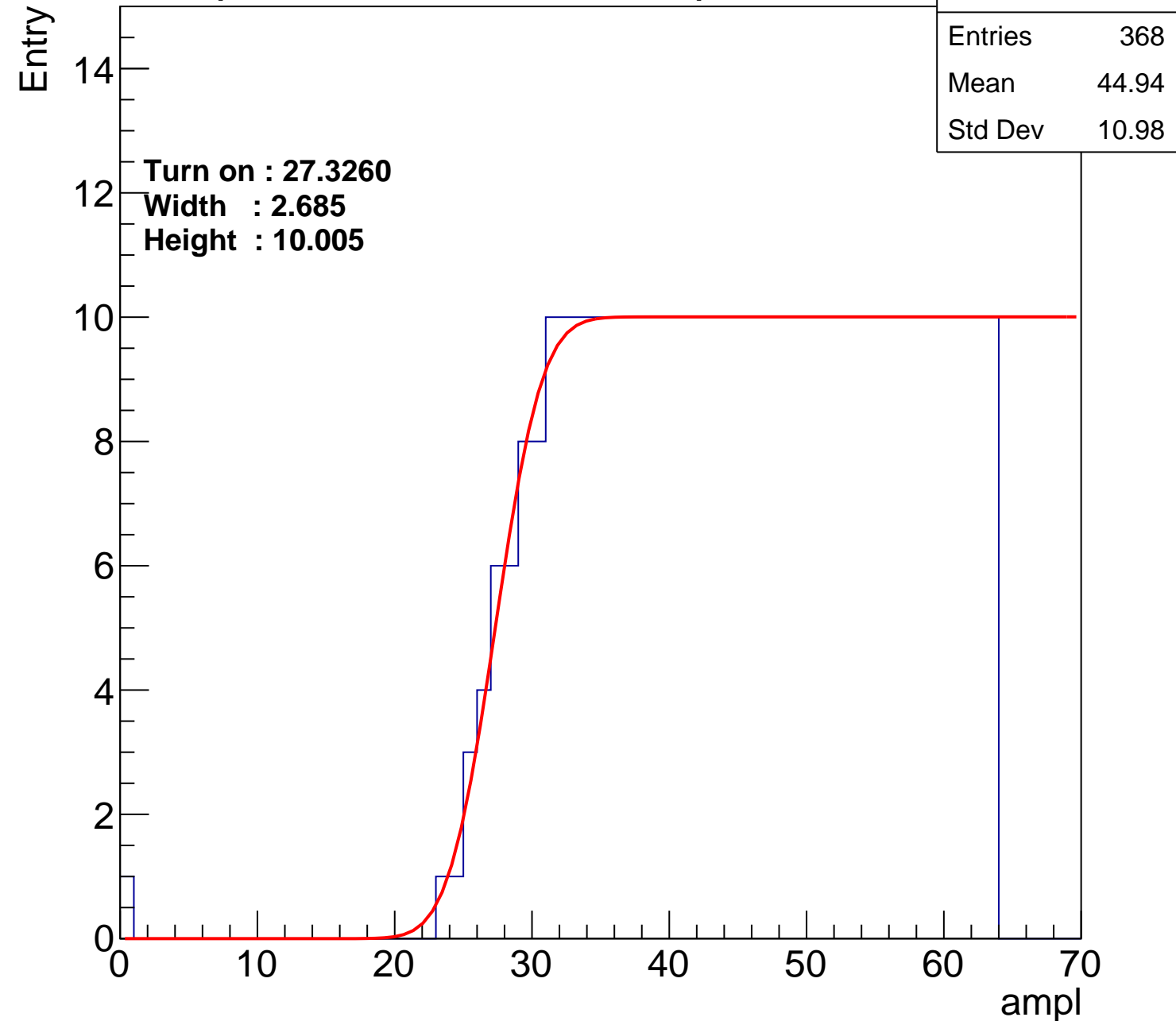
Width : 2.685

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch80

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.57
Std Dev	11.84

Turn on : 27.7793

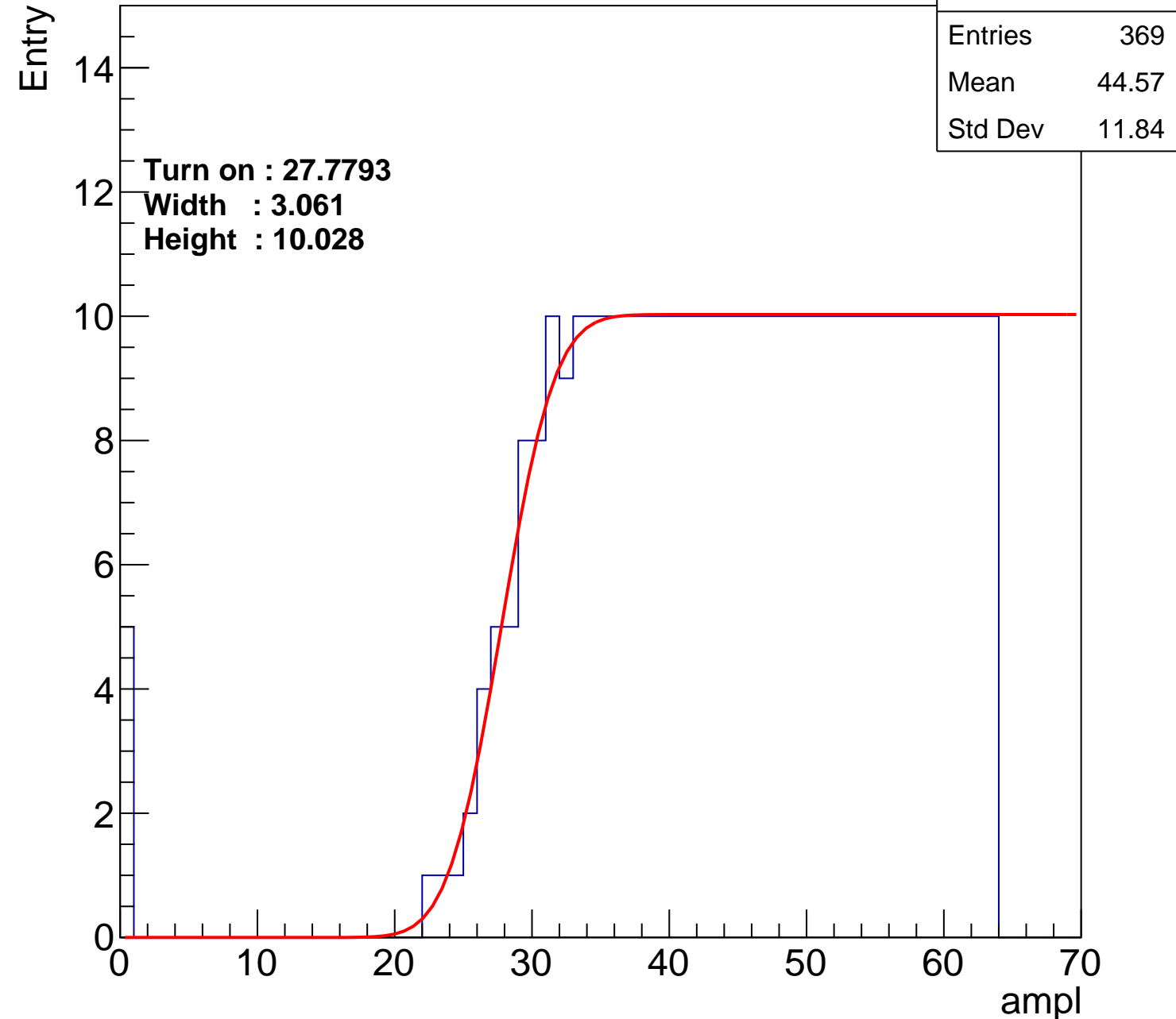
Width : 3.061

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch81

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.76
Std Dev	11.05

Turn on : 27.2901

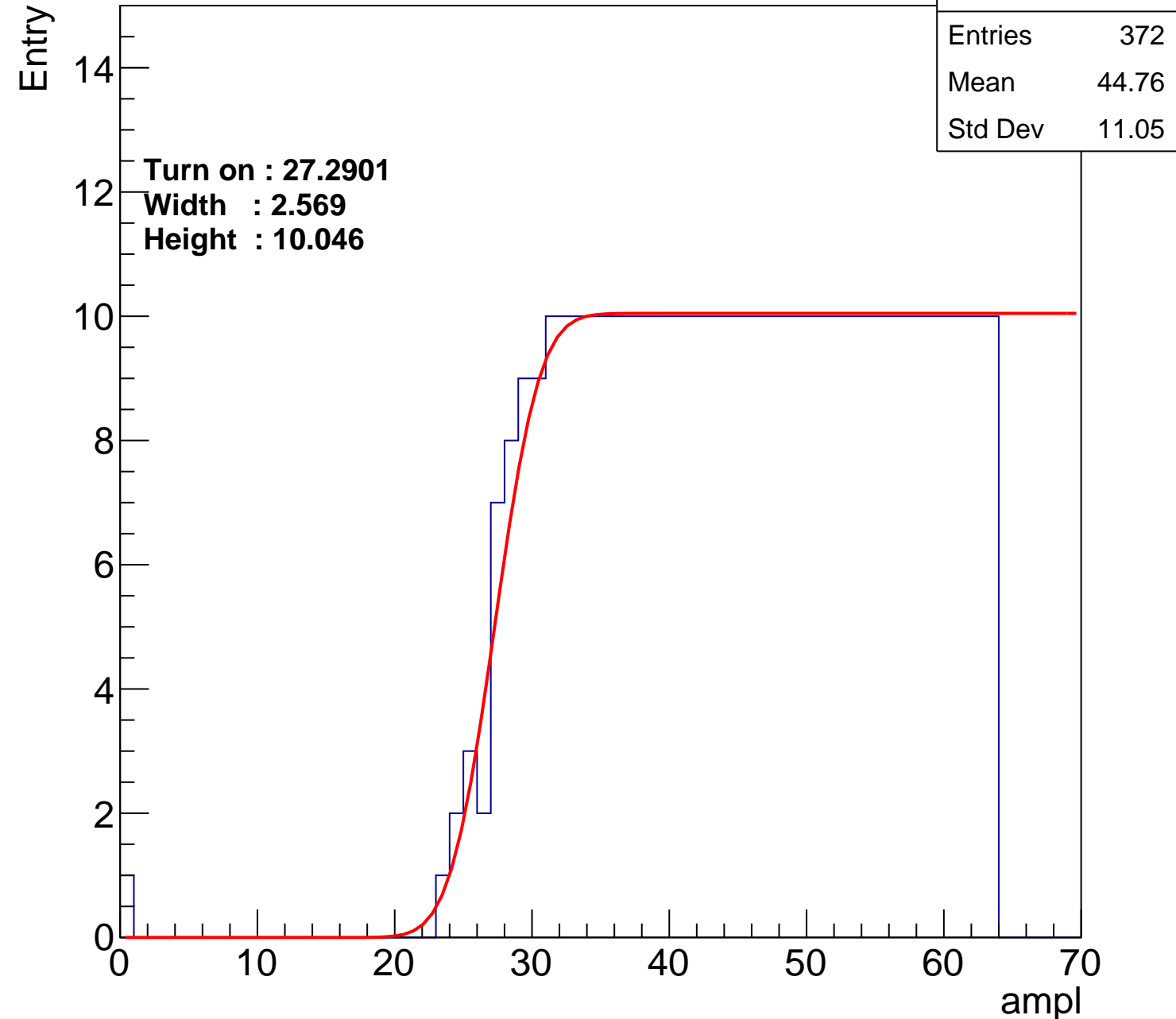
Width : 2.569

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch82

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 27.1563

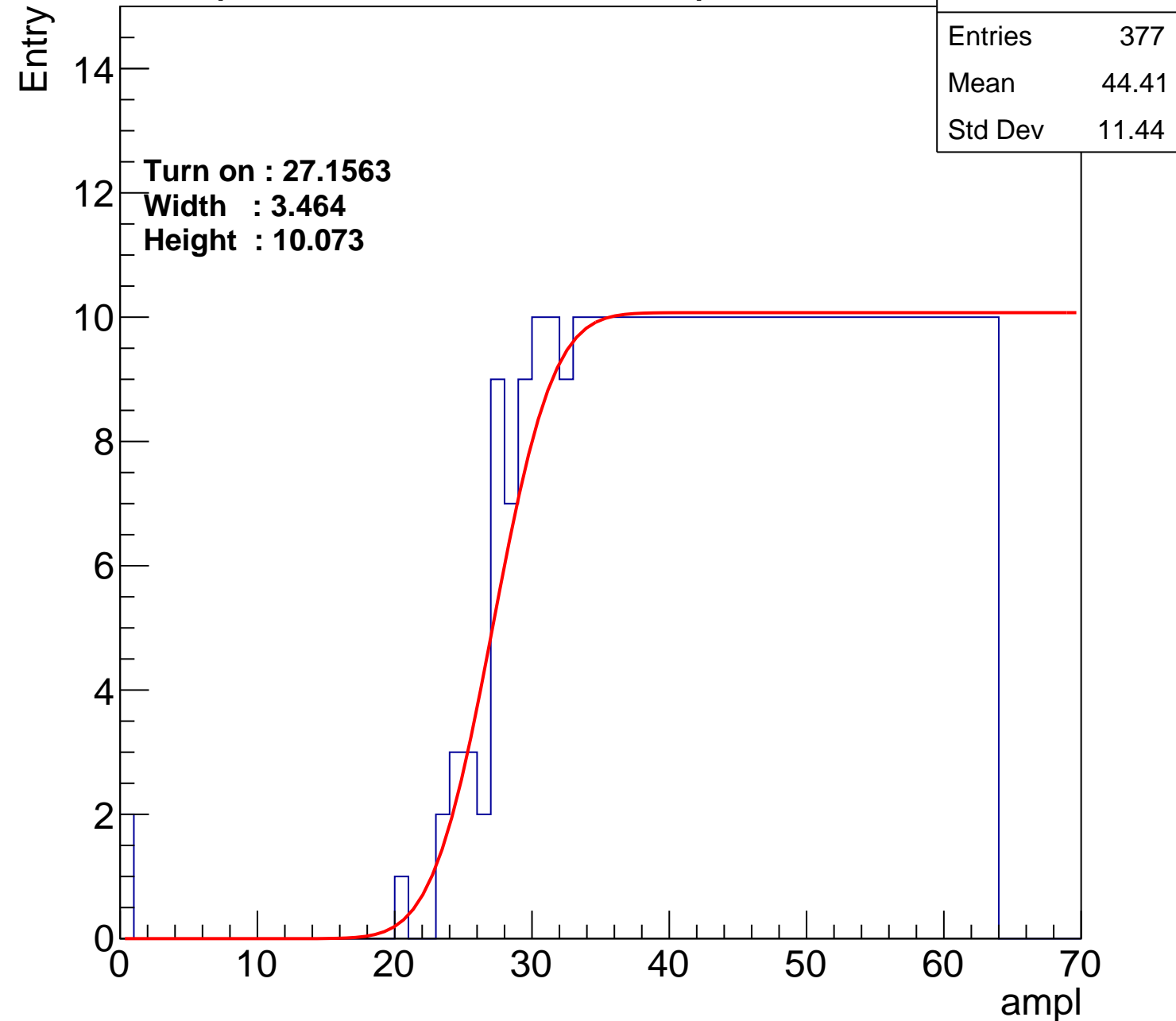
Width : 3.464

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.37
Std Dev	11.48

Turn on : 26.6284

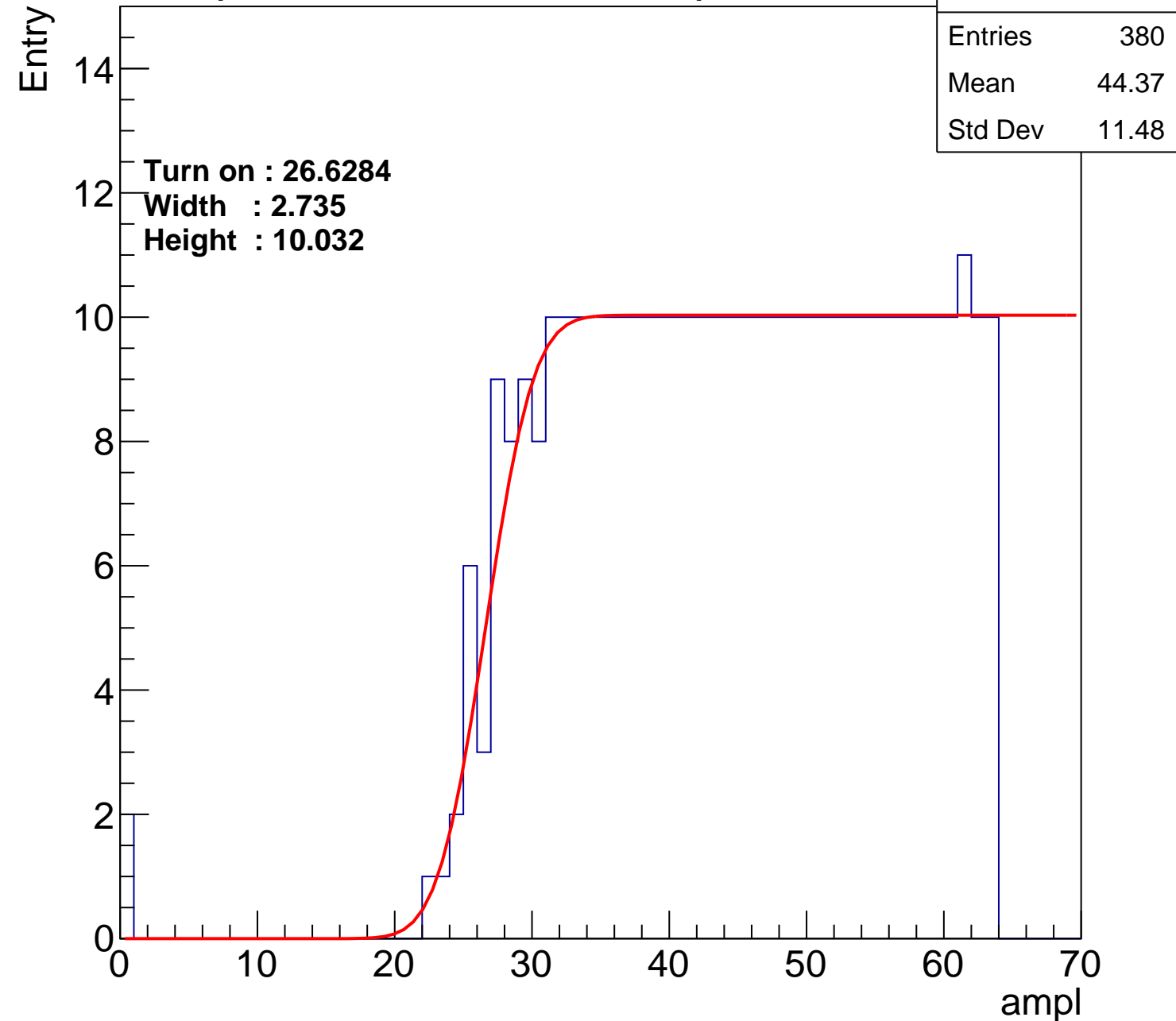
Width : 2.735

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	398
Mean	43.39
Std Dev	11.95

Turn on : 24.5698

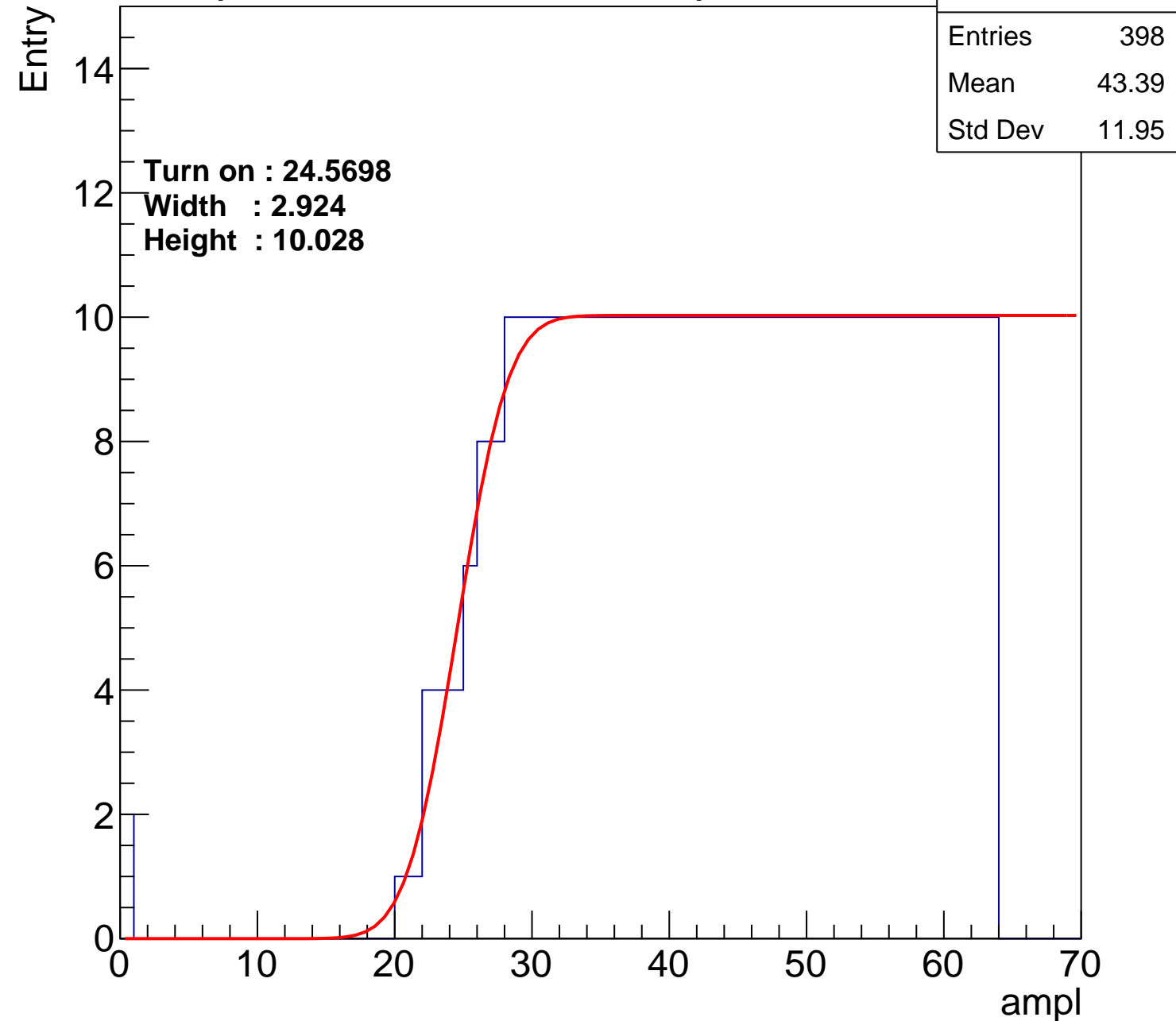
Width : 2.924

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch85

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.5174

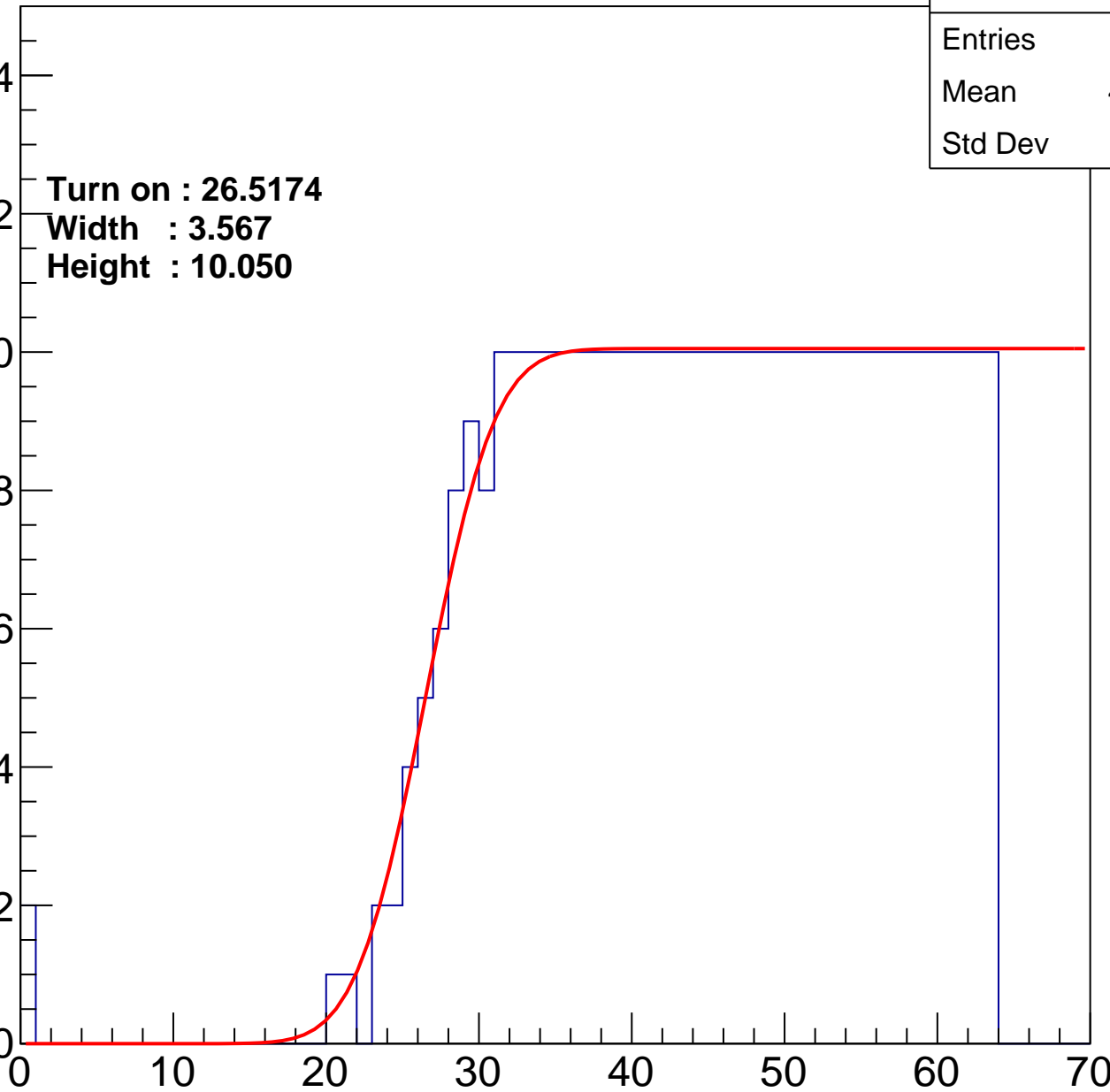
Width : 3.567

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch86

calib_packv5_042523_0143.root, FC#4, port A2

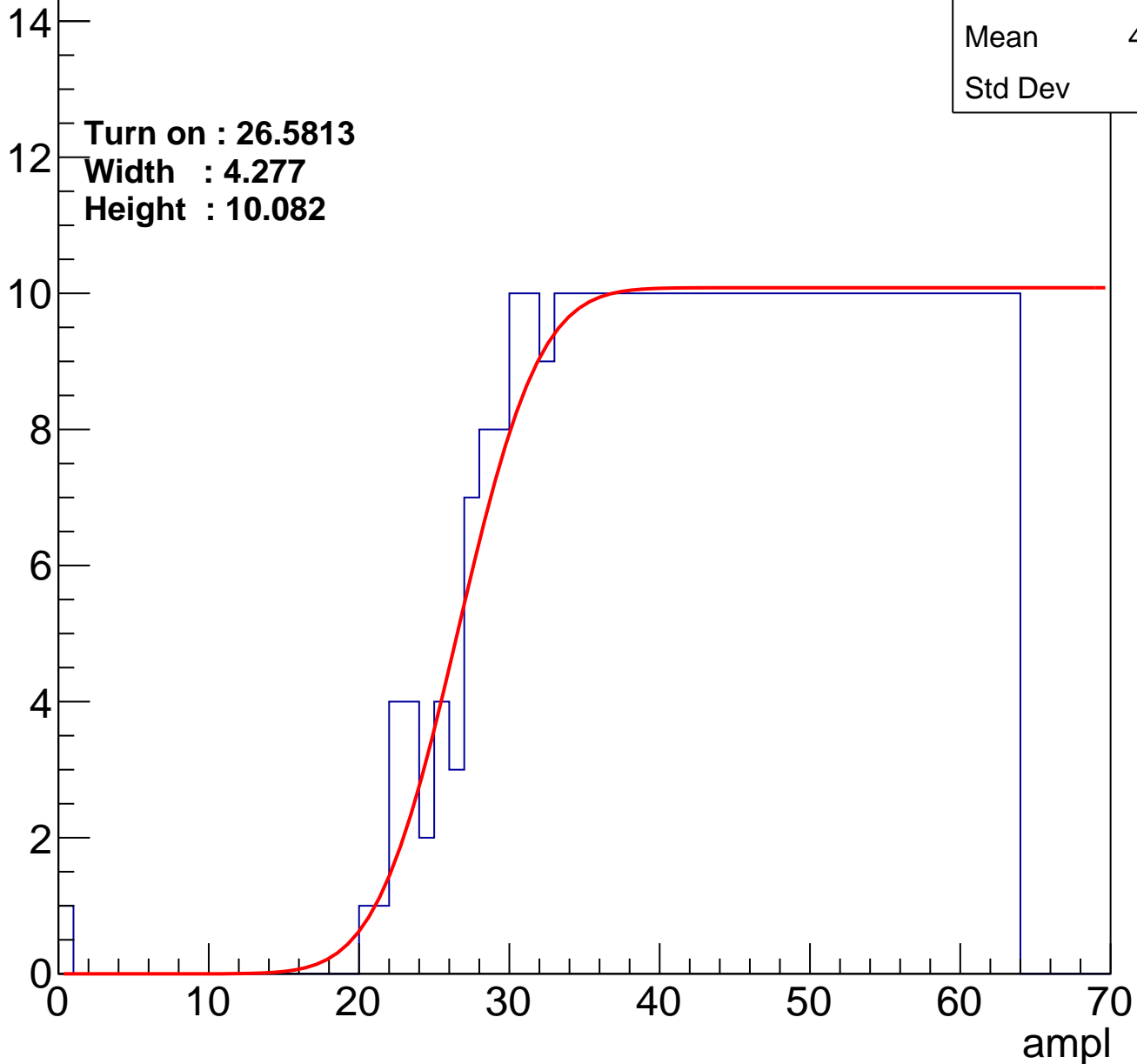
Entries	382
Mean	44.16
Std Dev	11.5

Turn on : 26.5813

Width : 4.277

Height : 10.082

Entry



B1L100S, U22-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.65
Std Dev	12.09

Turn on : 25.6296

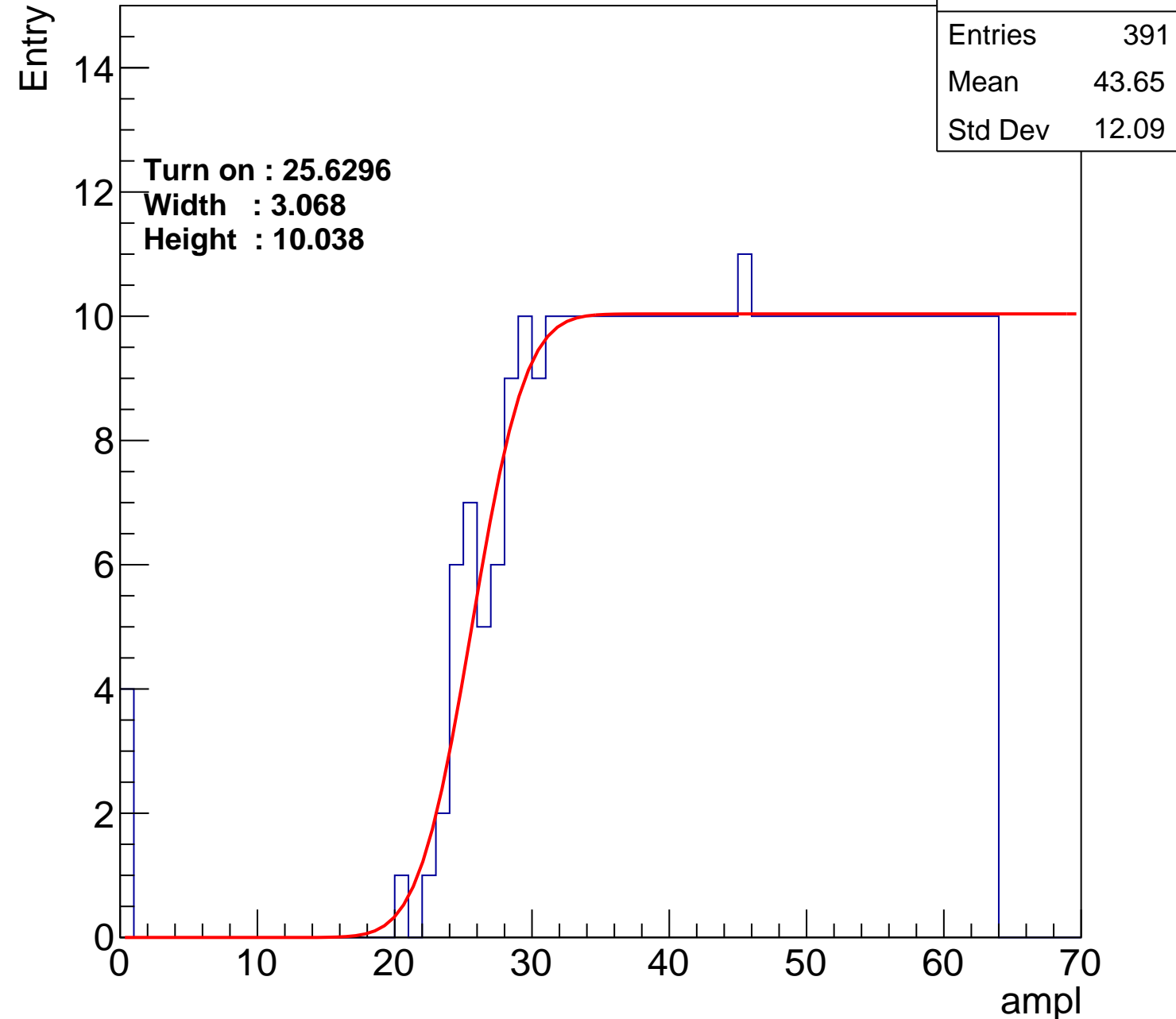
Width : 3.068

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch88

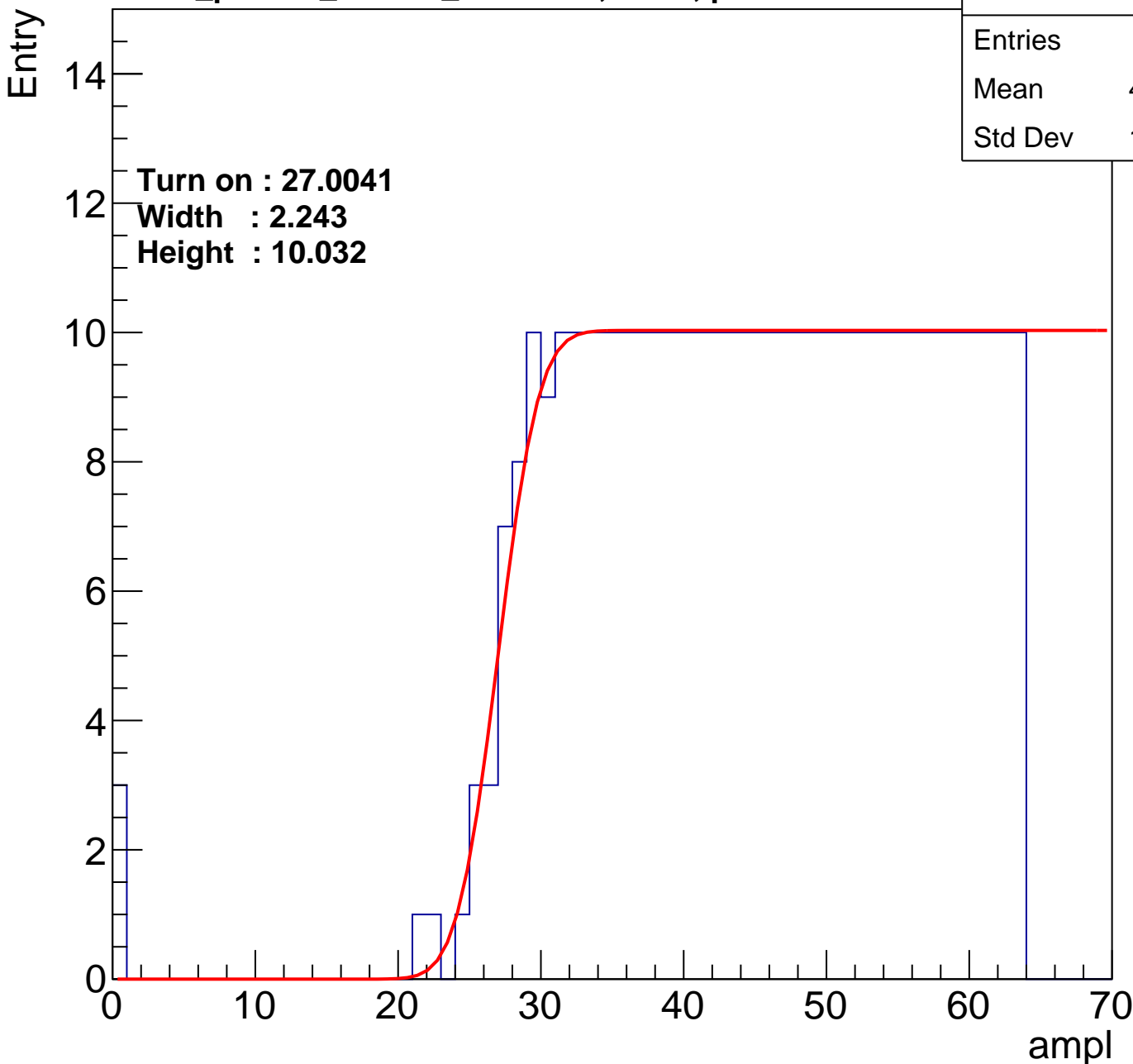
calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.42
Std Dev	11.55

Turn on : 27.0041

Width : 2.243

Height : 10.032



B1L100S, U22-ch89

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.78
Std Dev	11.24

Turn on : 27.1238

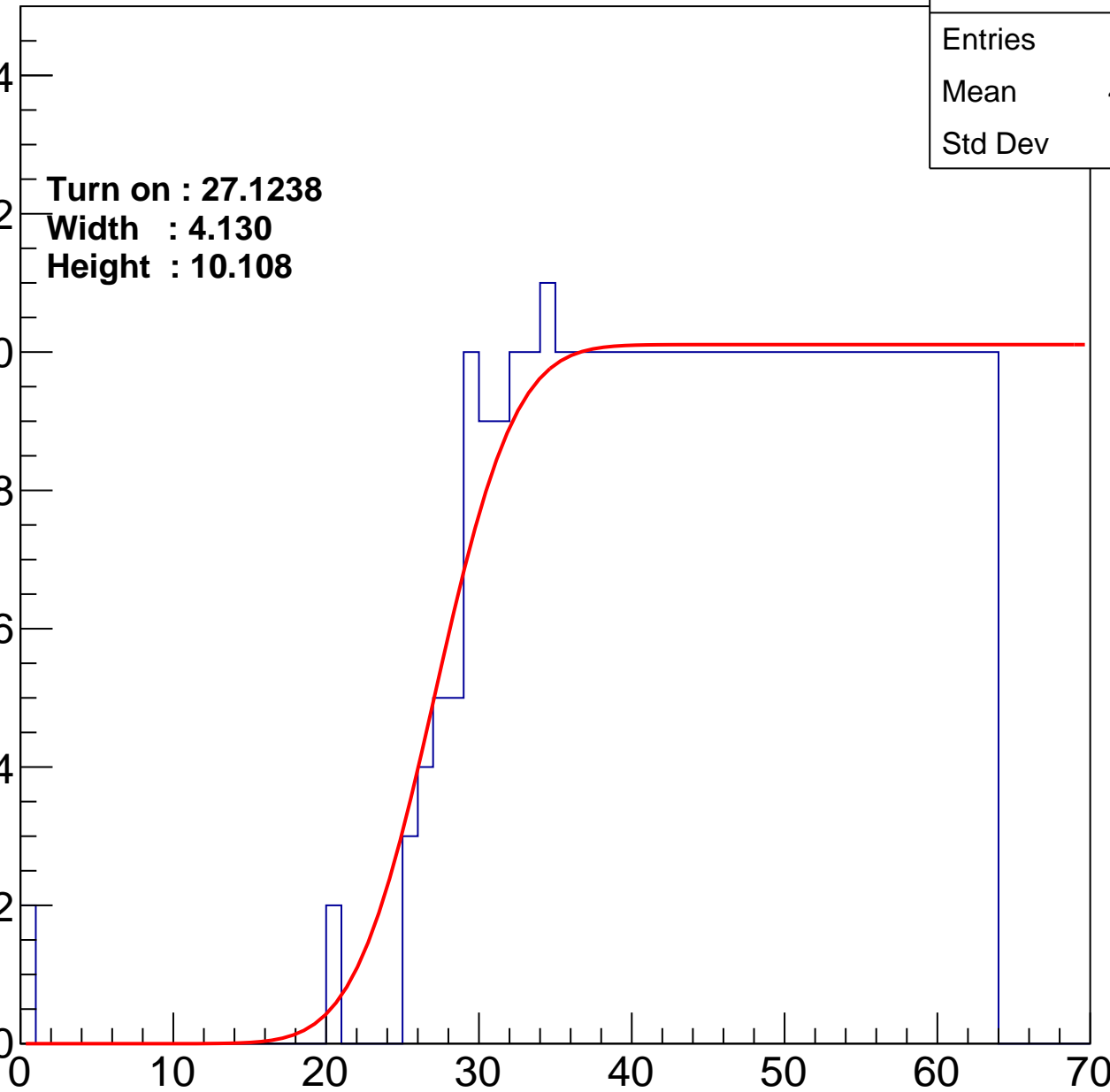
Width : 4.130

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#4, port A2

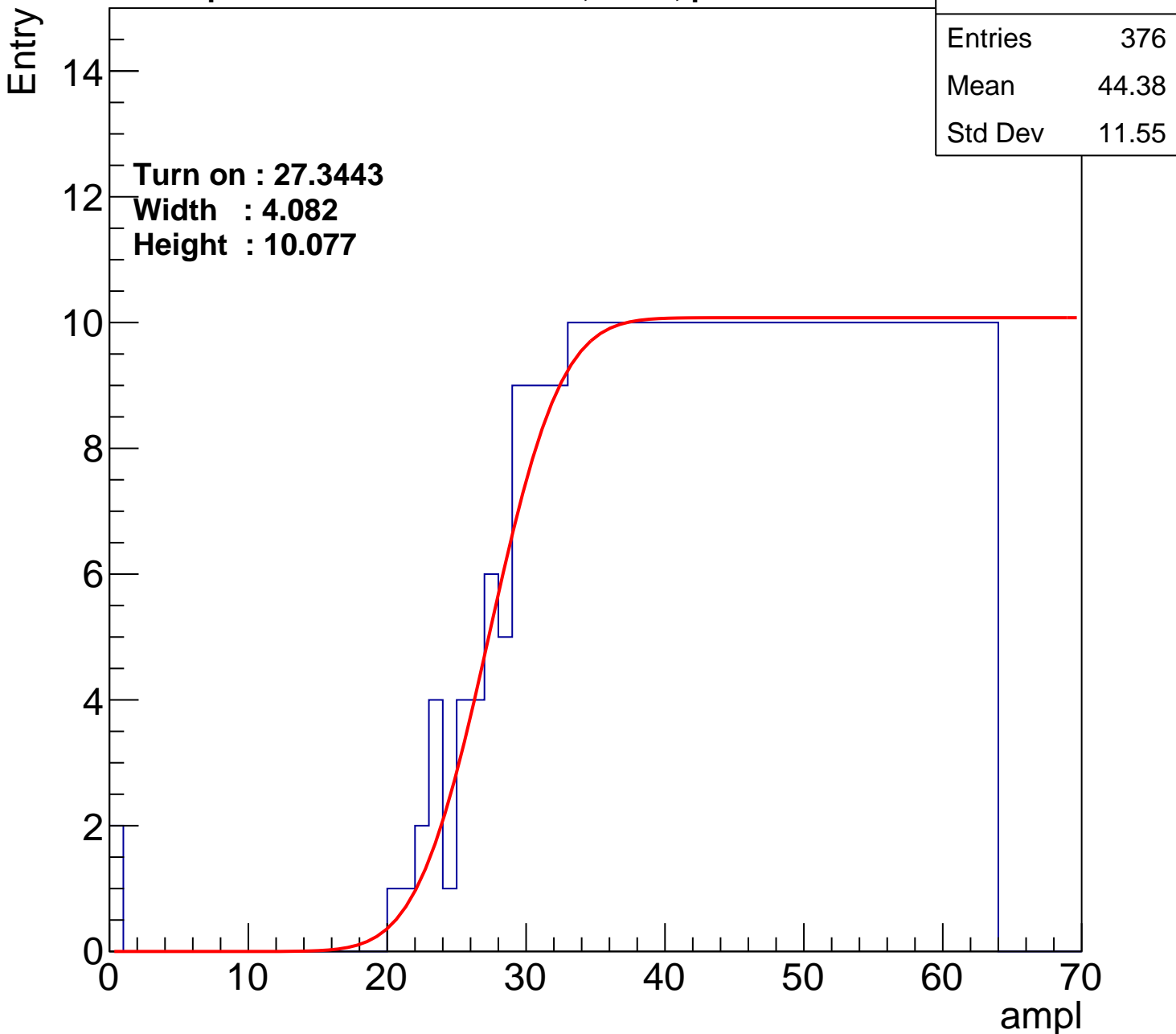
Entries	376
Mean	44.38
Std Dev	11.55

Mean	44.38
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Std Dev	11.55
---------	-------

Width : 4.082

Height : 10.077



B1L100S, U22-ch91

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 24.8468

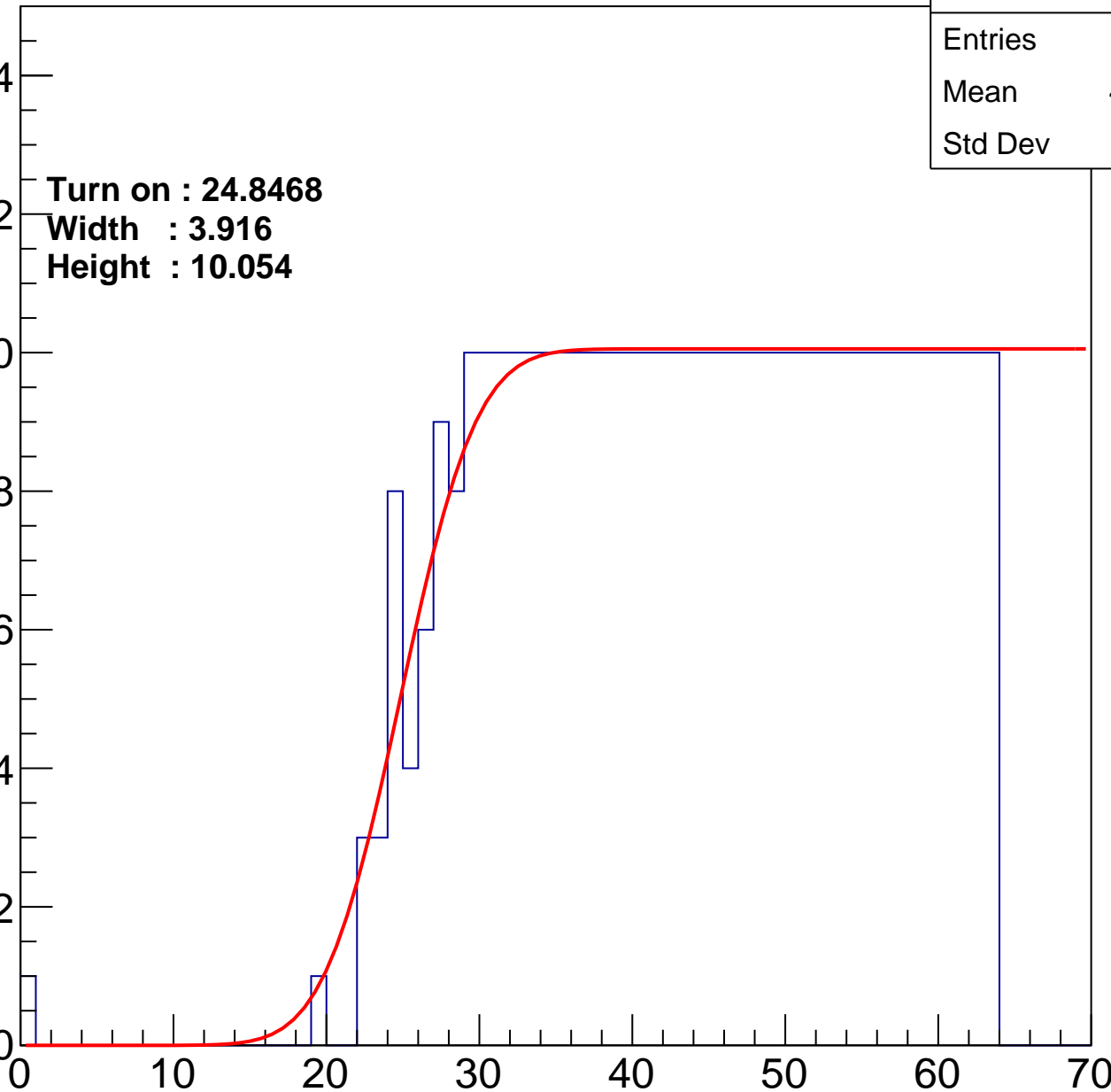
Width : 3.916

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch92

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 25.9889

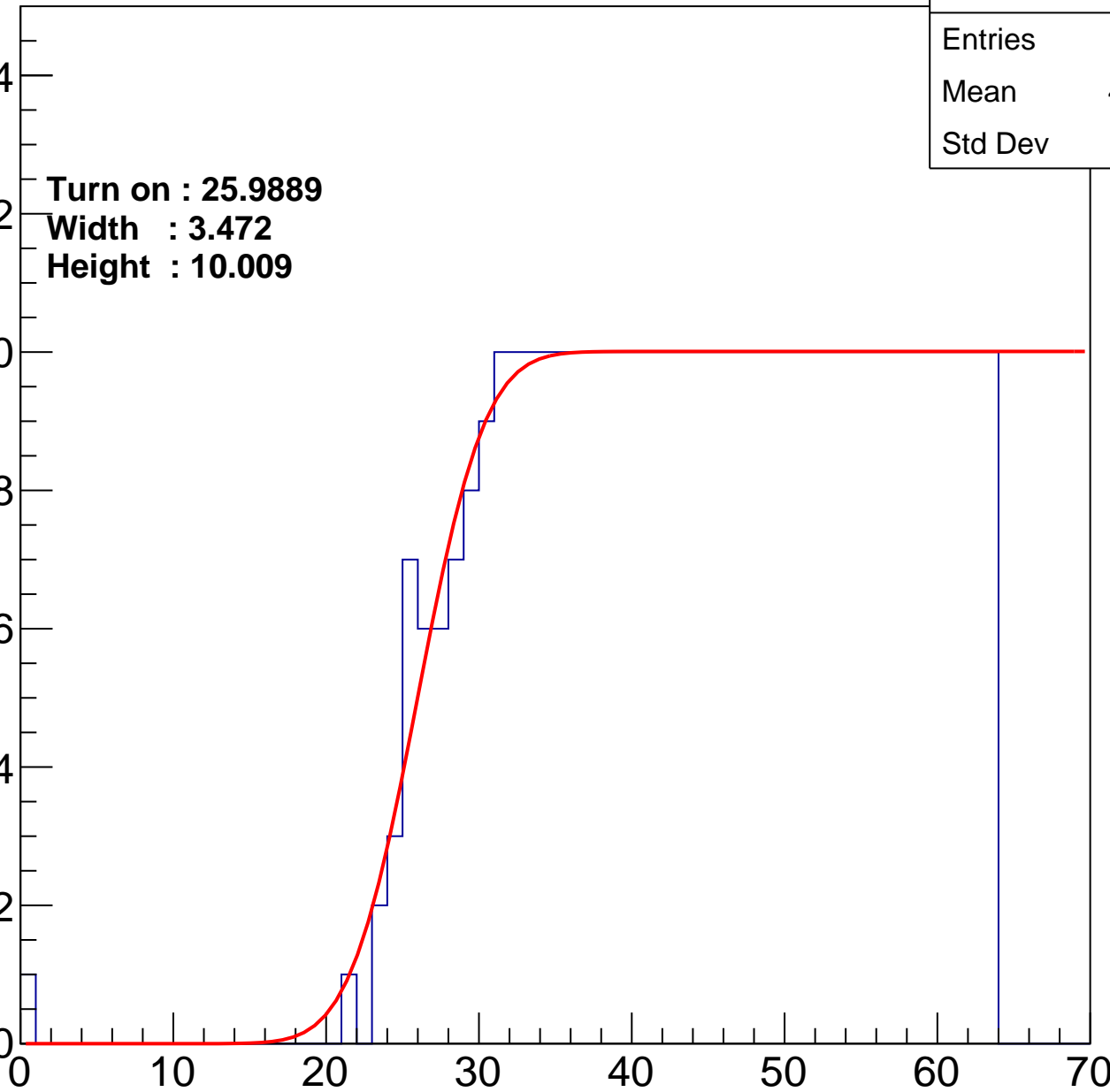
Width : 3.472

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.55
Std Dev	11.83

Turn on : 27.5864

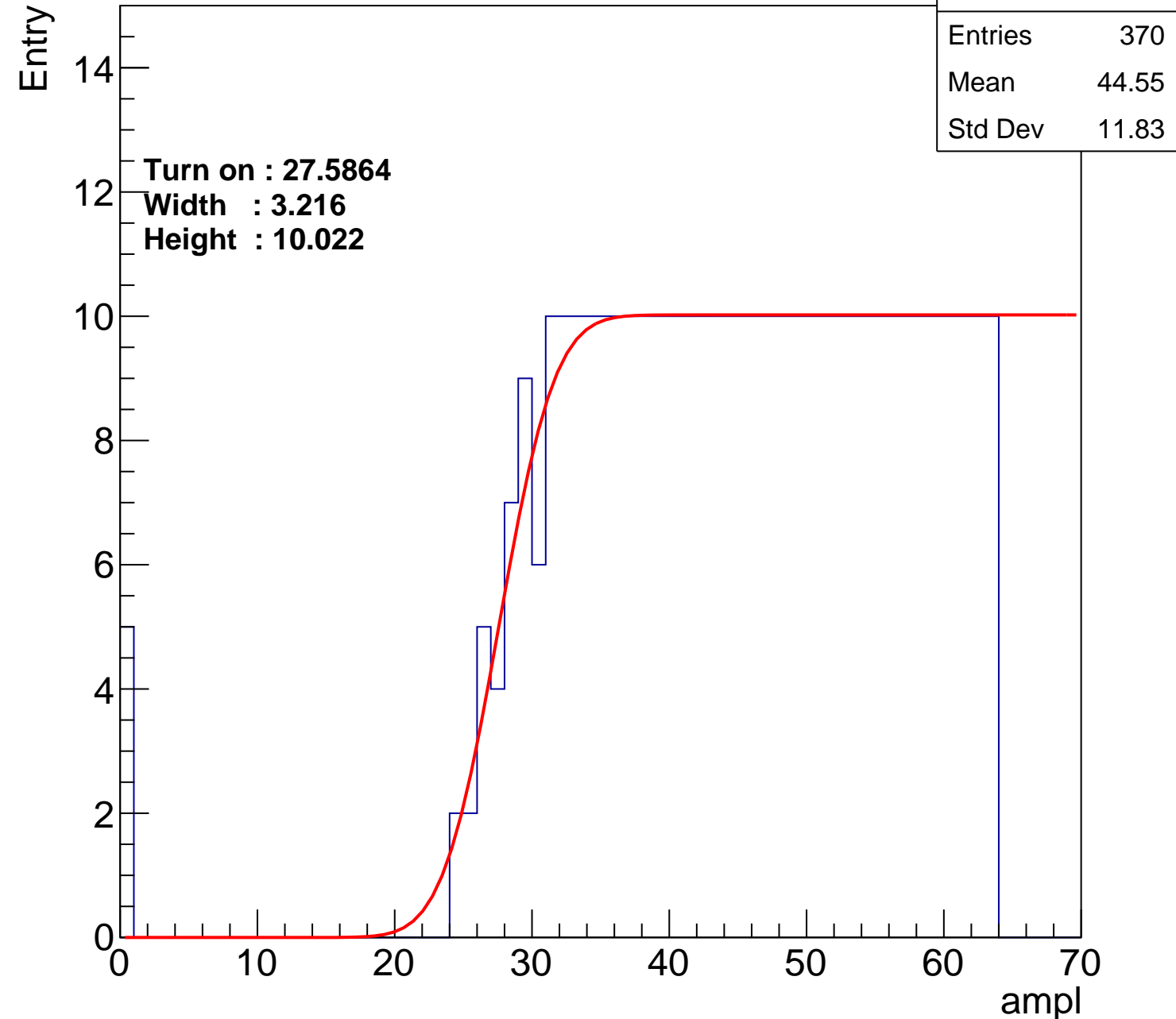
Width : 3.216

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.35
Std Dev	11.63

Turn on : 27.0690

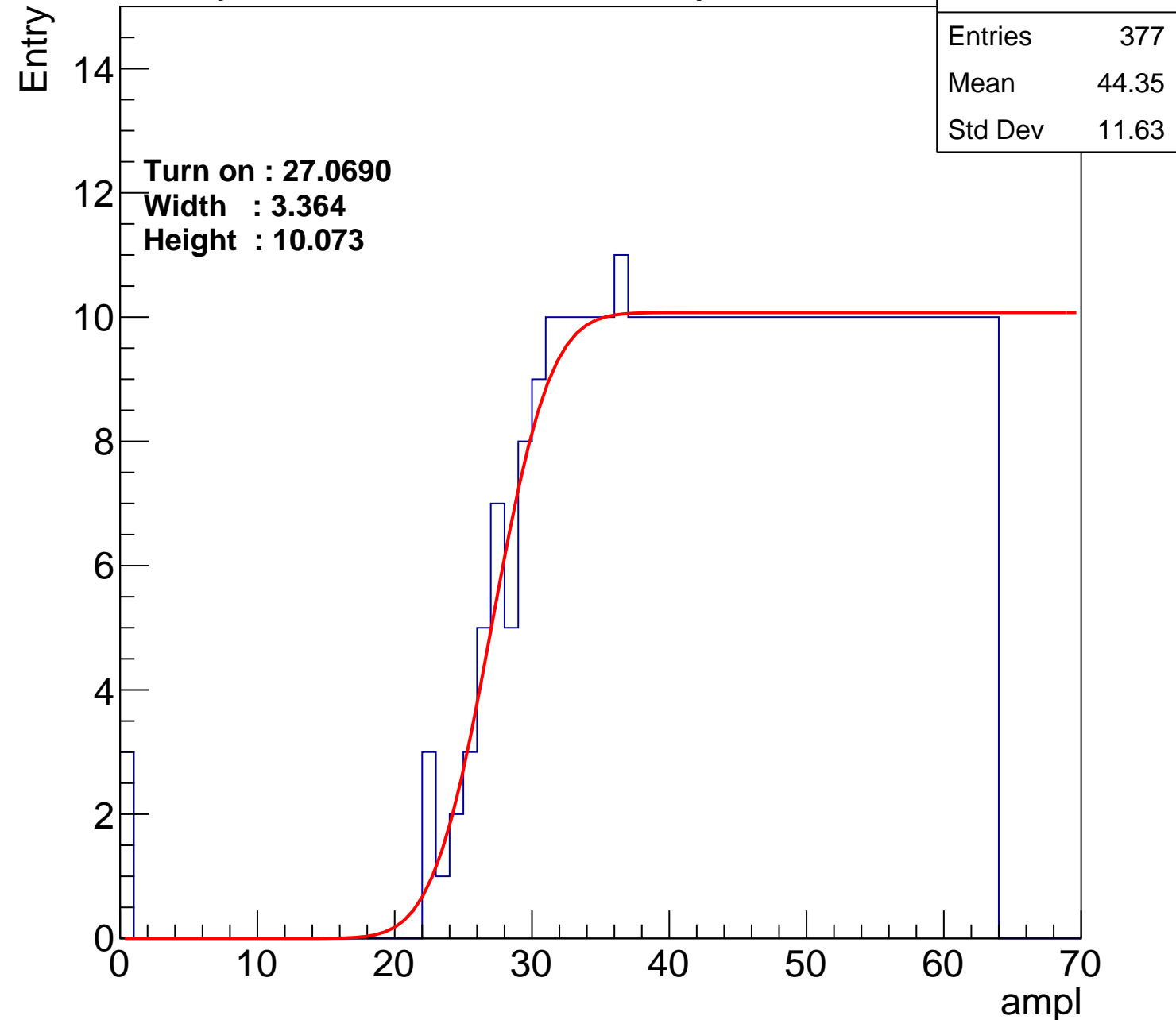
Width : 3.364

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch95

calib_packv5_042523_0143.root, FC#4, port A2

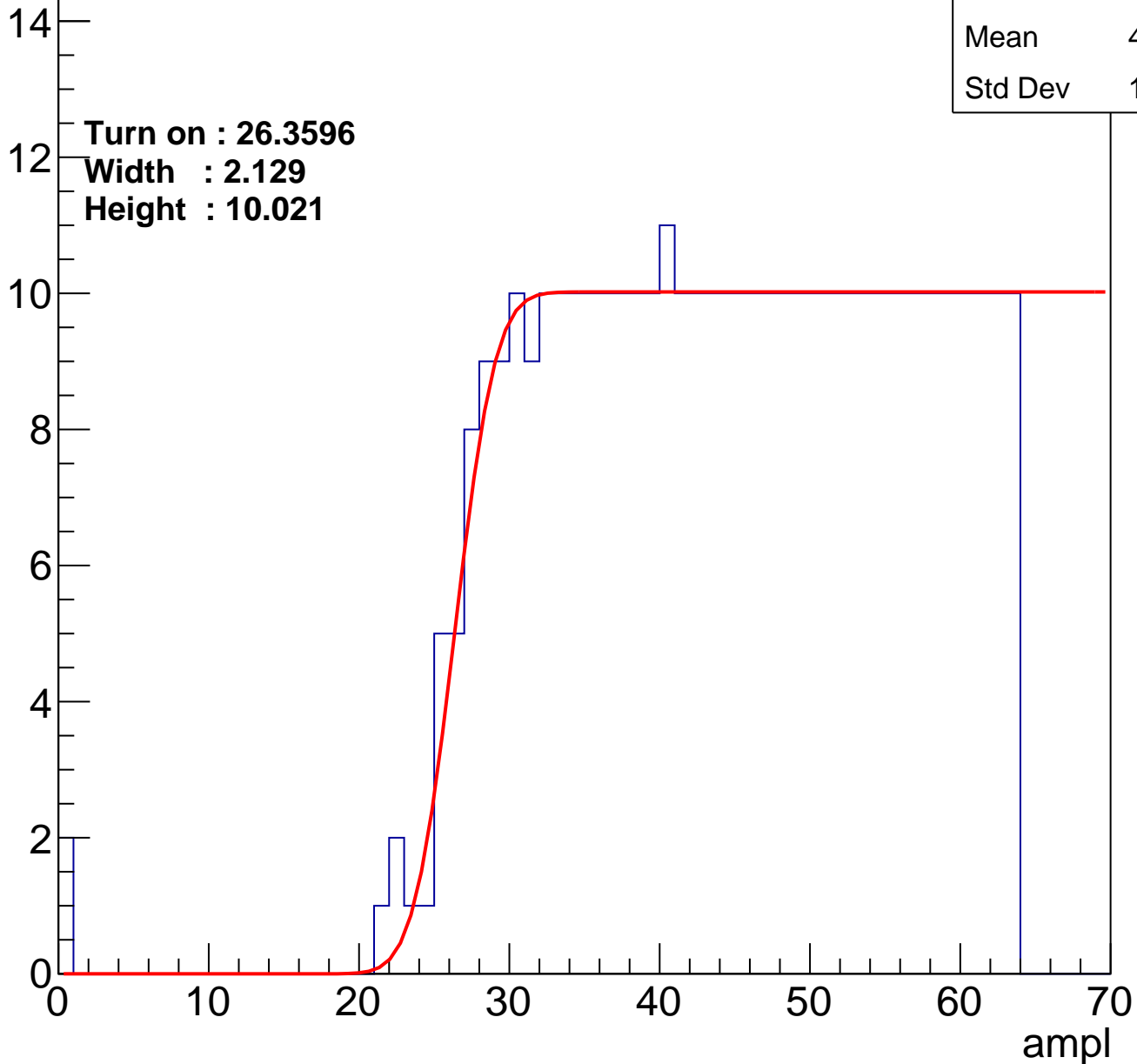
Entries	383
Mean	44.16
Std Dev	11.54

Turn on : 26.3596

Width : 2.129

Height : 10.021

Entry



B1L100S, U22-ch96

calib_packv5_042523_0143.root, FC#4, port A2

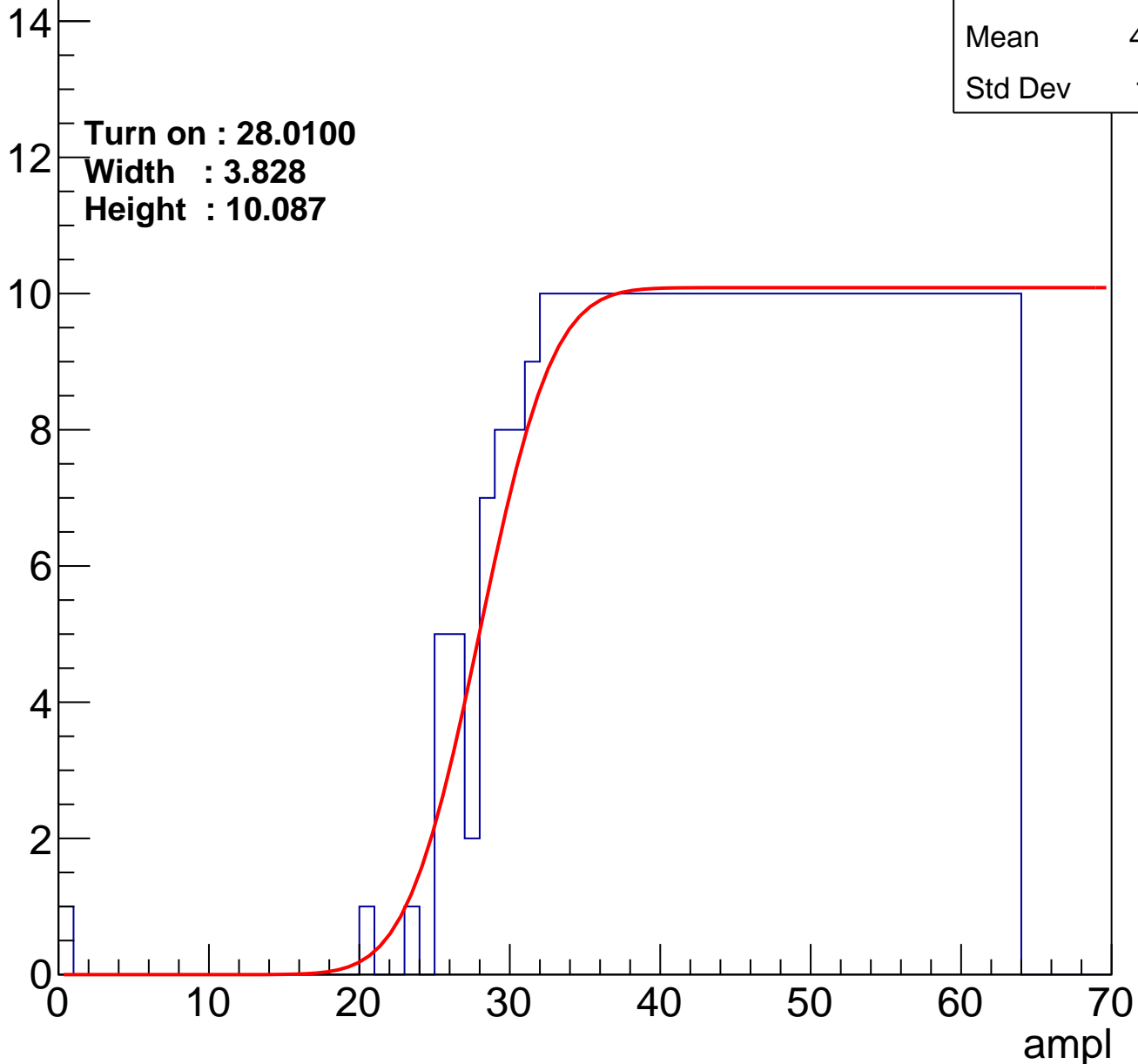
Entries	367
Mean	44.96
Std Dev	11.01

Turn on : 28.0100

Width : 3.828

Height : 10.087

Entry



B1L100S, U22-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.4285

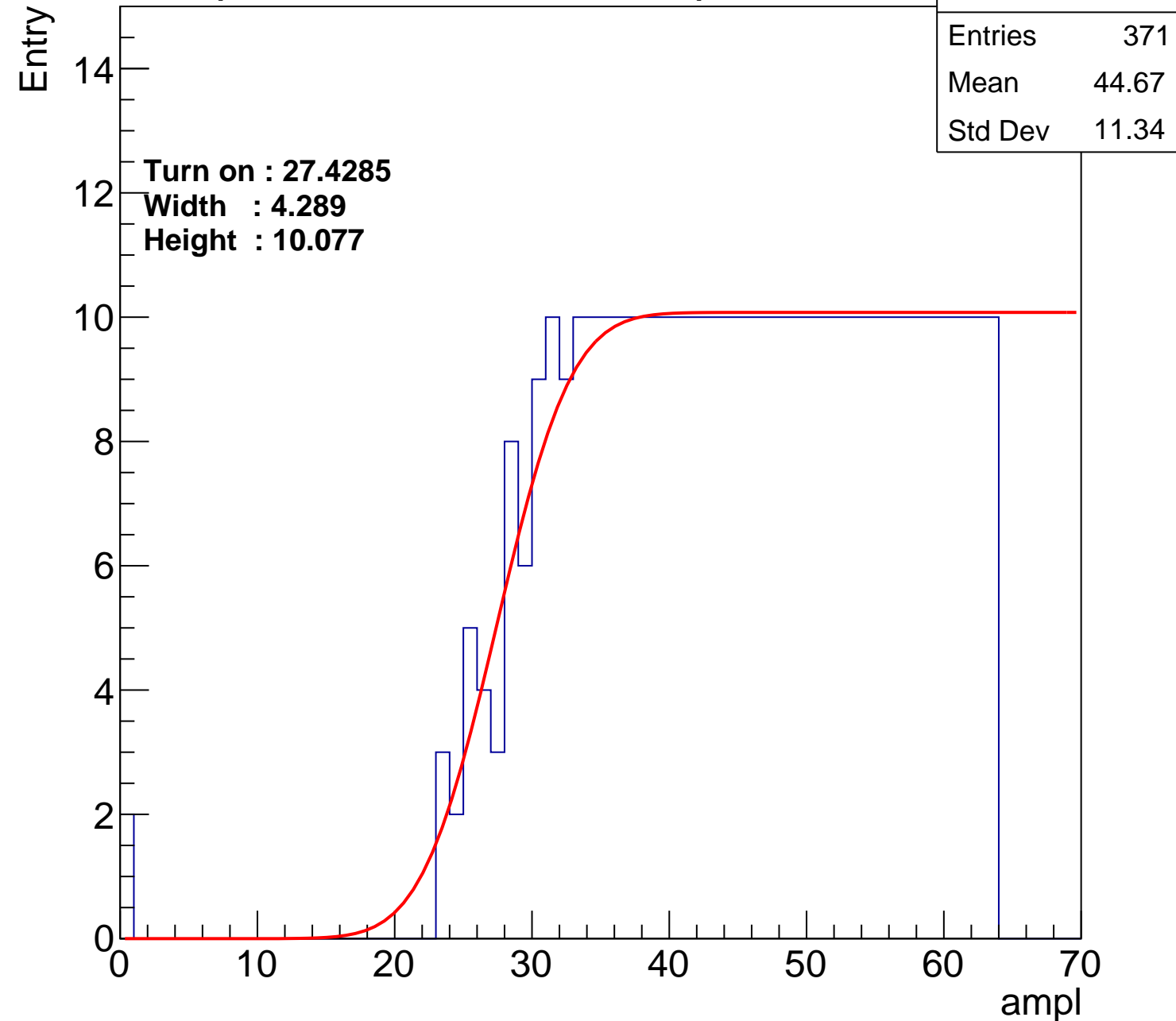
Width : 4.289

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch98

calib_packv5_042523_0143.root, FC#4, port A2

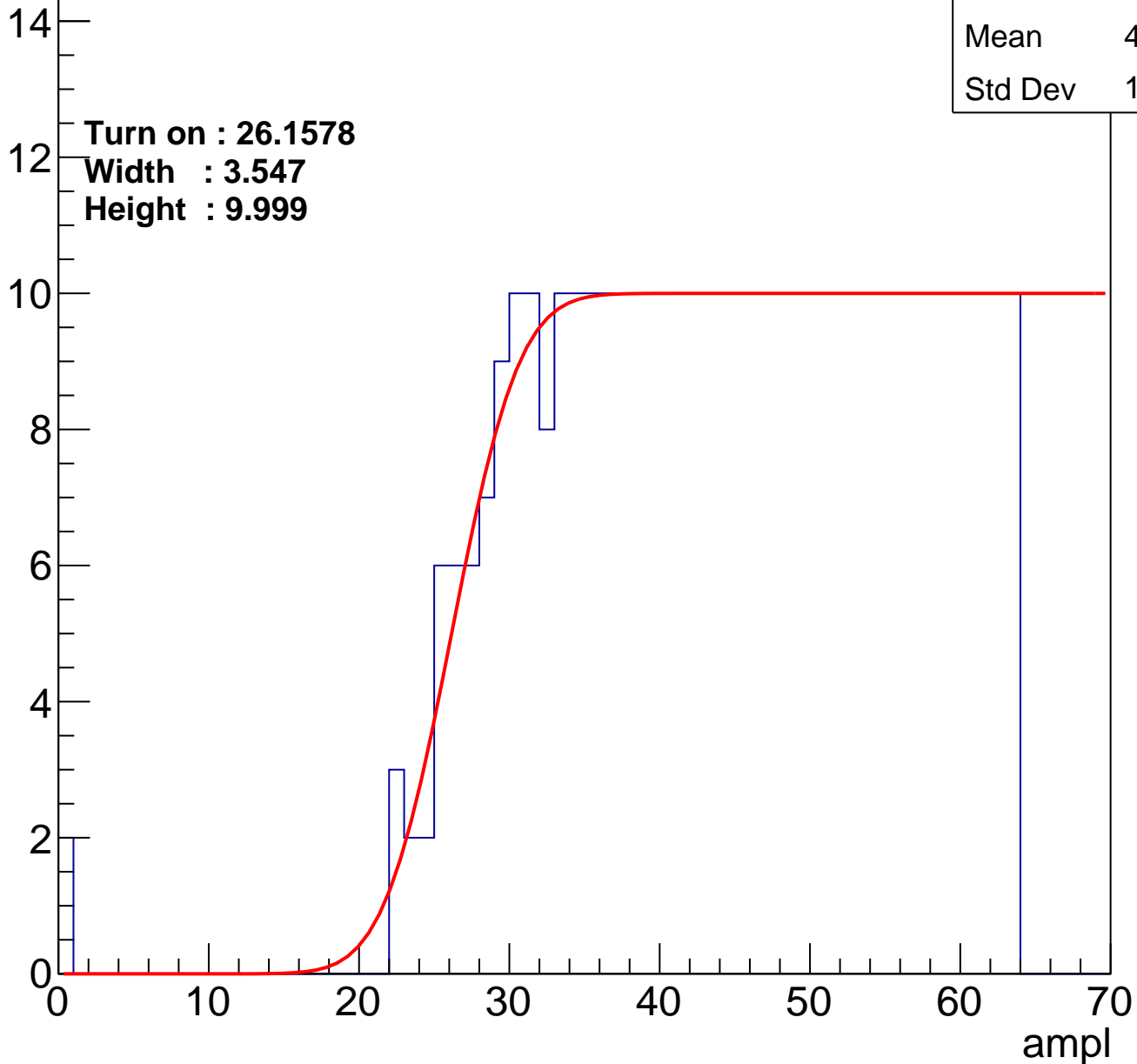
Entries	381
Mean	44.18
Std Dev	11.59

Turn on : 26.1578

Width : 3.547

Height : 9.999

Entry



B1L100S, U22-ch99

calib_packv5_042523_0143.root, FC#4, port A2

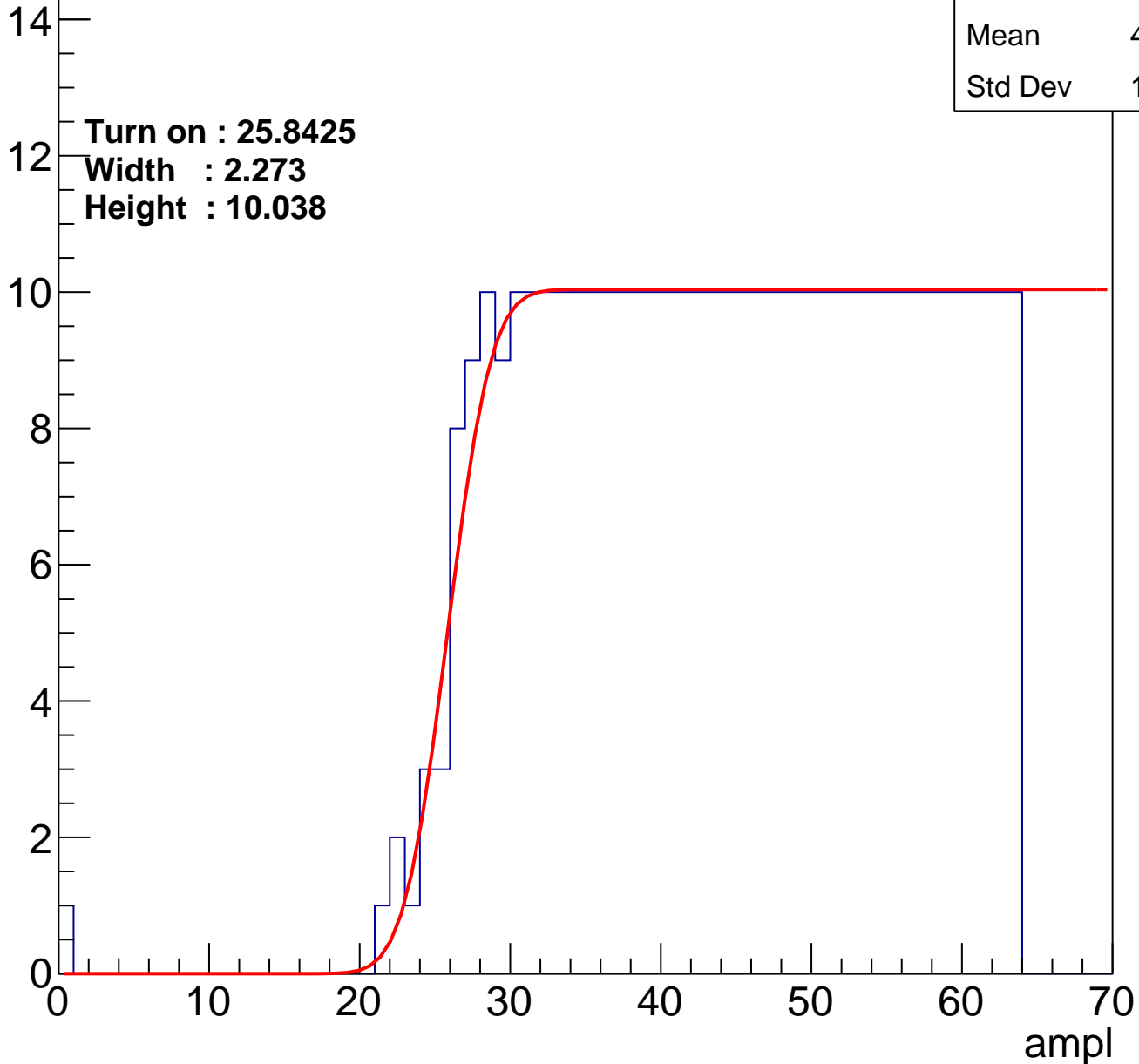
Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.8425

Width : 2.273

Height : 10.038

Entry



B1L100S, U22-ch100

calib_packv5_042523_0143.root, FC#4, port A2

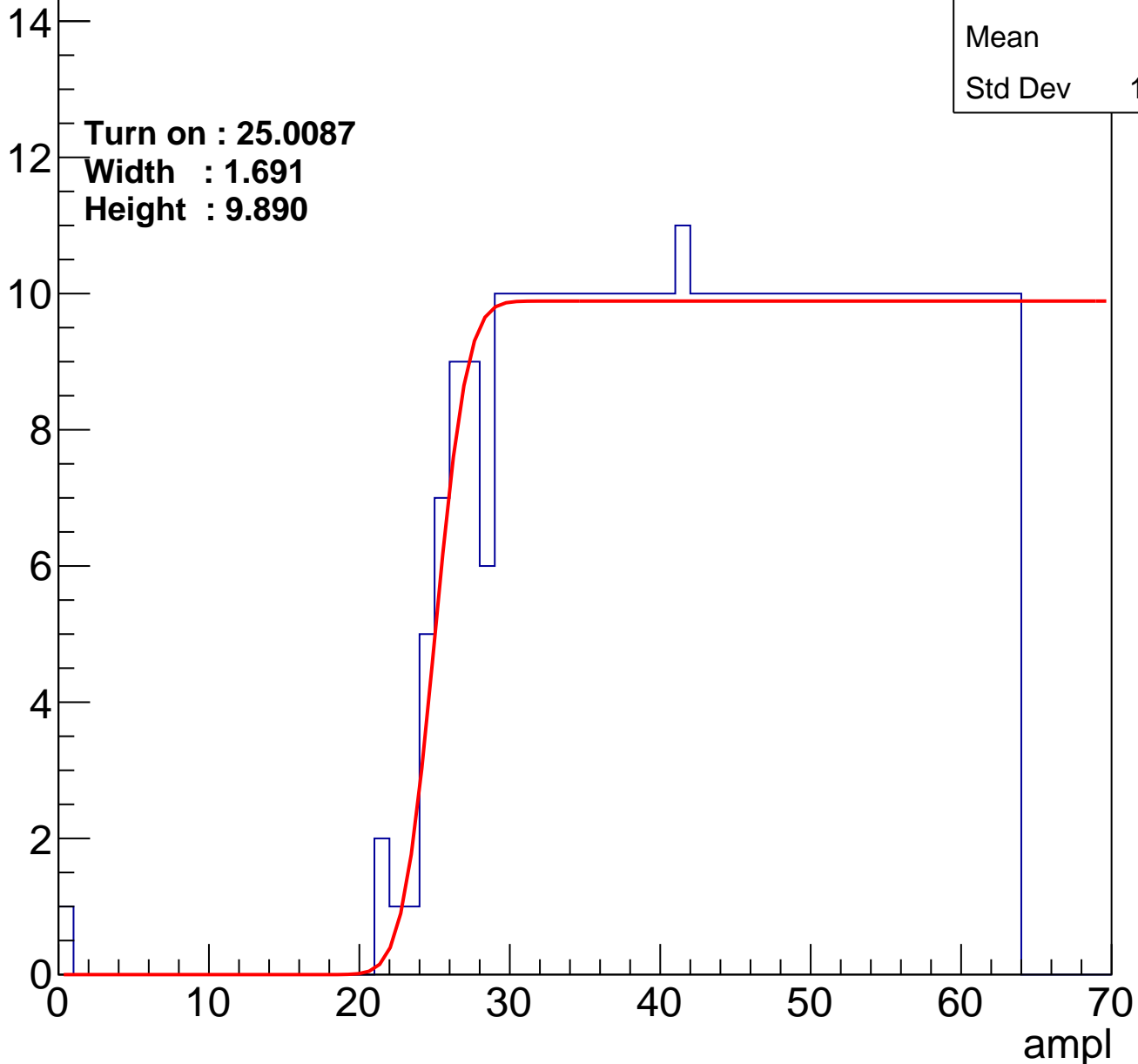
Entries	392
Mean	43.8
Std Dev	11.58

Turn on : 25.0087

Width : 1.691

Height : 9.890

Entry



B1L100S, U22-ch101

calib_packv5_042523_0143.root, FC#4, port A2

Entries	397
Mean	43.35
Std Dev	12.14

Turn on : 25.0182

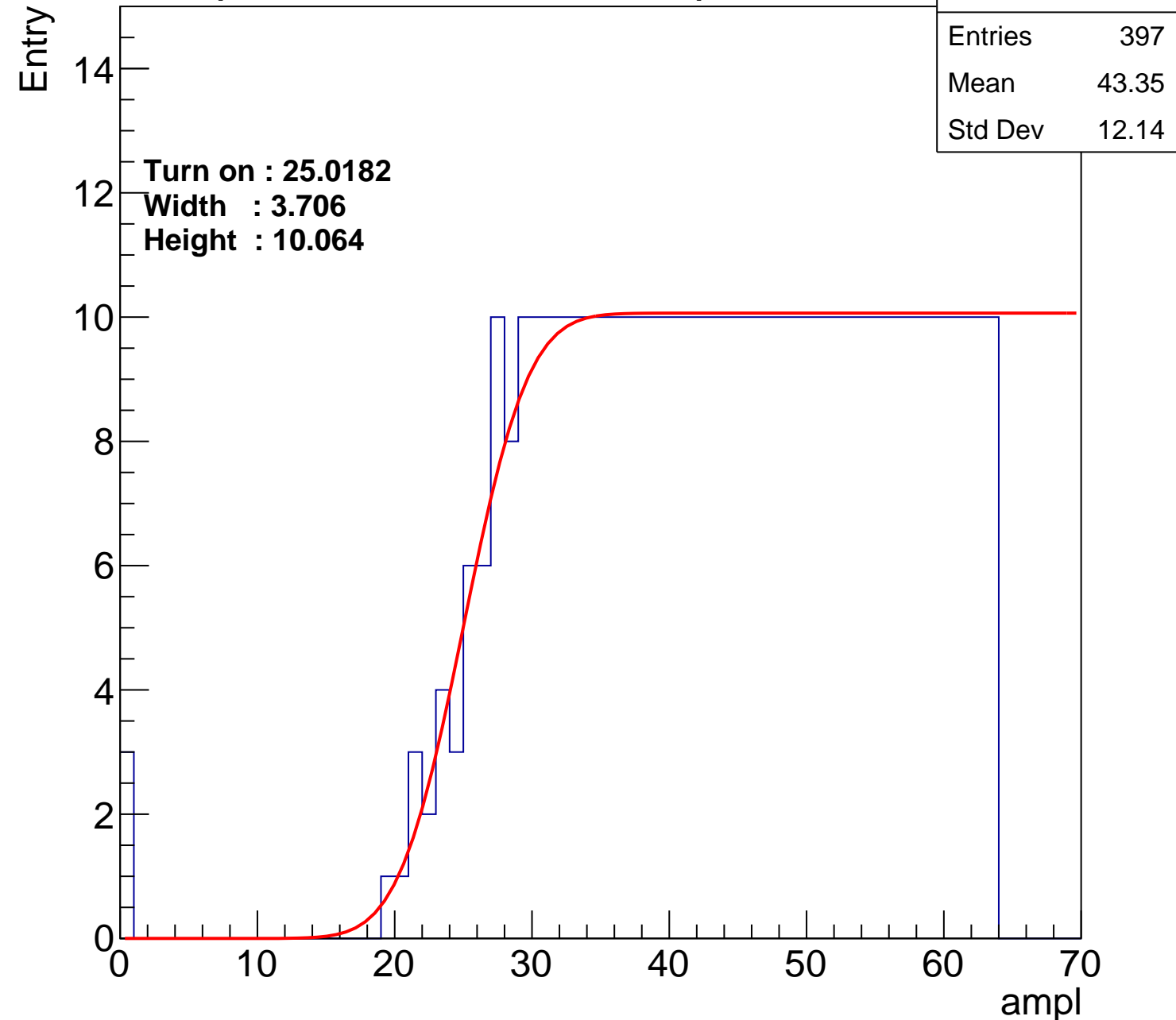
Width : 3.706

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.58
Std Dev	11.33

Turn on : 27.0353

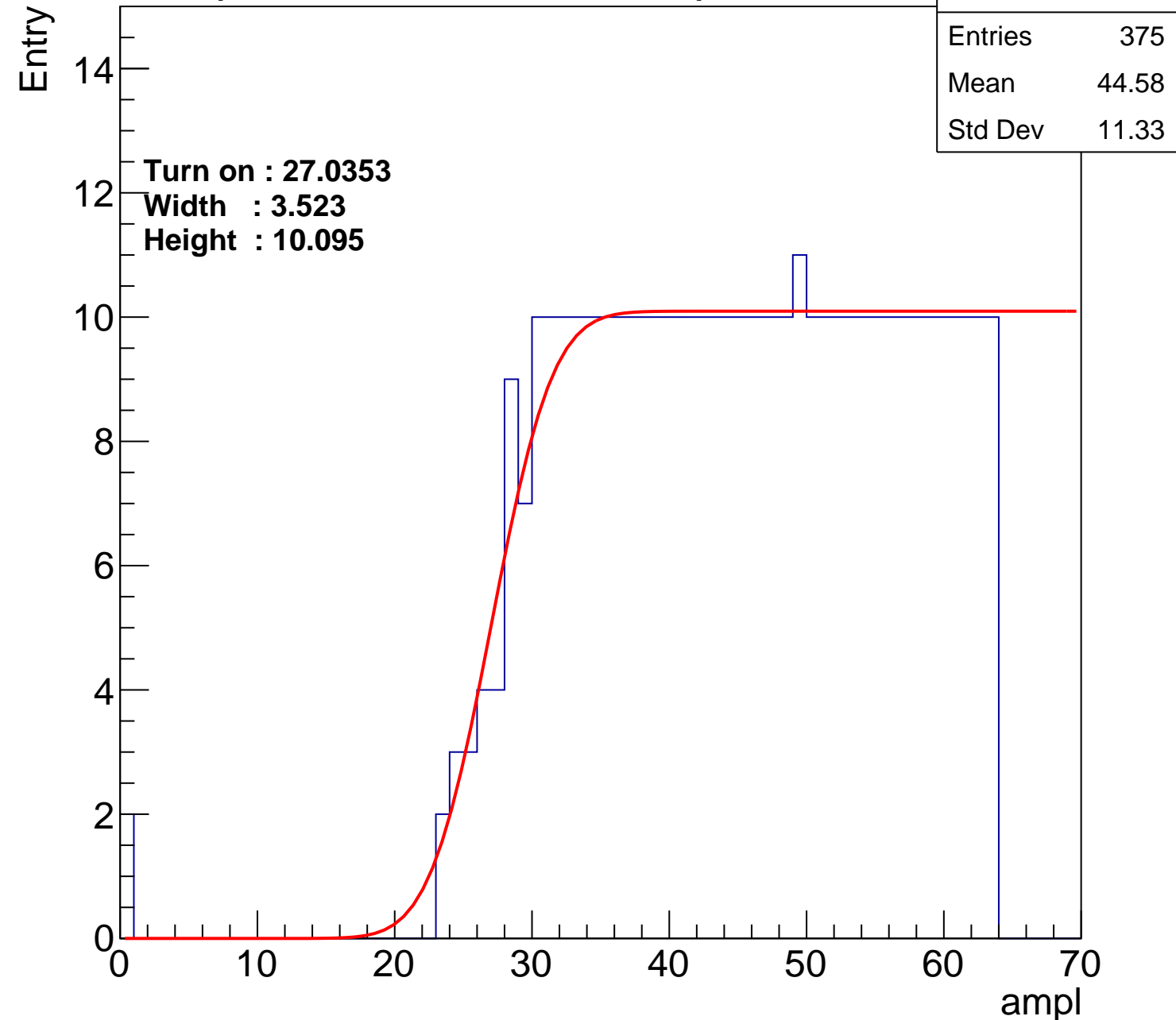
Width : 3.523

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45
Std Dev	11.27

Turn on : 27.7045

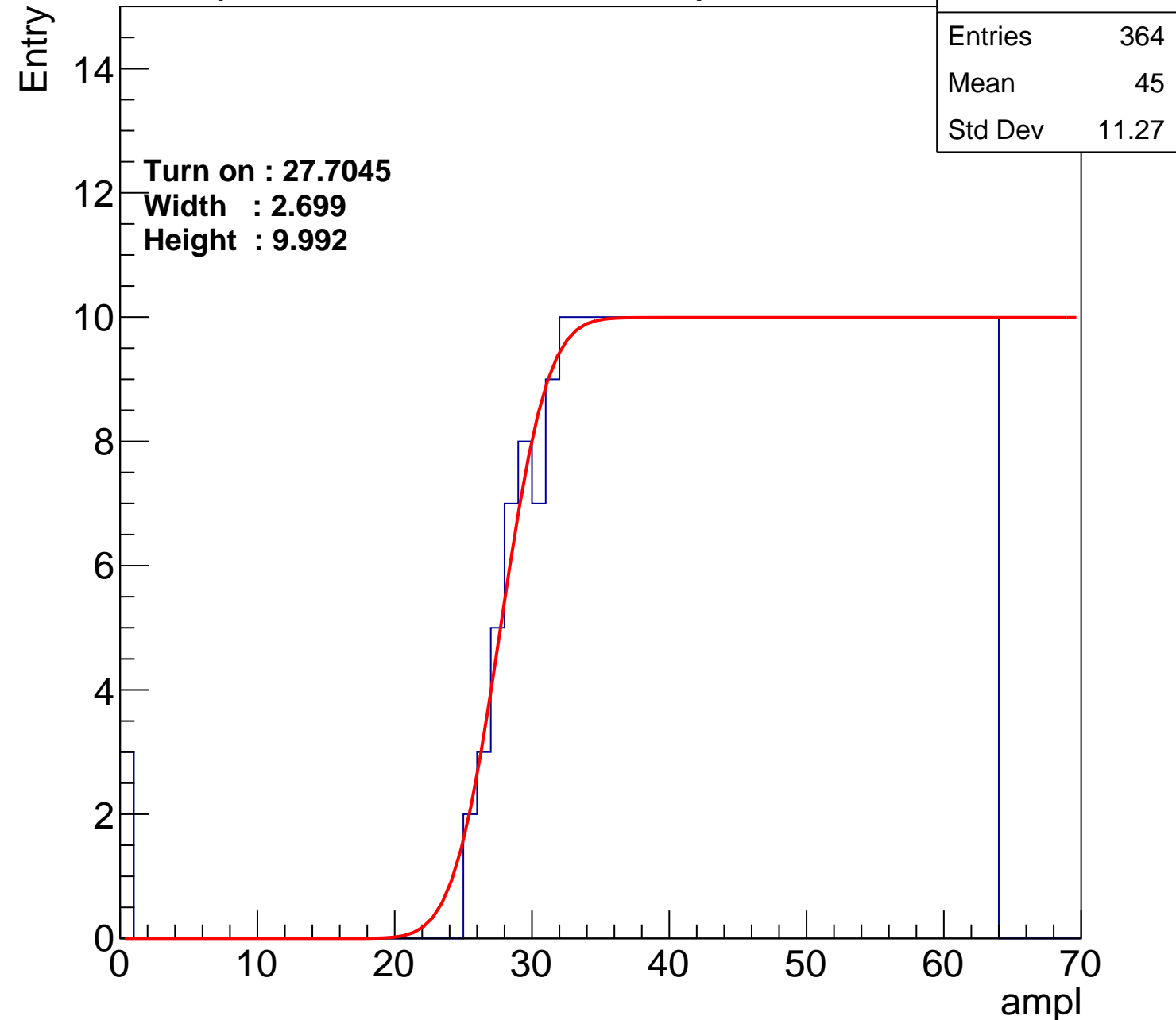
Width : 2.699

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch104

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.59
Std Dev	12.14

Turn on : 25.6615

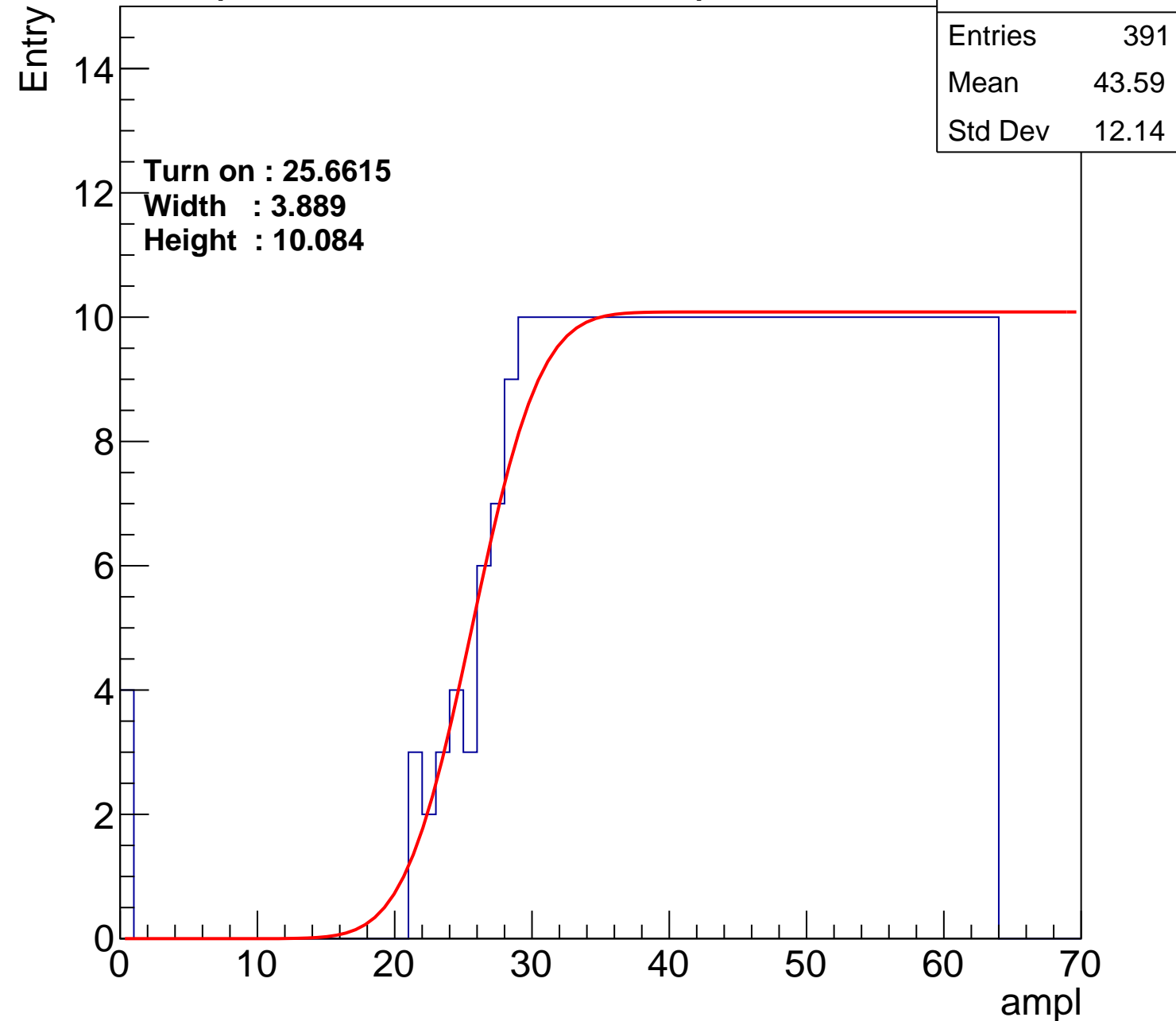
Width : 3.889

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.76
Std Dev	11.31

Turn on : 27.7501

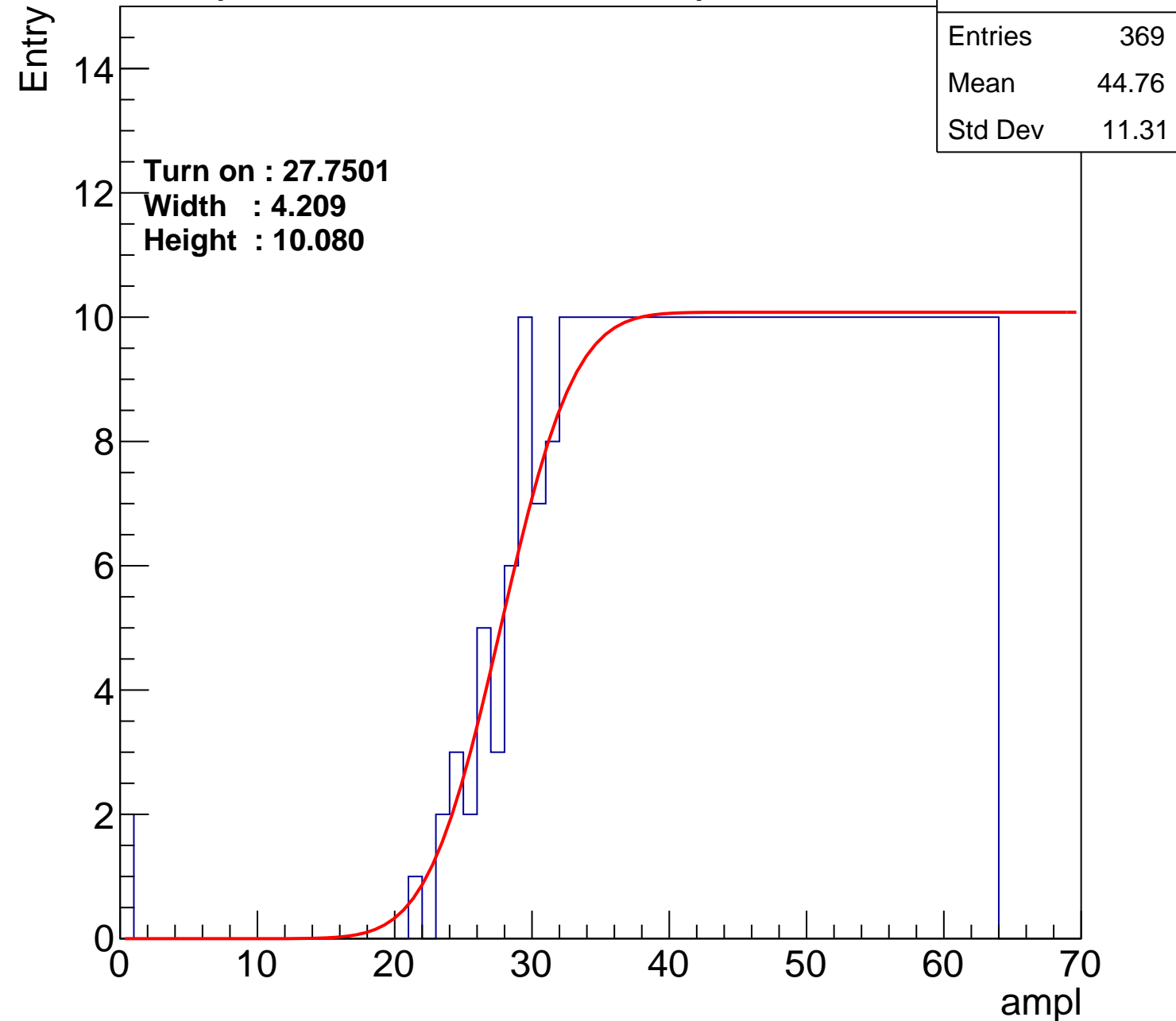
Width : 4.209

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch106

calib_packv5_042523_0143.root, FC#4, port A2

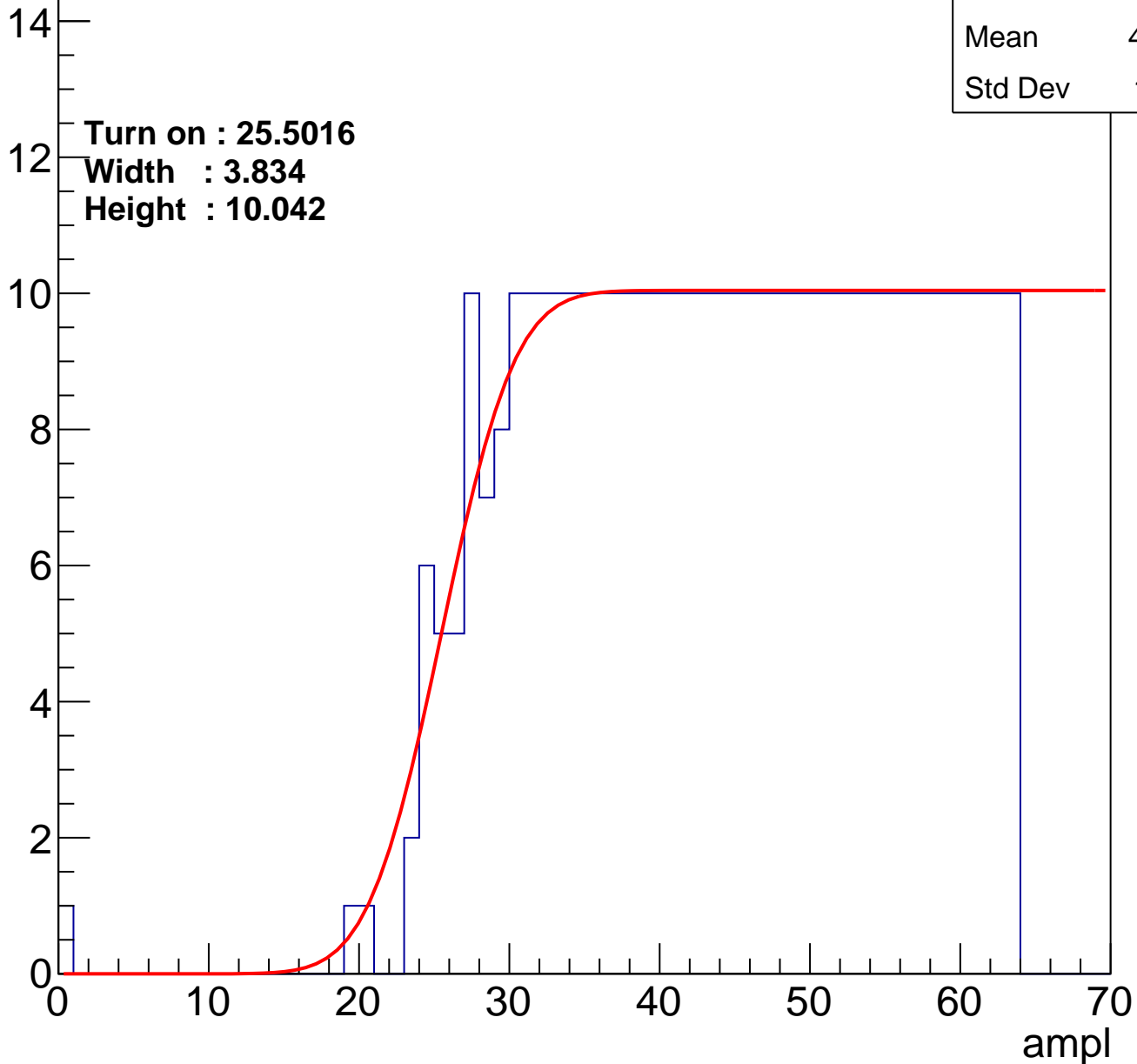
Entries	386
Mean	44.02
Std Dev	11.51

Turn on : 25.5016

Width : 3.834

Height : 10.042

Entry



B1L100S, U22-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.4
Std Dev	11.5

Turn on : 26.9160

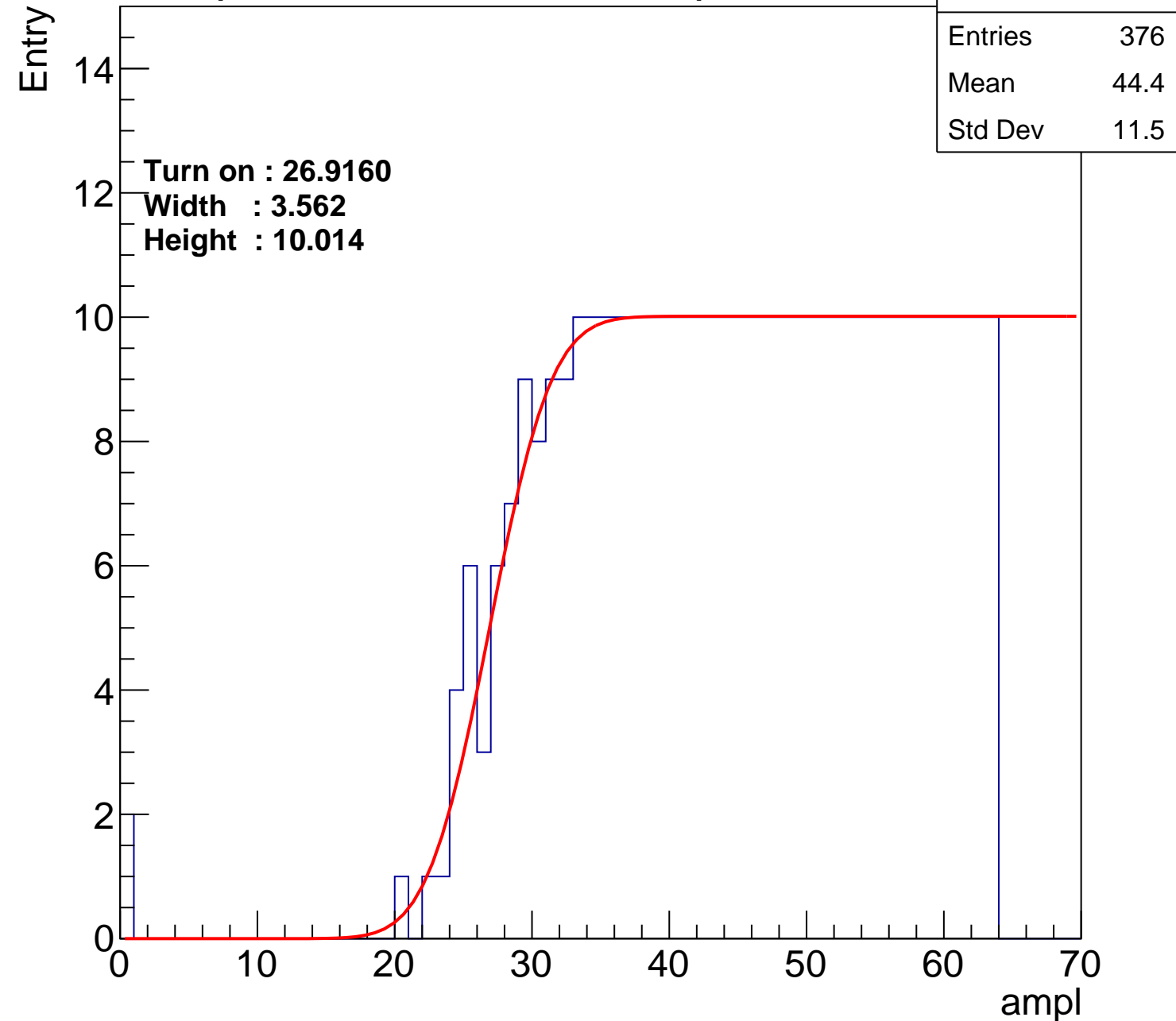
Width : 3.562

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch108

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.25
Std Dev	11.54

Turn on : 26.4199

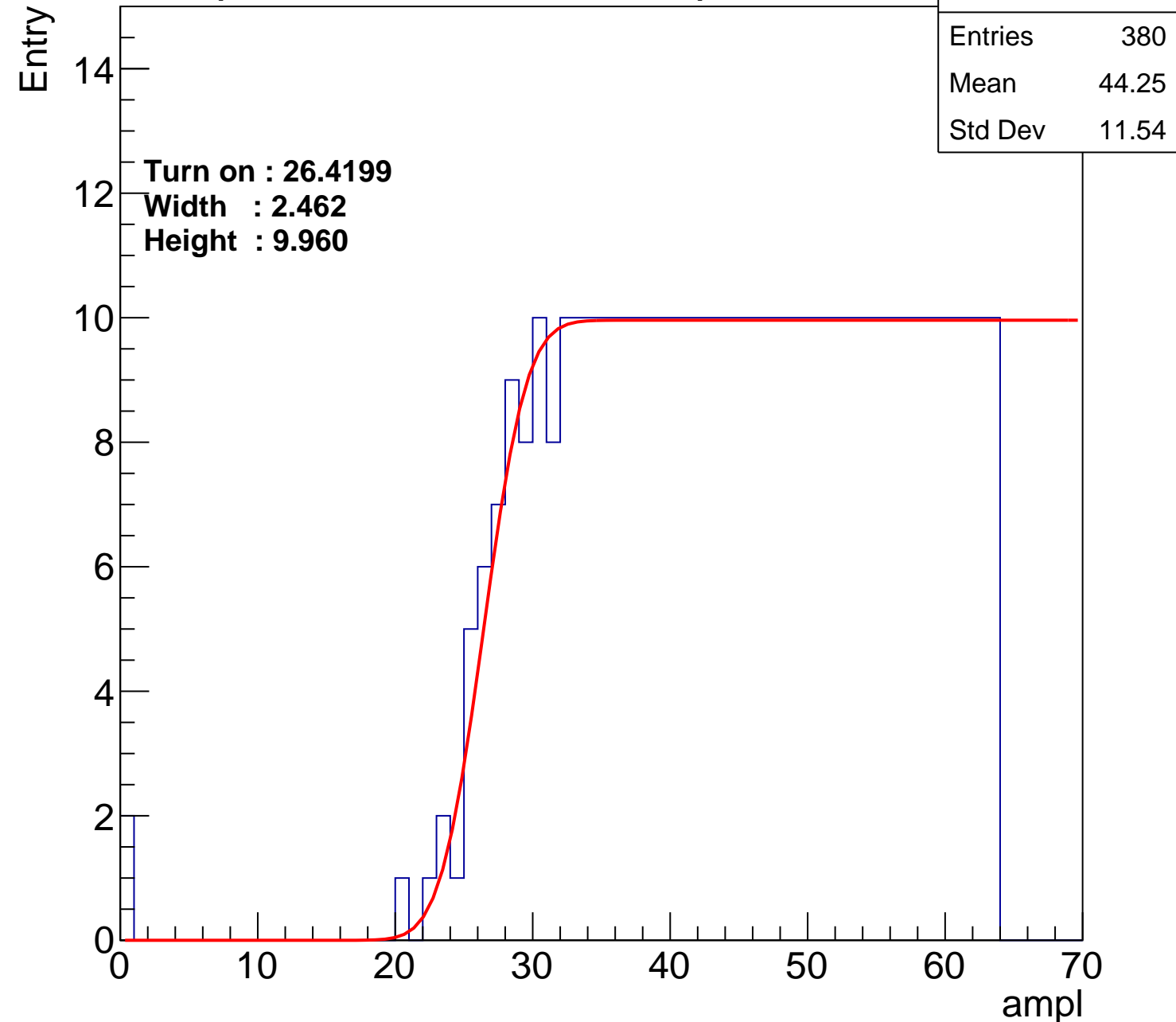
Width : 2.462

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch109

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.9049

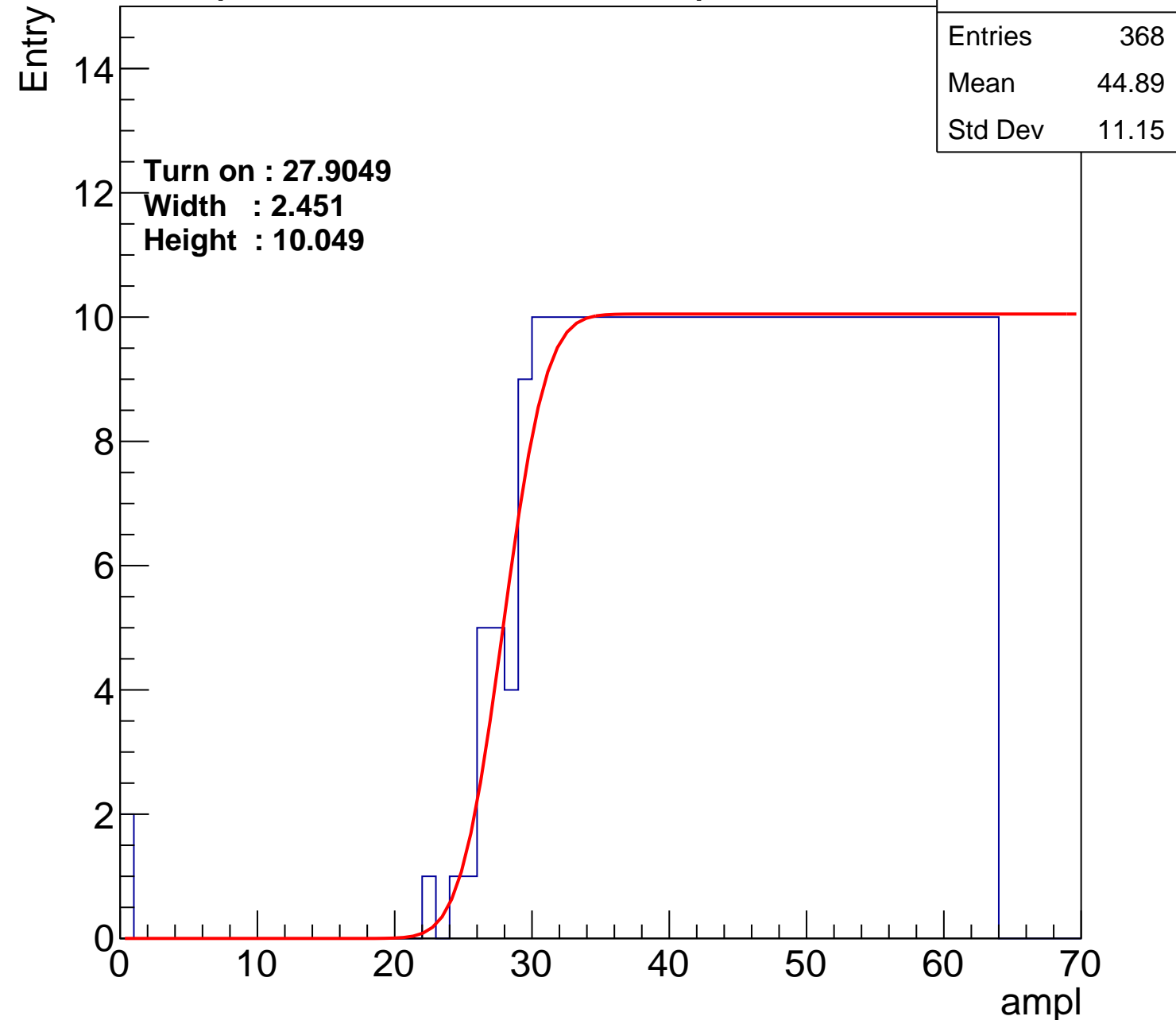
Width : 2.451

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch110

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.81
Std Dev	11.88

Turn on : 26.8825

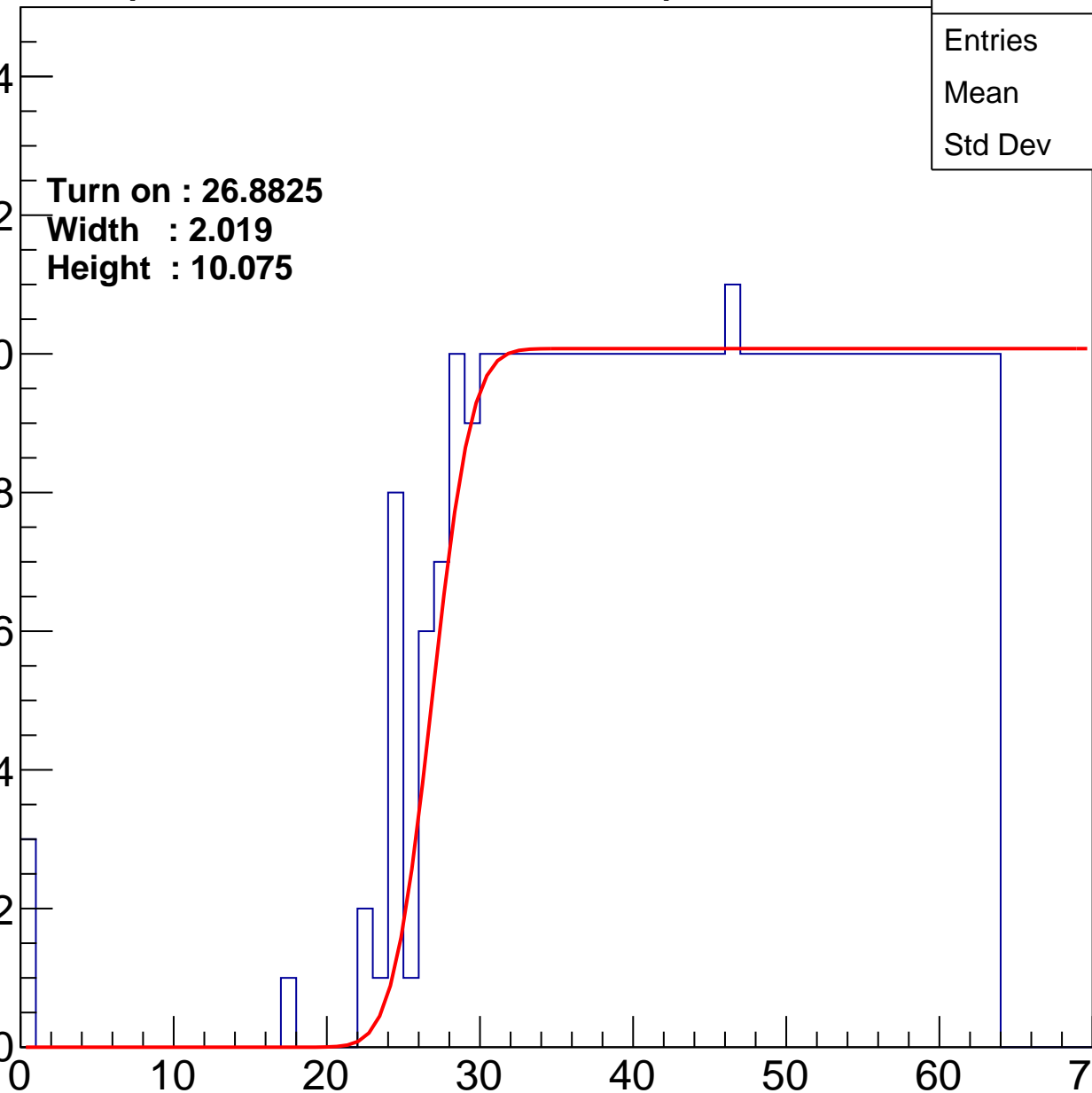
Width : 2.019

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.78
Std Dev	11.09

Turn on : 26.9134

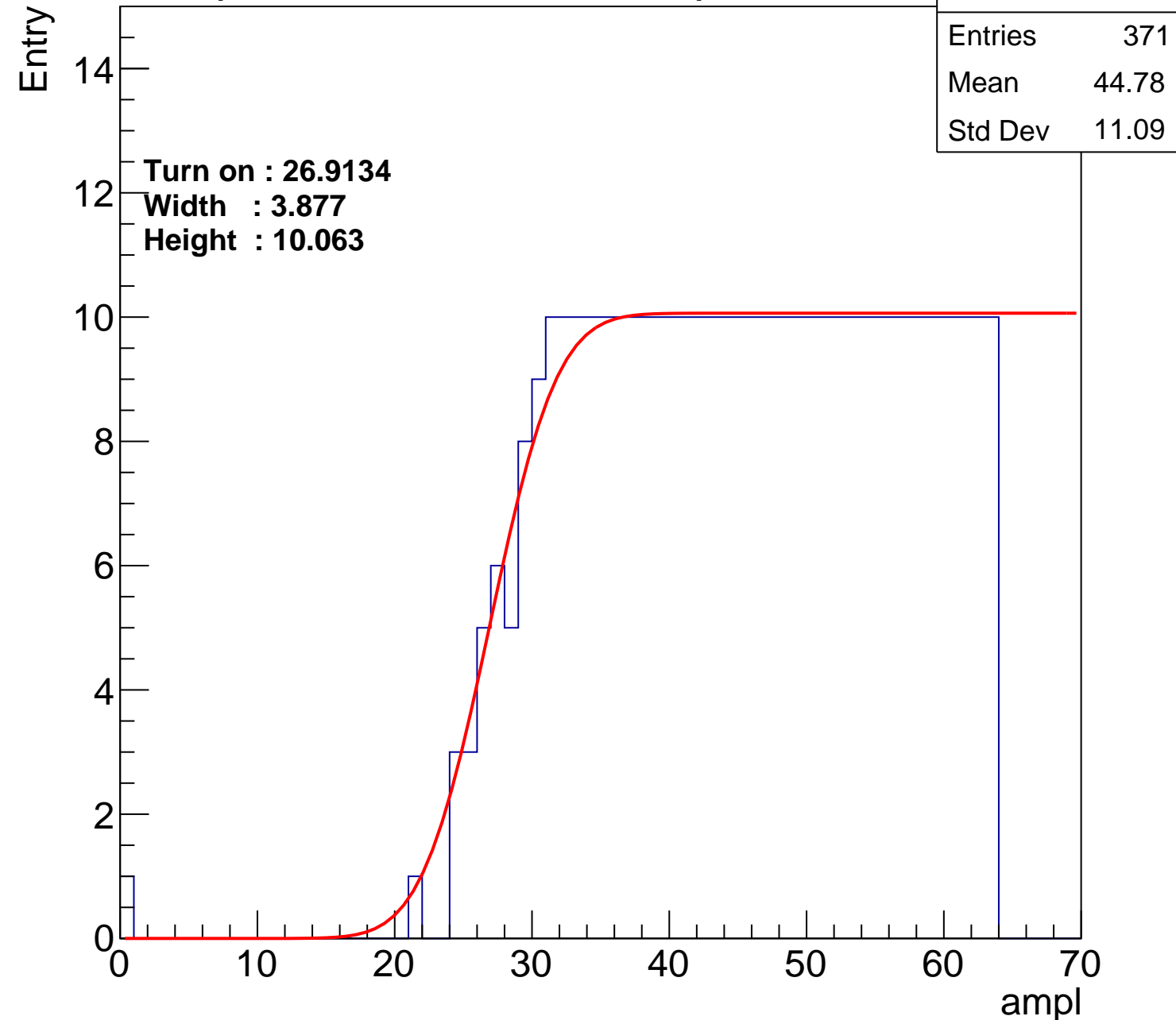
Width : 3.877

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch112

calib_packv5_042523_0143.root, FC#4, port A2

Entries	404
Mean	43
Std Dev	12.38

Turn on : 24.0570

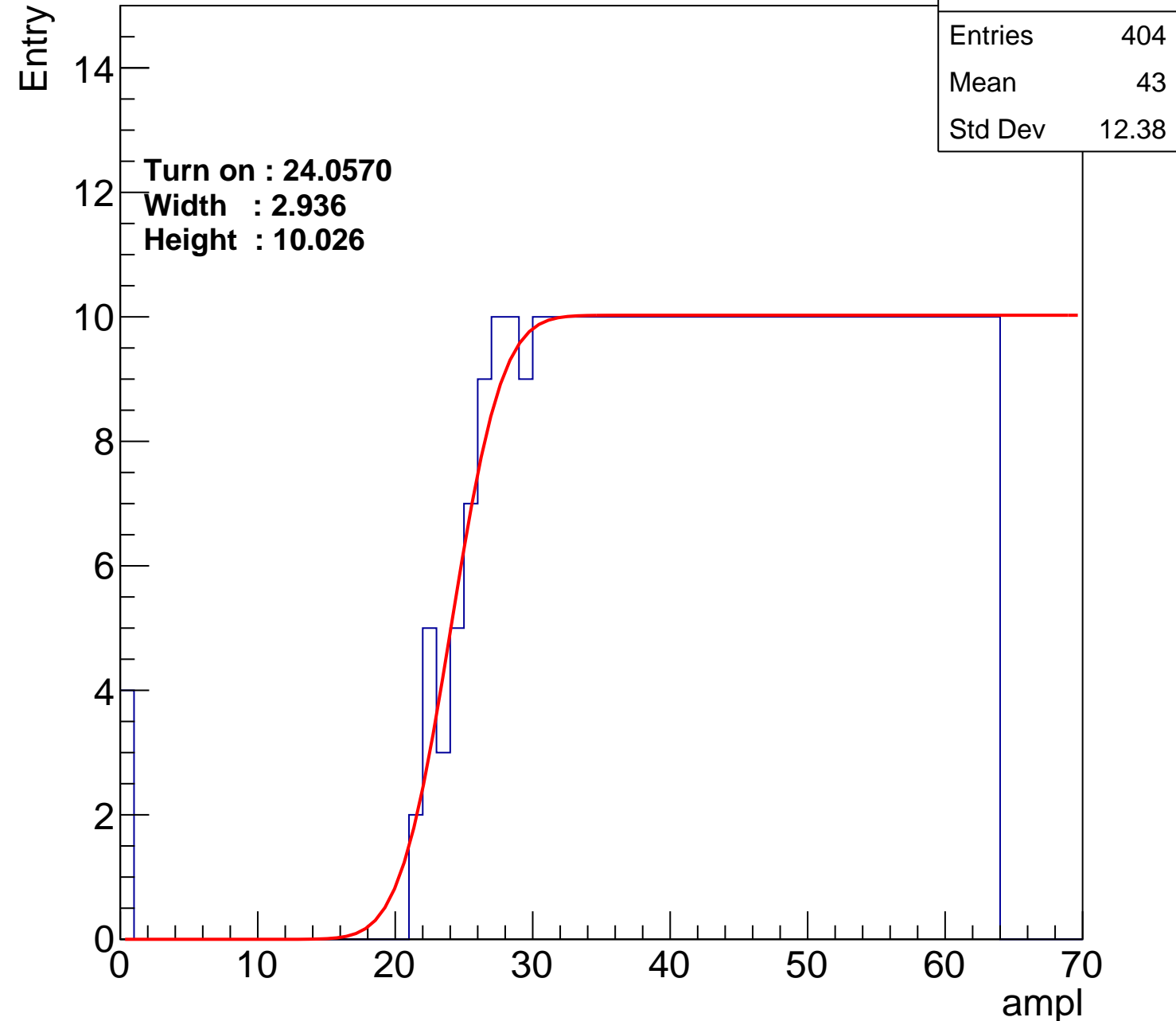
Width : 2.936

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.03
Std Dev	11.09

Turn on : 28.0196

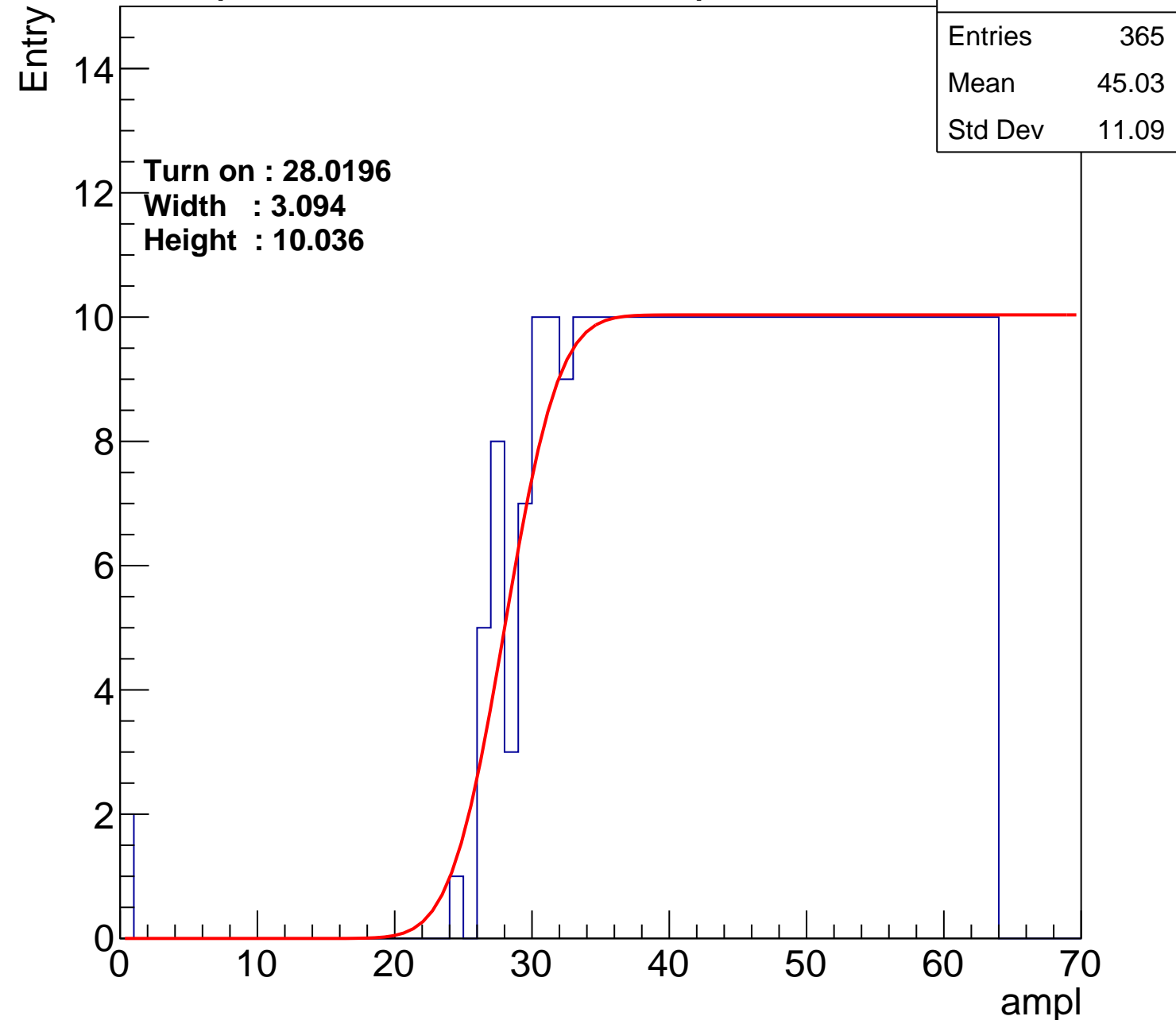
Width : 3.094

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch114

calib_packv5_042523_0143.root, FC#4, port A2

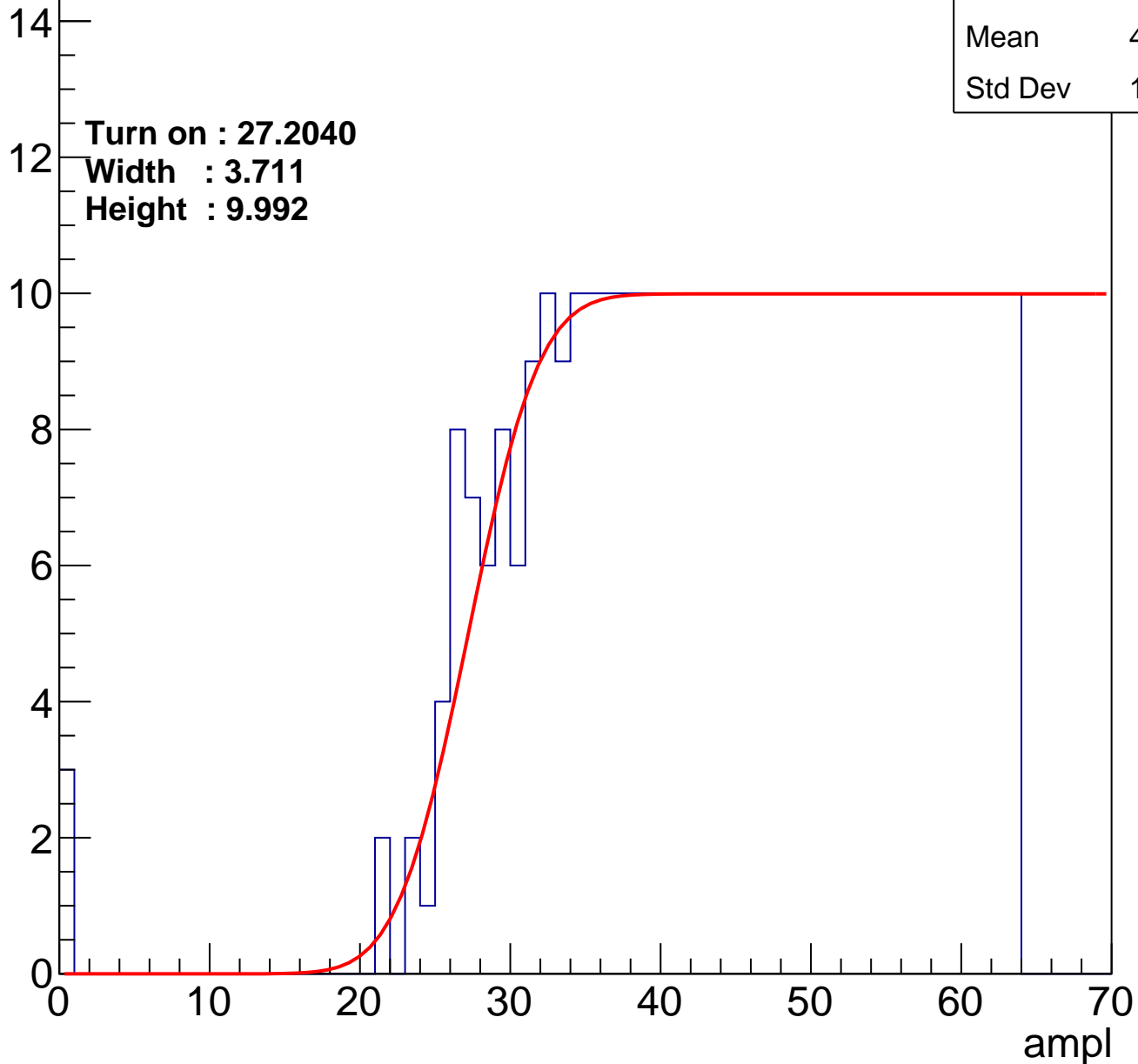
Entries	375
Mean	44.36
Std Dev	11.69

Turn on : 27.2040

Width : 3.711

Height : 9.992

Entry



B1L100S, U22-ch115

calib_packv5_042523_0143.root, FC#4, port A2

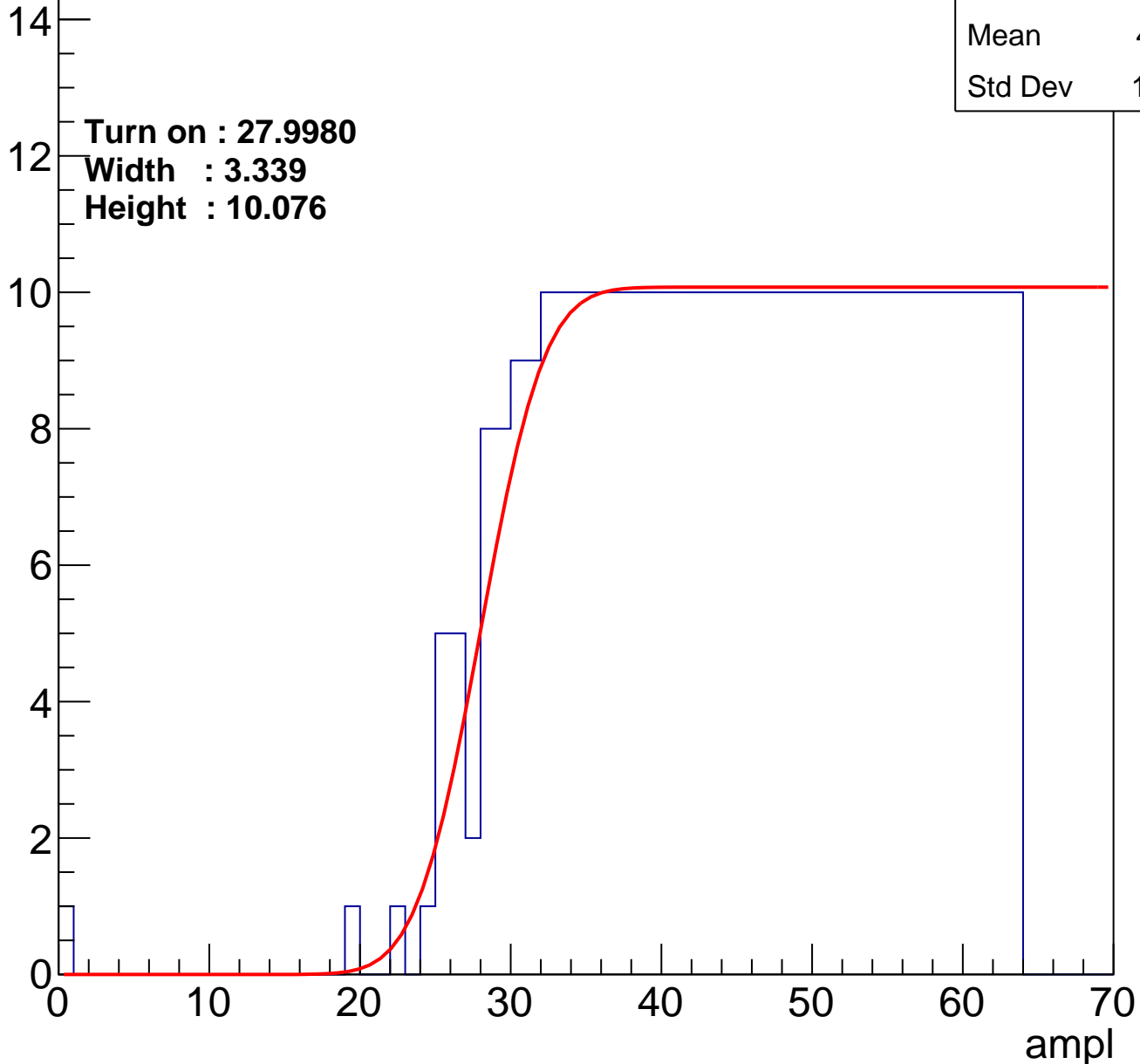
Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.9980

Width : 3.339

Height : 10.076

Entry



B1L100S, U22-ch116

calib_packv5_042523_0143.root, FC#4, port A2

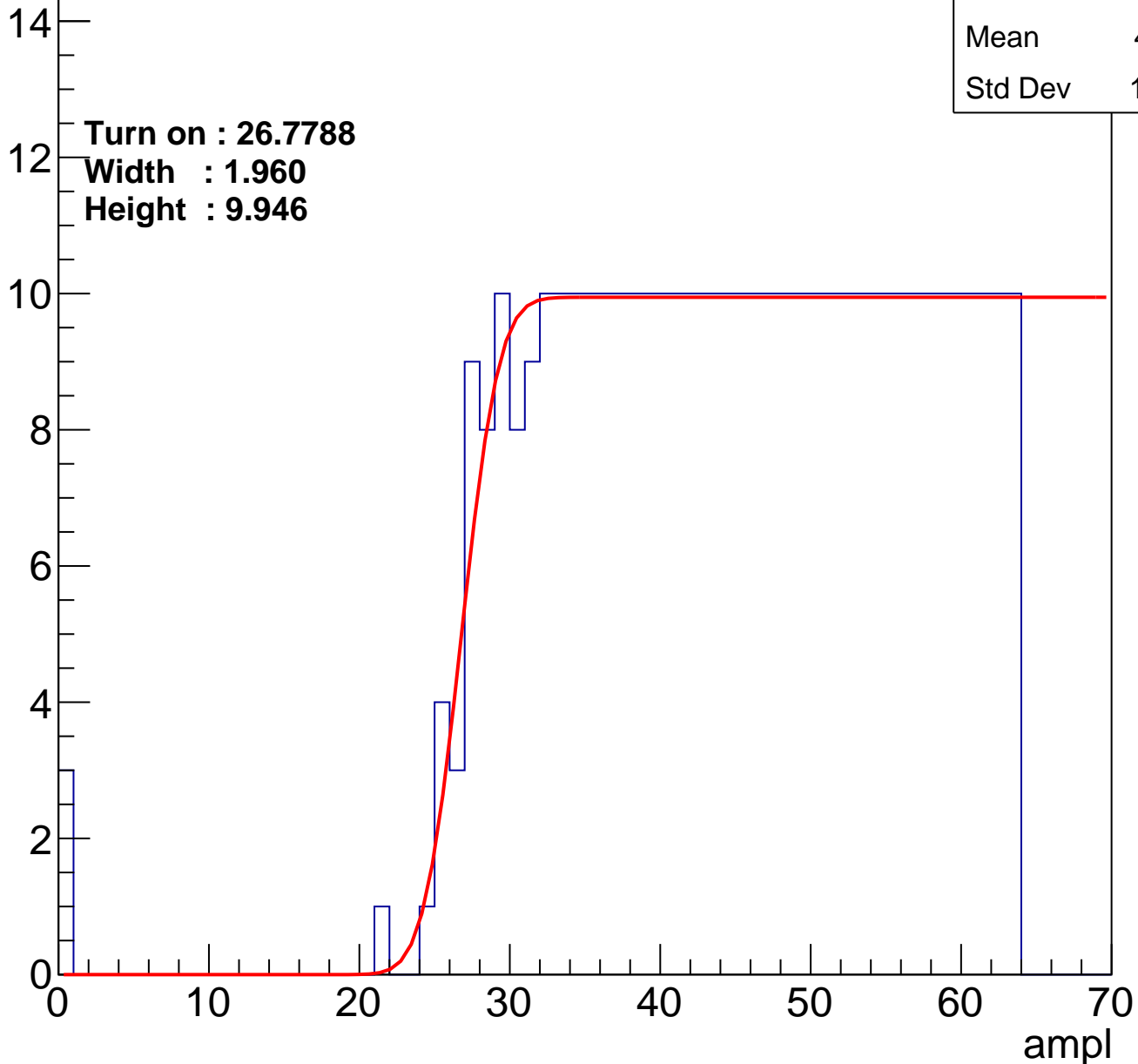
Entries	376
Mean	44.41
Std Dev	11.56

Turn on : 26.7788

Width : 1.960

Height : 9.946

Entry



B1L100S, U22-ch117

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.12
Std Dev	11.69

Turn on : 26.1971

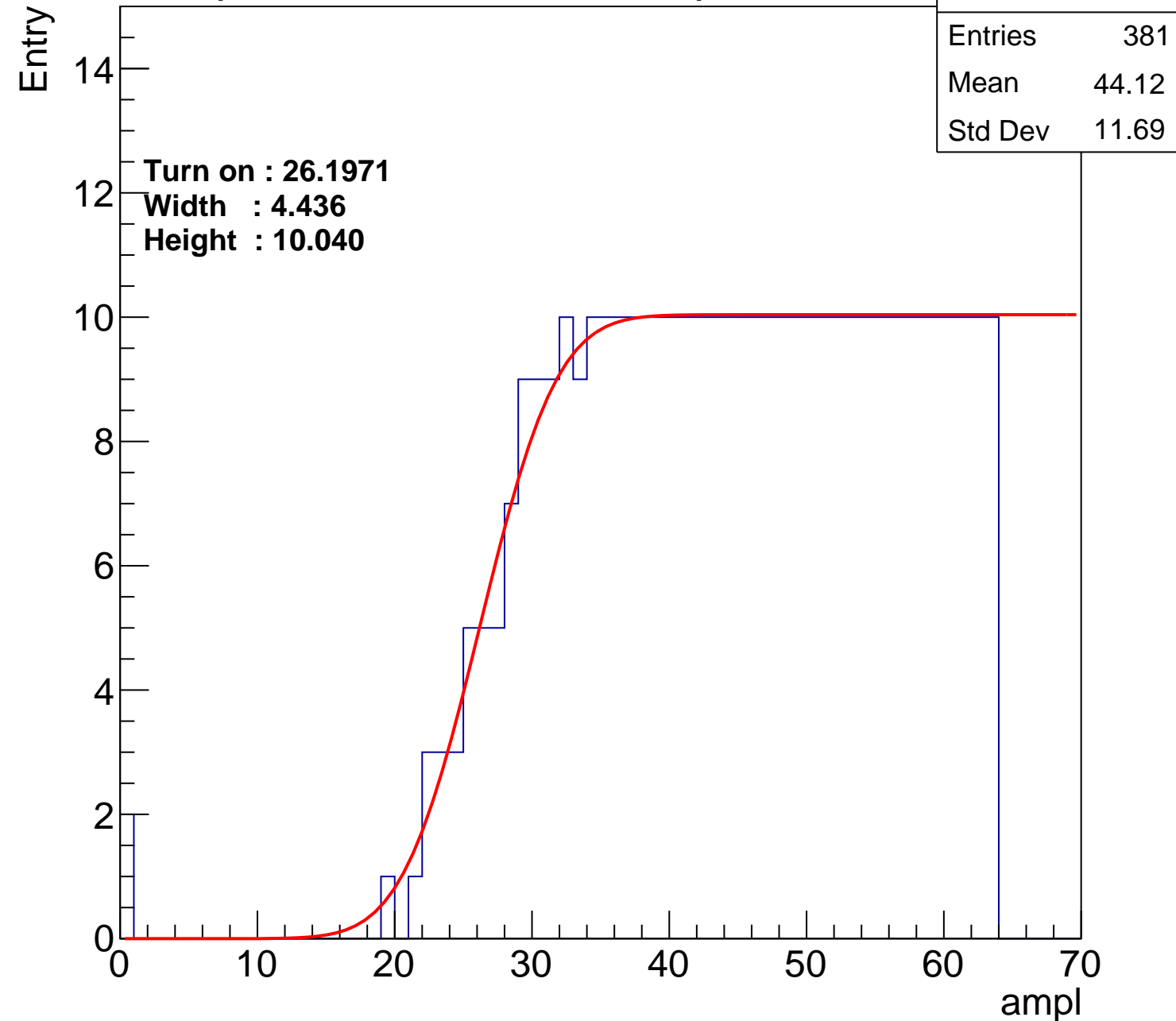
Width : 4.436

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch118

calib_packv5_042523_0143.root, FC#4, port A2

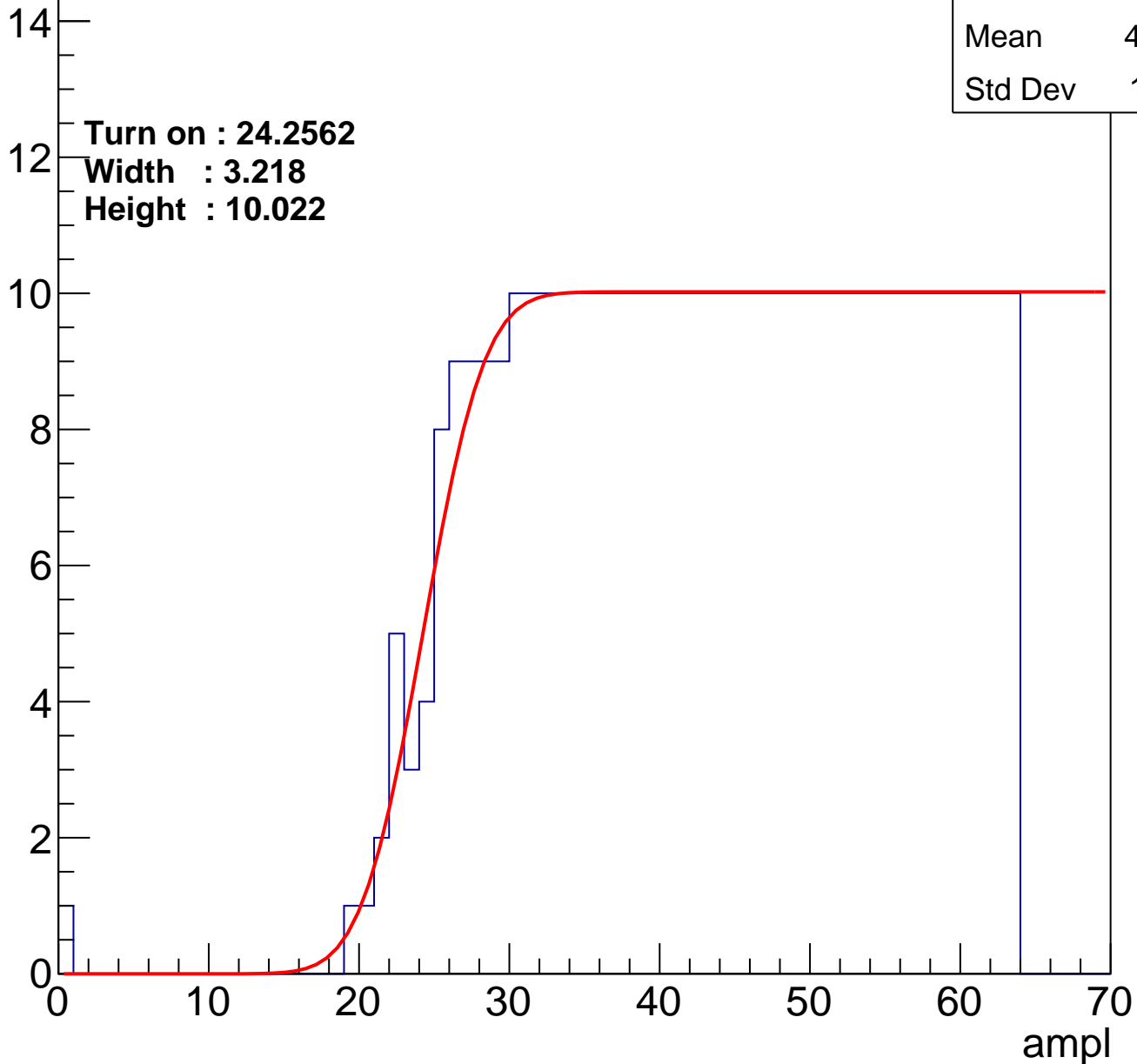
Entries	401
Mean	43.28
Std Dev	11.91

Turn on : 24.2562

Width : 3.218

Height : 10.022

Entry



B1L100S, U22-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.41
Std Dev	11.58

Turn on : 26.7916

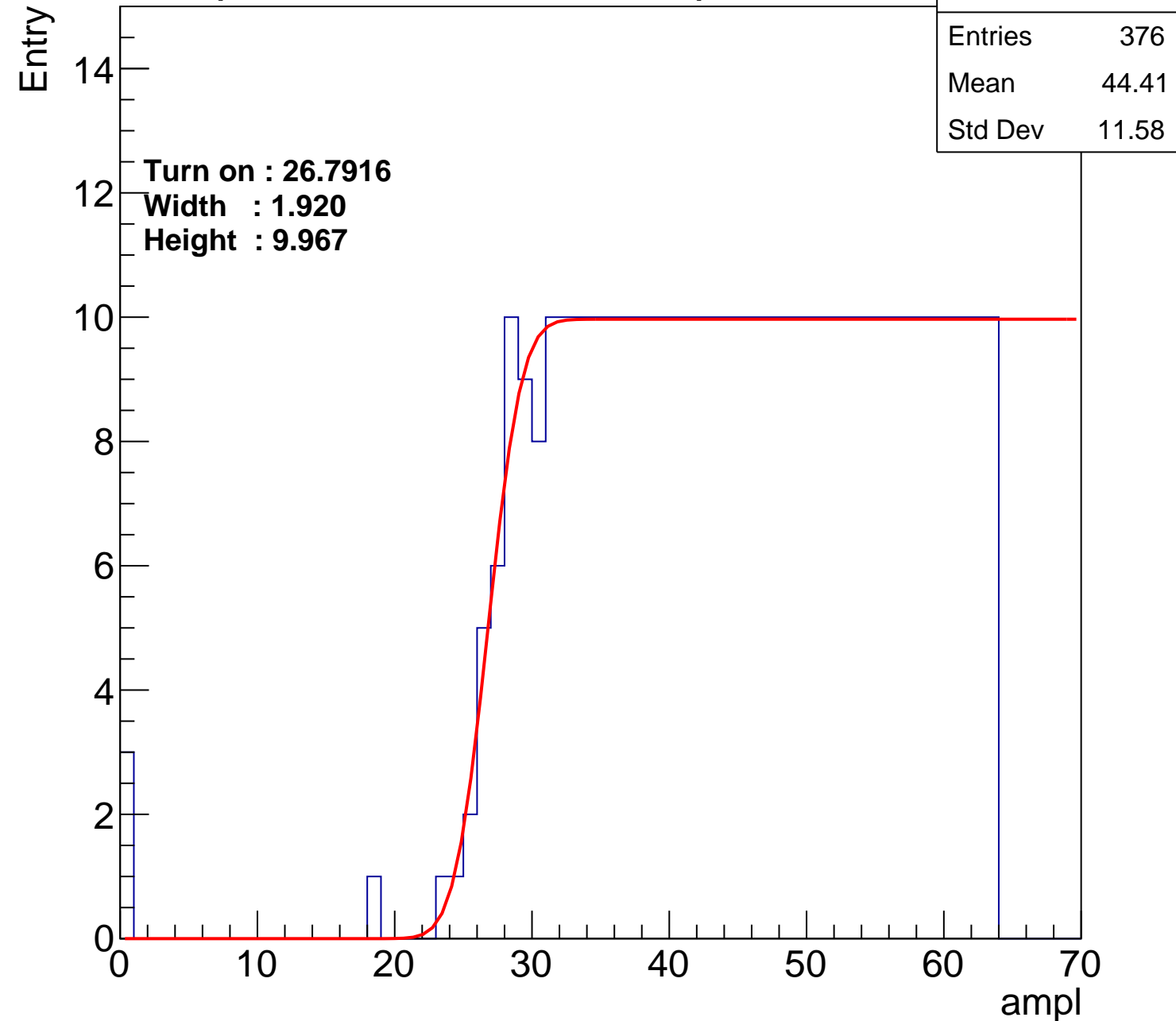
Width : 1.920

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch120

calib_packv5_042523_0143.root, FC#4, port A2

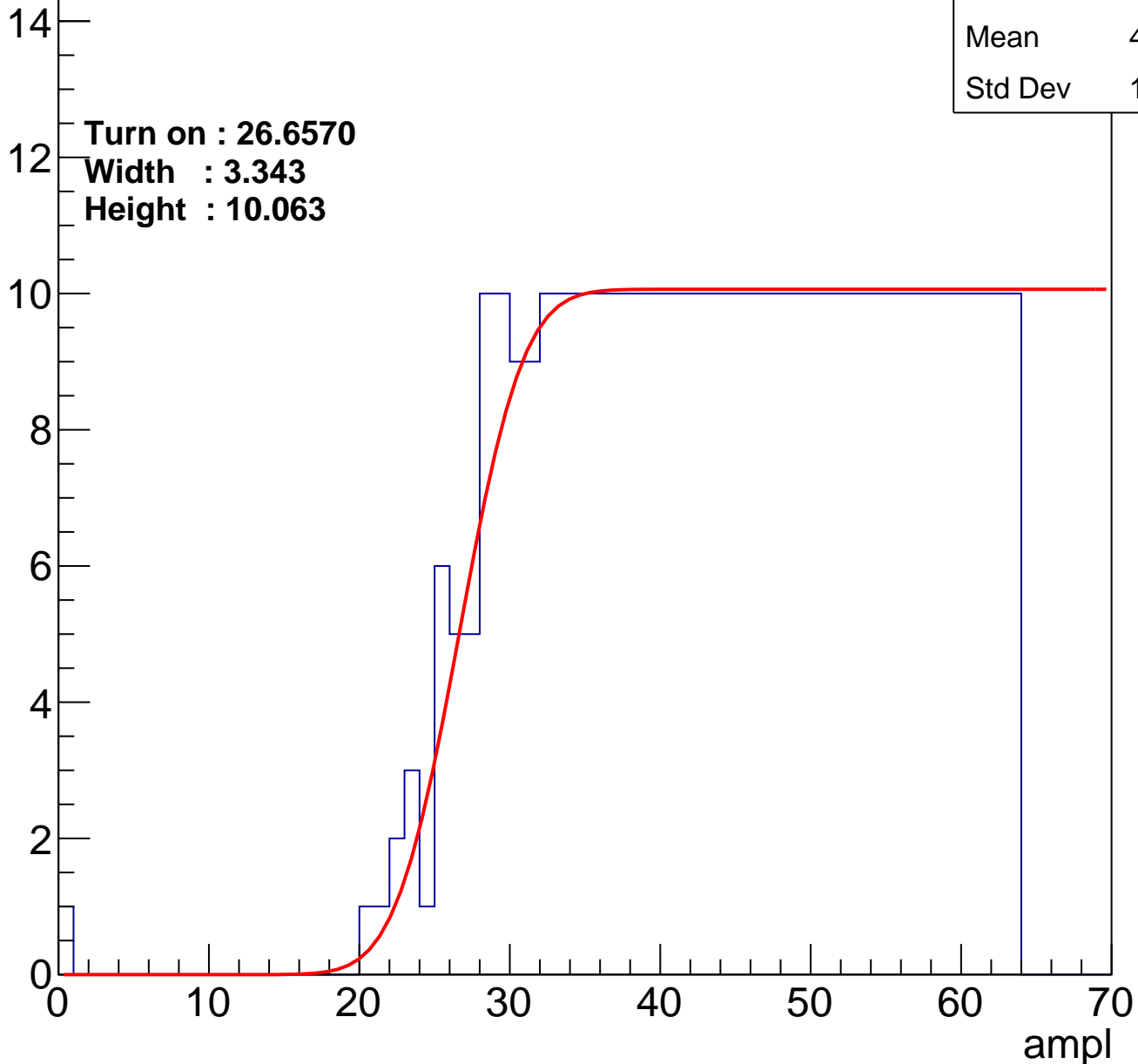
Entries	383
Mean	44.16
Std Dev	11.45

Turn on : 26.6570

Width : 3.343

Height : 10.063

Entry



B1L100S, U22-ch121

calib_packv5_042523_0143.root, FC#4, port A2

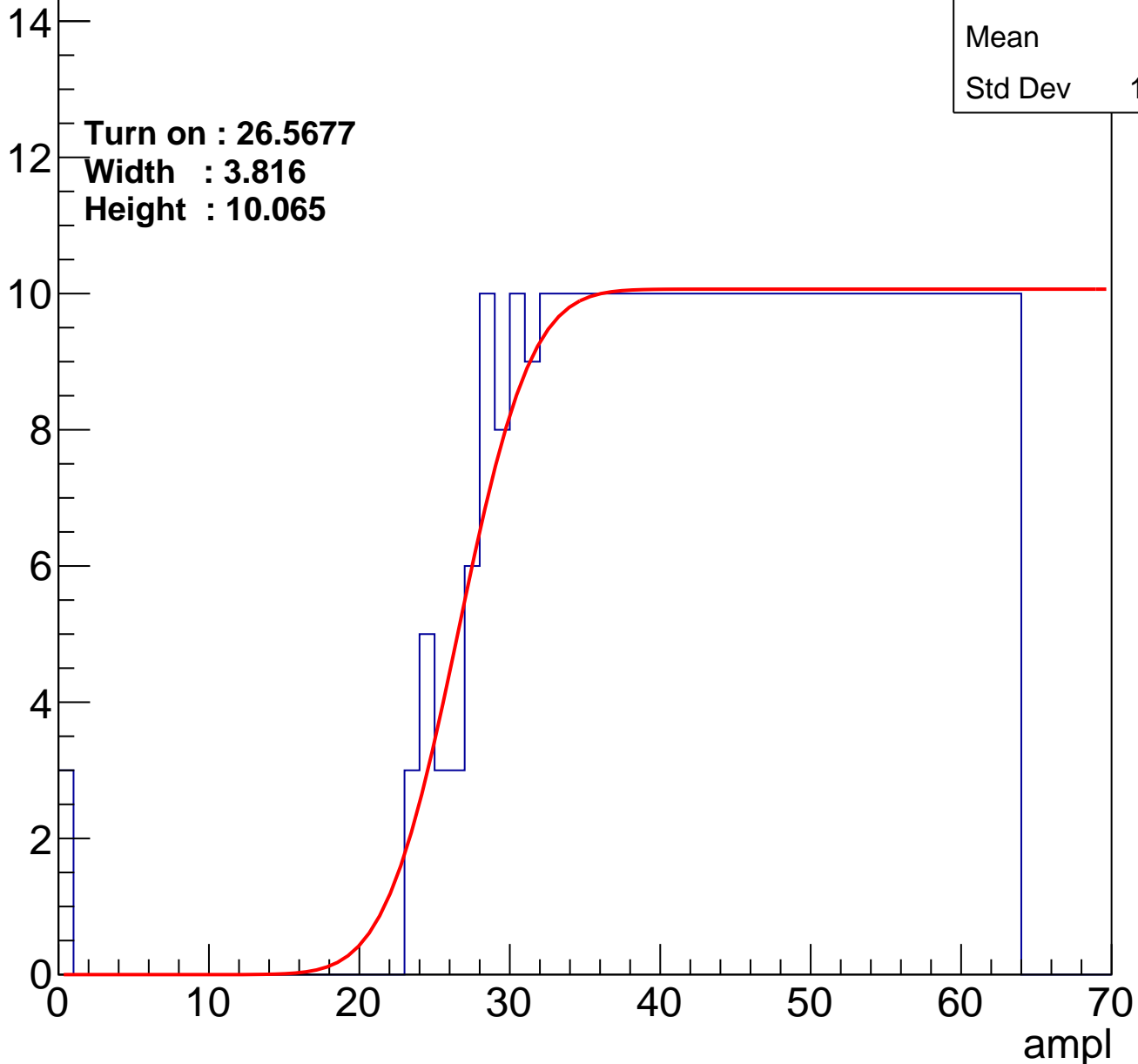
Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.5677

Width : 3.816

Height : 10.065

Entry



B1L100S, U22-ch122

calib_packv5_042523_0143.root, FC#4, port A2

Entries	400
Mean	43.04
Std Dev	12.59

Turn on : 24.7579

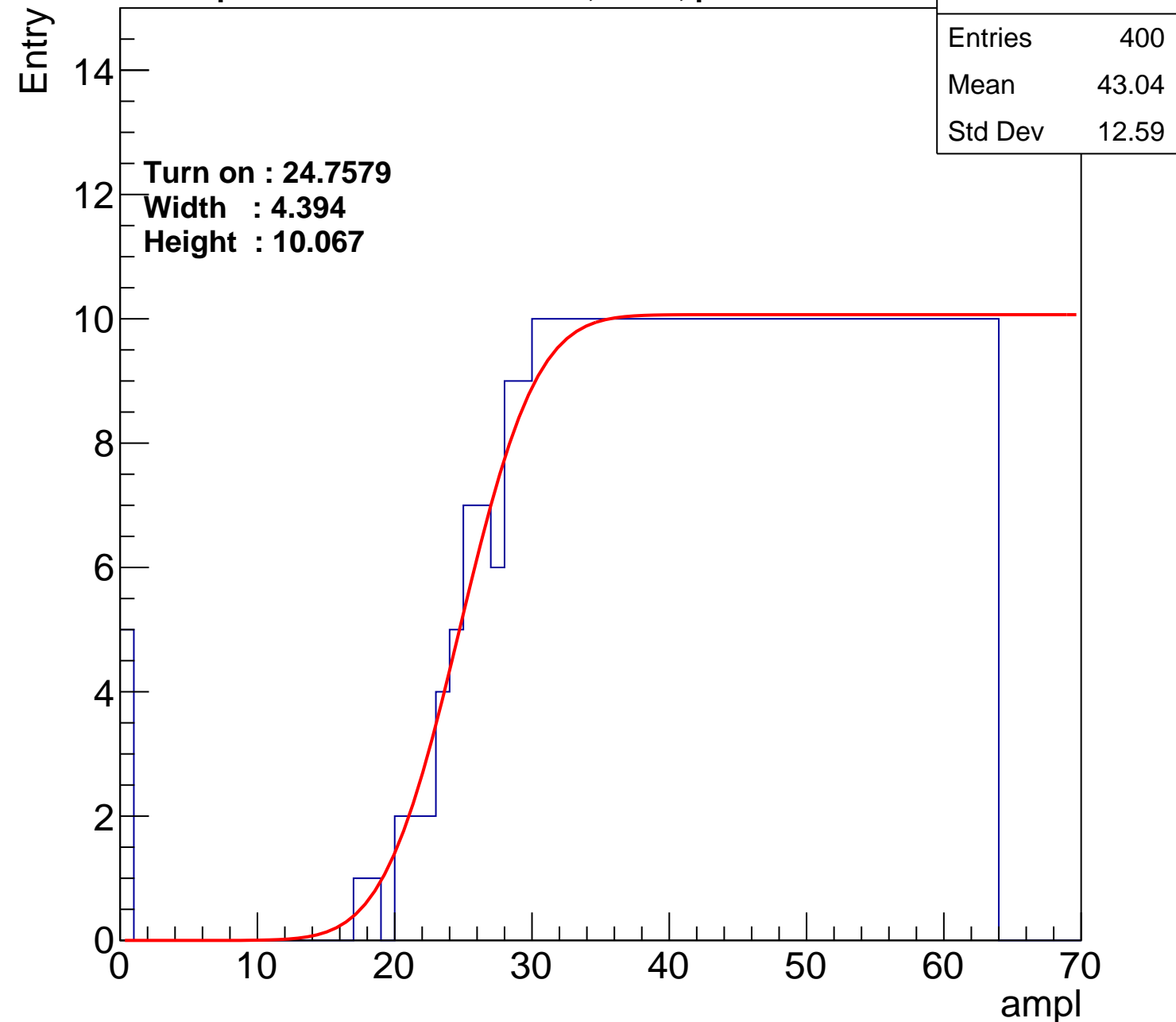
Width : 4.394

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch123

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.59
Std Dev	11.31

Turn on : 27.0966

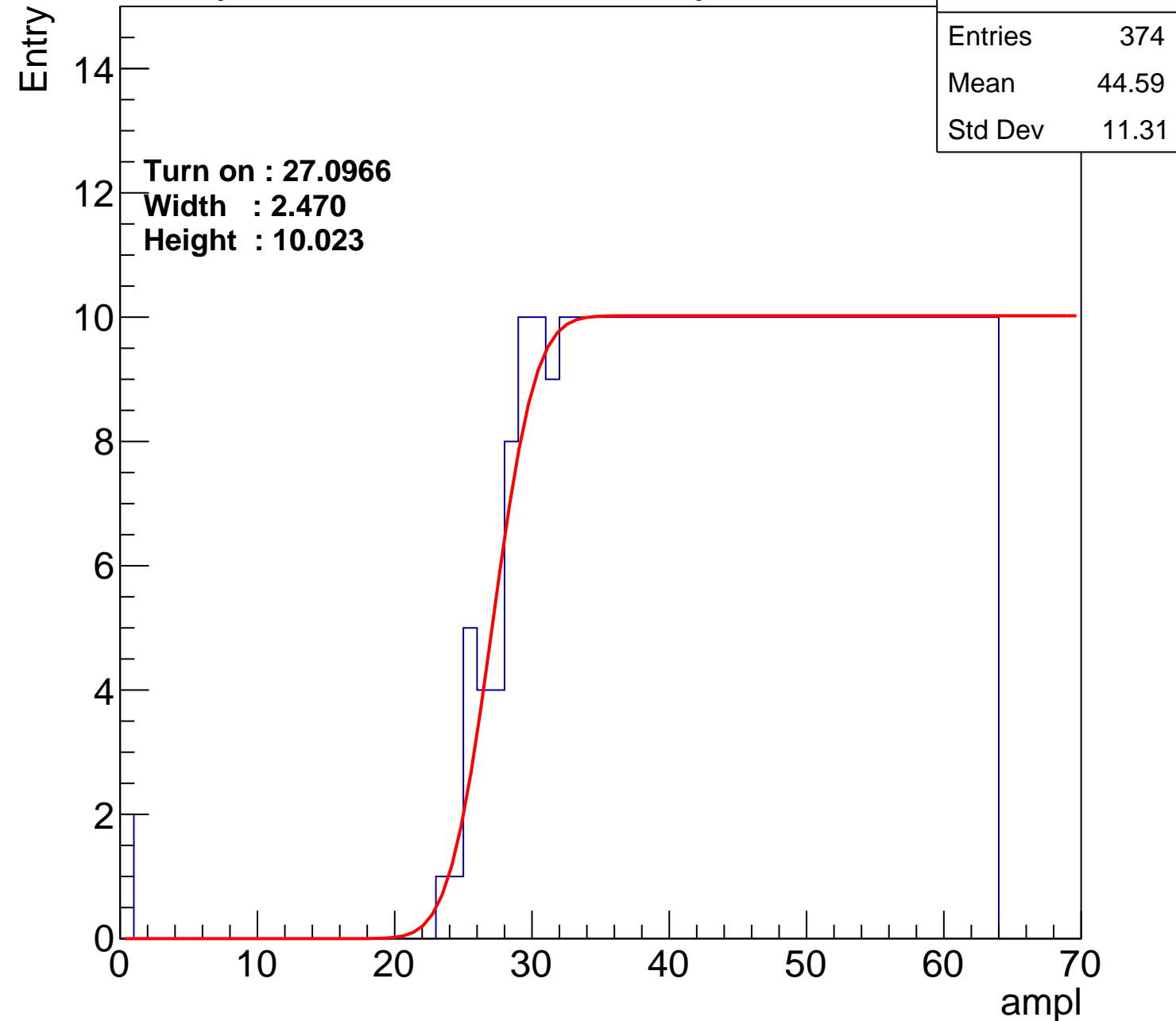
Width : 2.470

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch124

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
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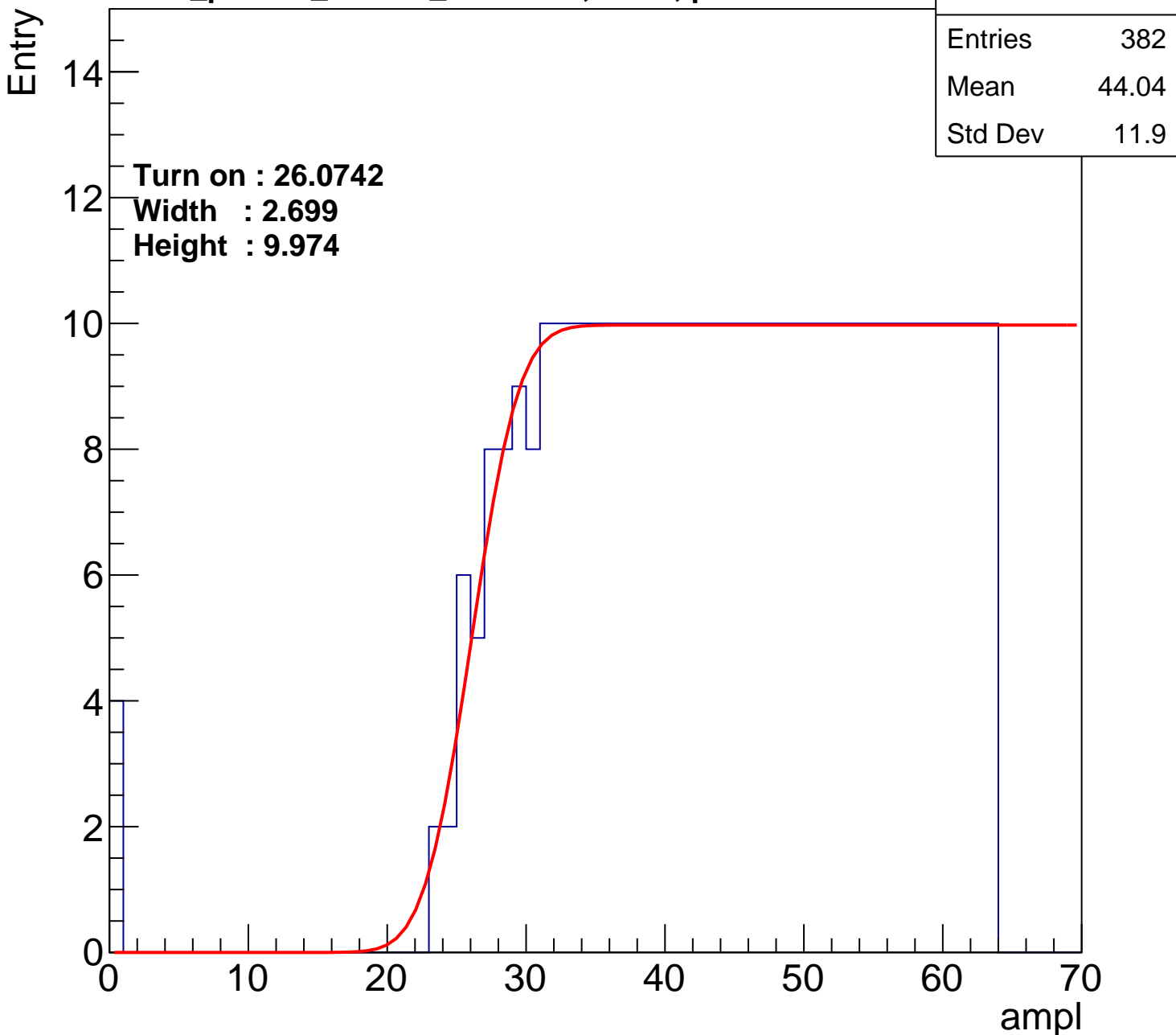
Mean	44.04
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Std Dev	11.9
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Turn on : 26.0742

Width : 2.699

Height : 9.974



B1L100S, U22-ch125

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.73
Std Dev	11.76

Turn on : 28.4034

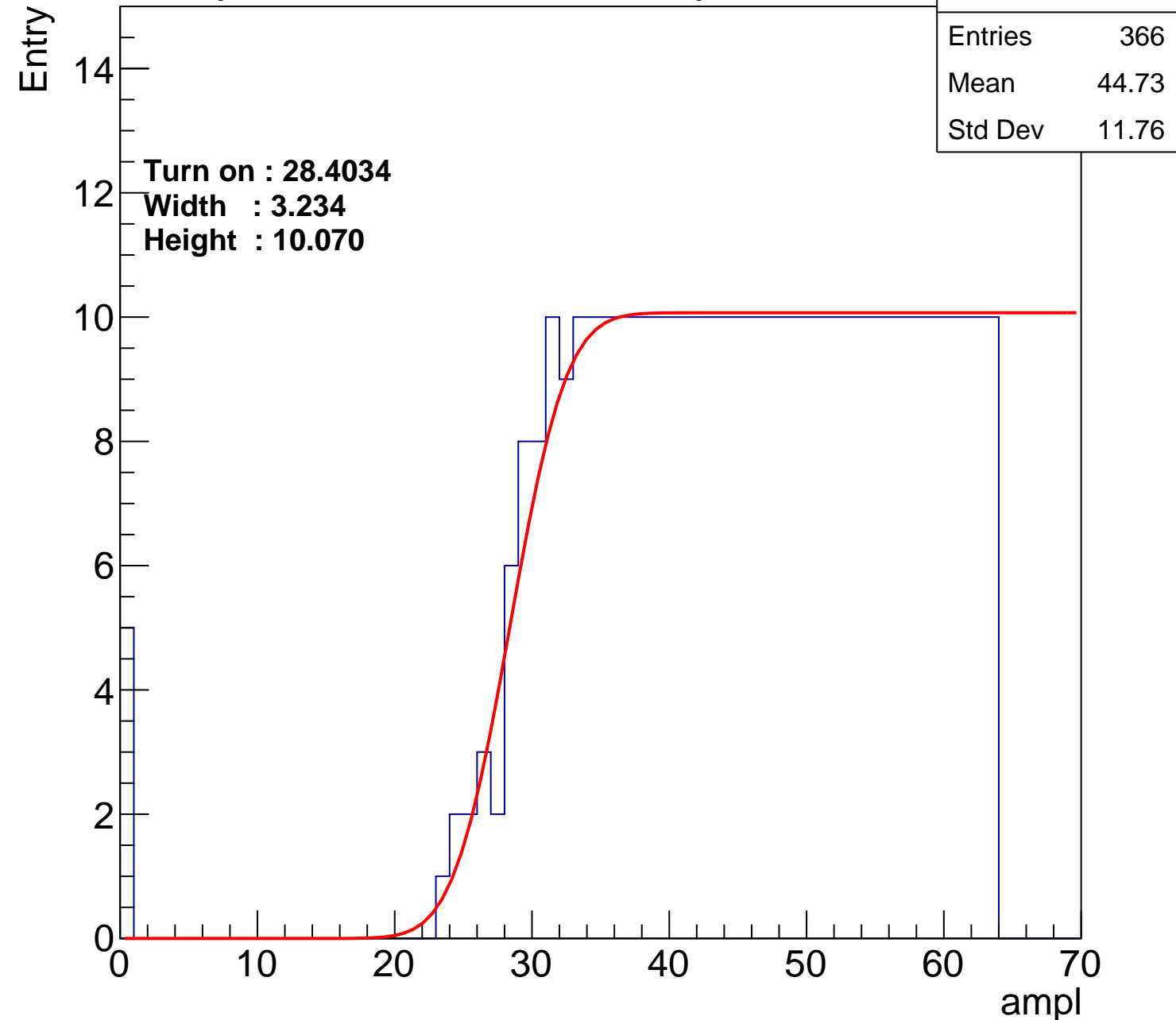
Width : 3.234

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch126

calib_packv5_042523_0143.root, FC#4, port A2

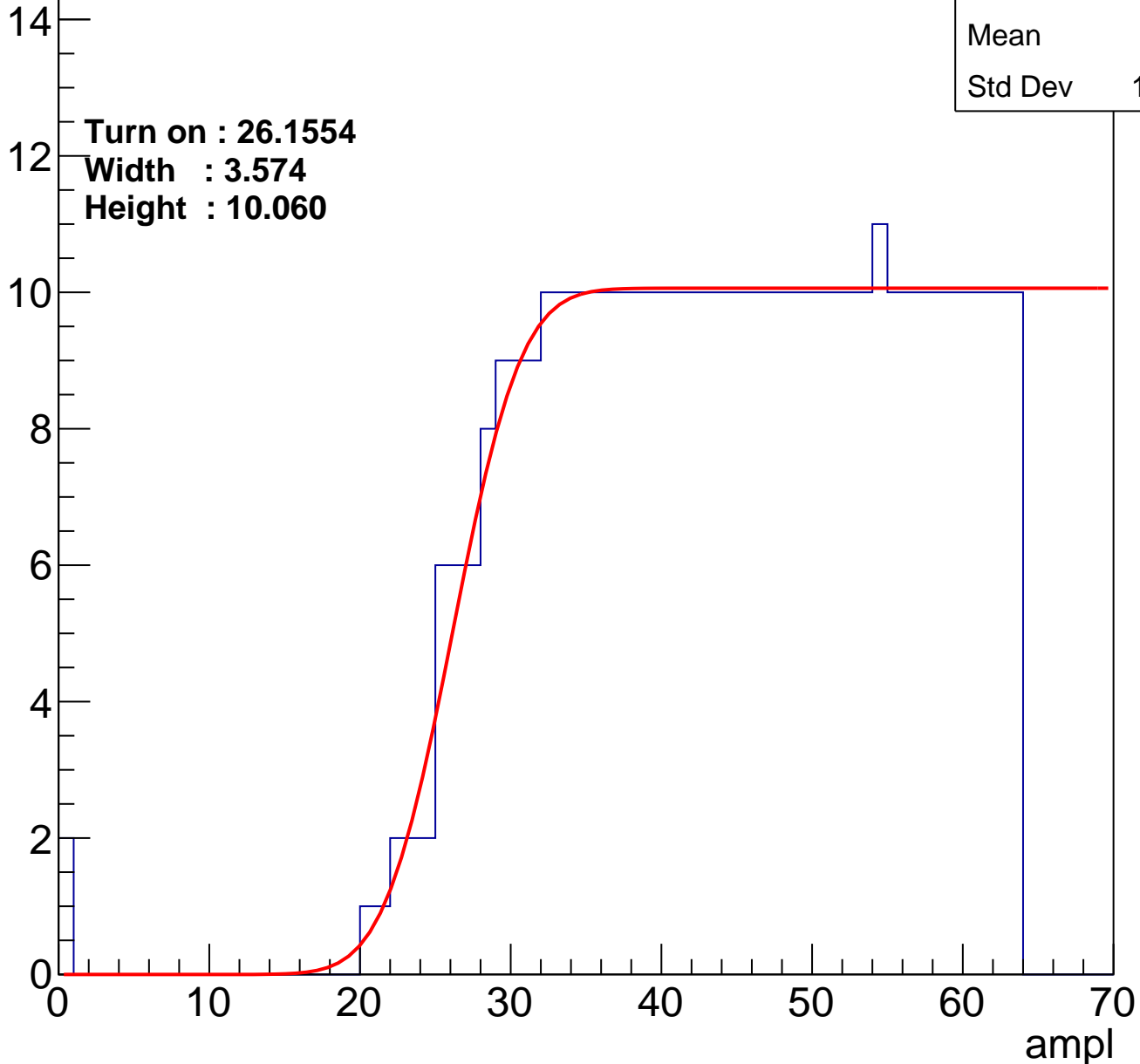
Entries	384
Mean	44.1
Std Dev	11.65

Turn on : 26.1554

Width : 3.574

Height : 10.060

Entry



B1L100S, U22-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 27.2664

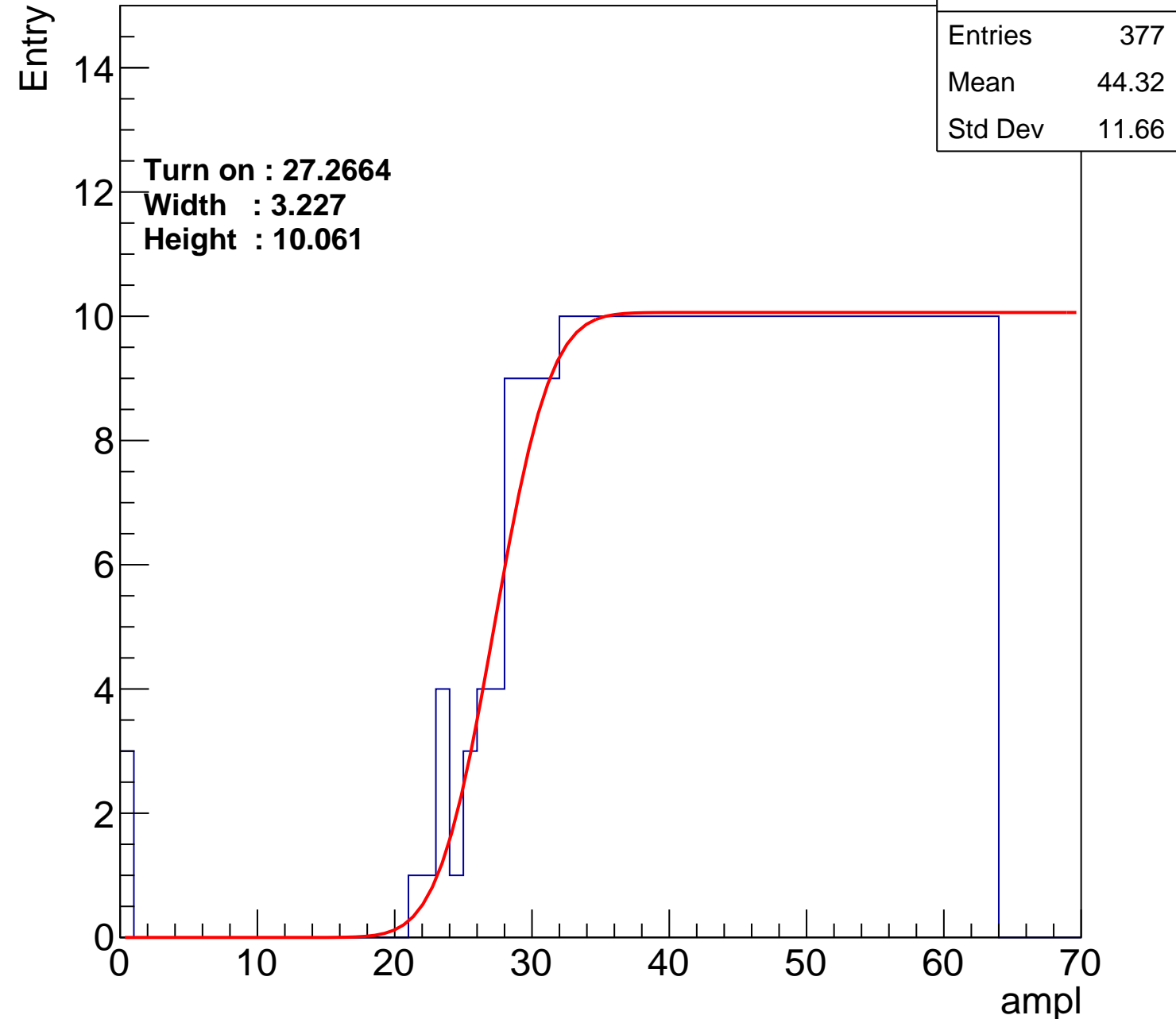
Width : 3.227

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U22-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 27.2664

Width : 3.227

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl

