

B1L100S, U1-ch0

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.09 |
| Std Dev | 12.45 |

Turn on : 24.1705

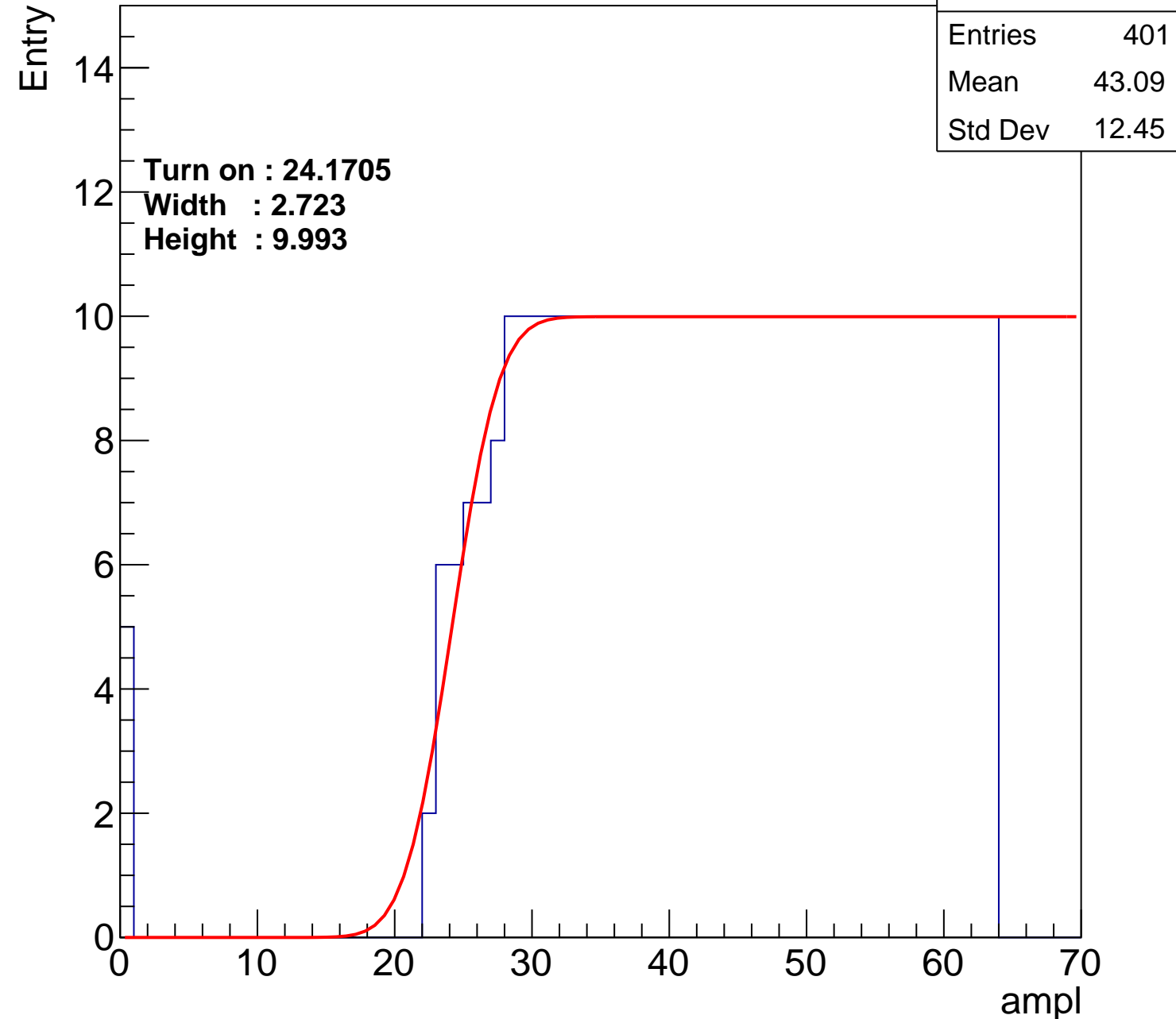
Width : 2.723

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch1

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.95 |
| Std Dev | 11.69 |

Turn on : 25.7997

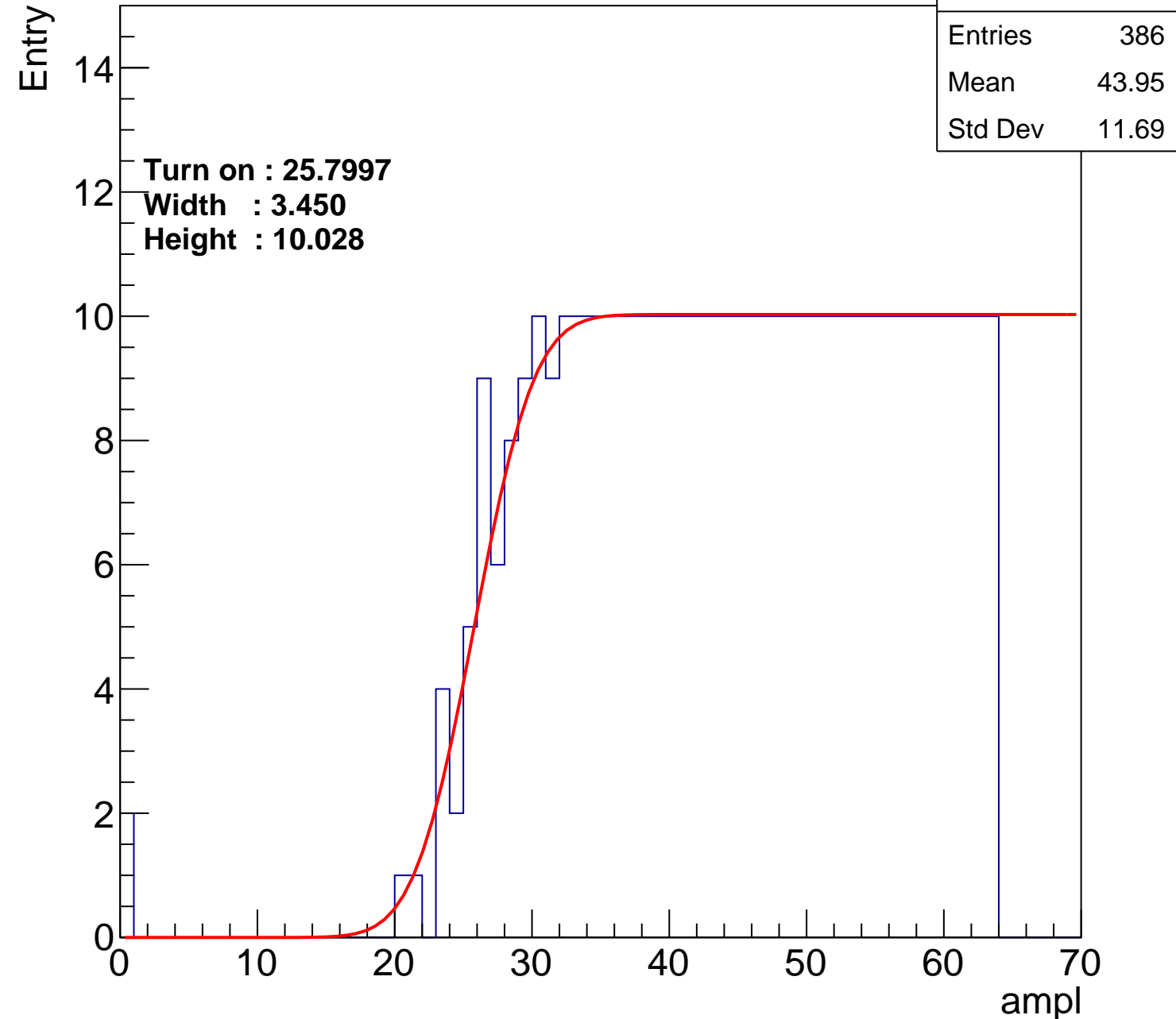
Width : 3.450

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch2

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.92 |
| Std Dev | 11.99 |

Turn on : 26.3741

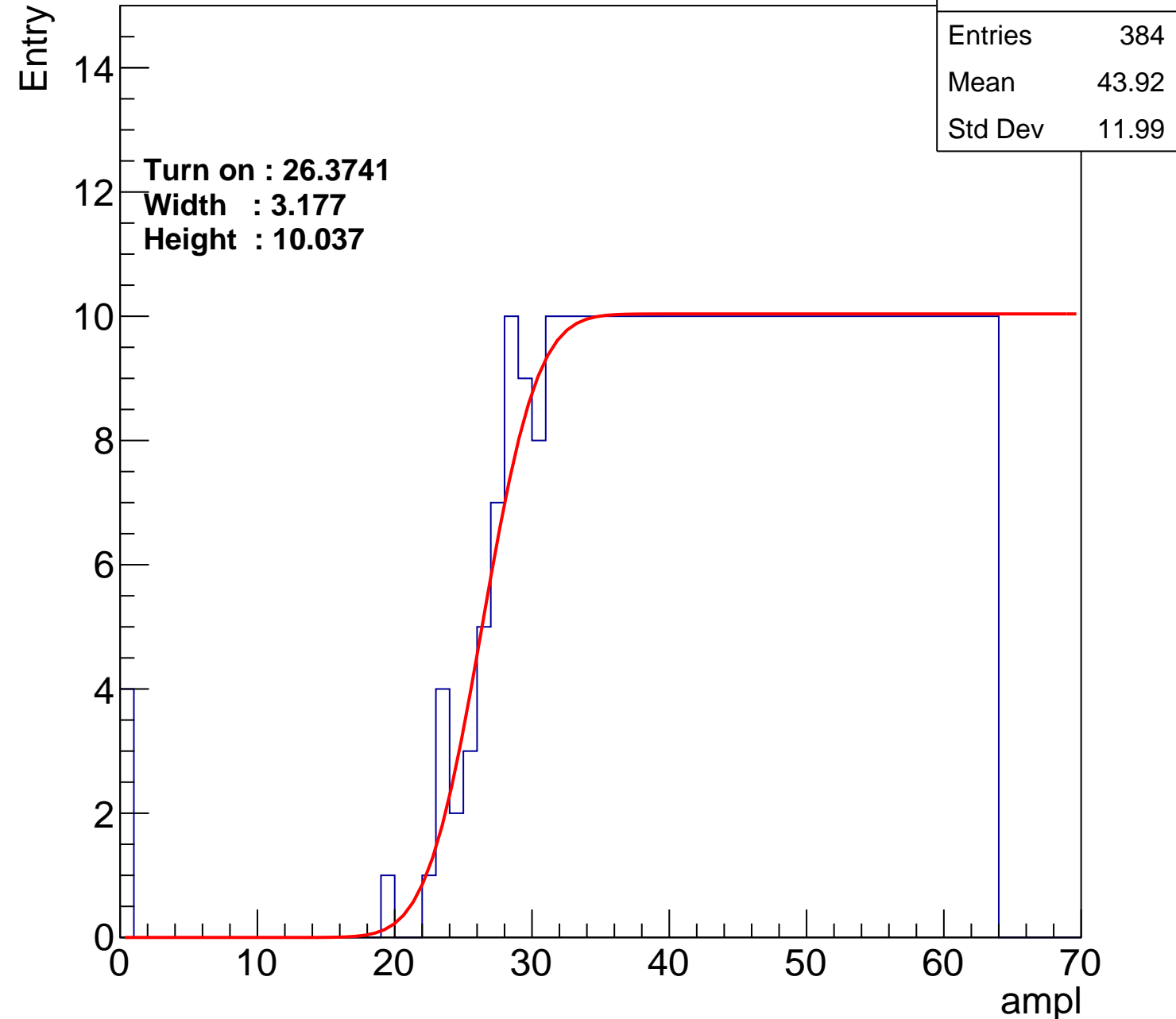
Width : 3.177

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch3

calib_packv5_042523_0143.root, FC#4, port A2

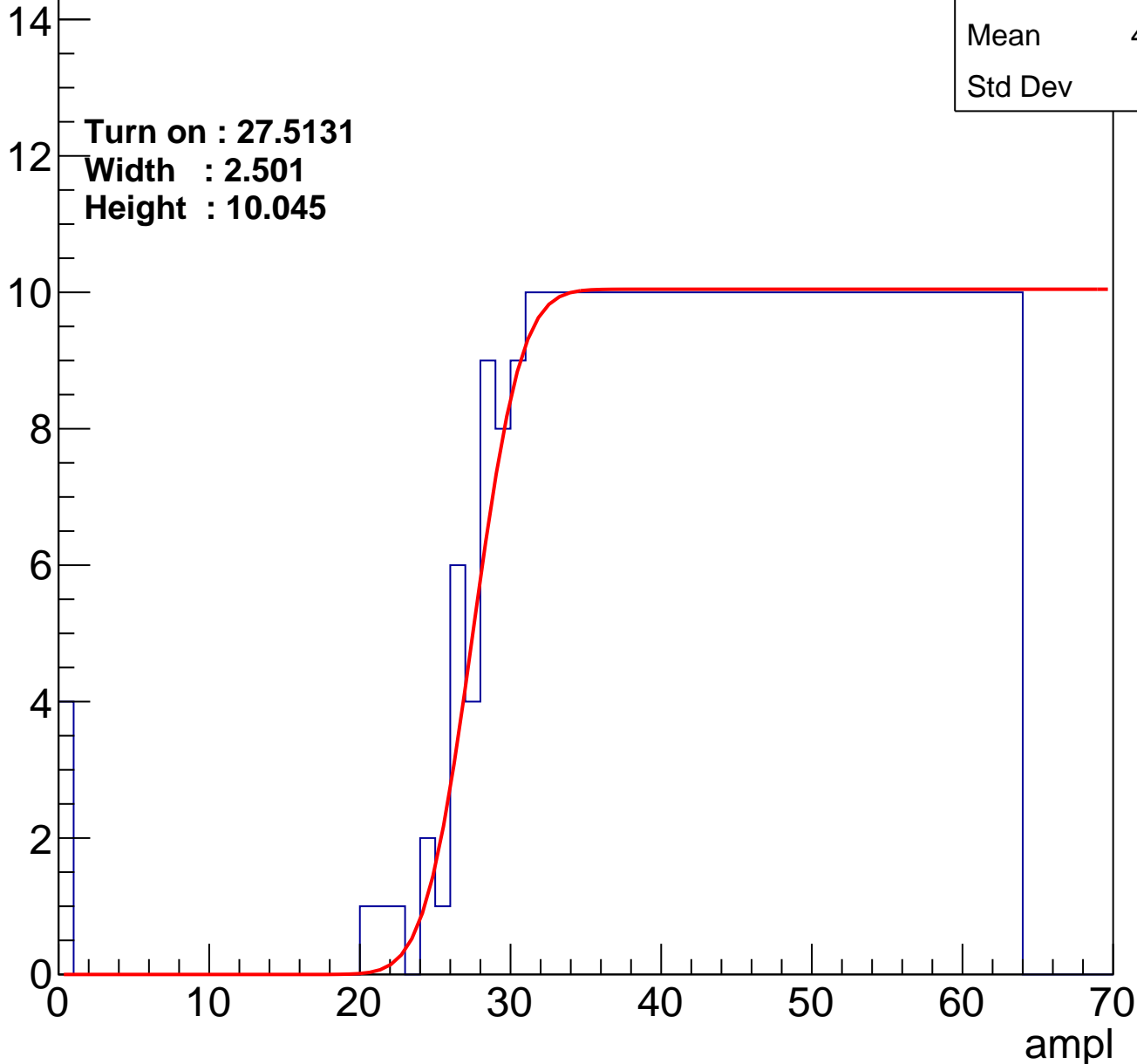
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.32 |
| Std Dev | 11.8 |

Turn on : 27.5131

Width : 2.501

Height : 10.045

Entry



B1L100S, U1-ch4

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.1 |
| Std Dev | 12.16 |

Turn on : 26.9094

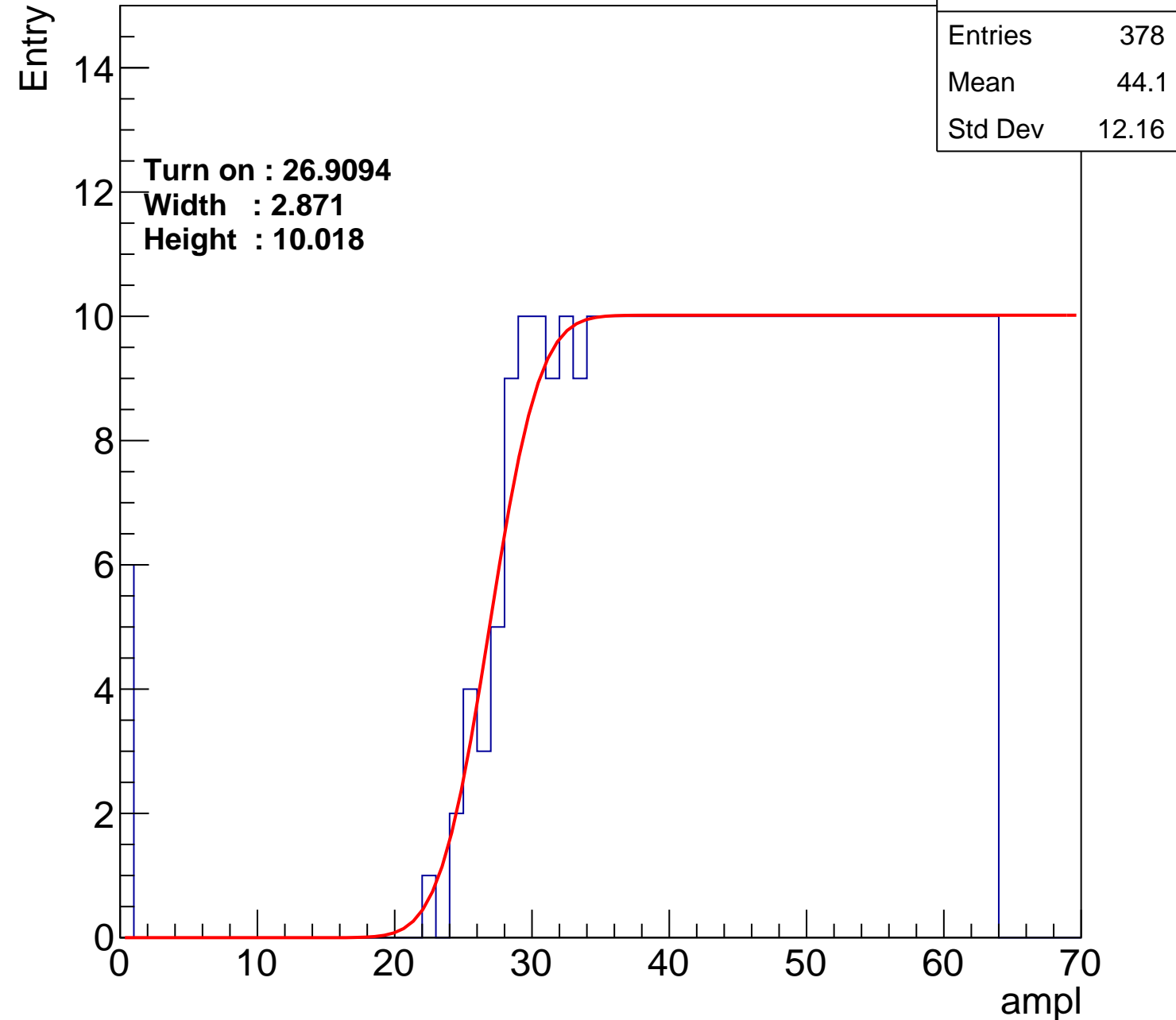
Width : 2.871

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch5

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.74 |
| Std Dev | 11.09 |

Turn on : 27.4608

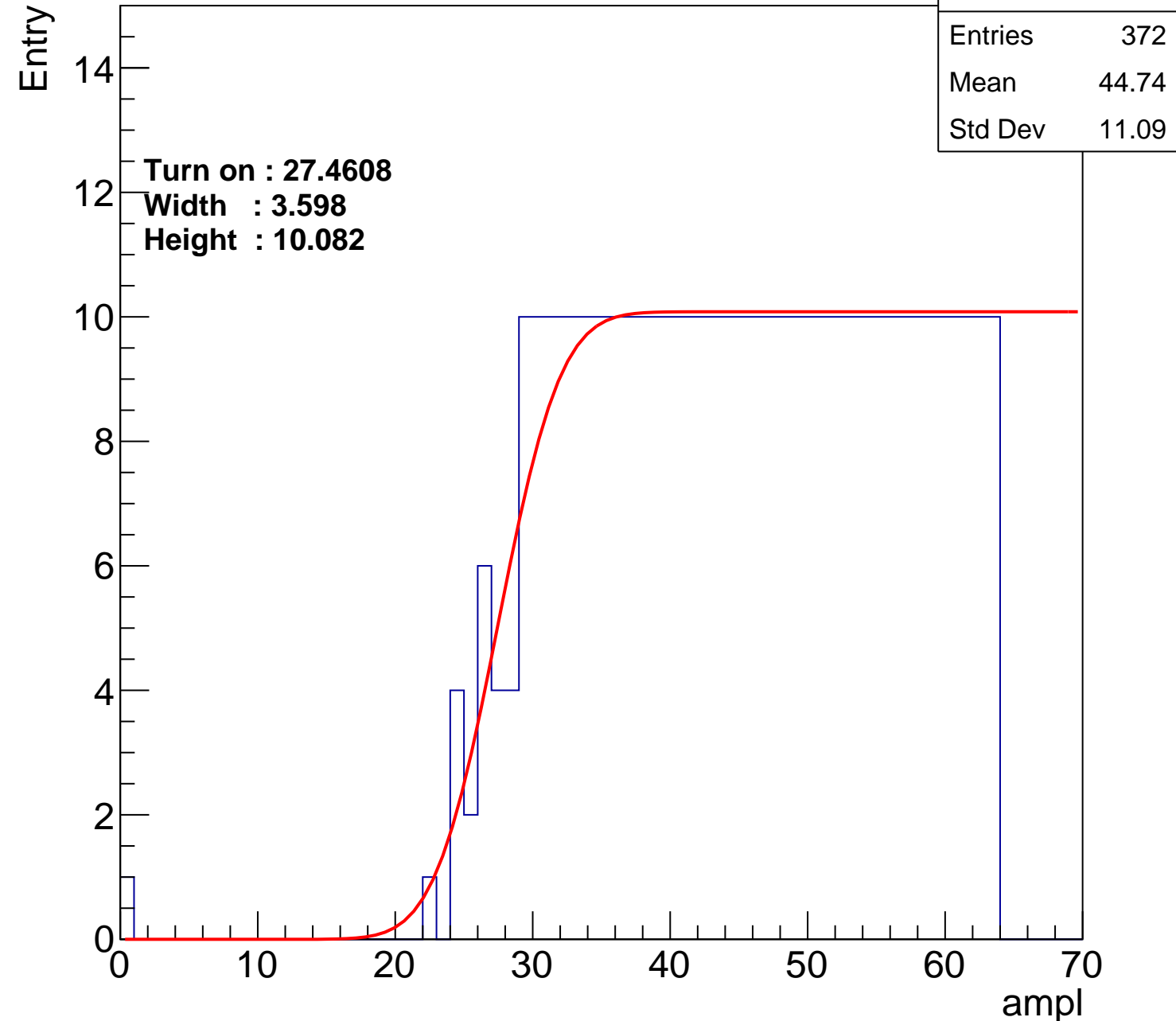
Width : 3.598

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch6

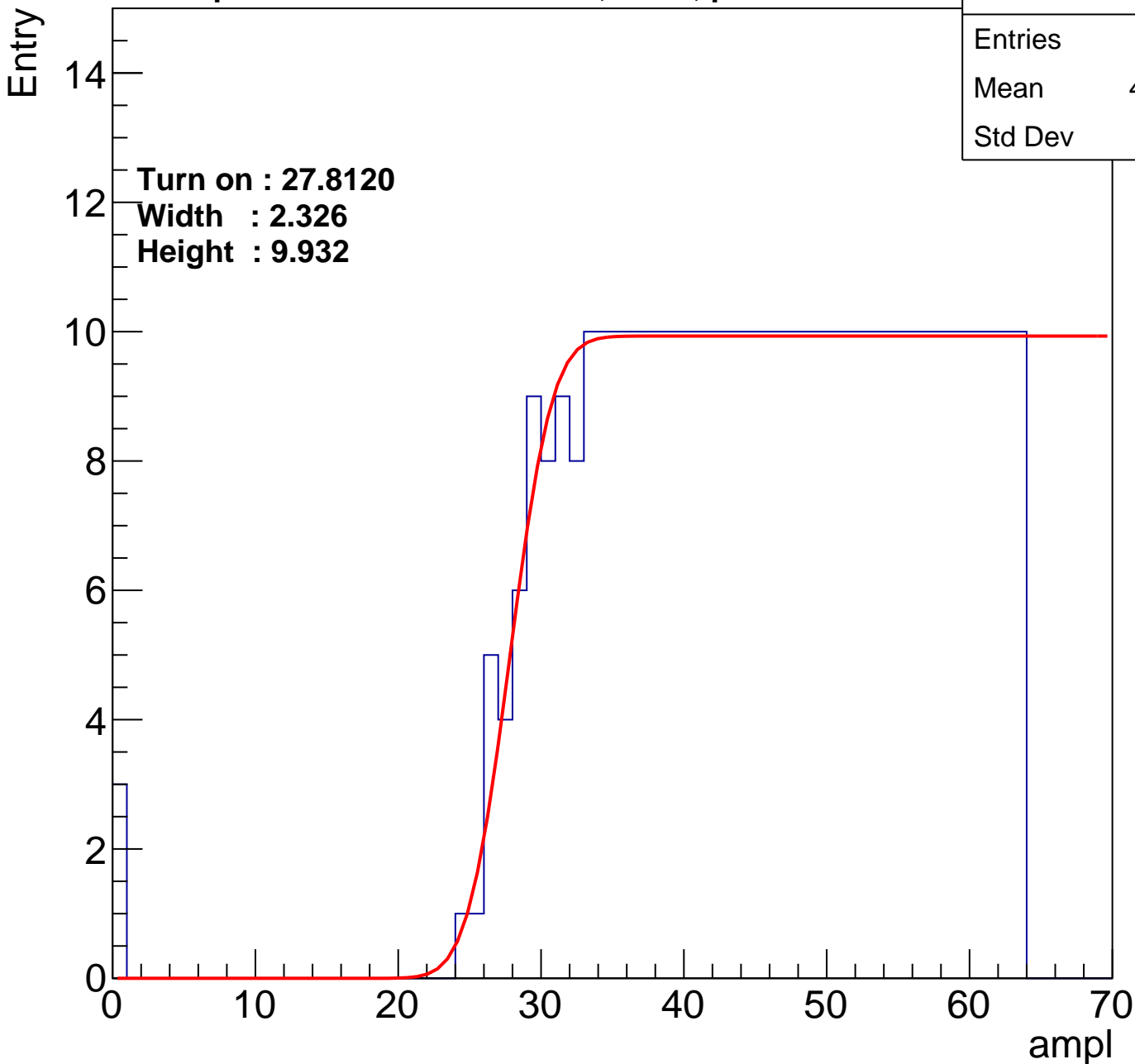
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.98 |
| Std Dev | 11.31 |

Turn on : 27.8120

Width : 2.326

Height : 9.932



B1L100S, U1-ch7

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.61 |
| Std Dev | 11.52 |

Turn on : 27.6060

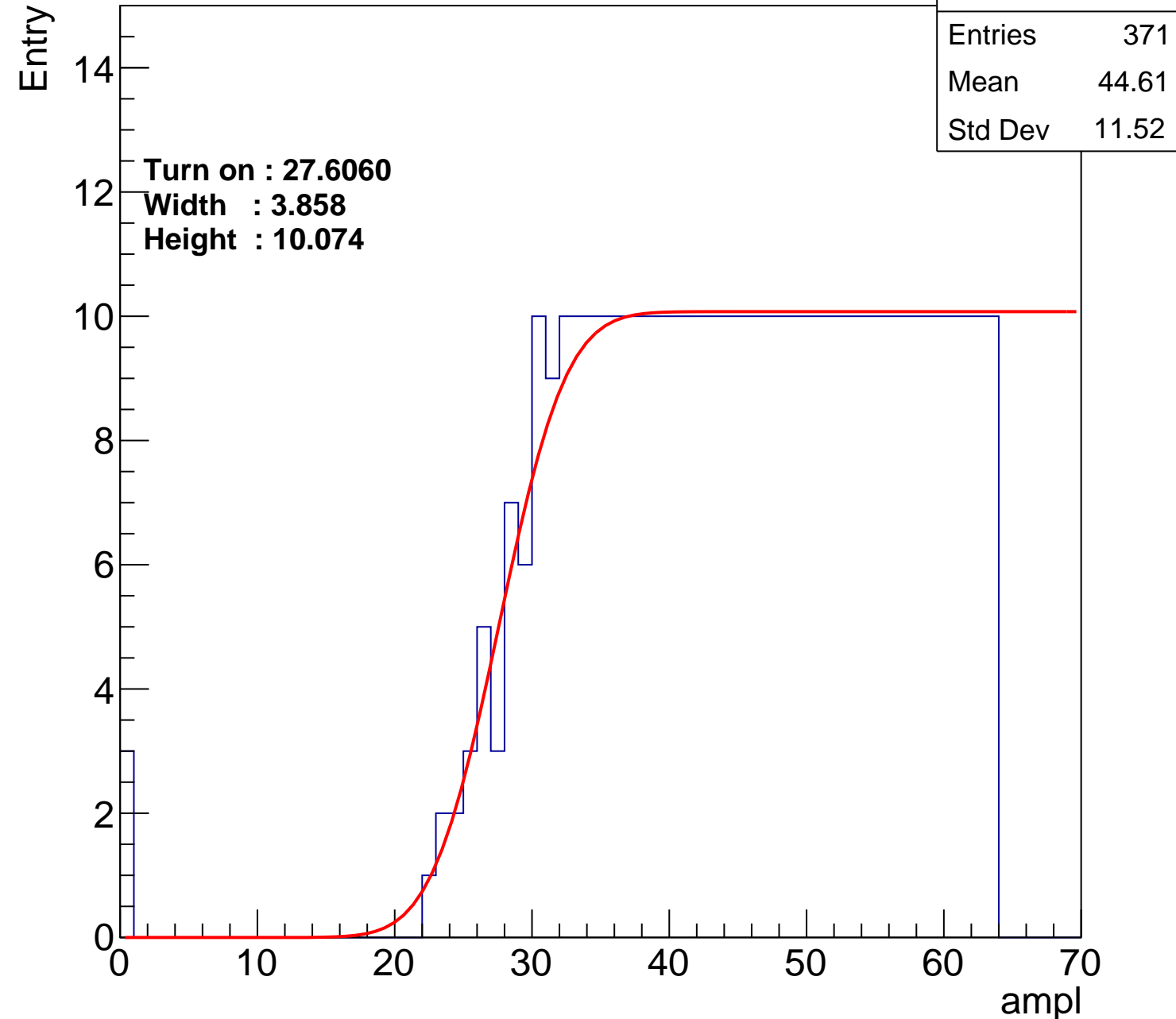
Width : 3.858

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch8

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.47 |
| Std Dev | 11.77 |

Turn on : 24.5999

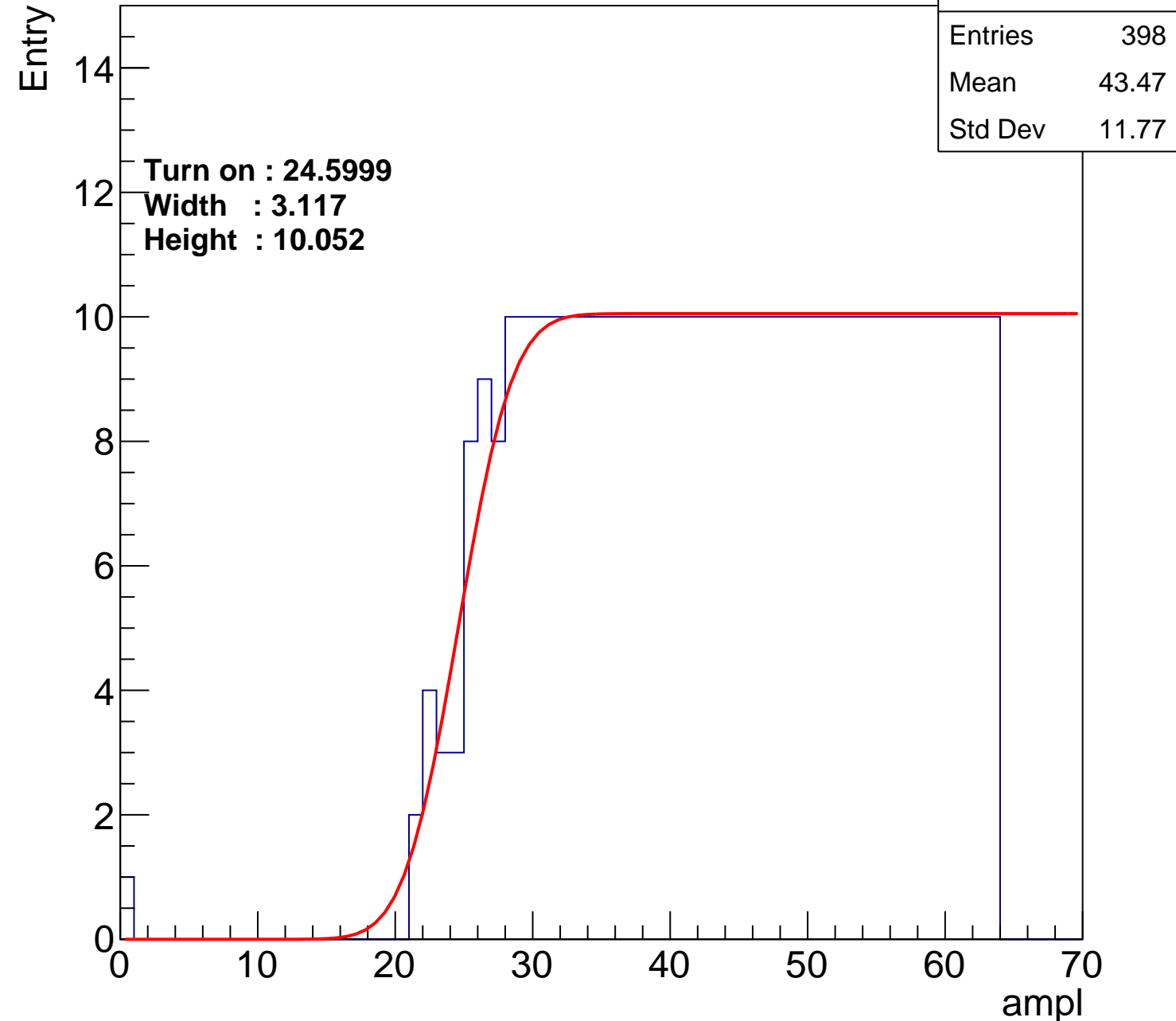
Width : 3.117

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch9

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.71 |
| Std Dev | 11.81 |

Turn on : 25.5458

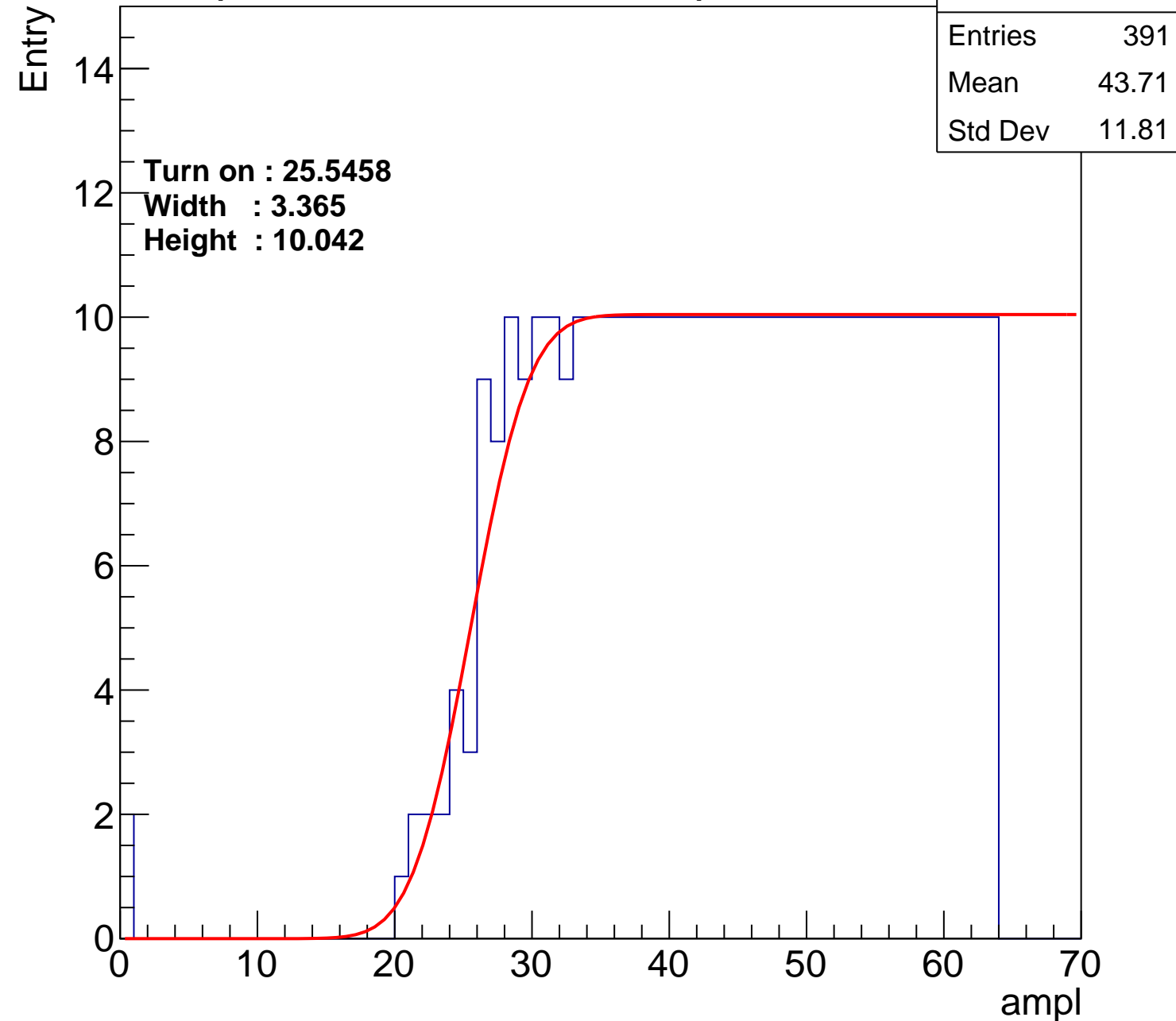
Width : 3.365

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch10

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.67 |
| Std Dev | 11.53 |

Turn on : 27.8169

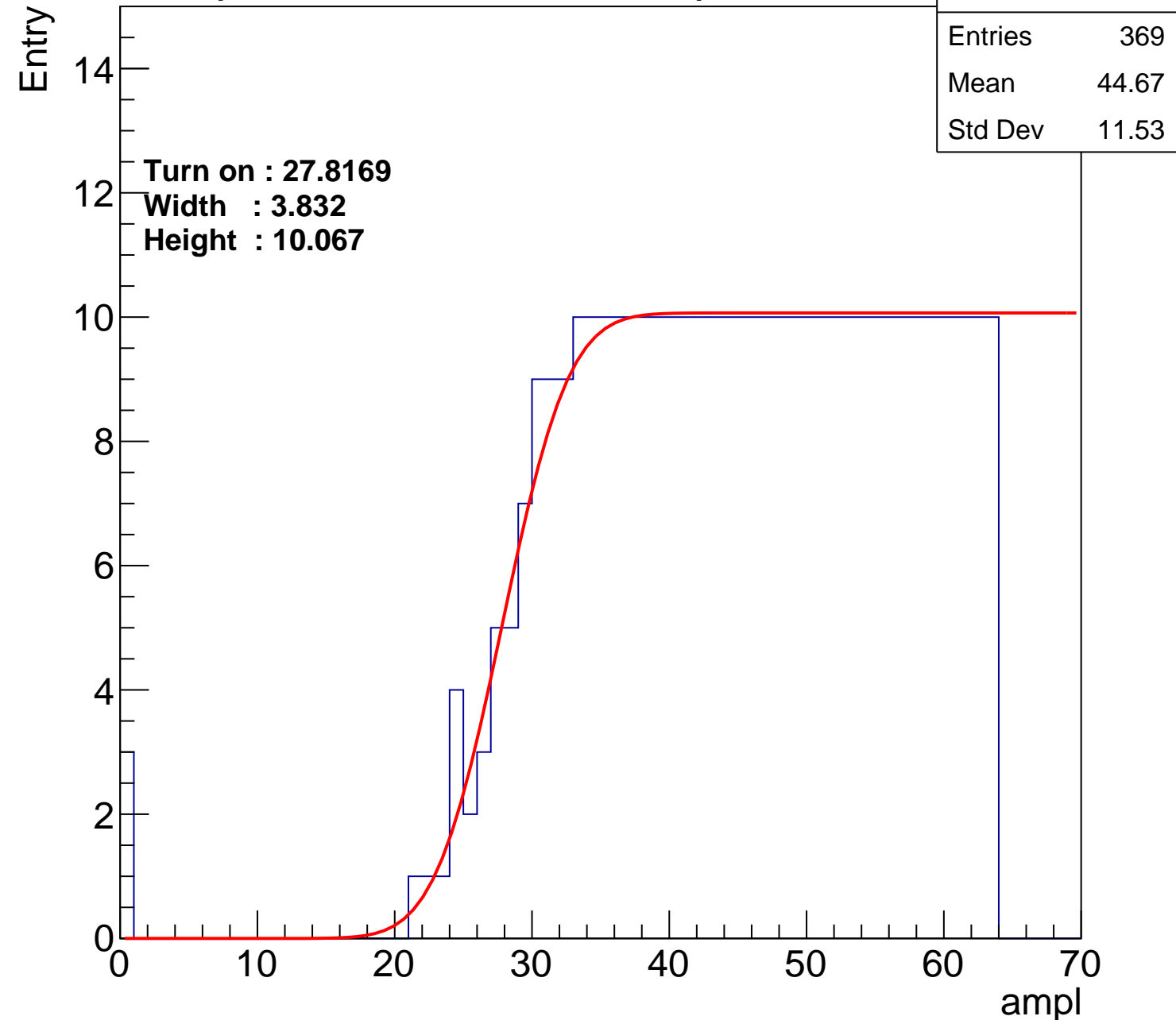
Width : 3.832

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch11

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.86 |
| Std Dev | 11.85 |

Turn on : 25.4405

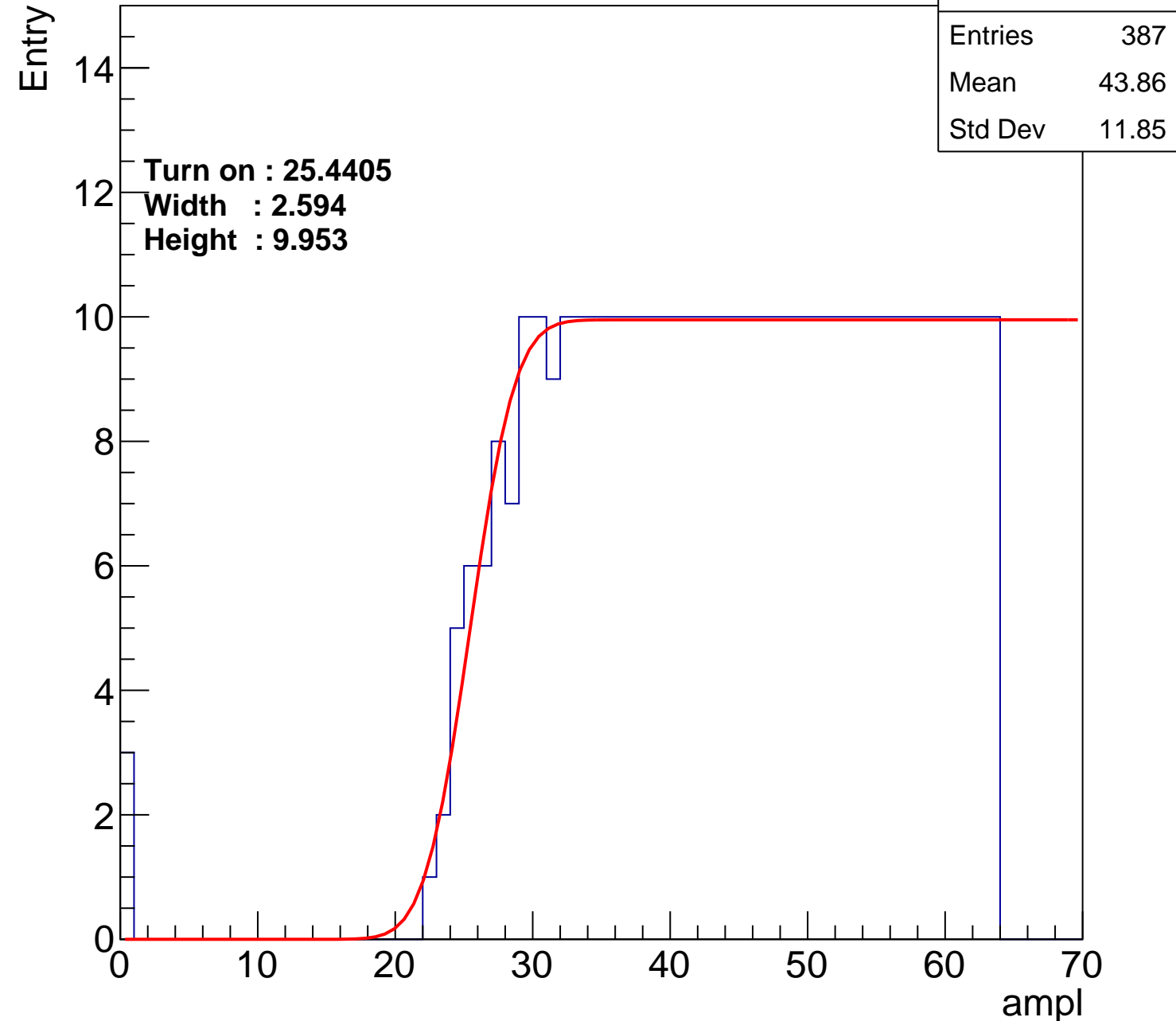
Width : 2.594

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch12

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.94 |
| Std Dev | 11.36 |

Turn on : 27.7902

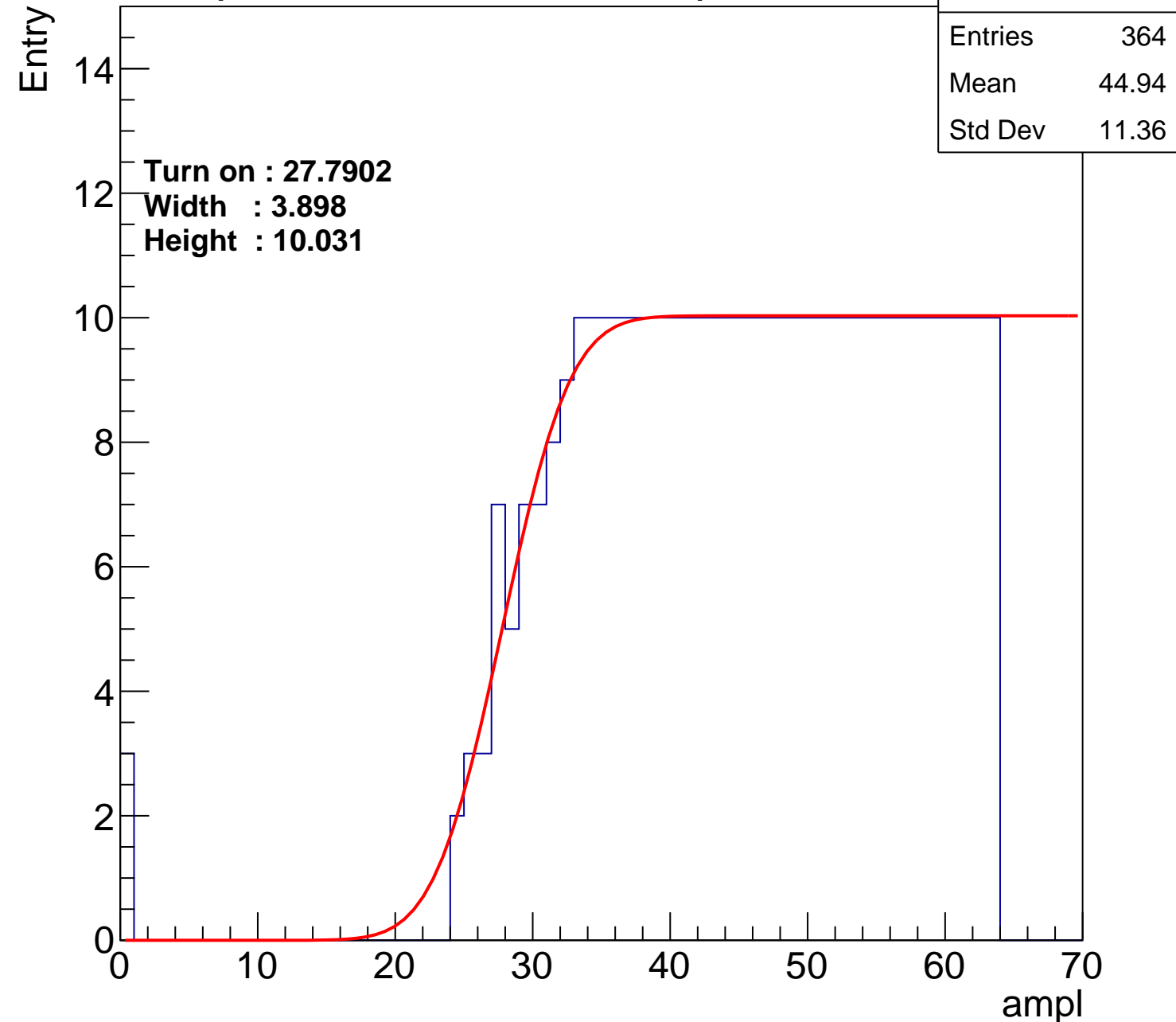
Width : 3.898

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch13

calib_packv5_042523_0143.root, FC#4, port A2

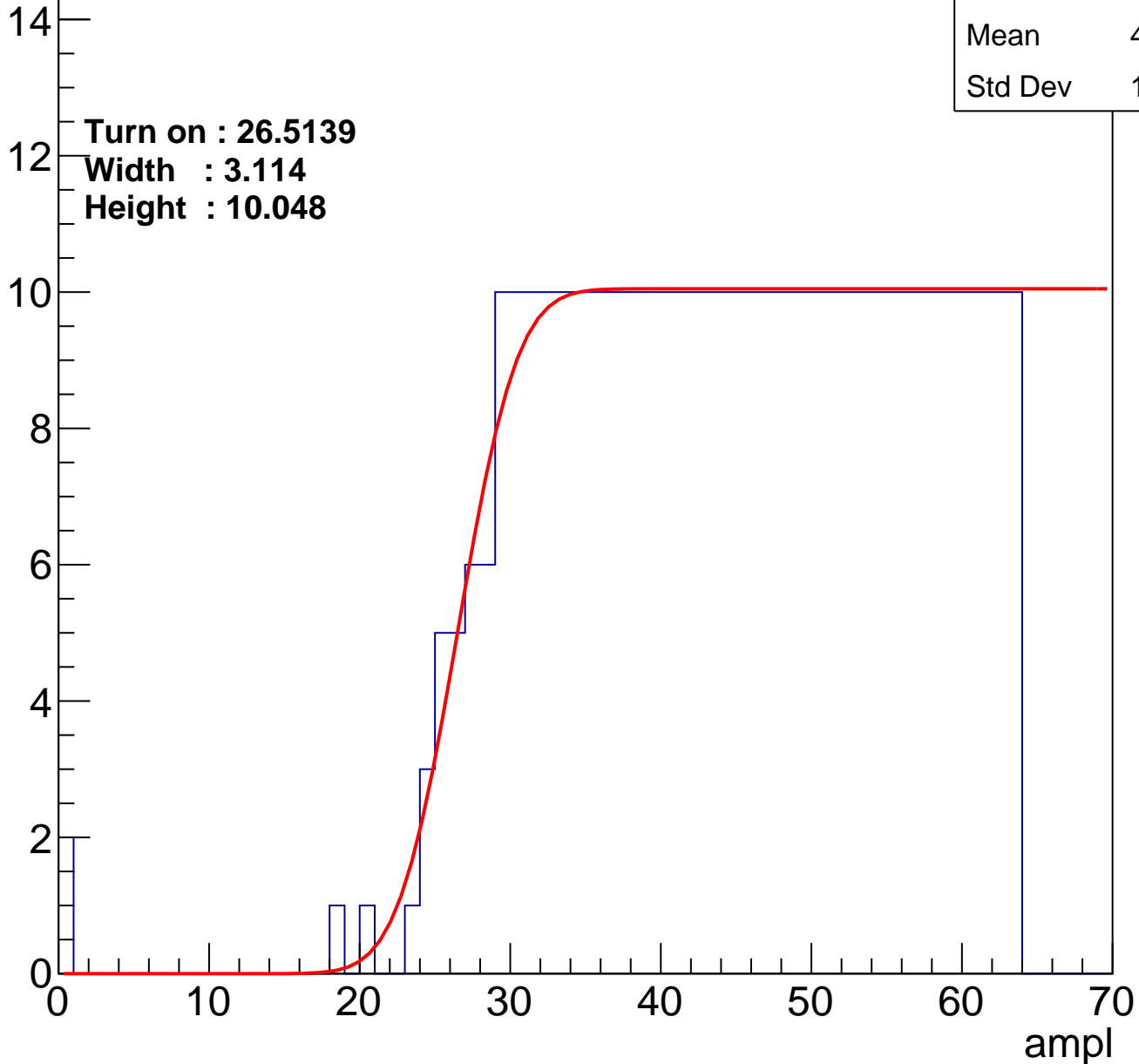
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.26 |
| Std Dev | 11.53 |

Turn on : 26.5139

Width : 3.114

Height : 10.048

Entry



B1L100S, U1-ch14

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.13 |
| Std Dev | 11.44 |

Turn on : 25.7895

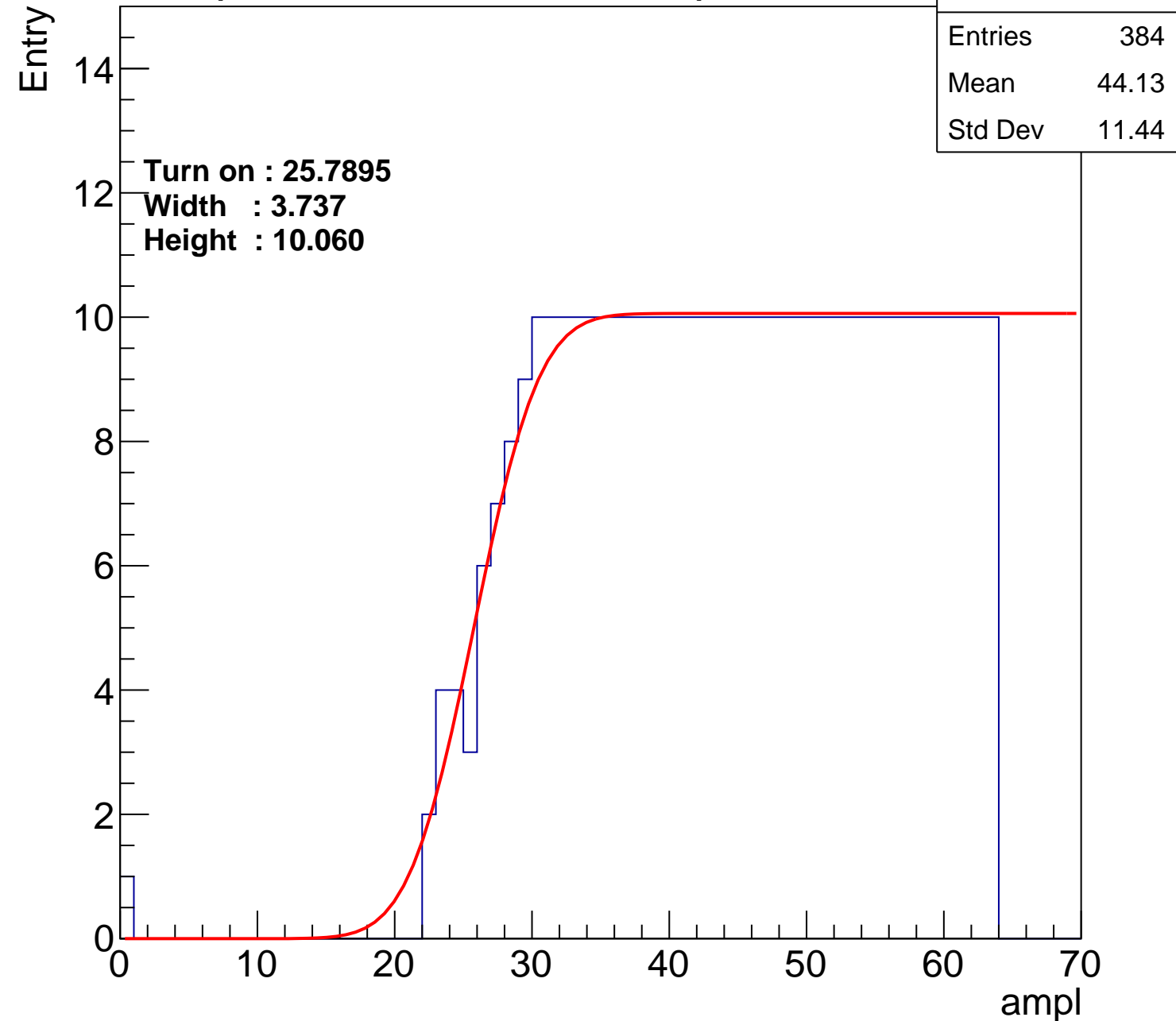
Width : 3.737

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch15

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 45.01 |
| Std Dev | 10.92 |

Turn on : 27.8525

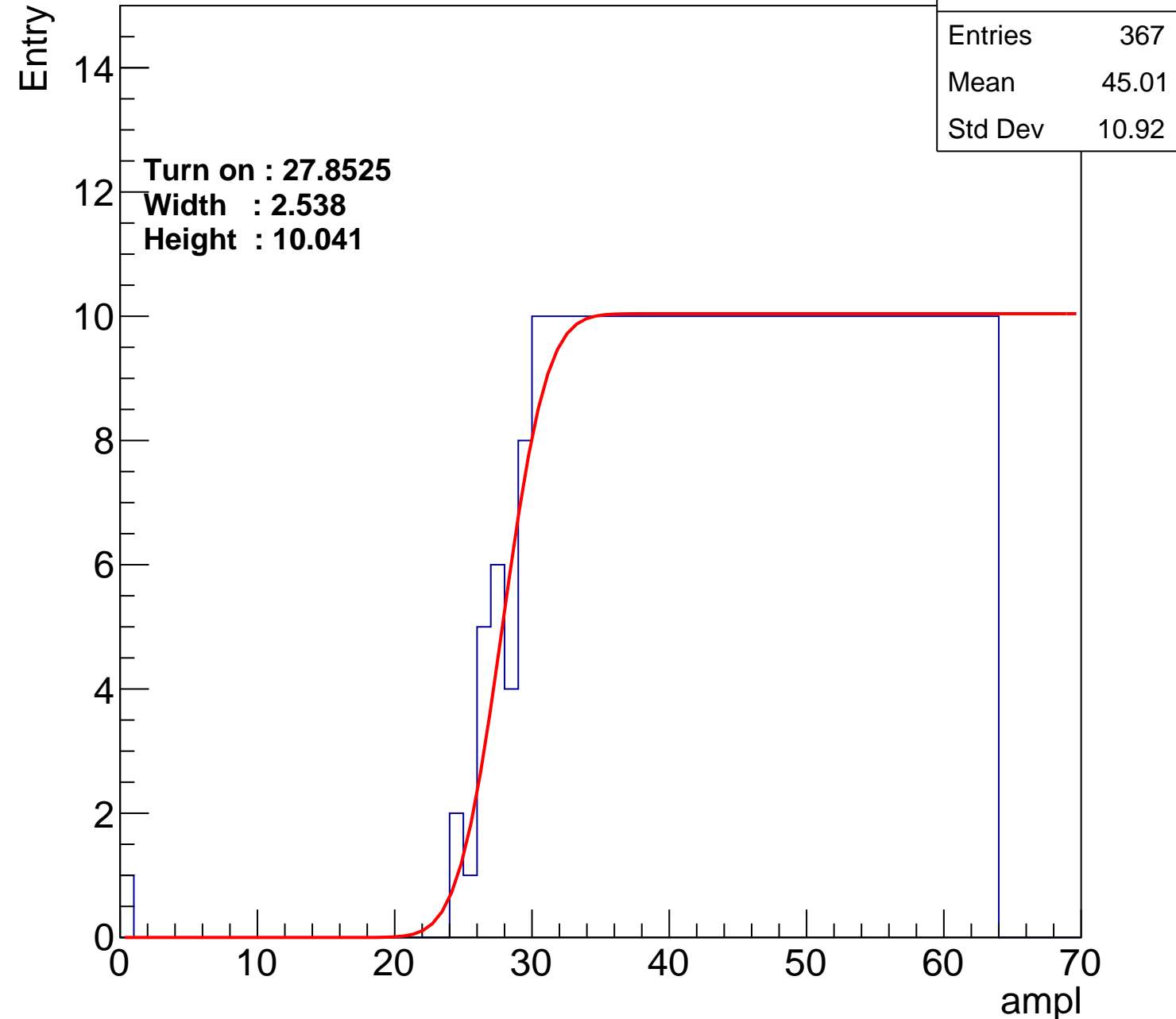
Width : 2.538

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch16

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.87 |
| Std Dev | 11.72 |

Turn on : 25.6724

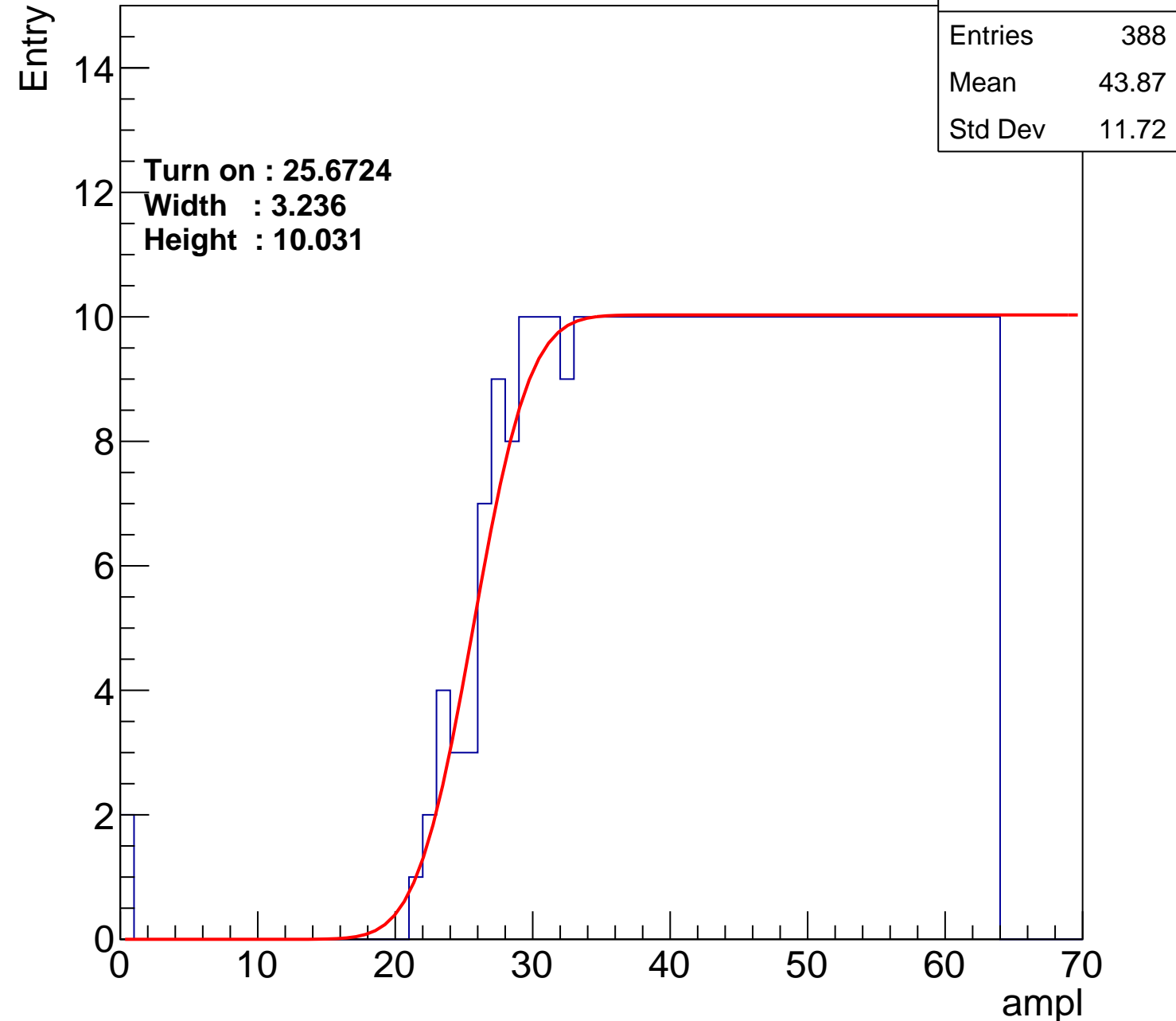
Width : 3.236

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch17

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.04 |
| Std Dev | 11.48 |

Turn on : 25.7053

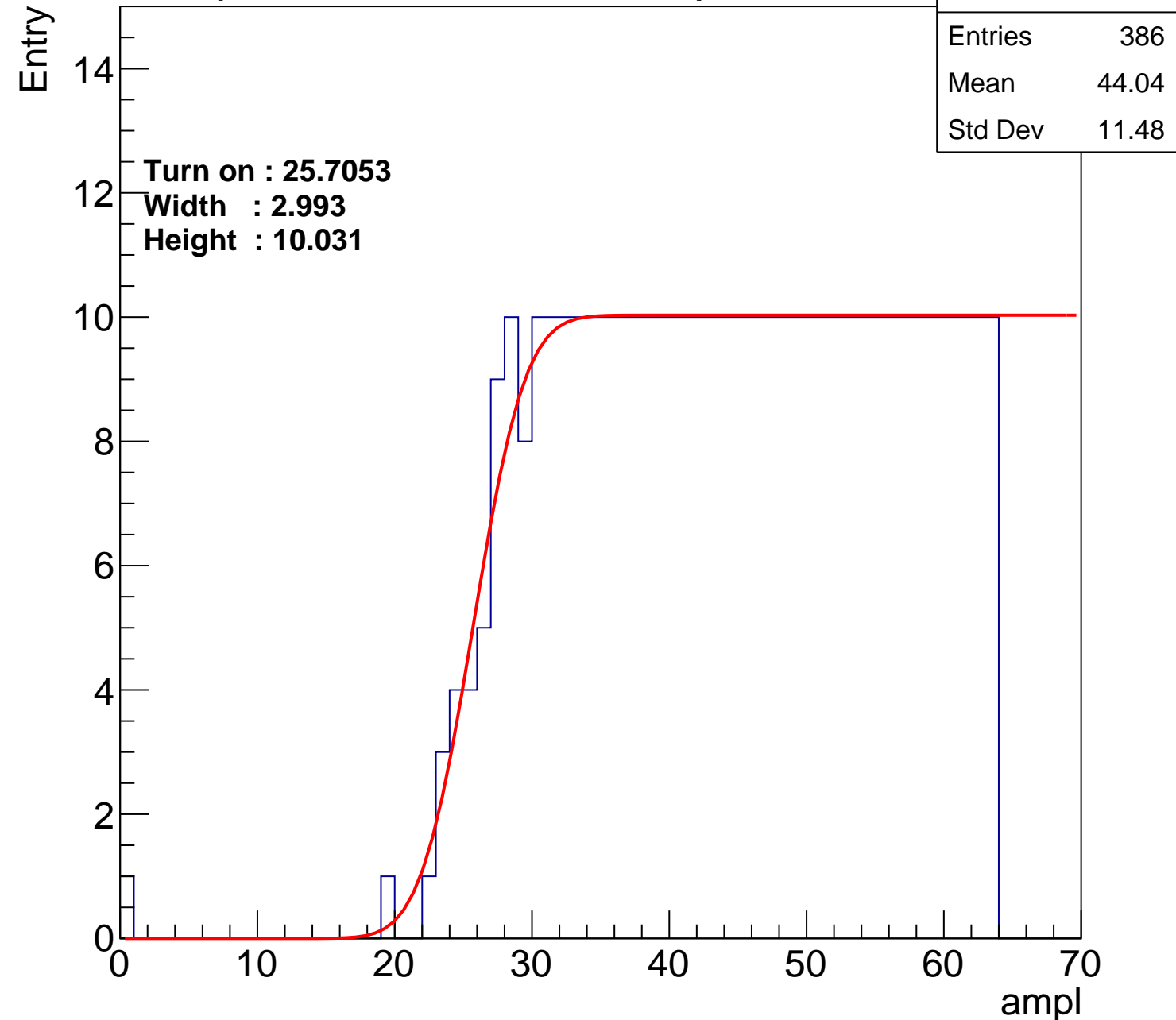
Width : 2.993

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch18

calib_packv5_042523_0143.root, FC#4, port A2

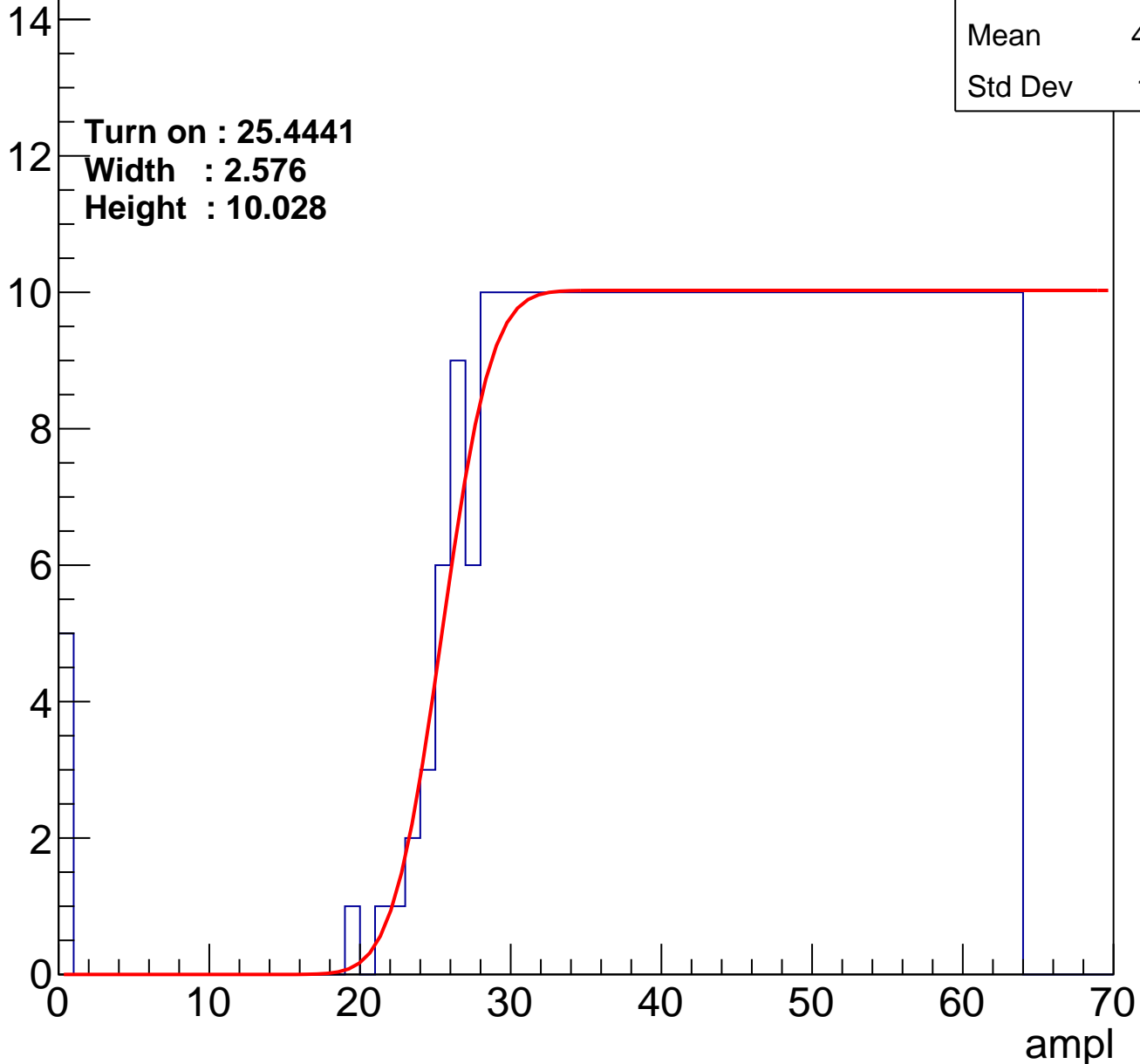
| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.42 |
| Std Dev | 12.31 |

Turn on : 25.4441

Width : 2.576

Height : 10.028

Entry



B1L100S, U1-ch19

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.86 |
| Std Dev | 11.21 |

Turn on : 27.6392

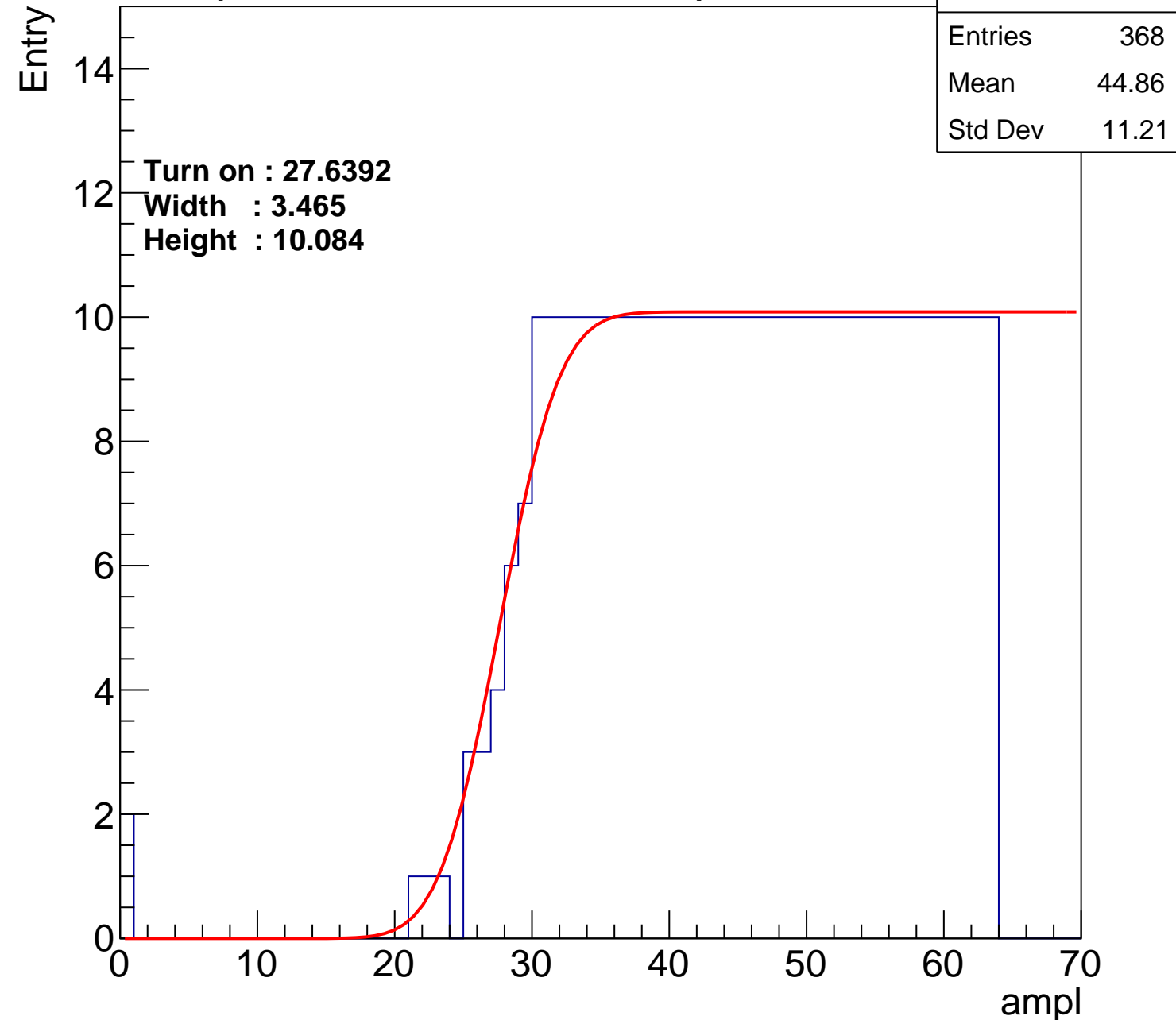
Width : 3.465

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch20

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.13 |
| Std Dev | 11.44 |

Turn on : 26.0748

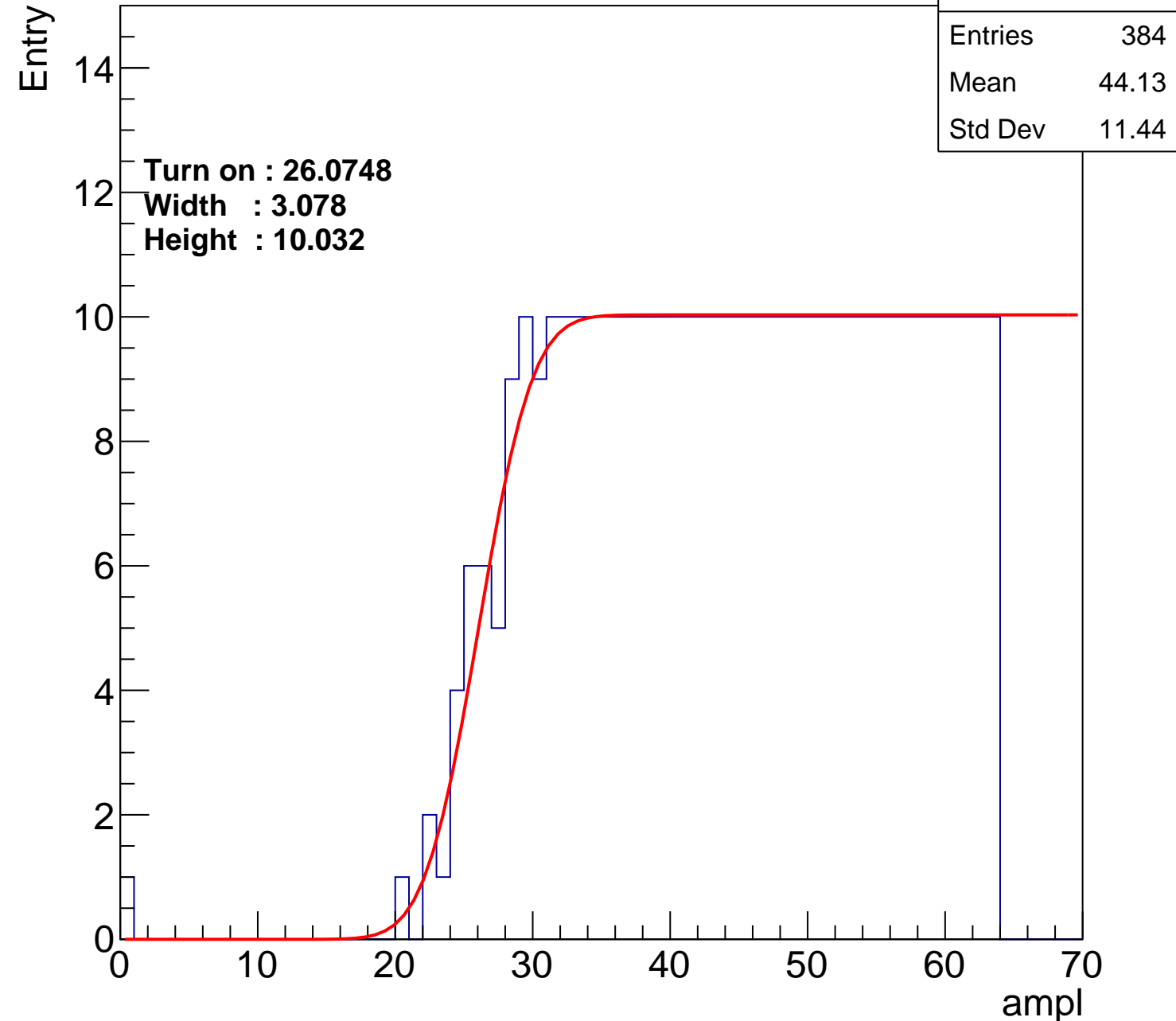
Width : 3.078

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch21

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.83 |
| Std Dev | 11.06 |

Turn on : 27.6264

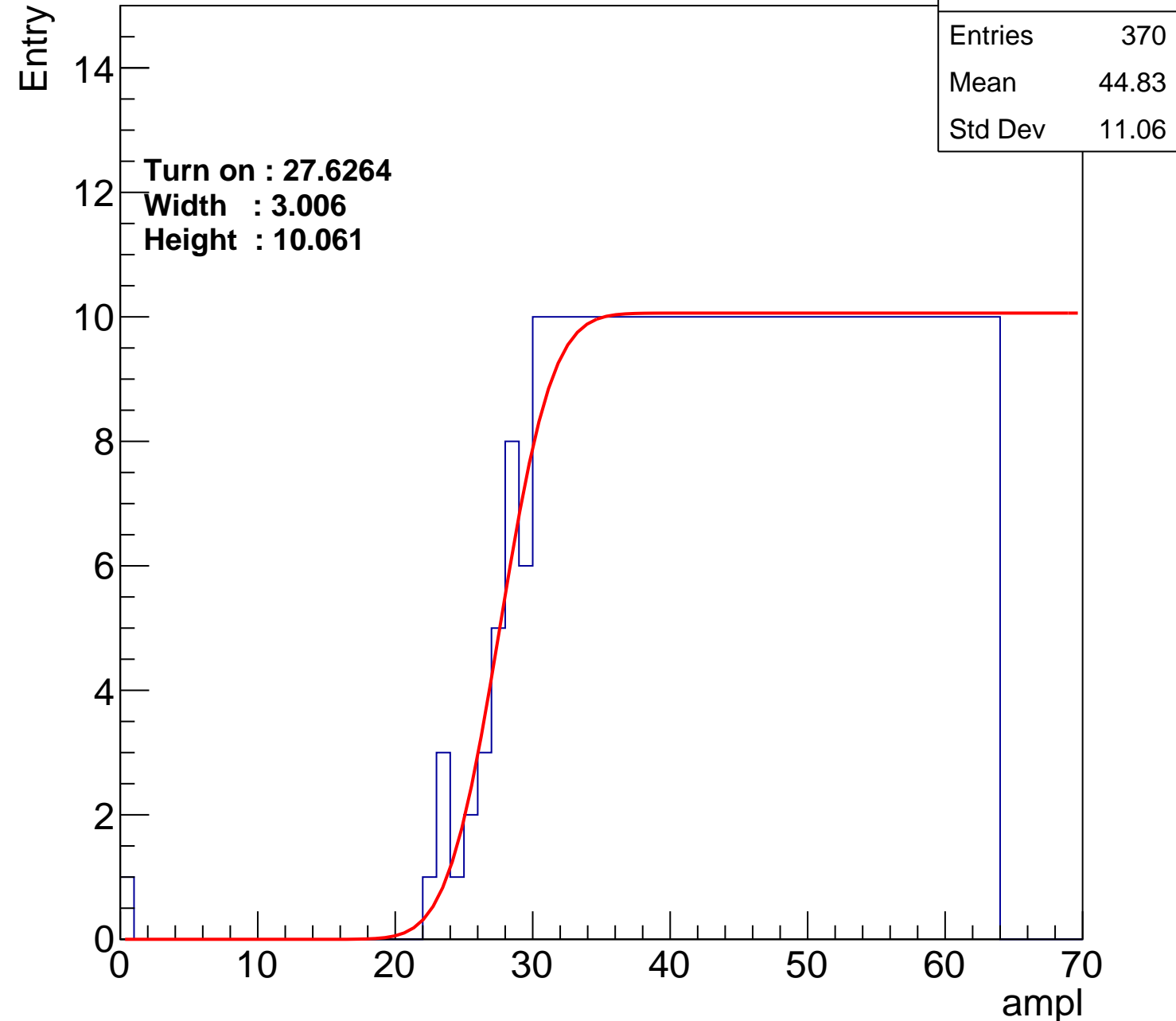
Width : 3.006

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch22

calib_packv5_042523_0143.root, FC#4, port A2

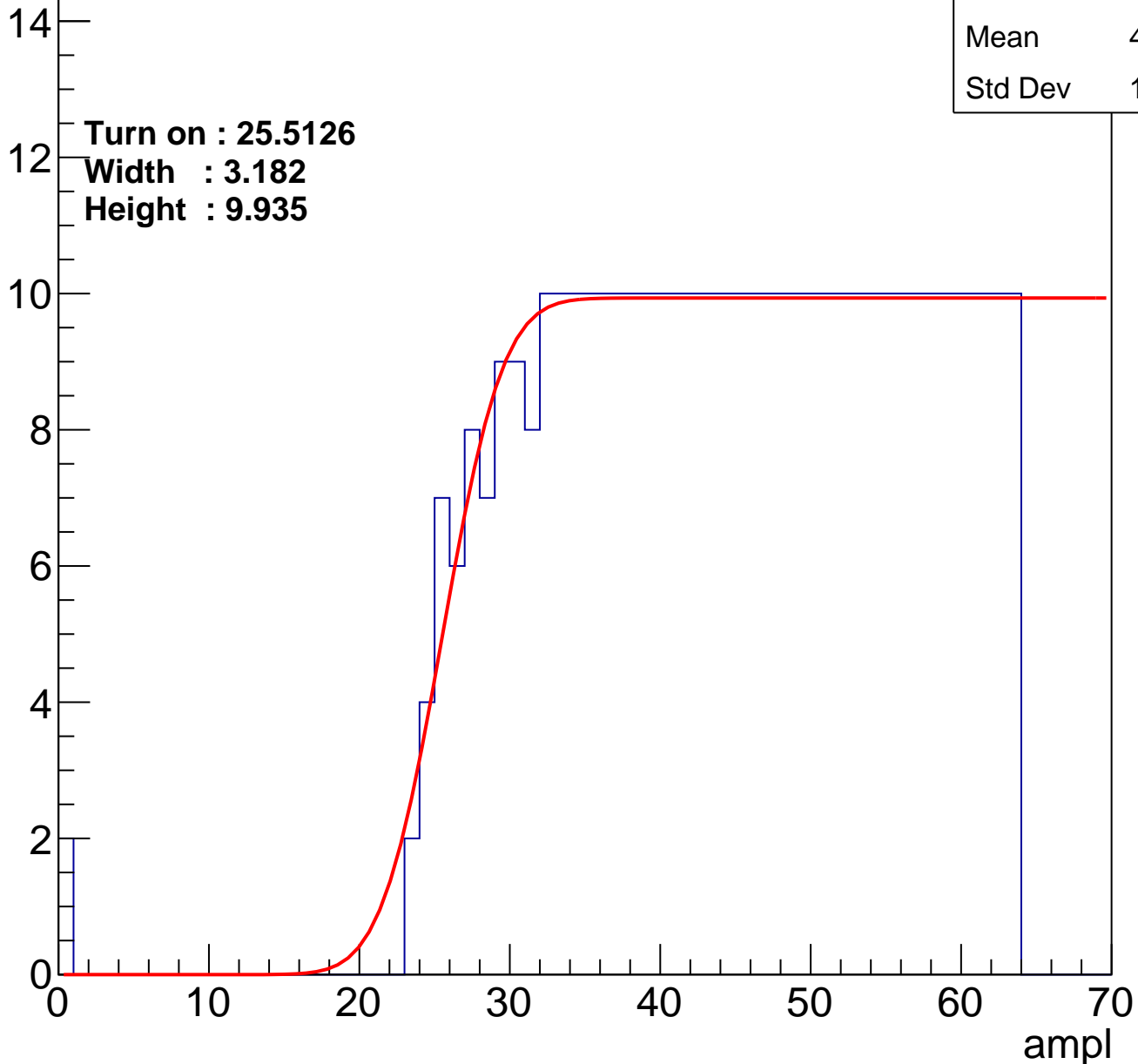
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.15 |
| Std Dev | 11.58 |

Turn on : 25.5126

Width : 3.182

Height : 9.935

Entry



B1L100S, U1-ch23

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.06 |
| Std Dev | 11.28 |

Turn on : 28.2071

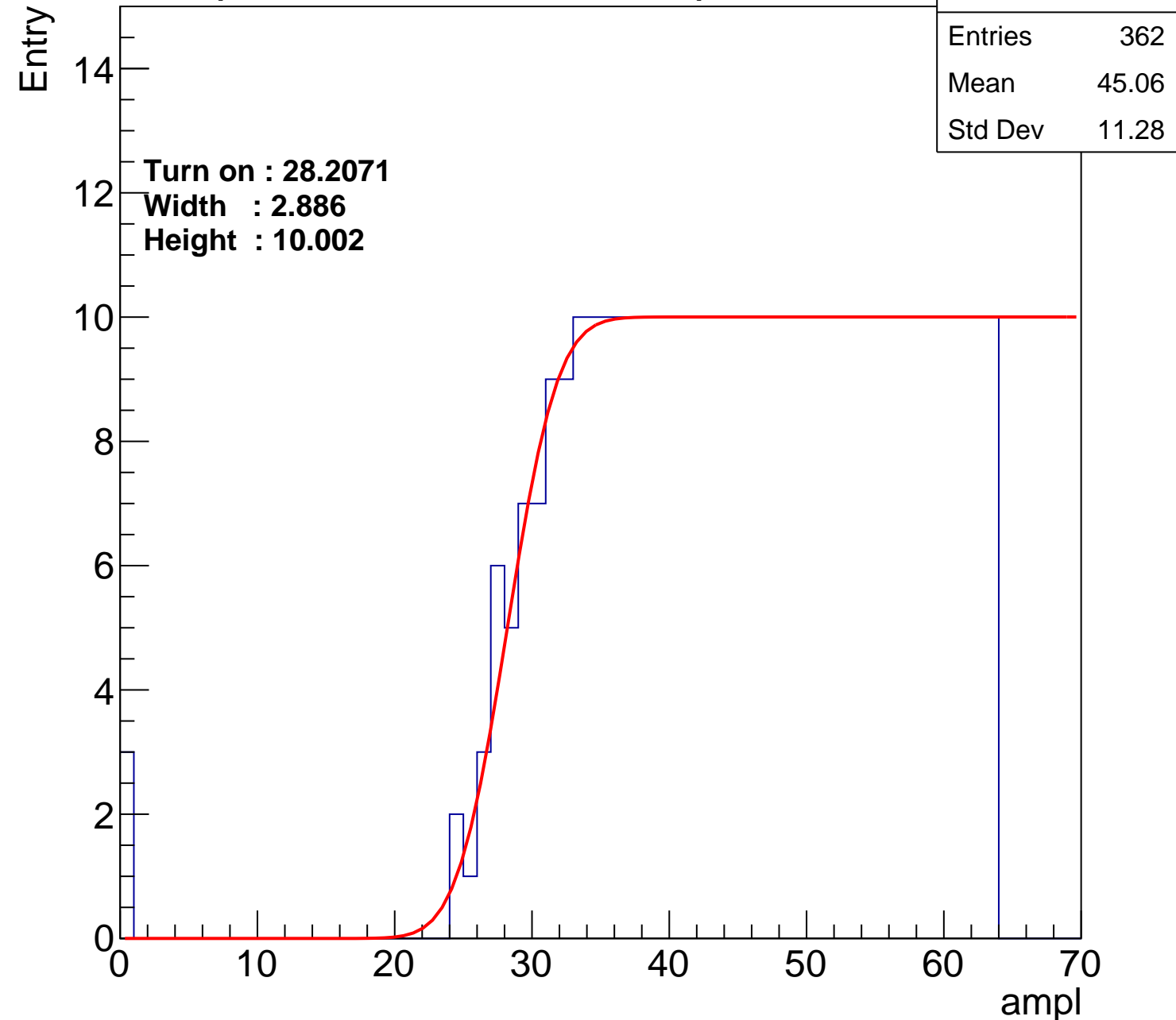
Width : 2.886

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch24

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.84 |
| Std Dev | 12.19 |

Turn on : 26.2779

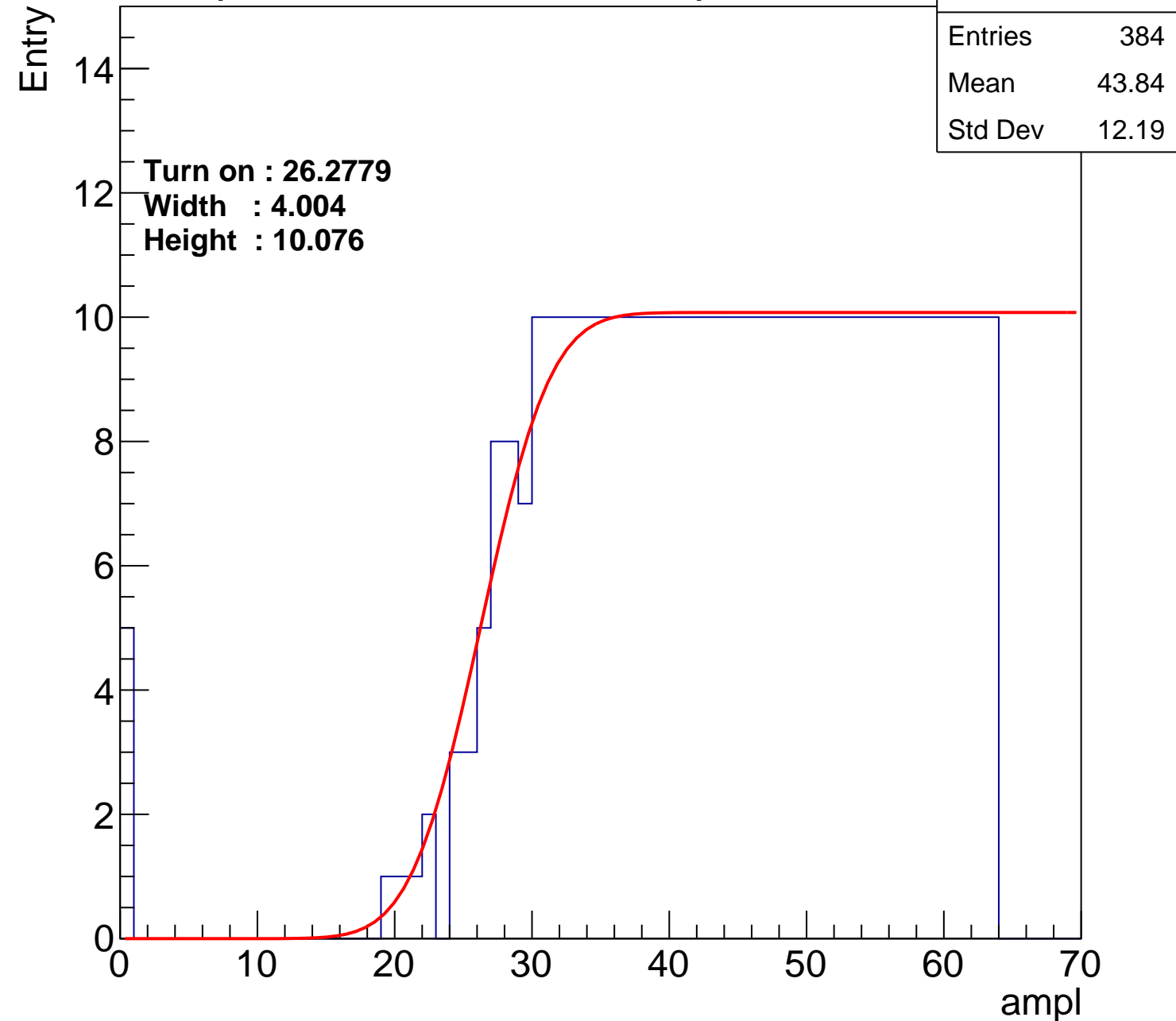
Width : 4.004

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch25

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.32 |
| Std Dev | 10.79 |

Turn on : 28.3569

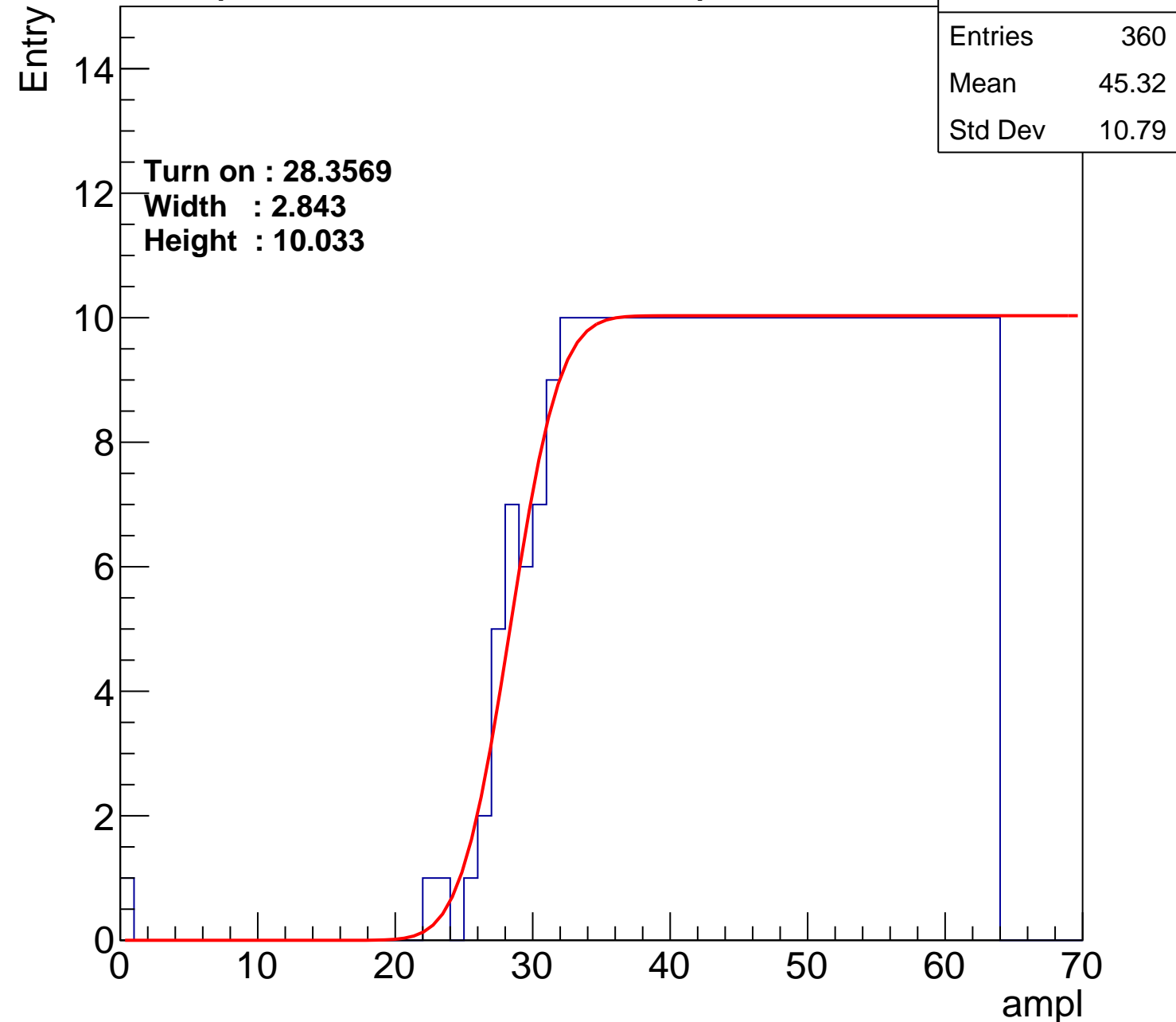
Width : 2.843

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch26

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.93 |
| Std Dev | 11.96 |

Turn on : 26.1526

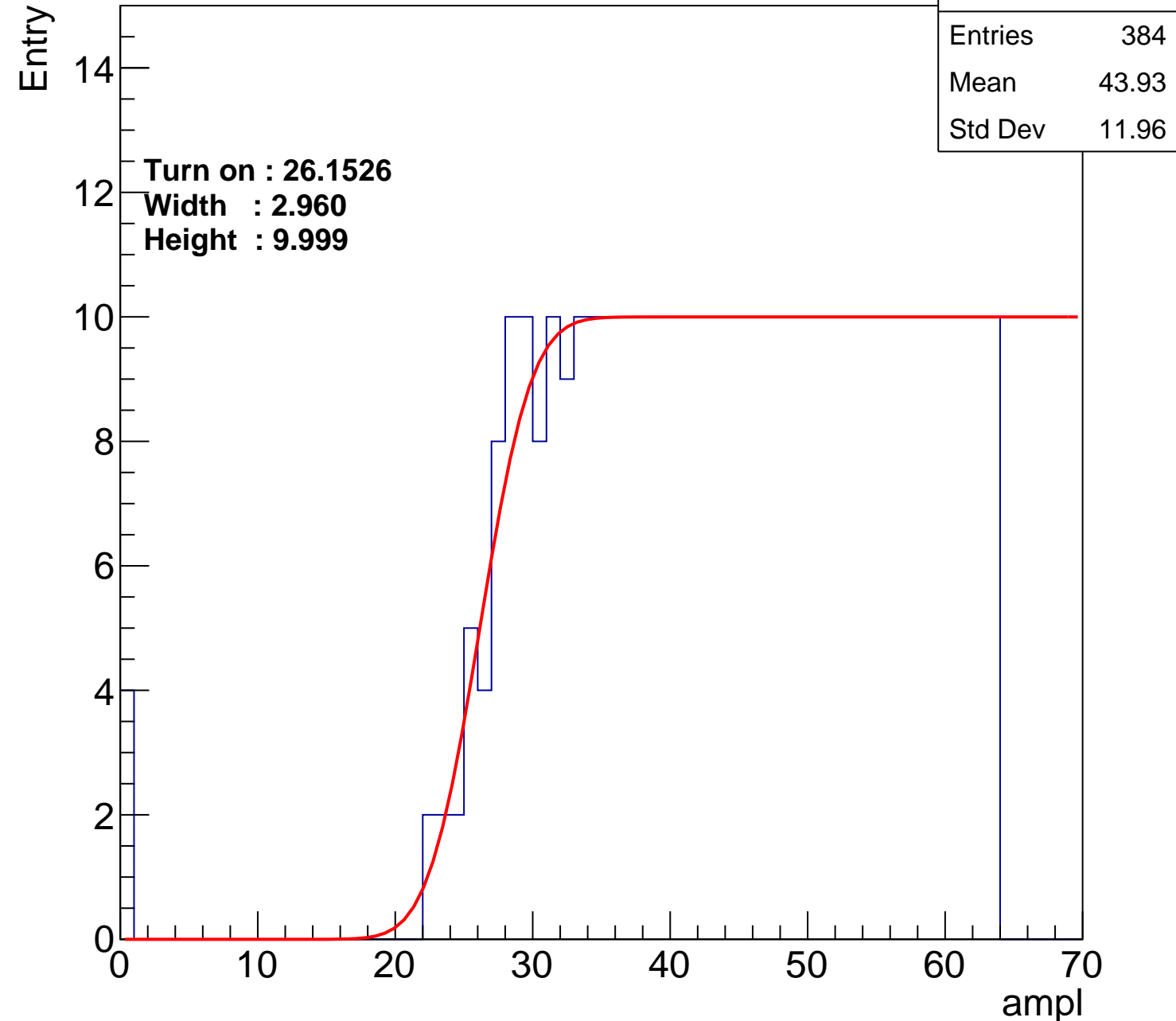
Width : 2.960

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch27

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.49 |
| Std Dev | 11.23 |

Turn on : 26.6337

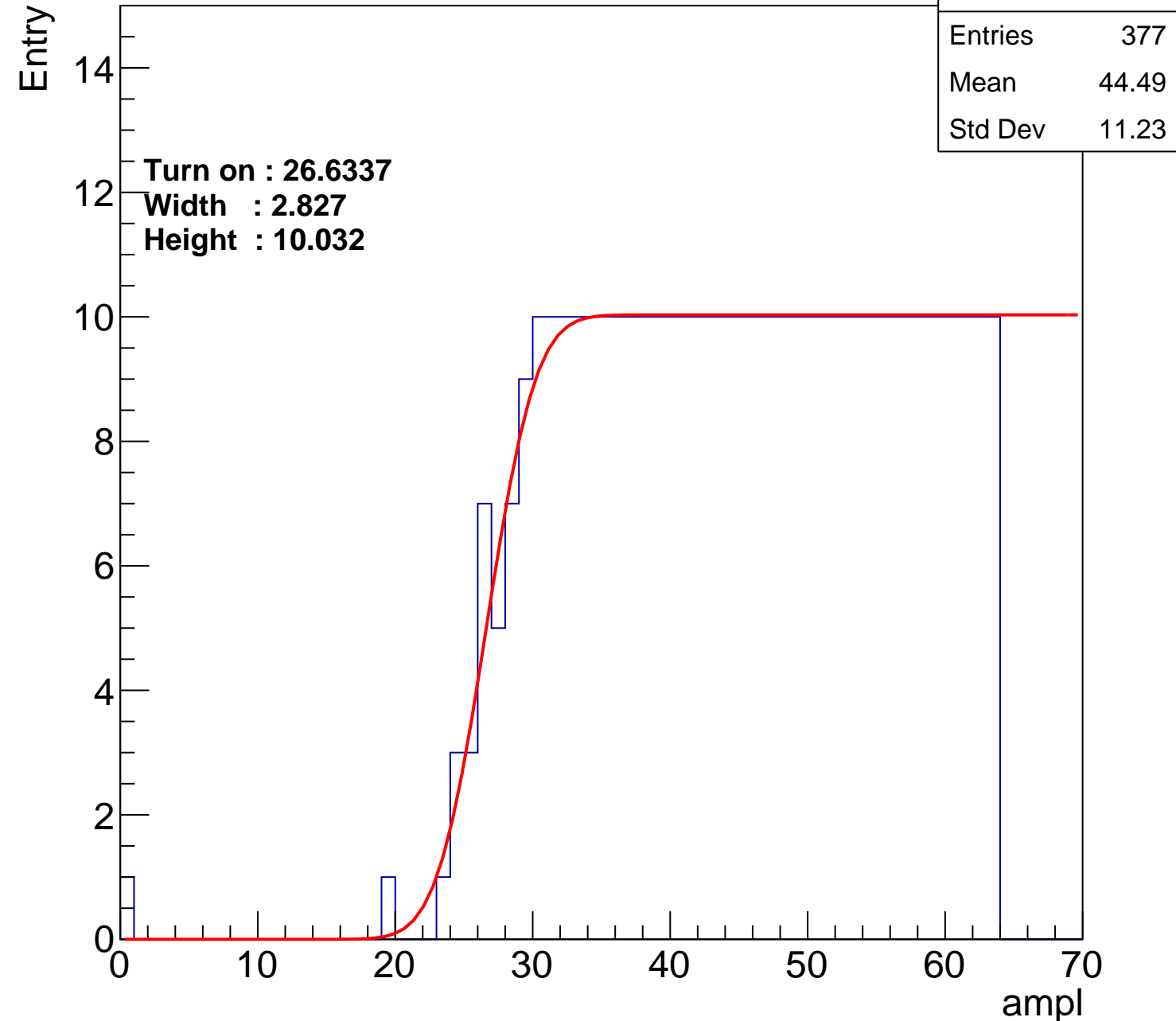
Width : 2.827

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch28

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.08 |
| Std Dev | 11.06 |

Turn on : 27.9790

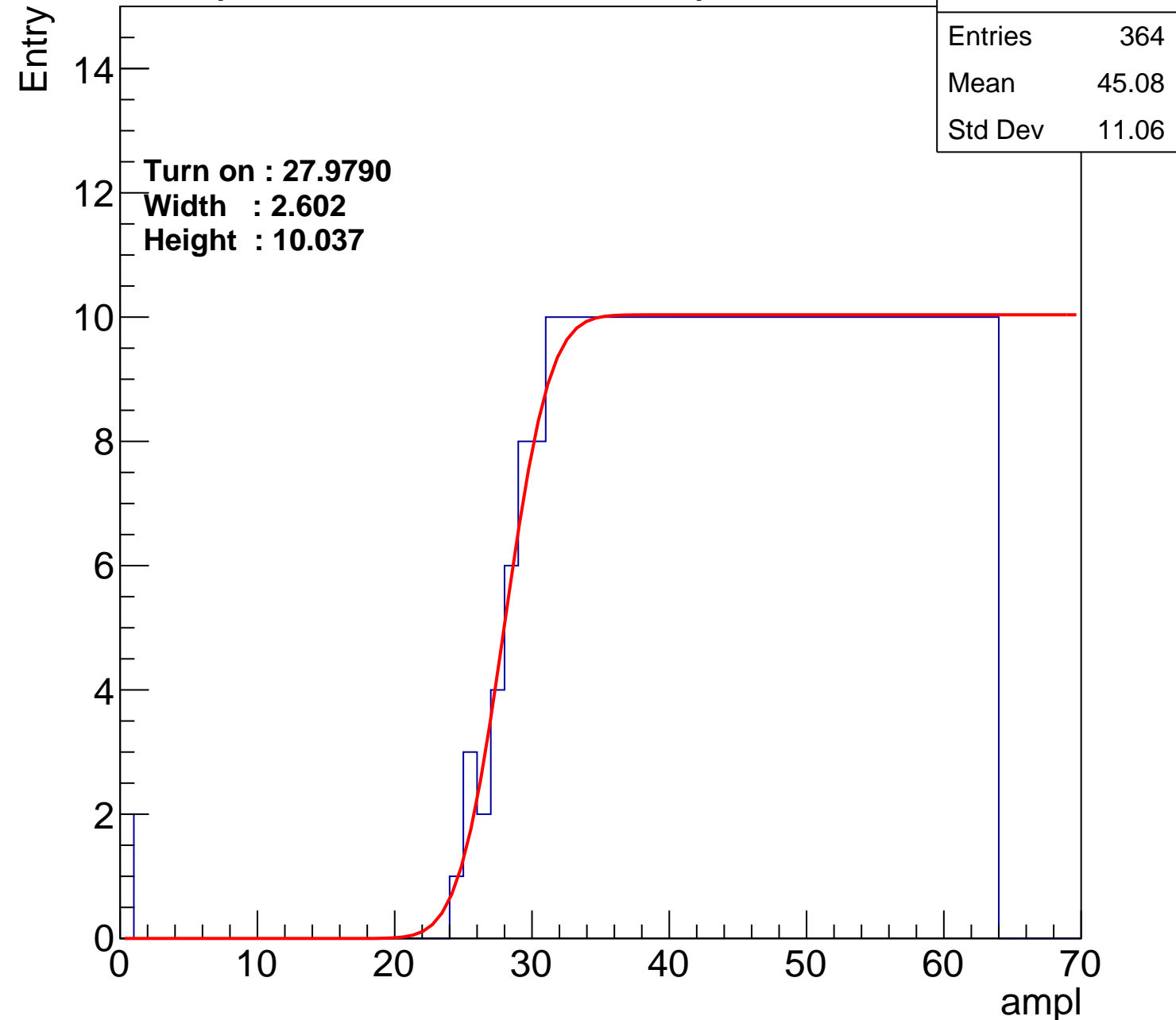
Width : 2.602

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch29

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.68 |
| Std Dev | 11.94 |

Turn on : 24.5774

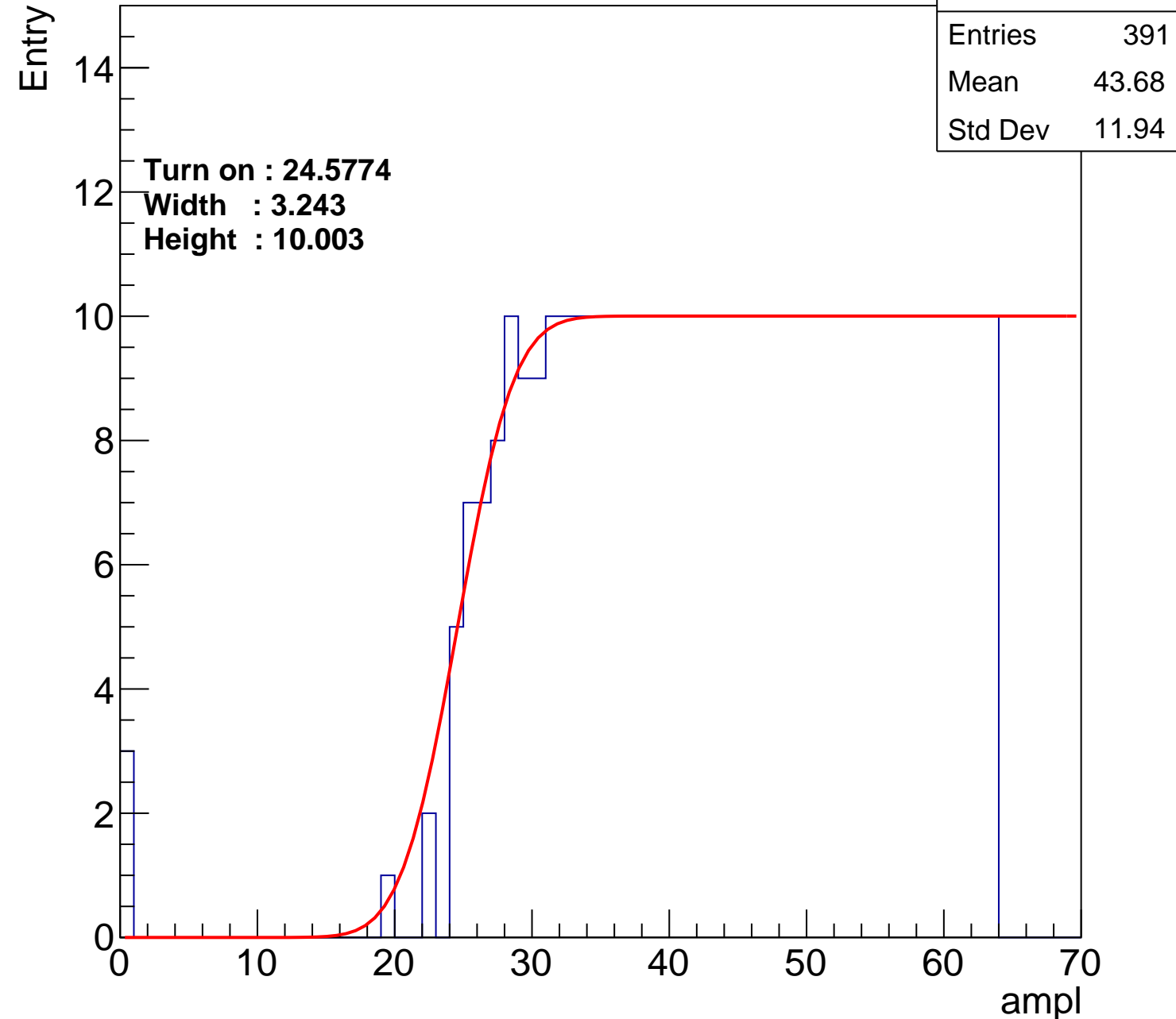
Width : 3.243

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch30

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 404 |
| Mean | 43.06 |
| Std Dev | 12.23 |

Turn on : 23.7683

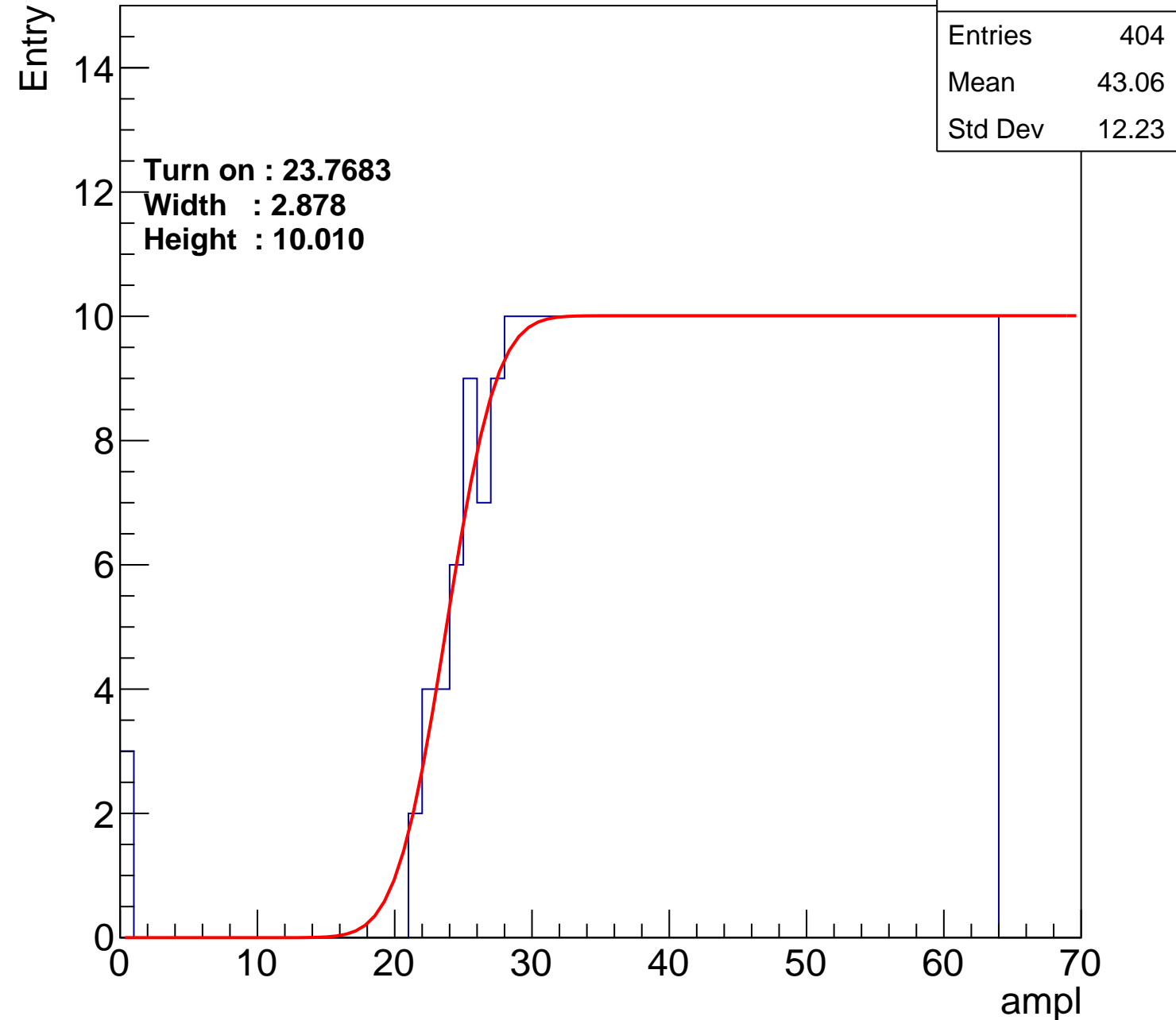
Width : 2.878

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch31

calib_packv5_042523_0143.root, FC#4, port A2

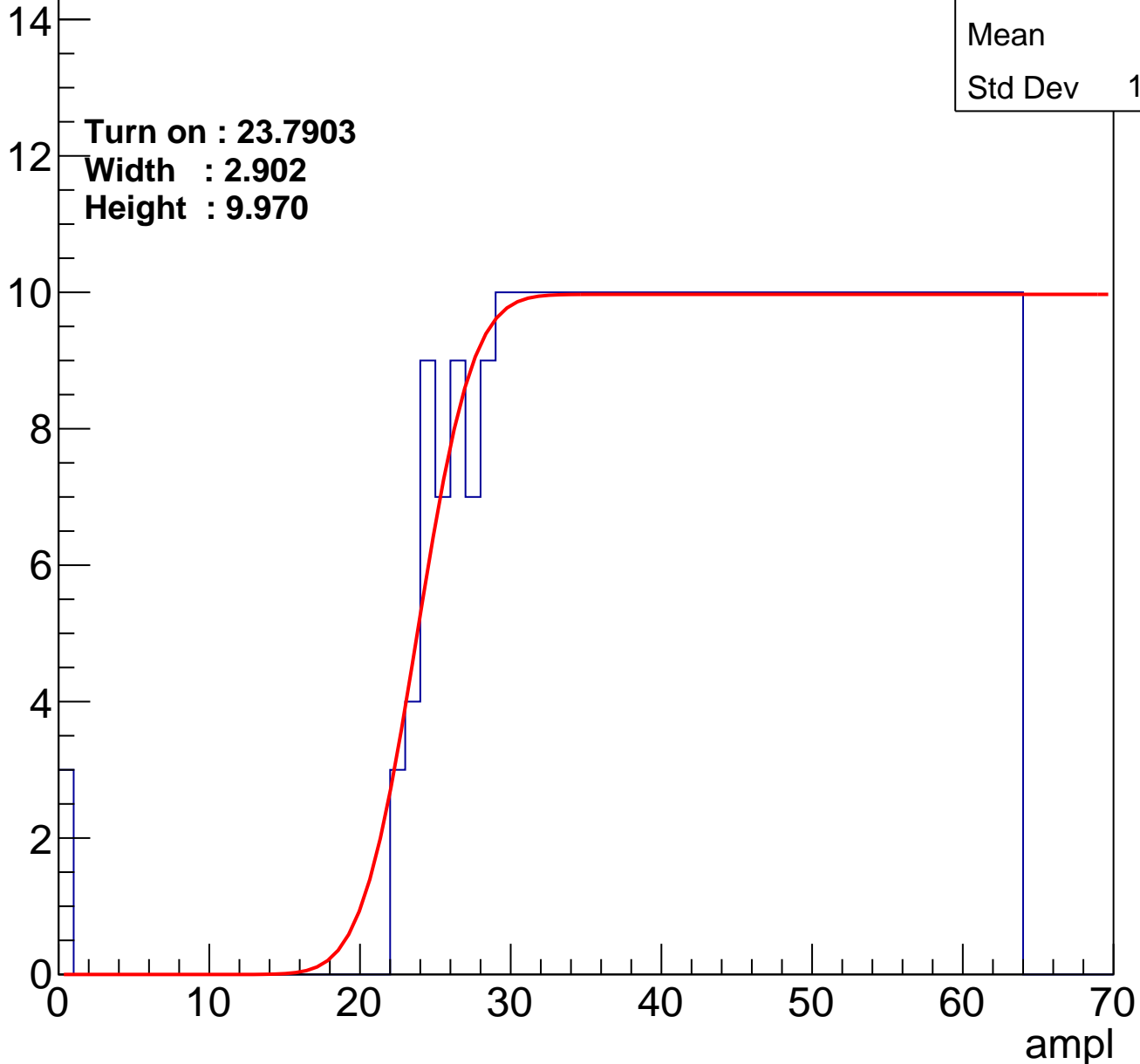
| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.2 |
| Std Dev | 12.15 |

Turn on : 23.7903

Width : 2.902

Height : 9.970

Entry



B1L100S, U1-ch32

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.28 |
| Std Dev | 11.67 |

Turn on : 26.2987

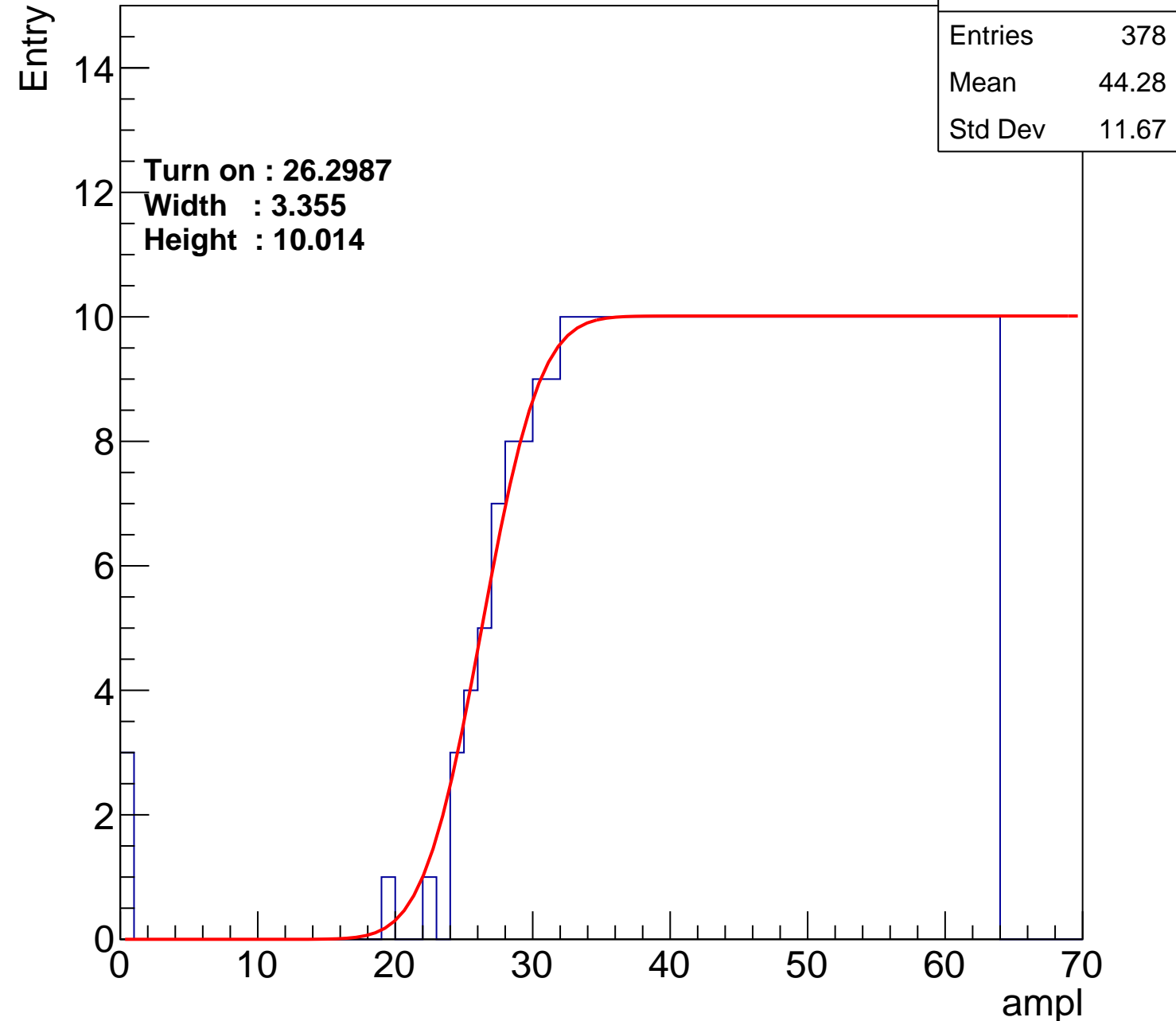
Width : 3.355

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch33

calib_packv5_042523_0143.root, FC#4, port A2

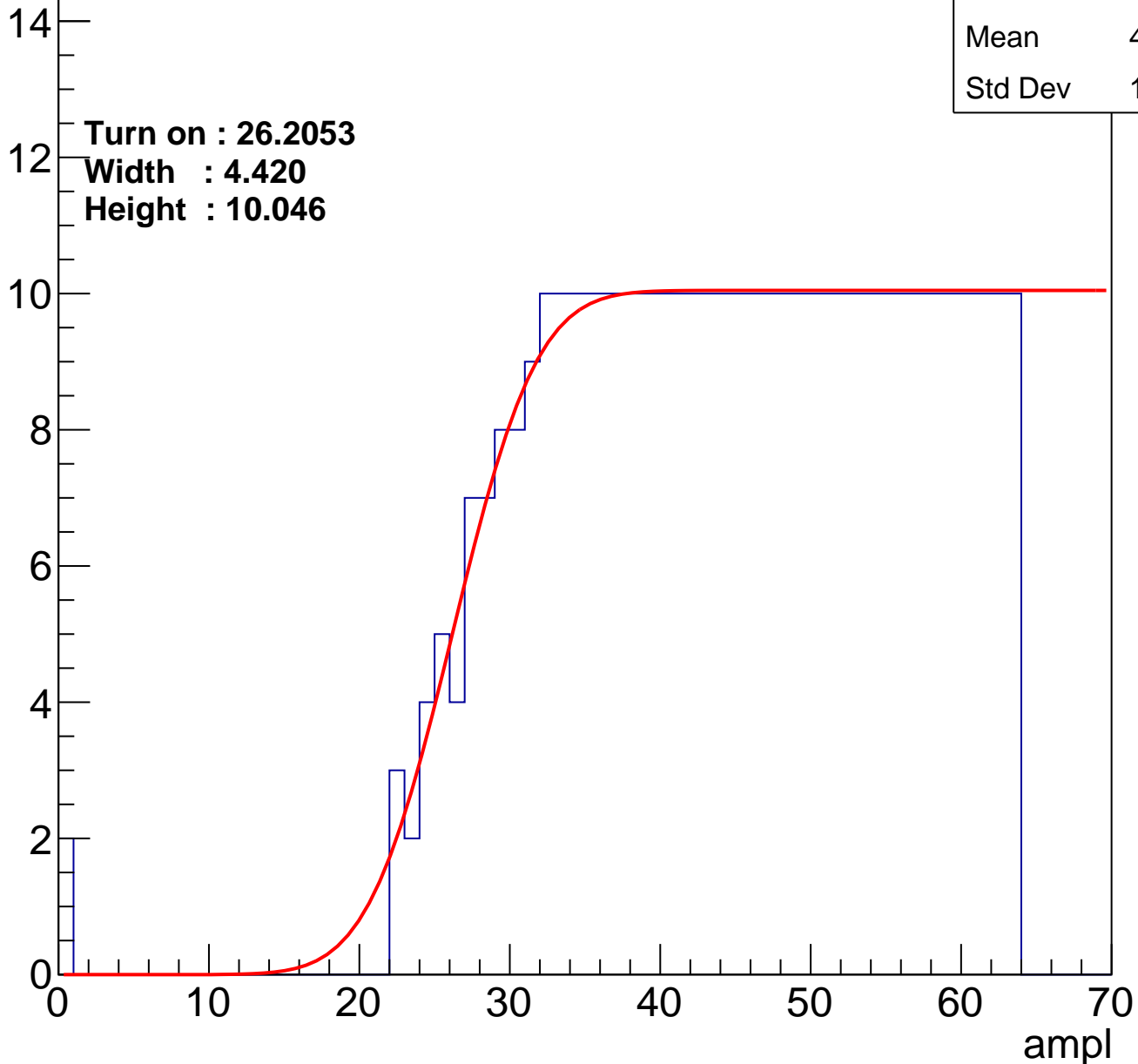
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.26 |
| Std Dev | 11.58 |

Turn on : 26.2053

Width : 4.420

Height : 10.046

Entry



B1L100S, U1-ch34

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 407 |
| Mean | 42.81 |
| Std Dev | 12.52 |

Turn on : 24.1054

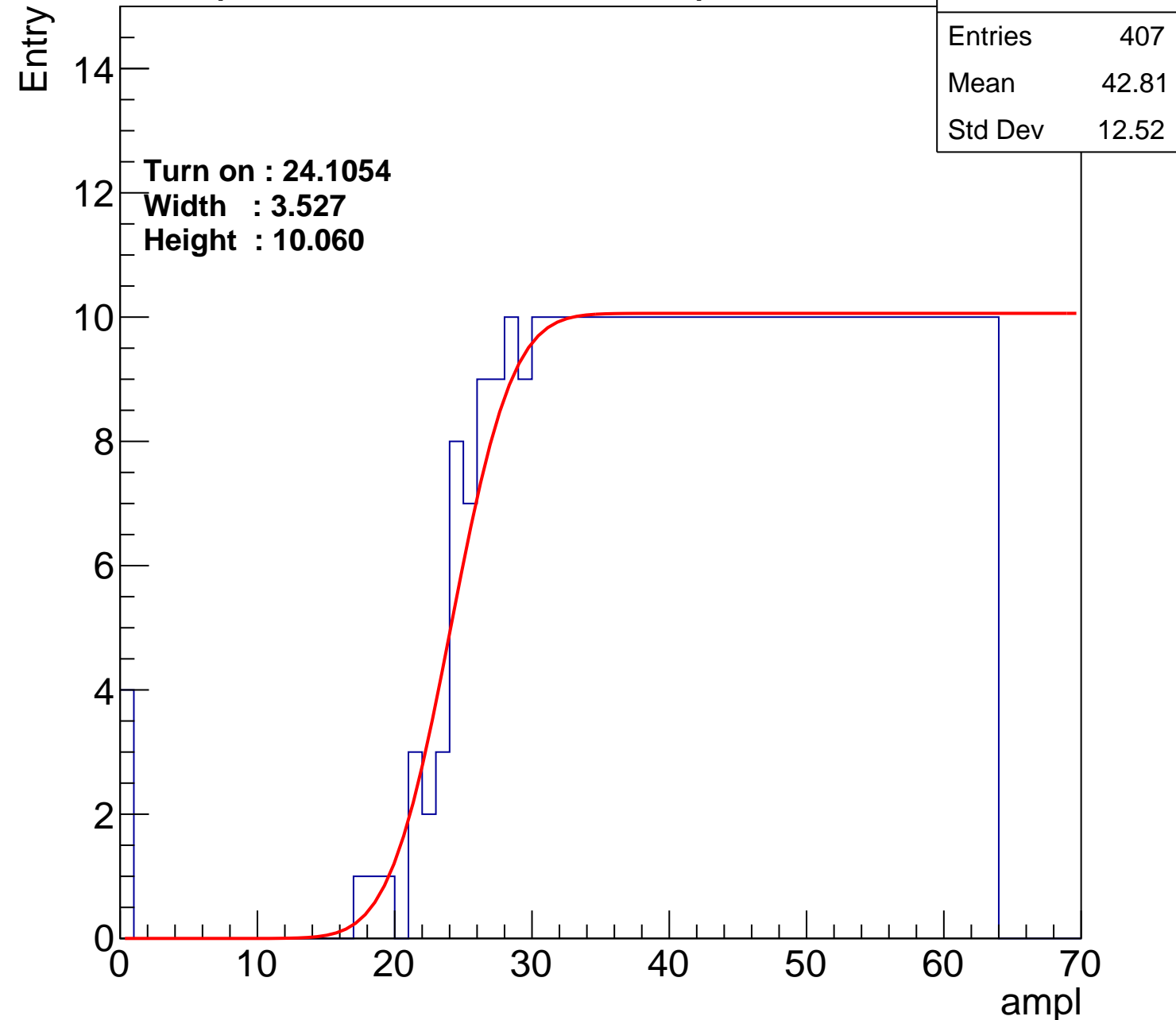
Width : 3.527

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch35

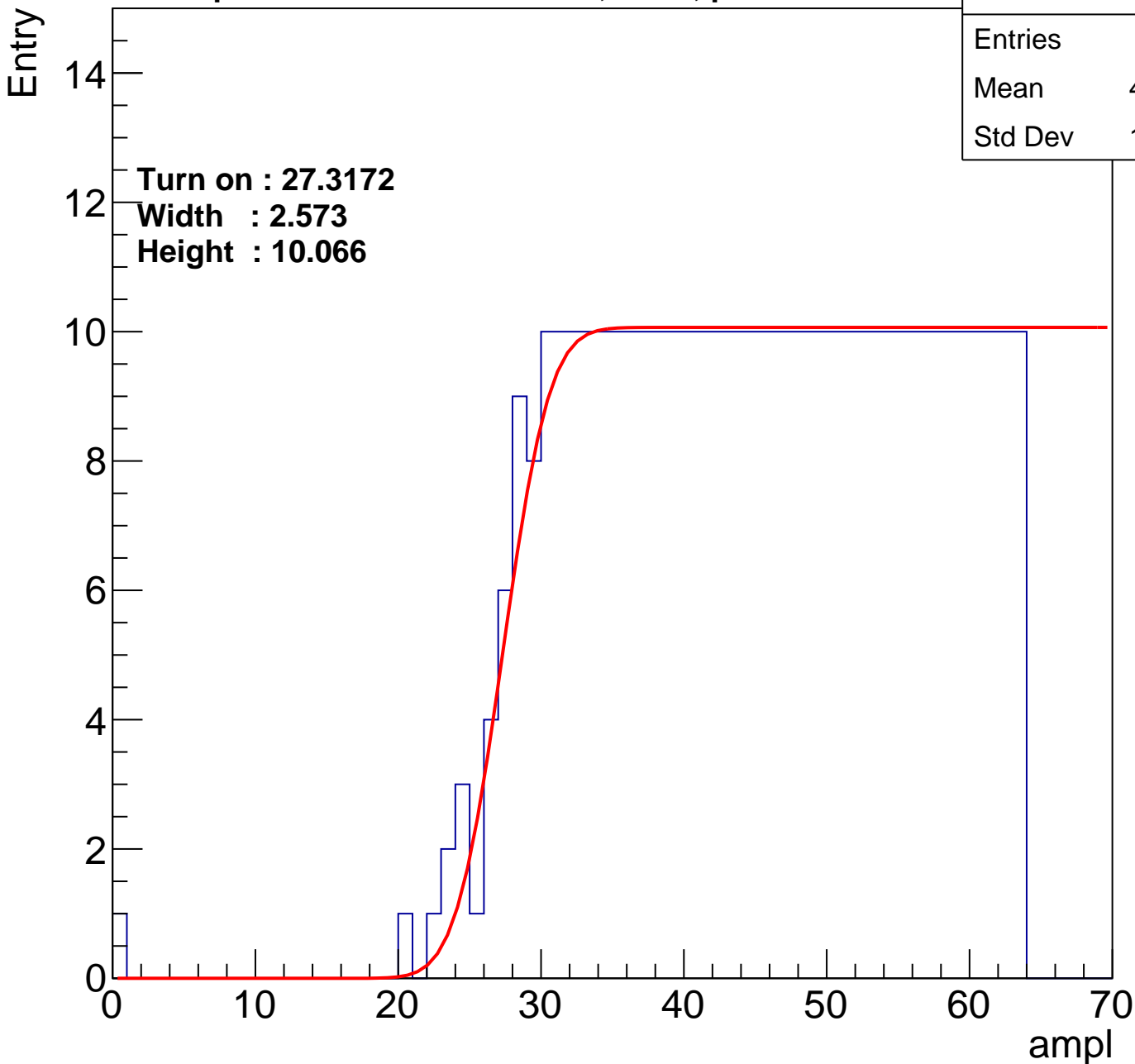
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.53 |
| Std Dev | 11.22 |

Turn on : 27.3172

Width : 2.573

Height : 10.066



B1L100S, U1-ch36

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.52 |
| Std Dev | 12.14 |

Turn on : 24.8628

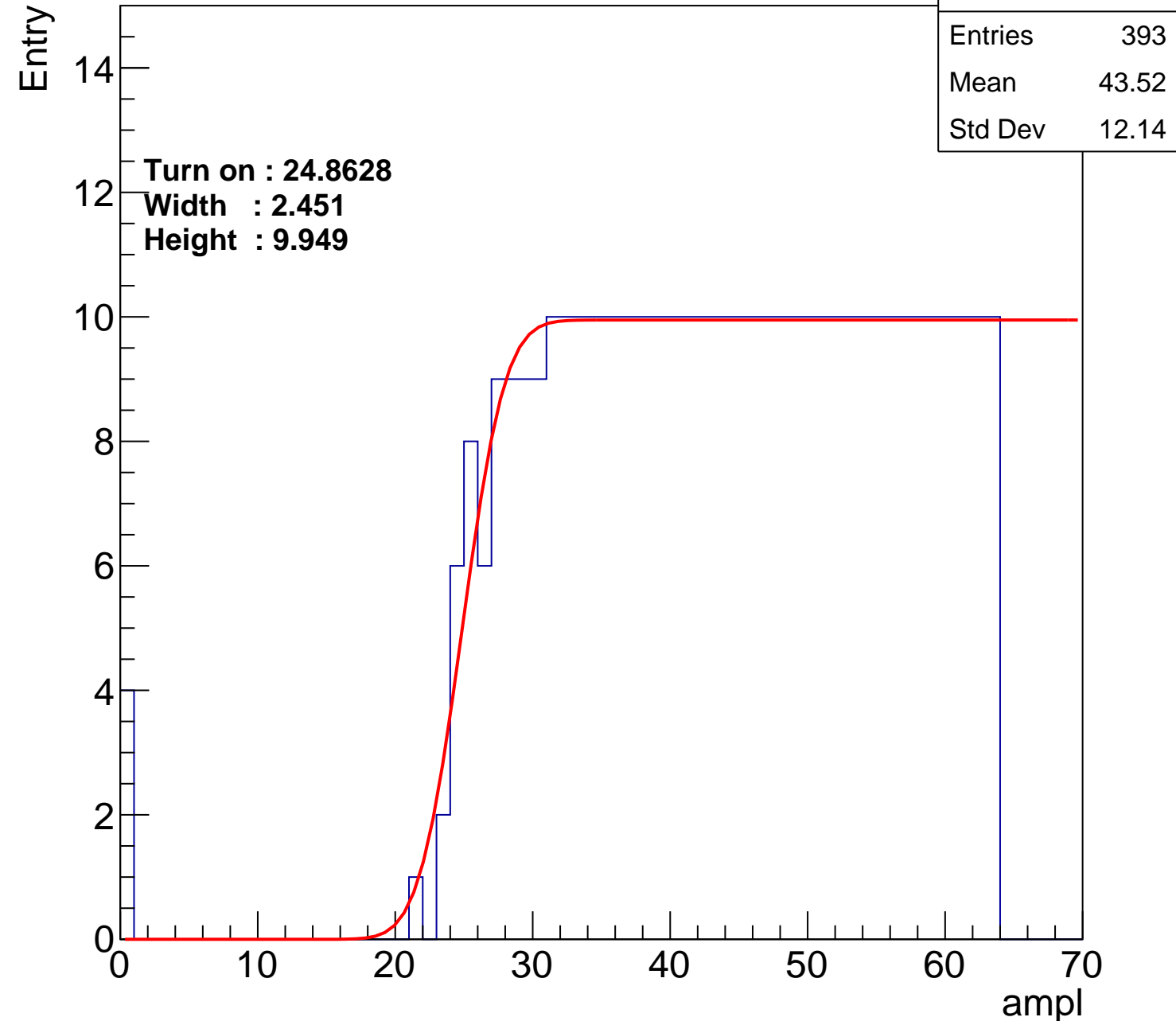
Width : 2.451

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch37

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.52 |
| Std Dev | 11.7 |

Turn on : 27.7958

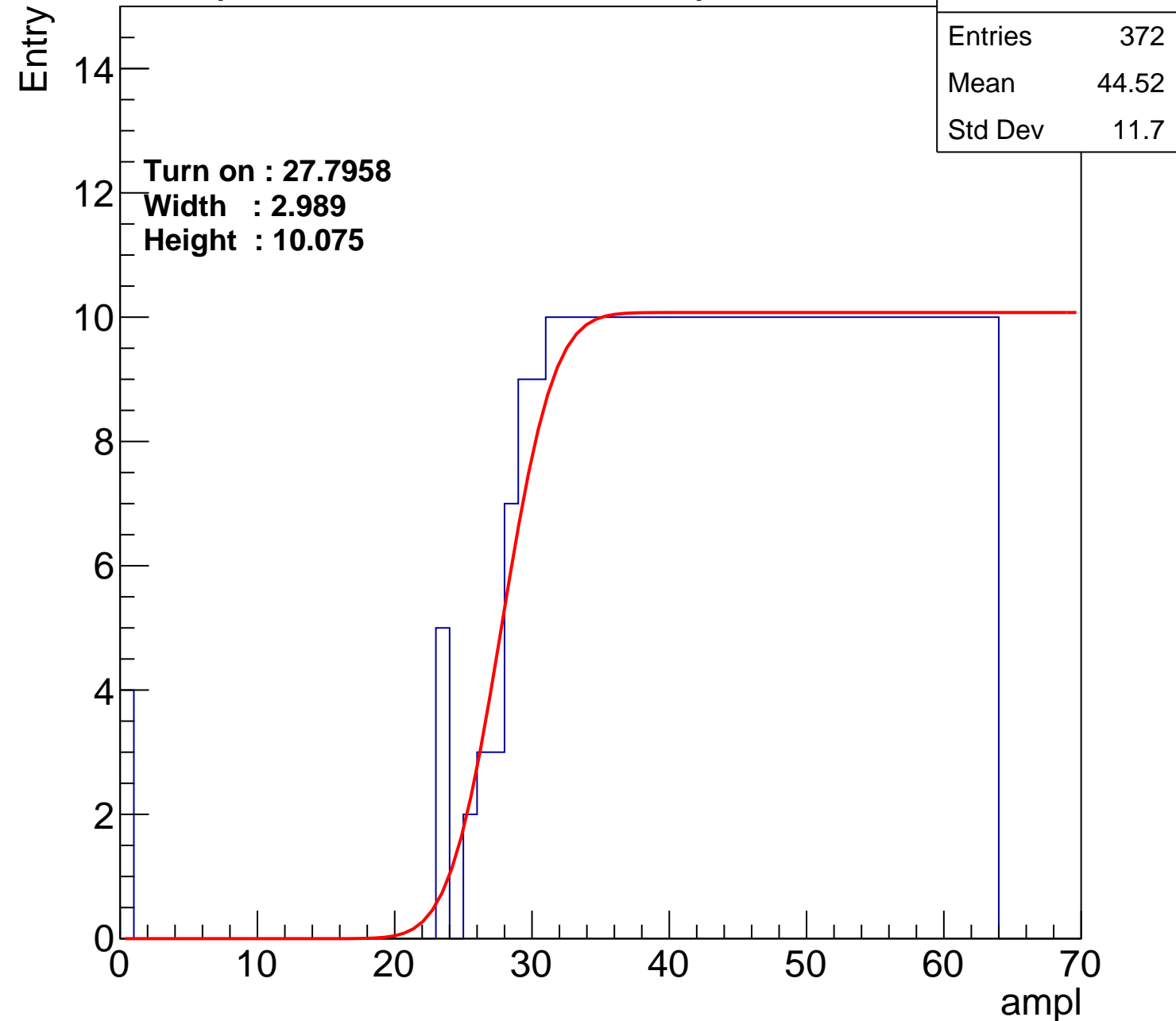
Width : 2.989

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch38

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.67 |
| Std Dev | 11.83 |

Turn on : 25.2616

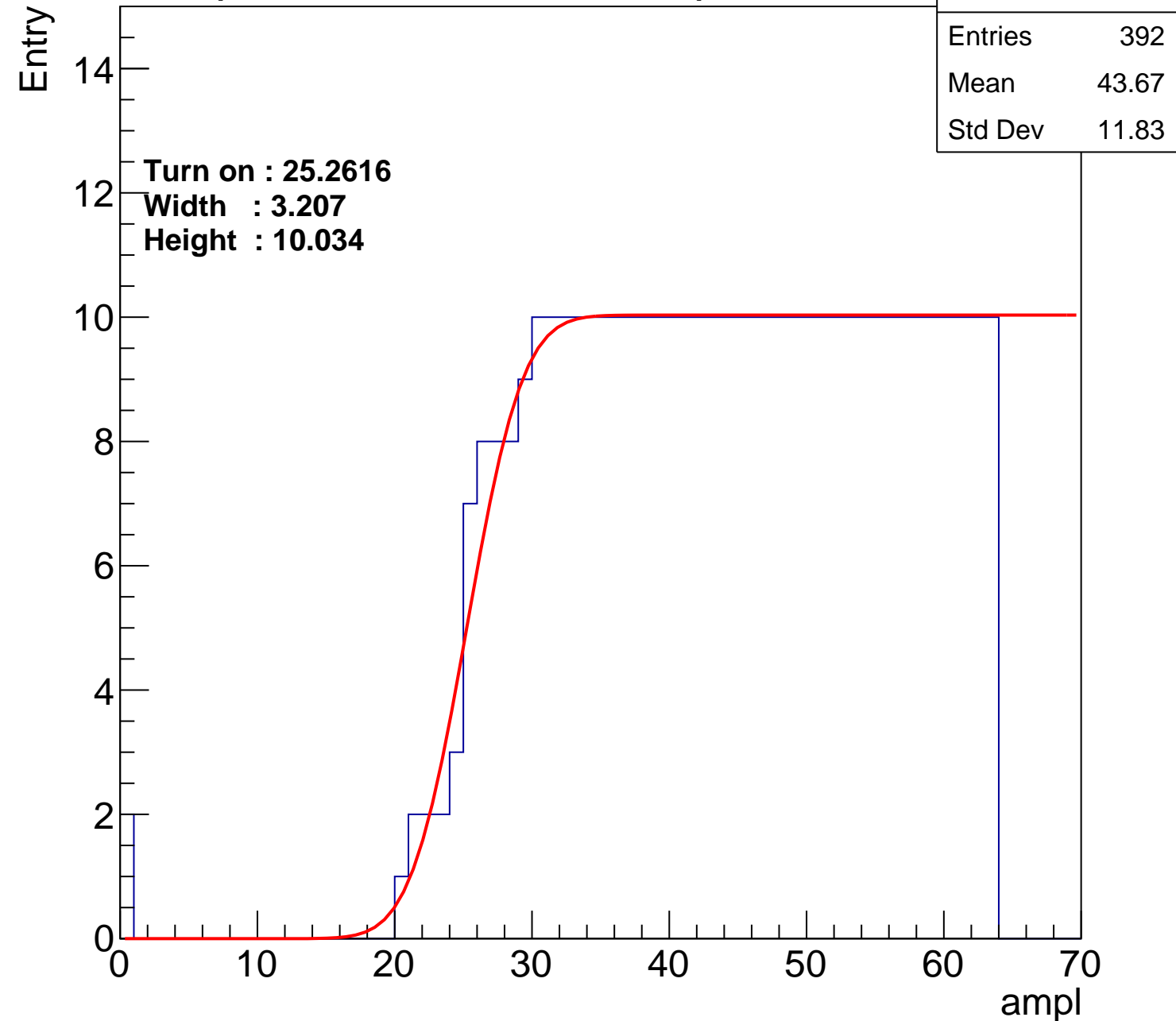
Width : 3.207

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch39

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.16 |
| Std Dev | 11.52 |

Turn on : 25.7892

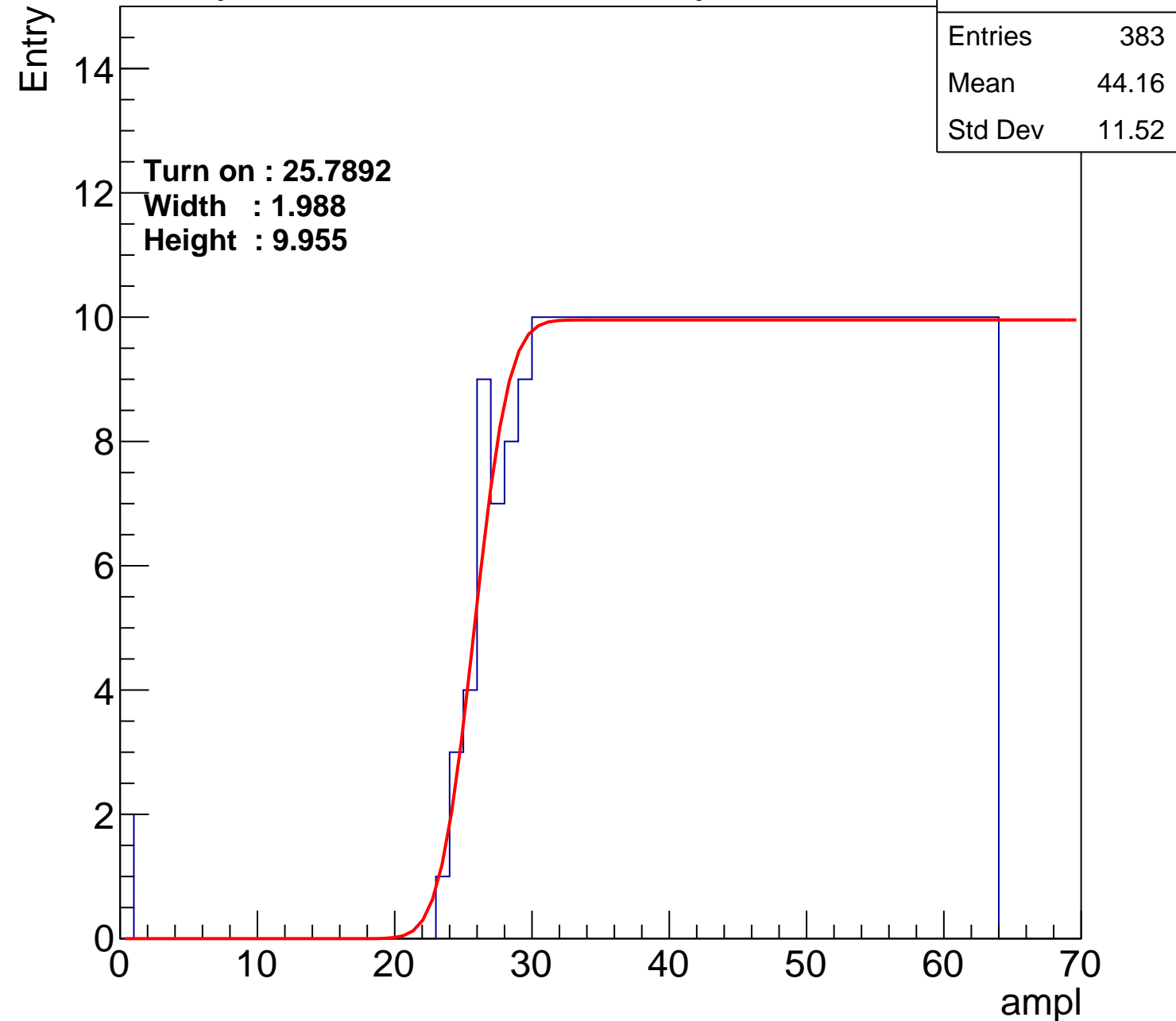
Width : 1.988

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch40

calib_packv5_042523_0143.root, FC#4, port A2

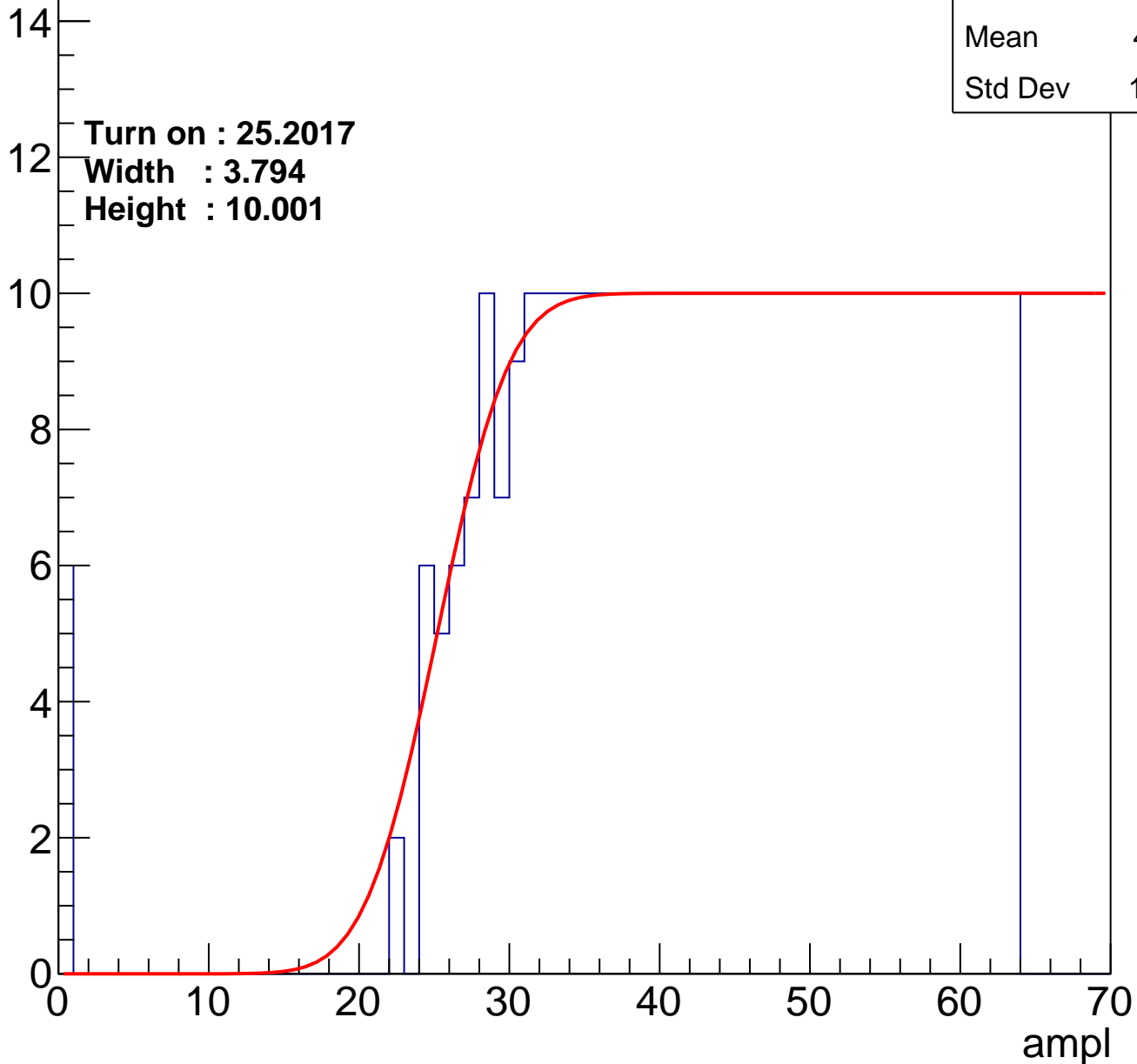
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.61 |
| Std Dev | 12.38 |

Turn on : 25.2017

Width : 3.794

Height : 10.001

Entry



B1L100S, U1-ch41

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.71 |
| Std Dev | 11.6 |

Turn on : 27.9356

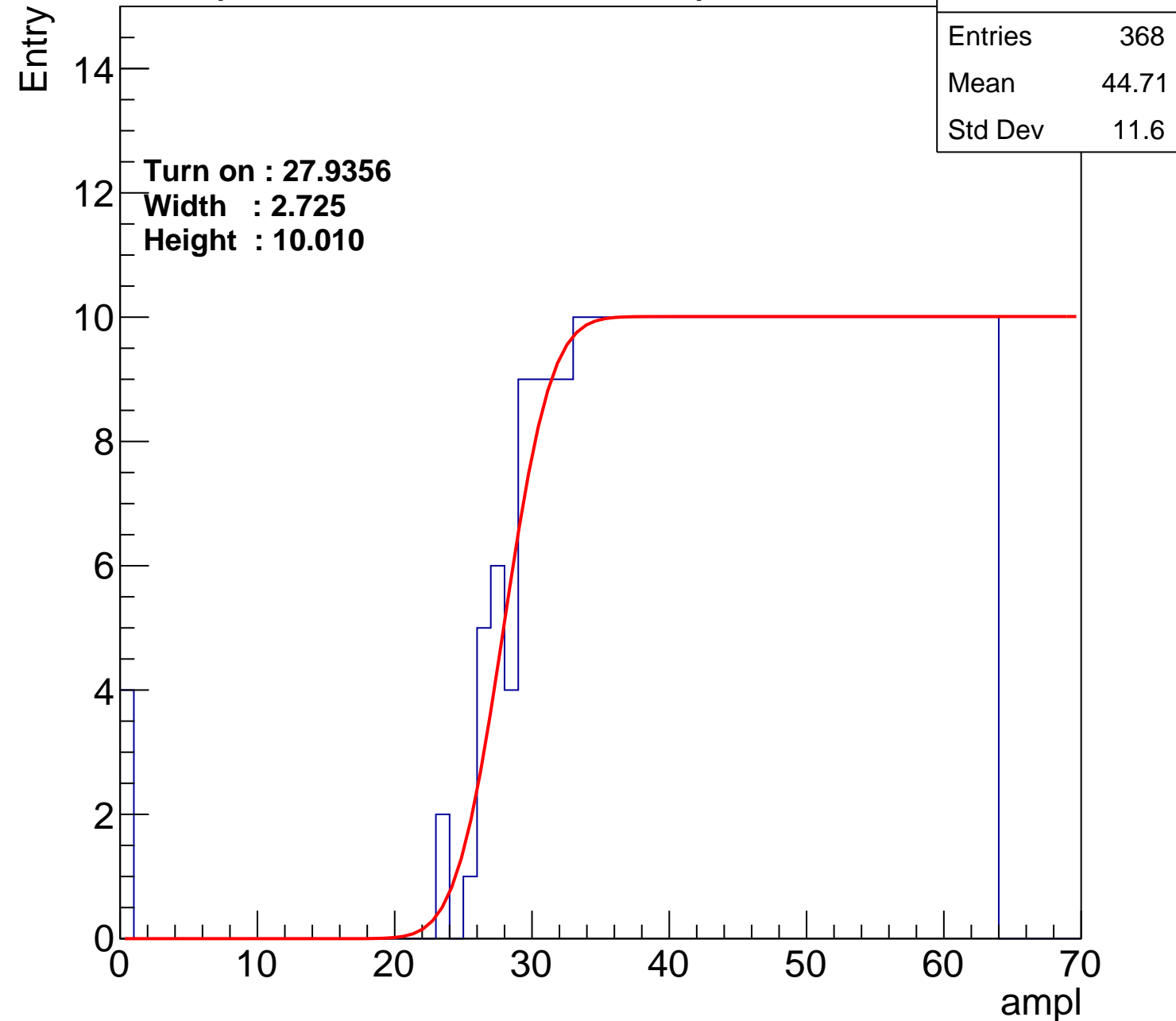
Width : 2.725

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch42

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.63 |
| Std Dev | 12.41 |

Turn on : 26.6030

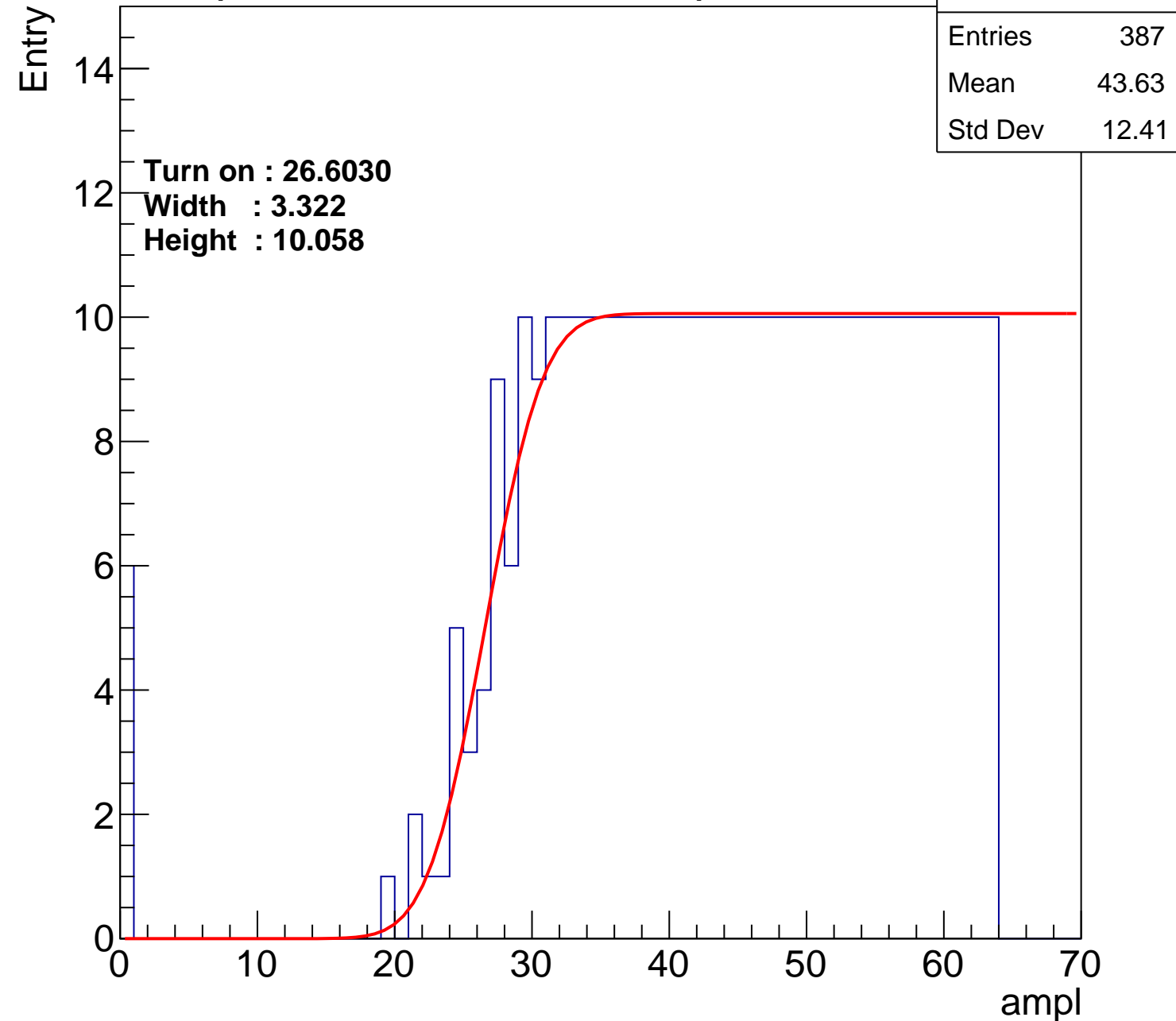
Width : 3.322

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch43

calib_packv5_042523_0143.root, FC#4, port A2

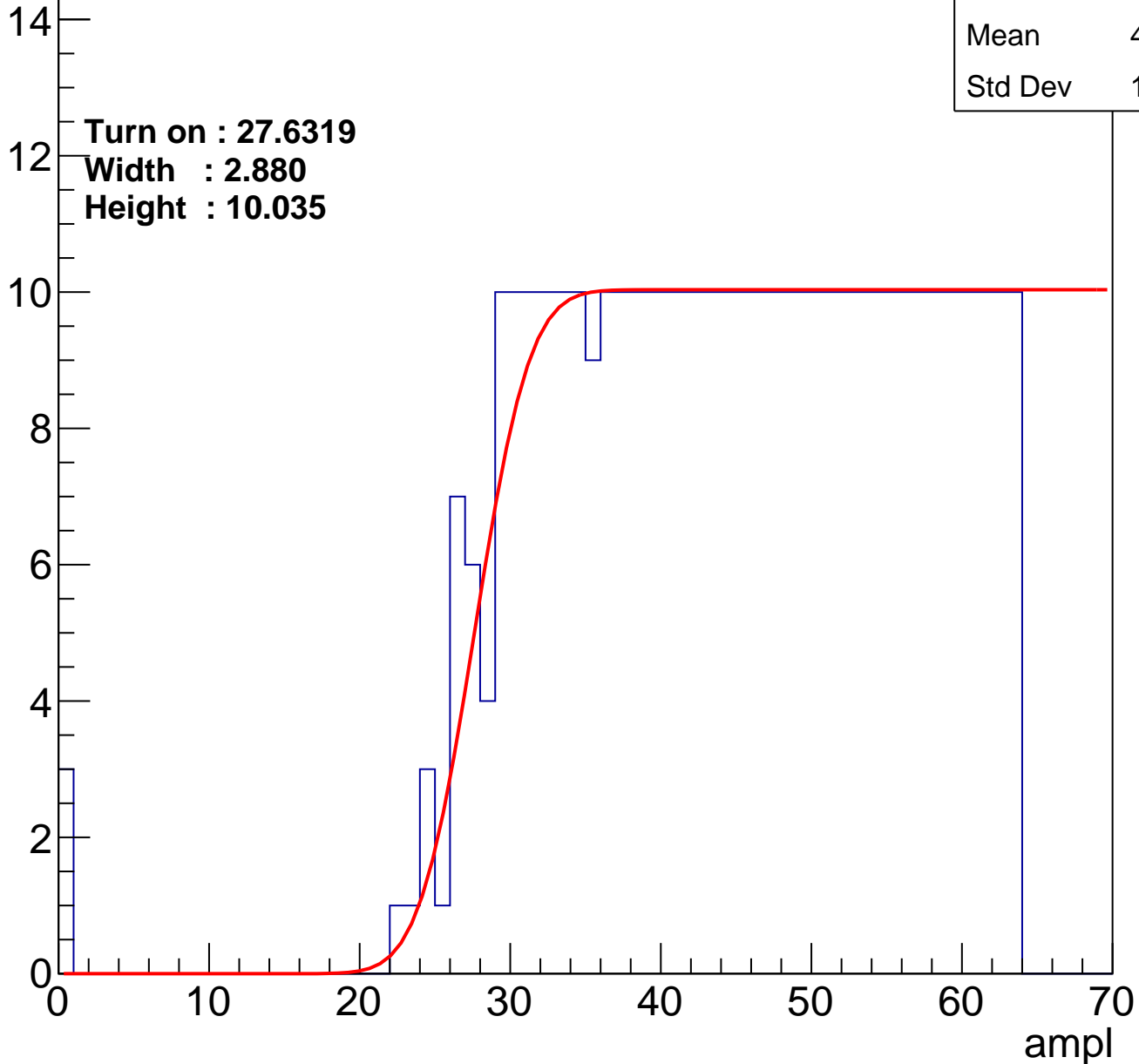
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.43 |
| Std Dev | 11.58 |

Turn on : 27.6319

Width : 2.880

Height : 10.035

Entry



B1L100S, U1-ch44

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.24 |
| Std Dev | 11.92 |

Turn on : 24.0513

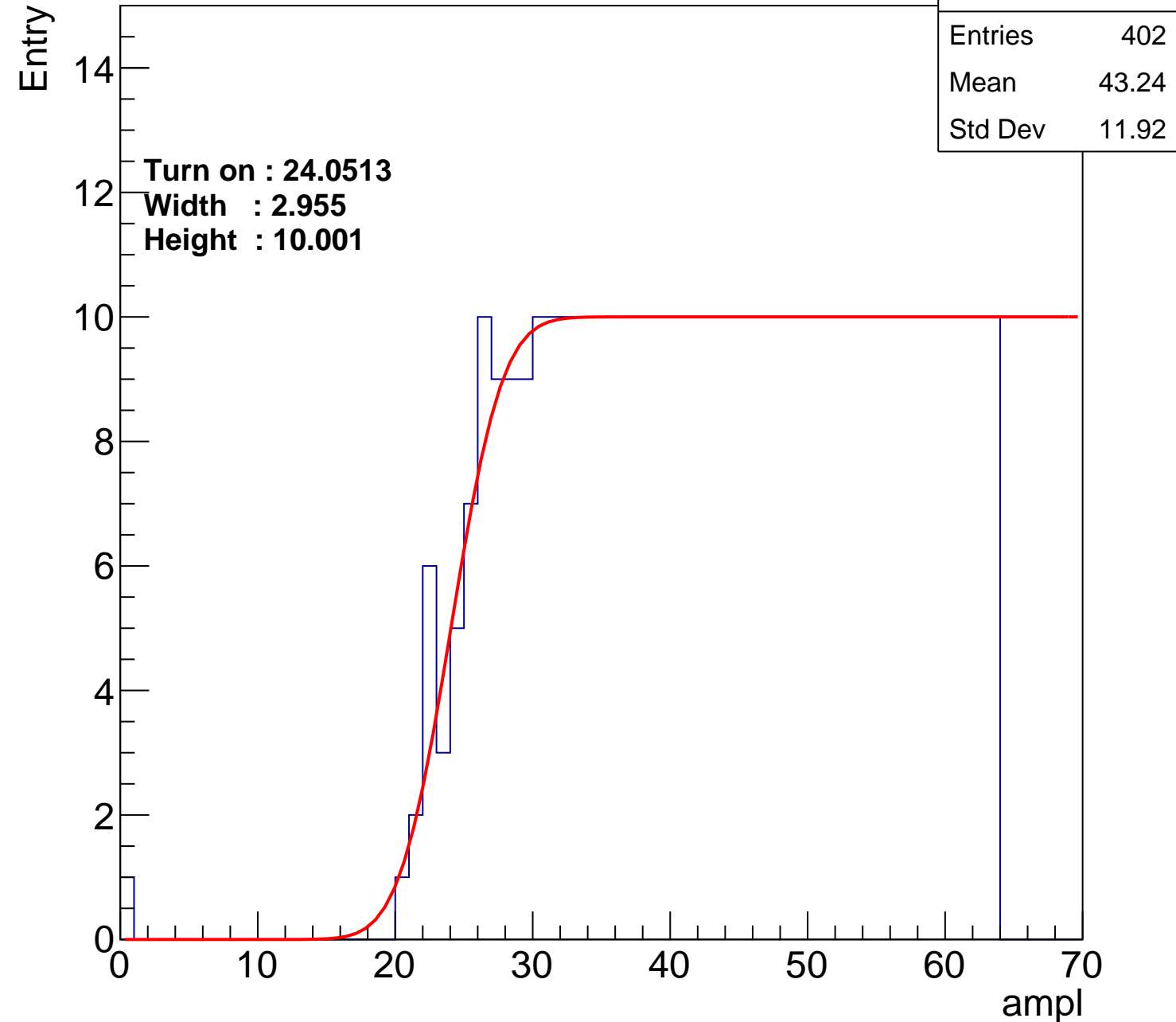
Width : 2.955

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch45

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.49 |
| Std Dev | 11.96 |

Turn on : 24.8079

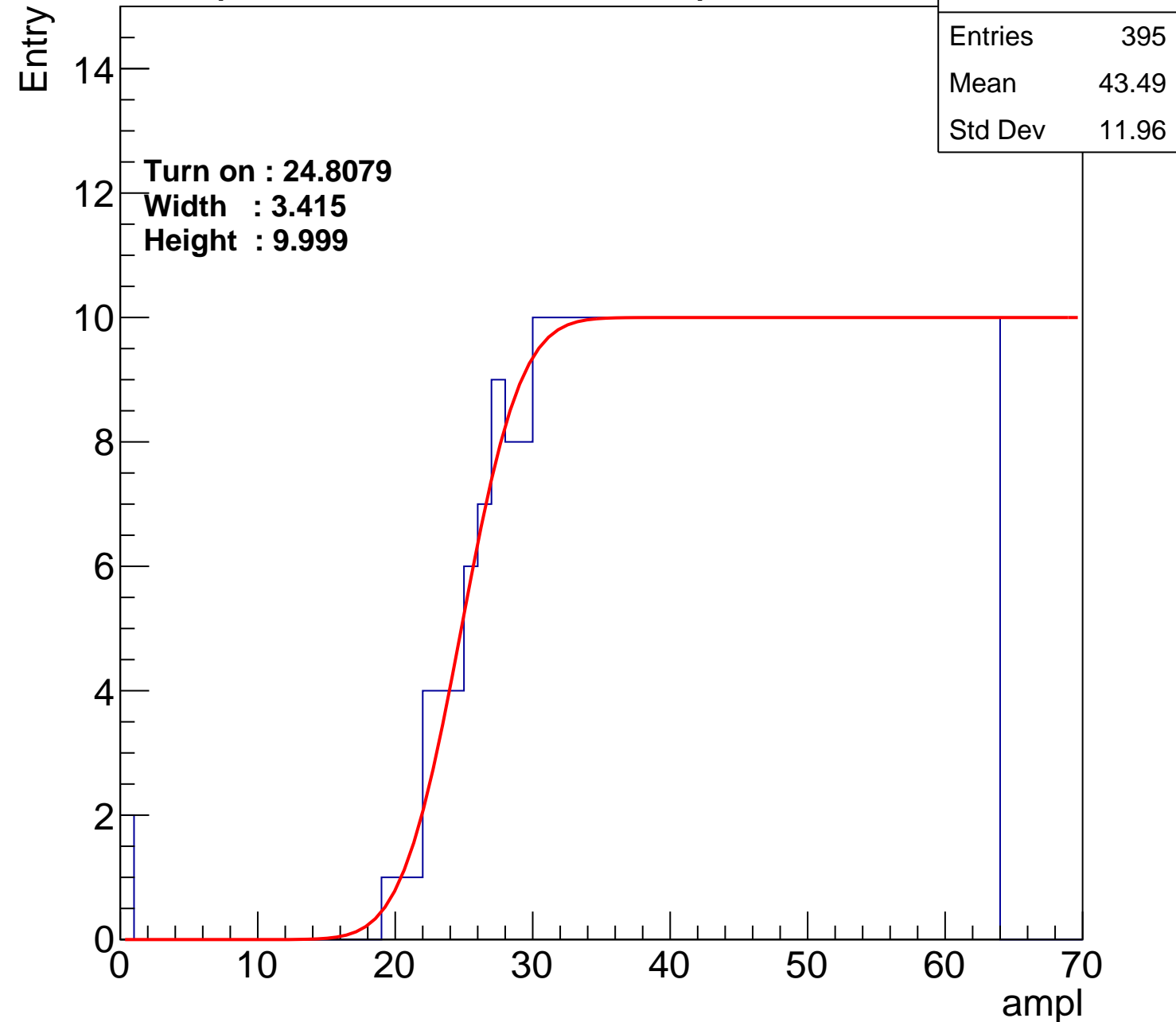
Width : 3.415

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch46

calib_packv5_042523_0143.root, FC#4, port A2

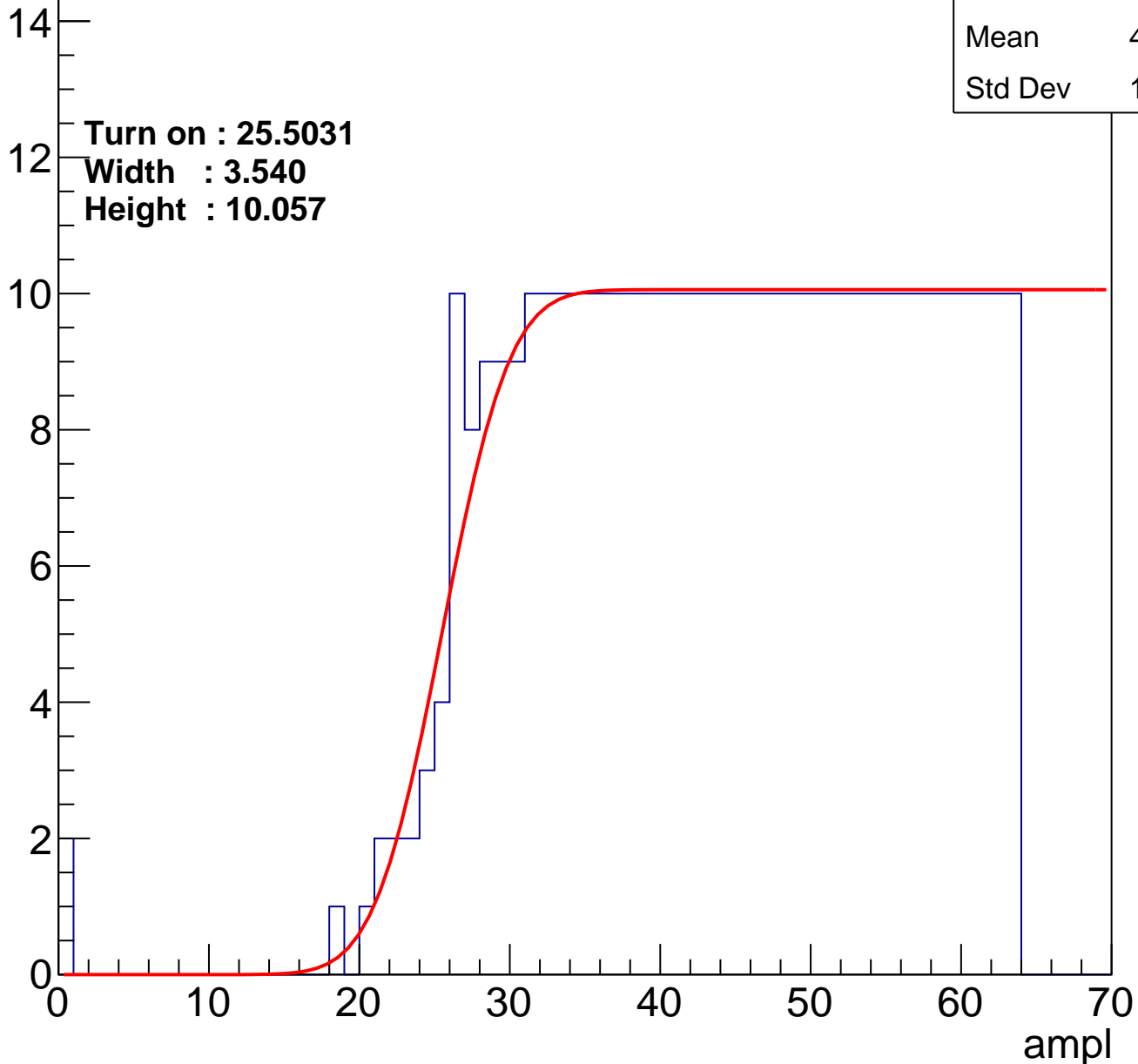
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.65 |
| Std Dev | 11.86 |

Turn on : 25.5031

Width : 3.540

Height : 10.057

Entry



B1L100S, U1-ch47

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.47 |
| Std Dev | 11.45 |

Turn on : 27.2245

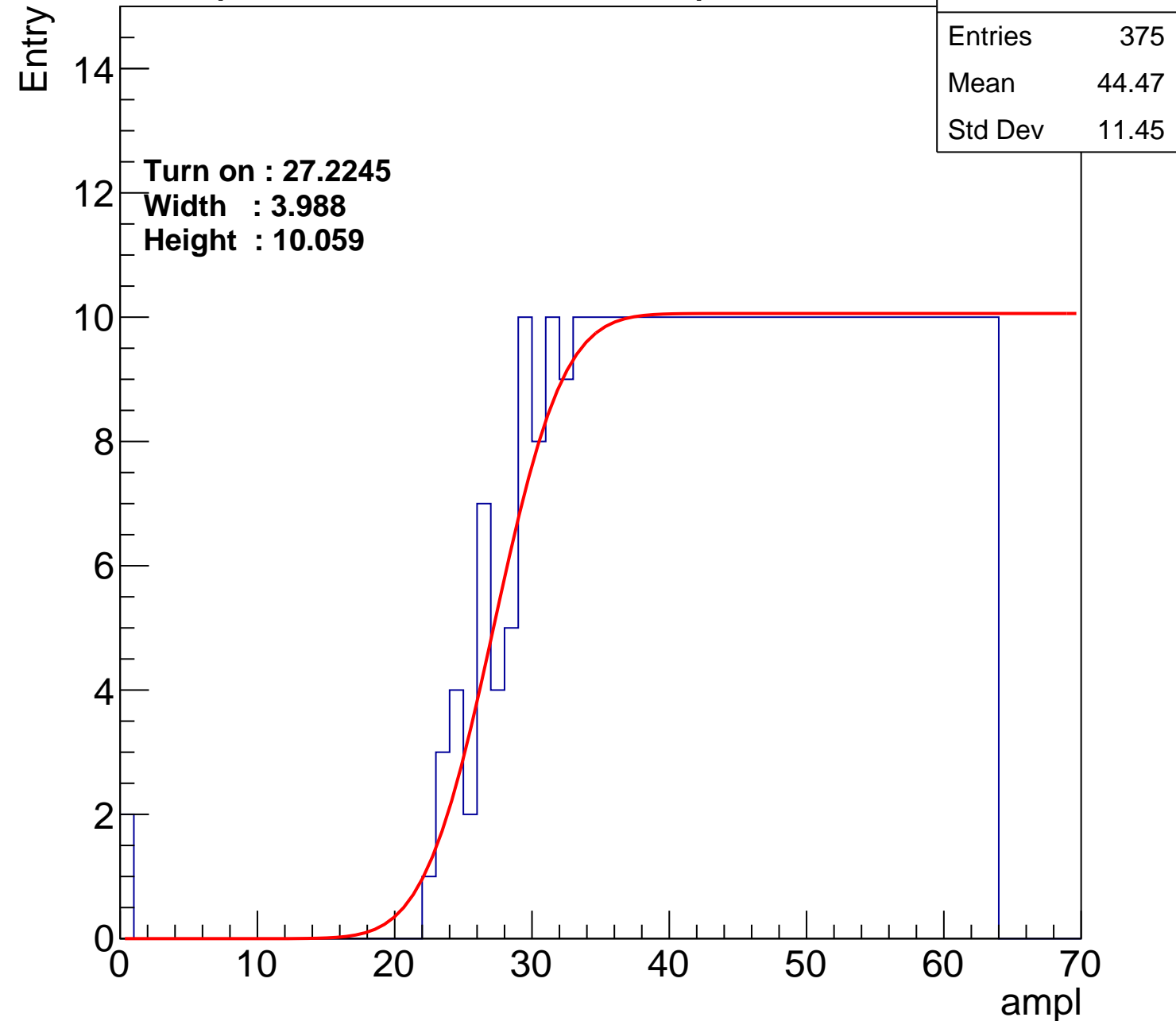
Width : 3.988

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch48

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.12 |
| Std Dev | 11.59 |

Turn on : 25.9774

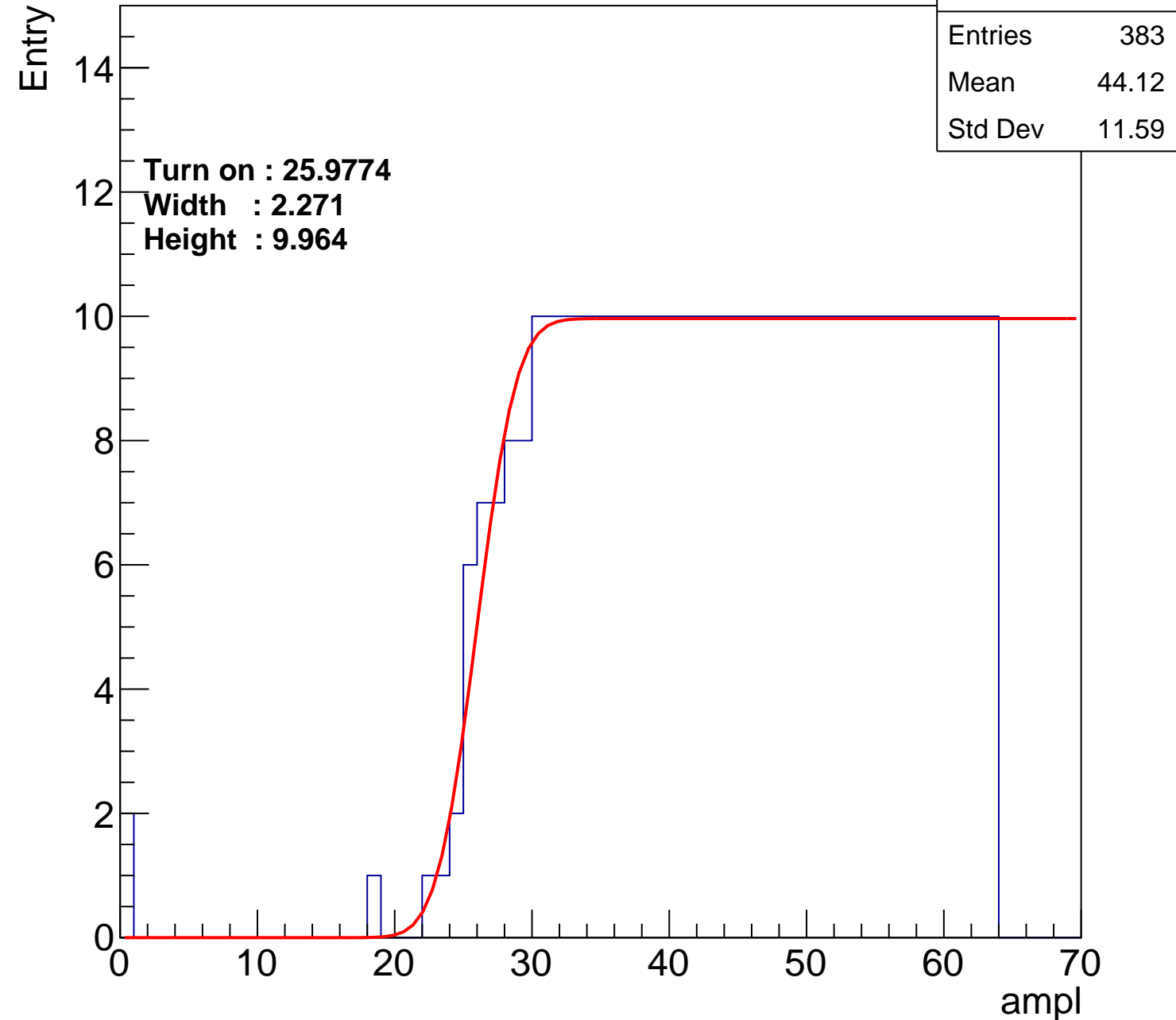
Width : 2.271

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch49

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.06 |
| Std Dev | 12.05 |

Turn on : 26.7140

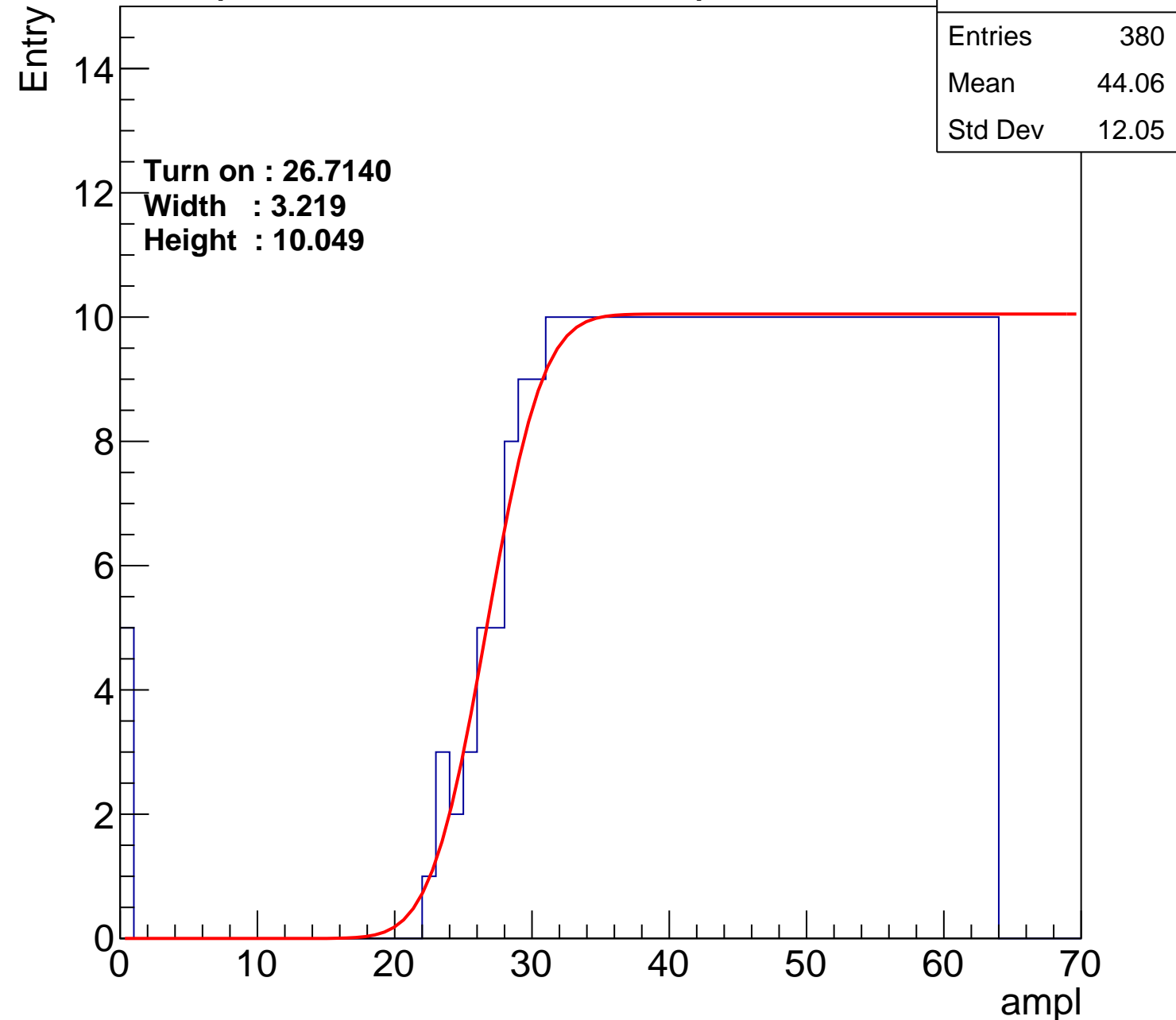
Width : 3.219

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch50

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.08 |
| Std Dev | 12.17 |

Turn on : 27.2204

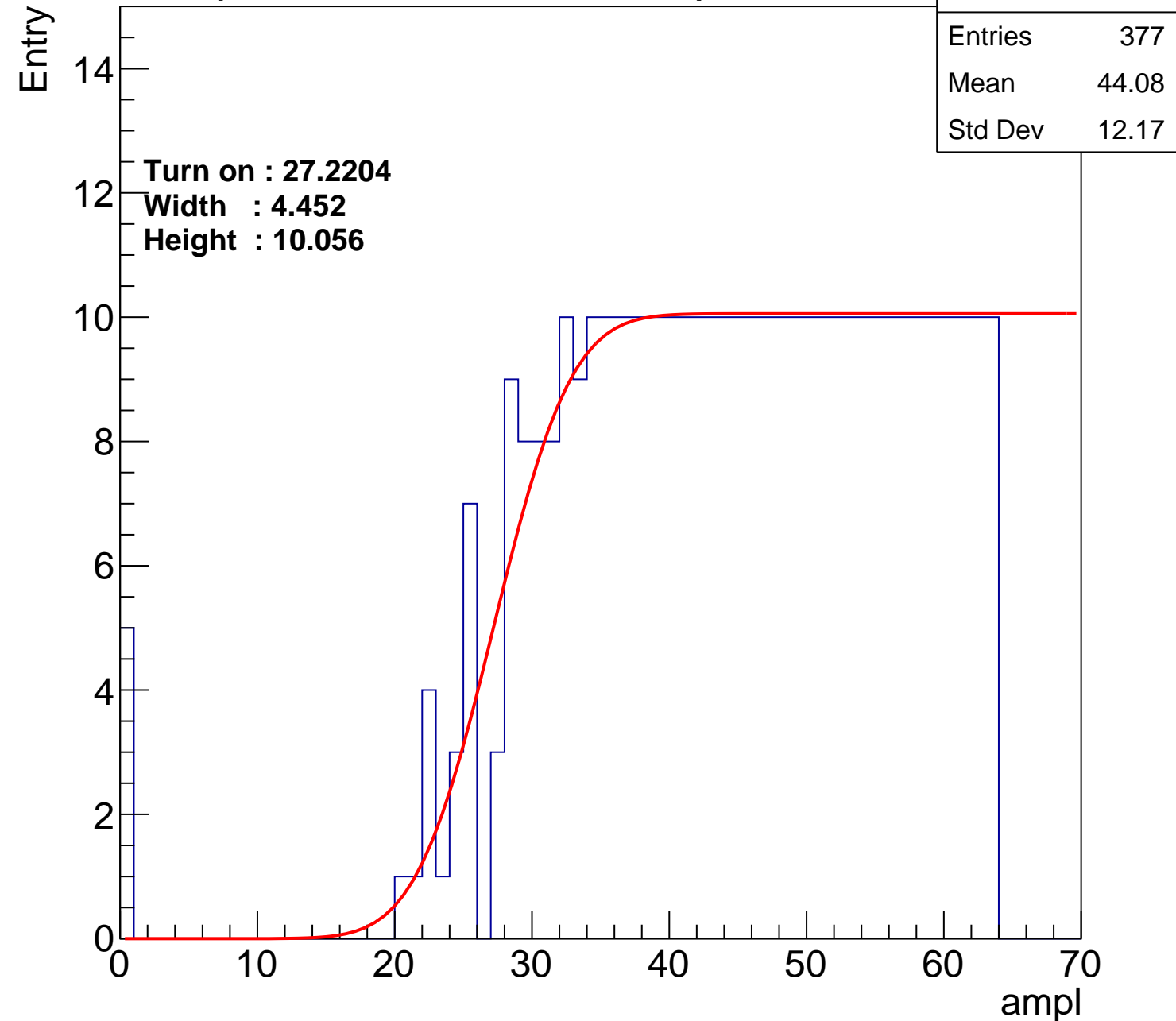
Width : 4.452

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch51

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.63 |
| Std Dev | 12.13 |

Turn on : 25.2252

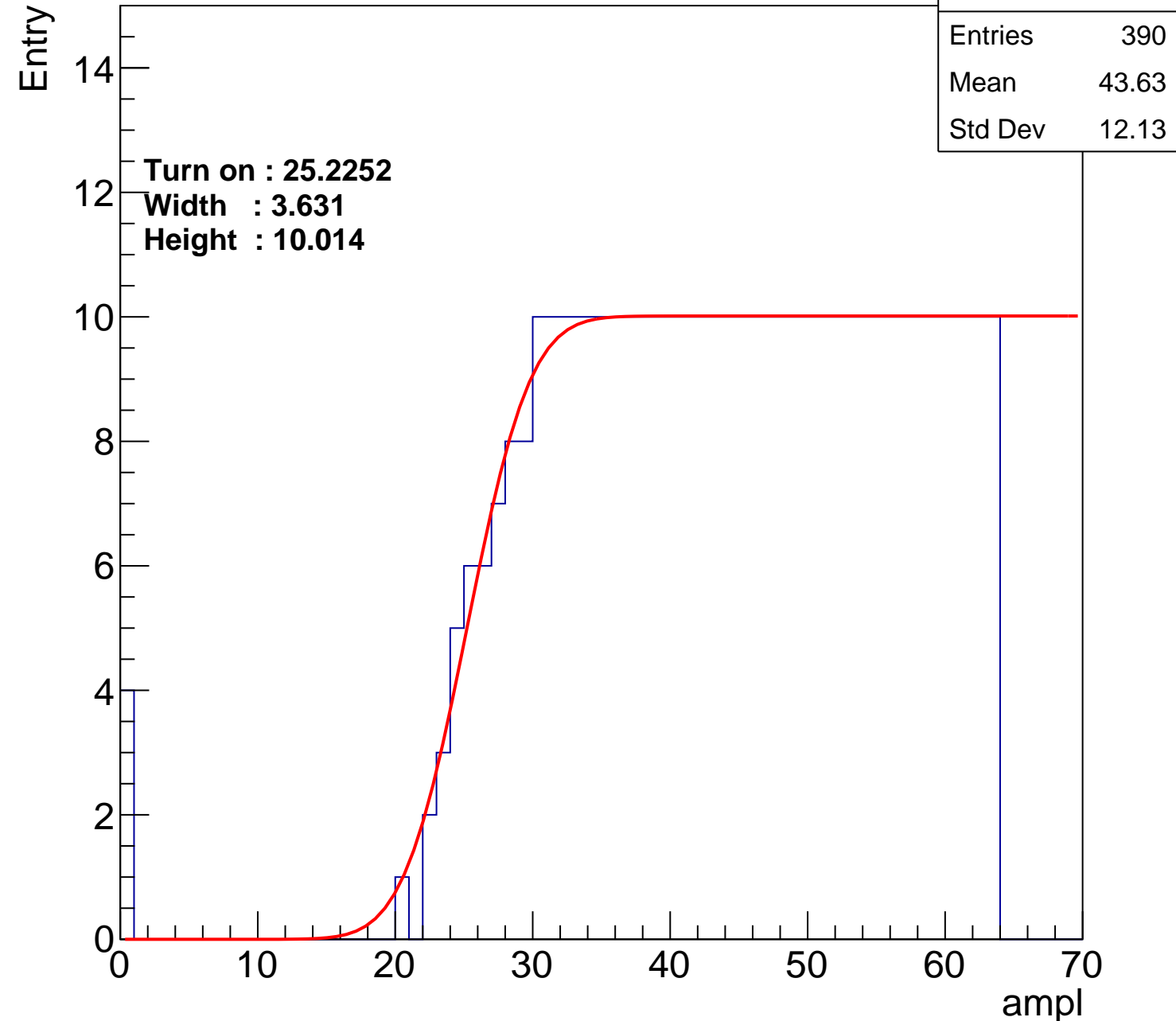
Width : 3.631

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch52

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.25 |
| Std Dev | 11.72 |

Turn on : 26.3787

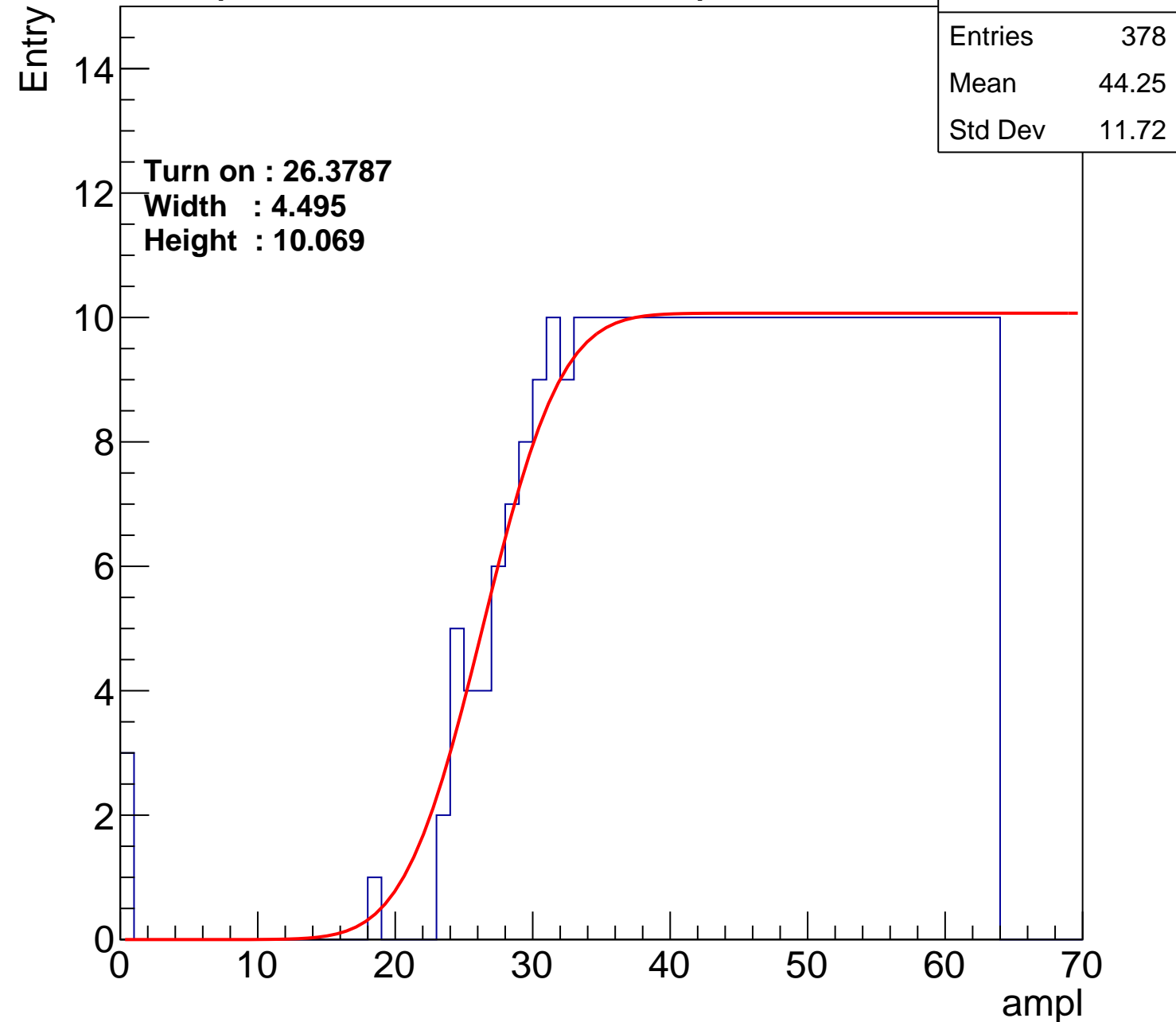
Width : 4.495

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch53

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.68 |
| Std Dev | 11.2 |

Turn on : 27.4700

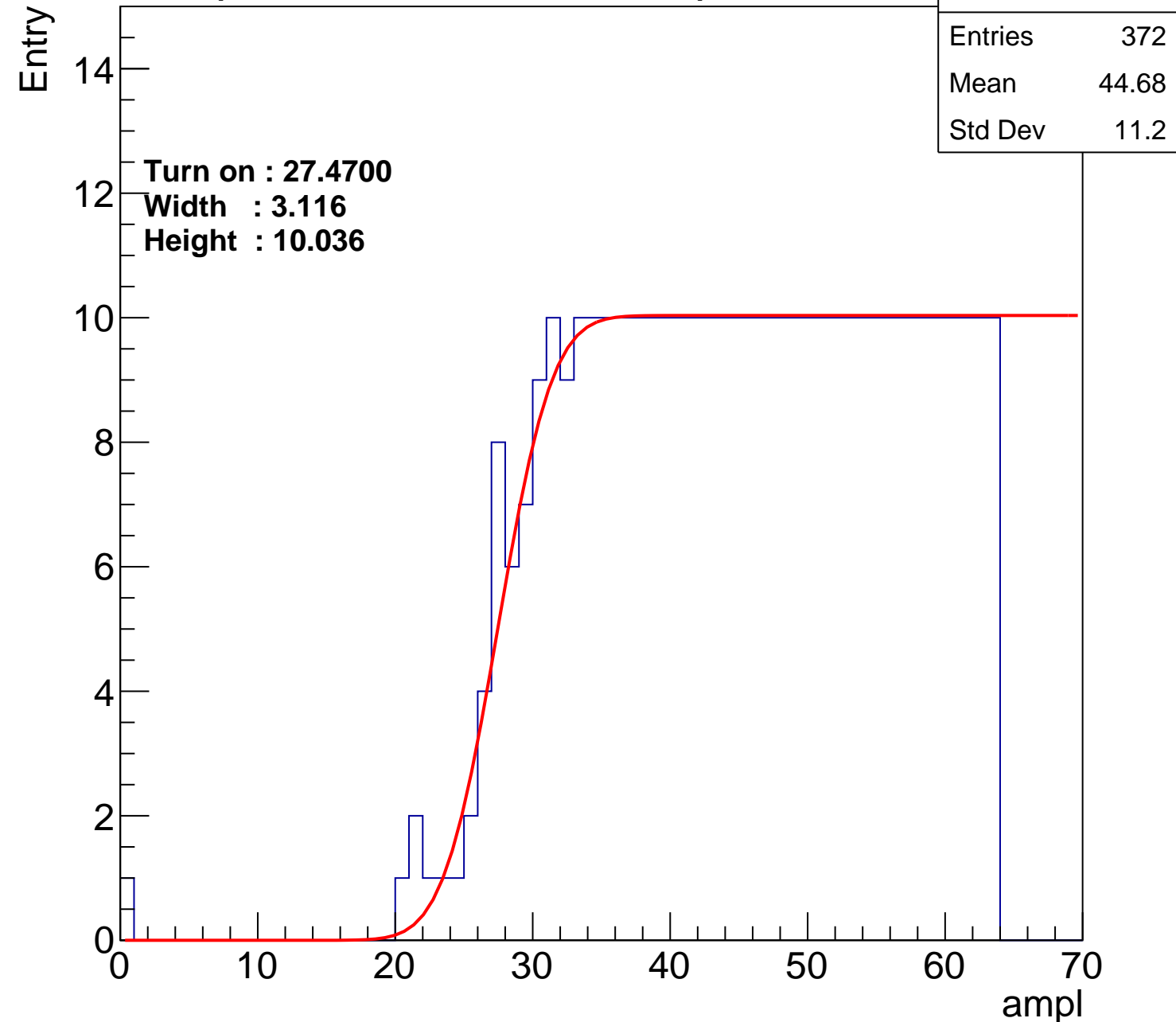
Width : 3.116

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch54

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.35 |
| Std Dev | 11.48 |

Turn on : 26.5592

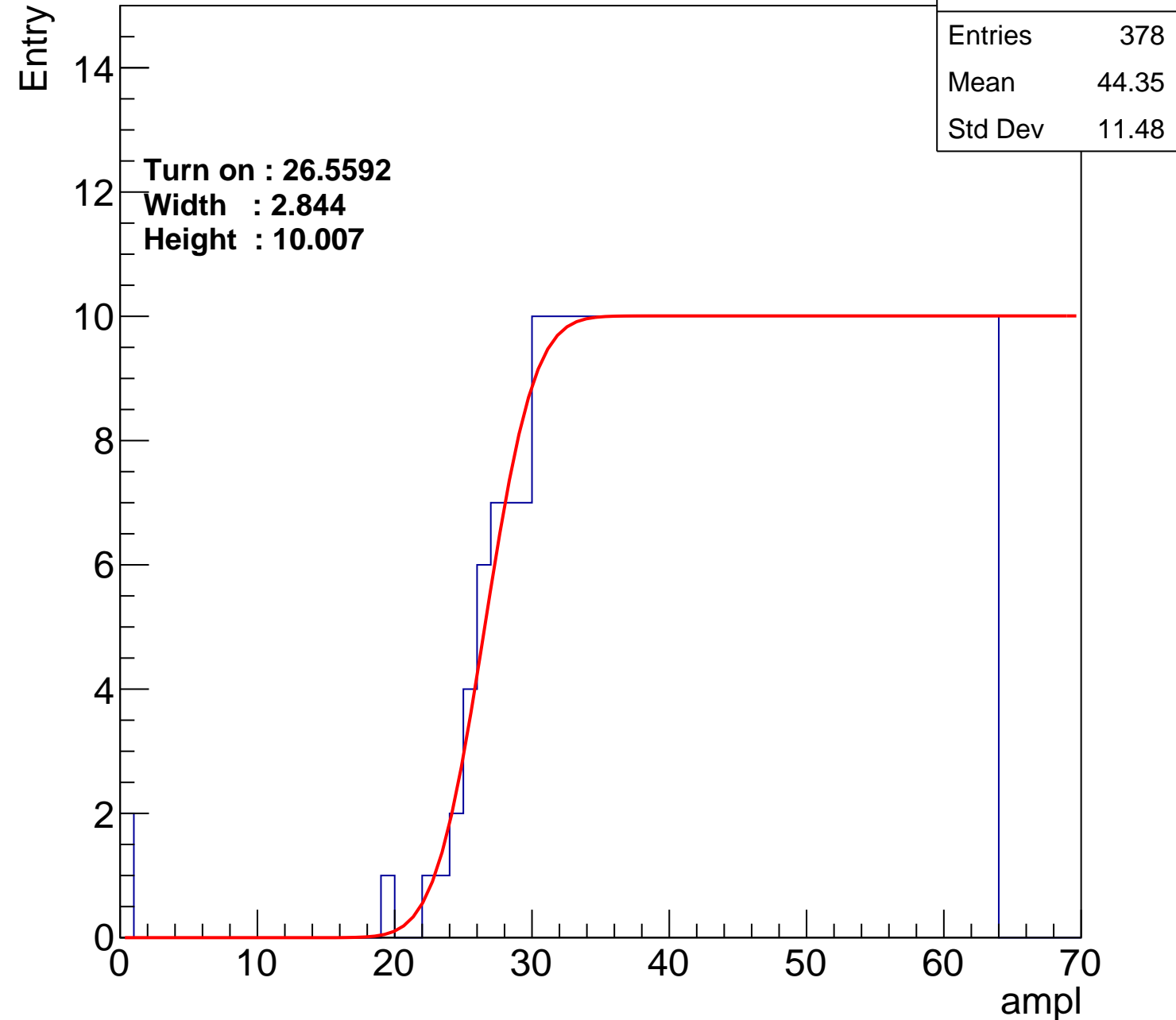
Width : 2.844

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch55

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.81 |
| Std Dev | 11.71 |

Turn on : 25.5324

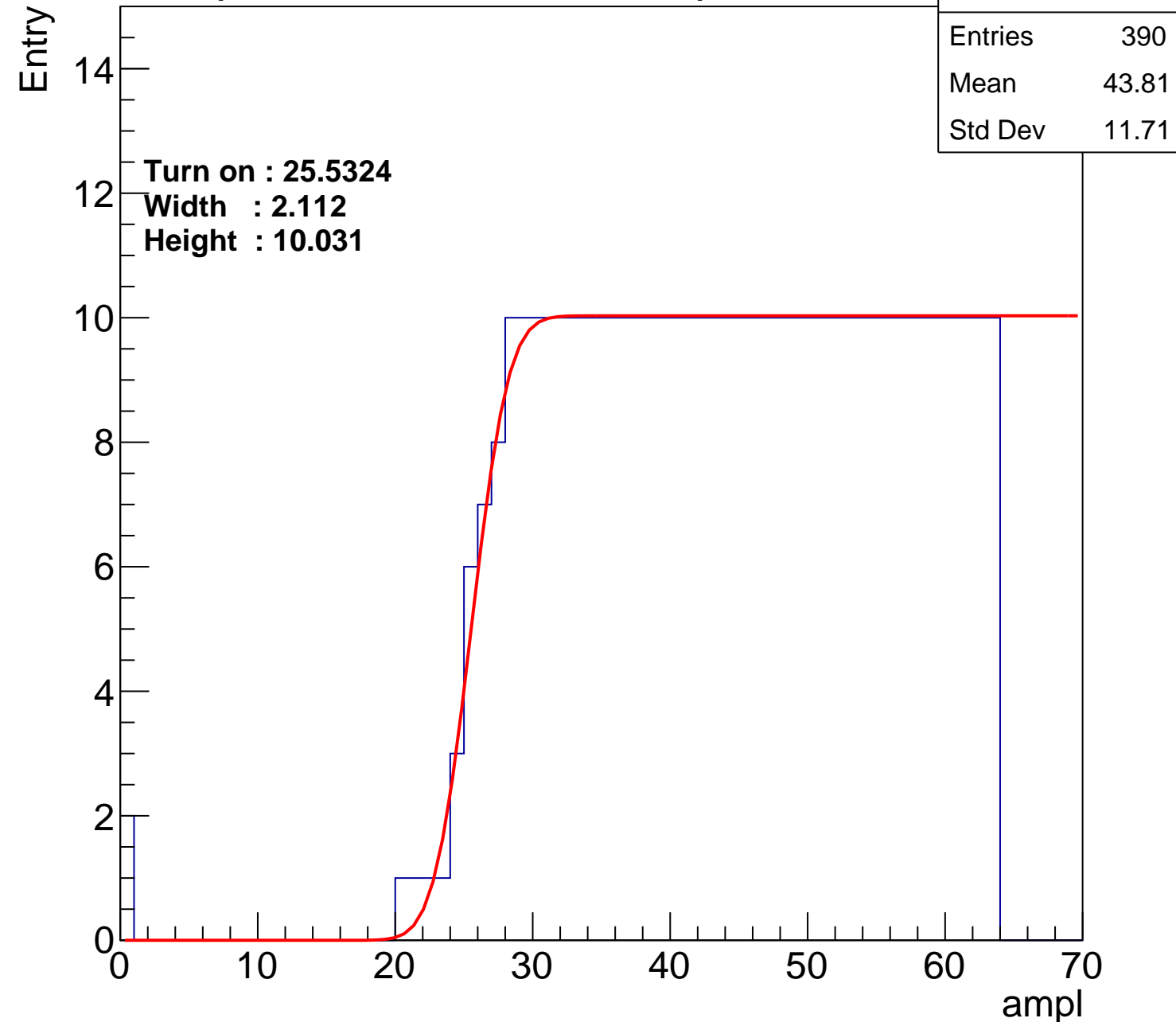
Width : 2.112

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch56

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.54 |
| Std Dev | 11.34 |

Turn on : 26.5490

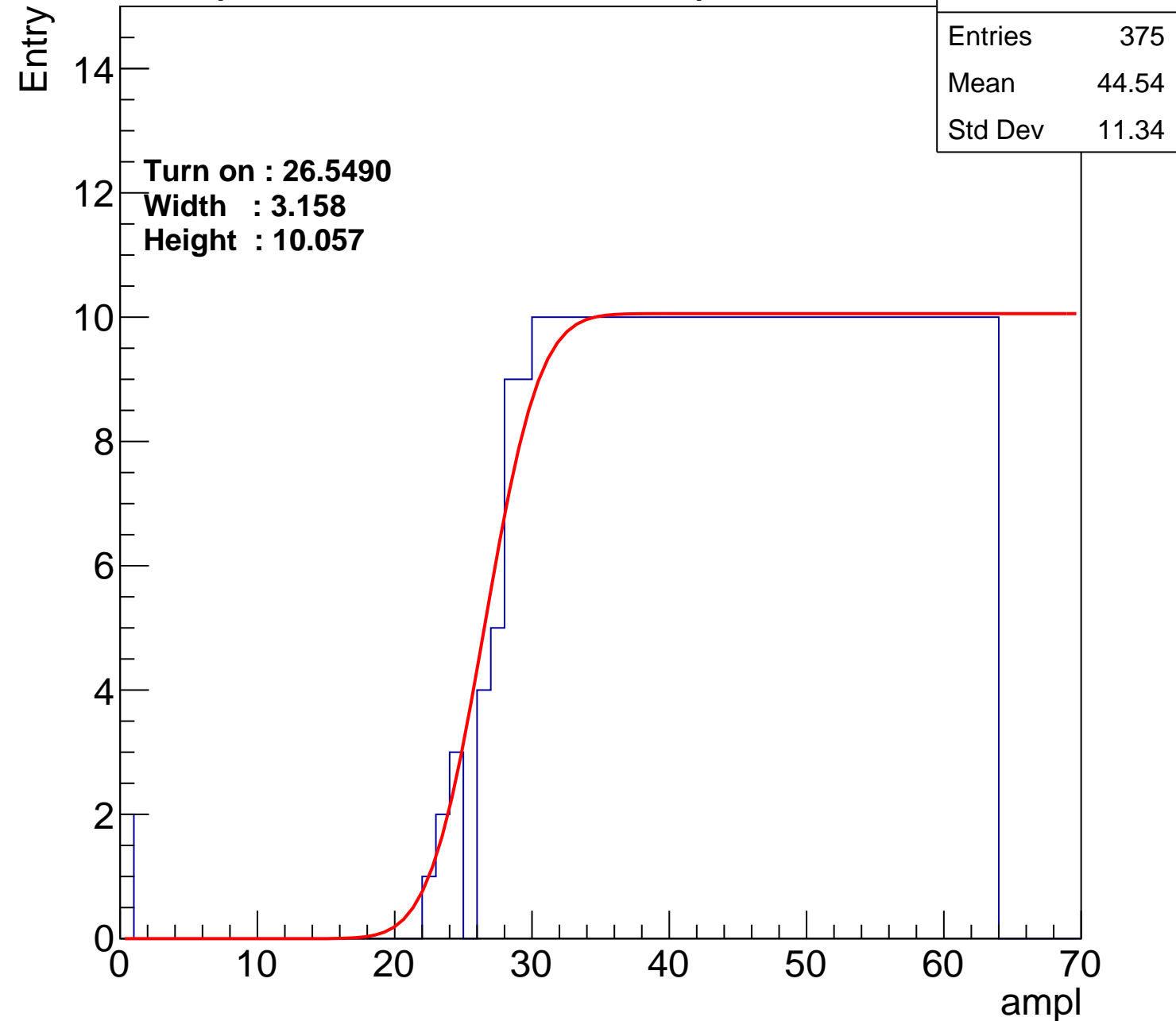
Width : 3.158

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch57

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.95 |
| Std Dev | 11.53 |

Turn on : 25.6914

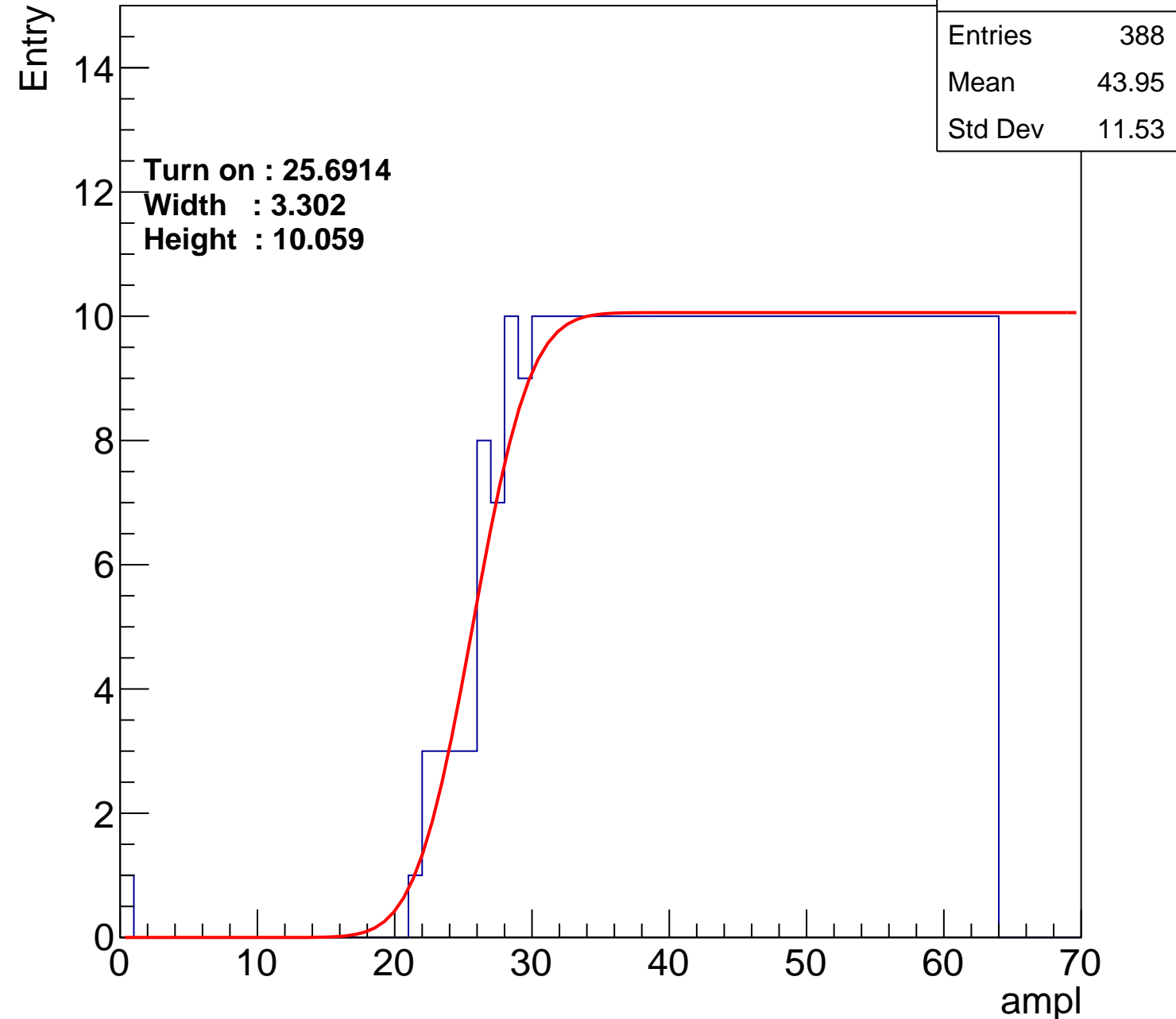
Width : 3.302

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch58

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.31 |
| Std Dev | 12.15 |

Turn on : 24.6055

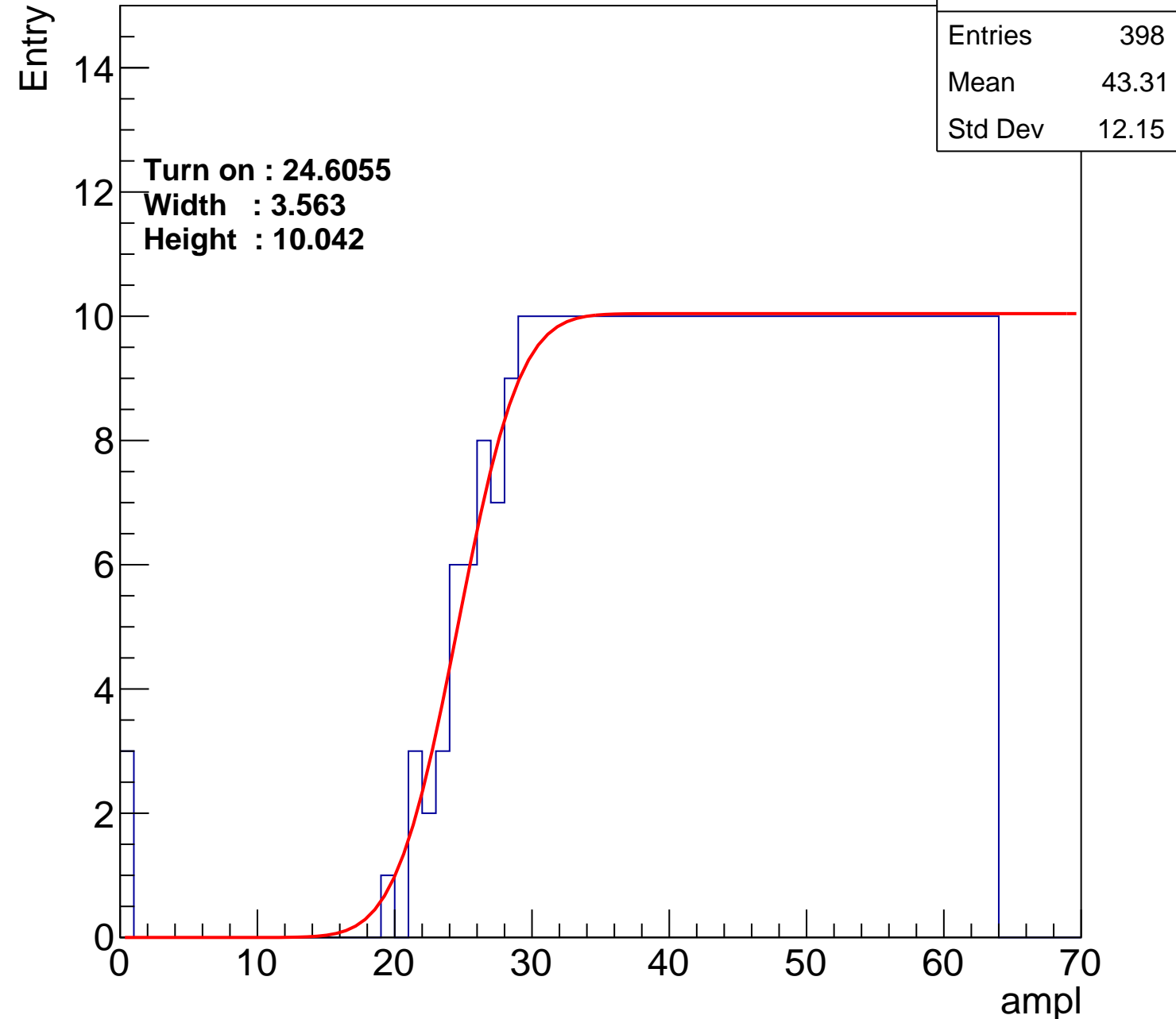
Width : 3.563

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch59

calib_packv5_042523_0143.root, FC#4, port A2

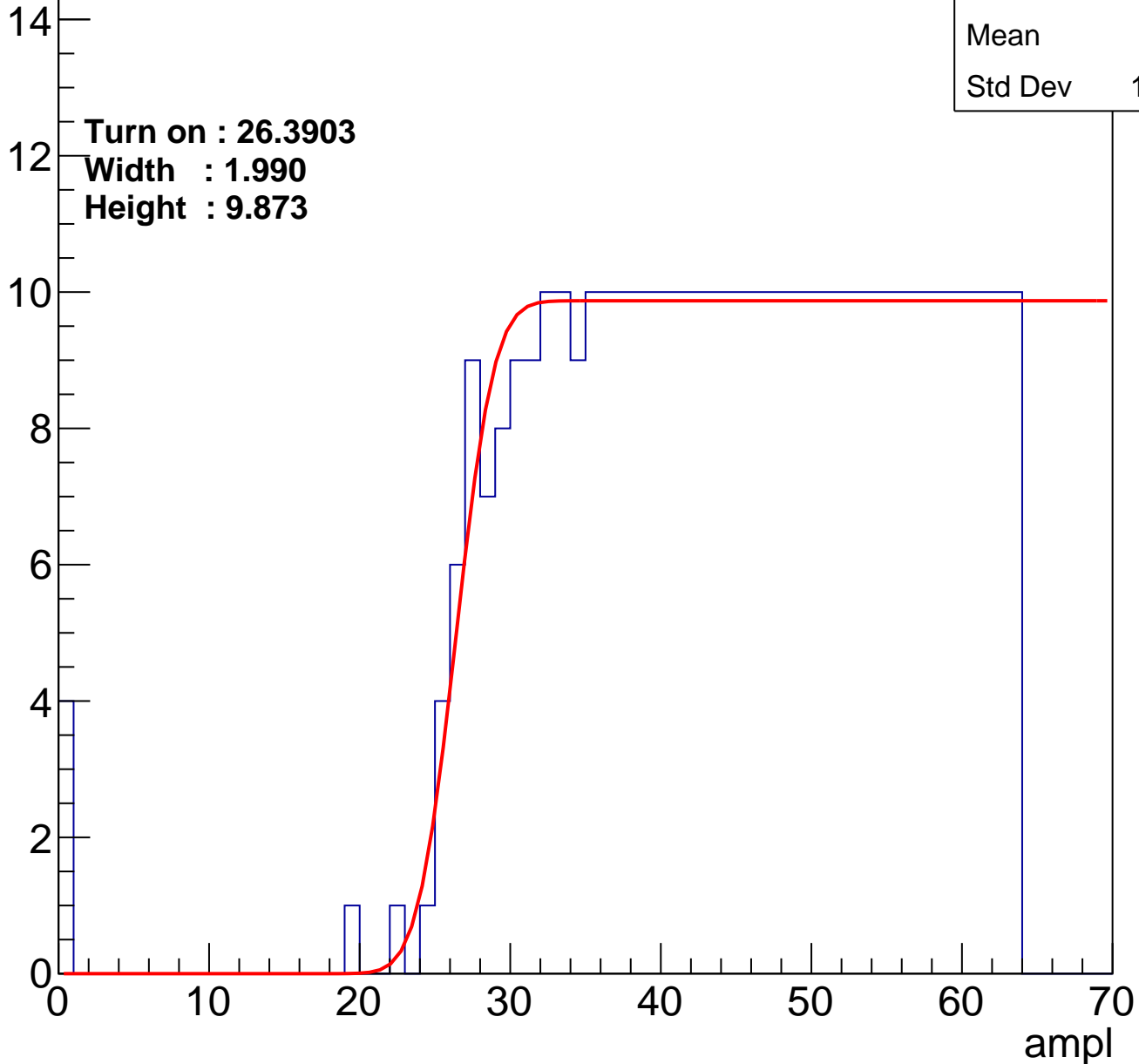
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.2 |
| Std Dev | 11.86 |

Turn on : 26.3903

Width : 1.990

Height : 9.873

Entry



B1L100S, U1-ch60

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.04 |
| Std Dev | 11.6 |

Turn on : 25.9520

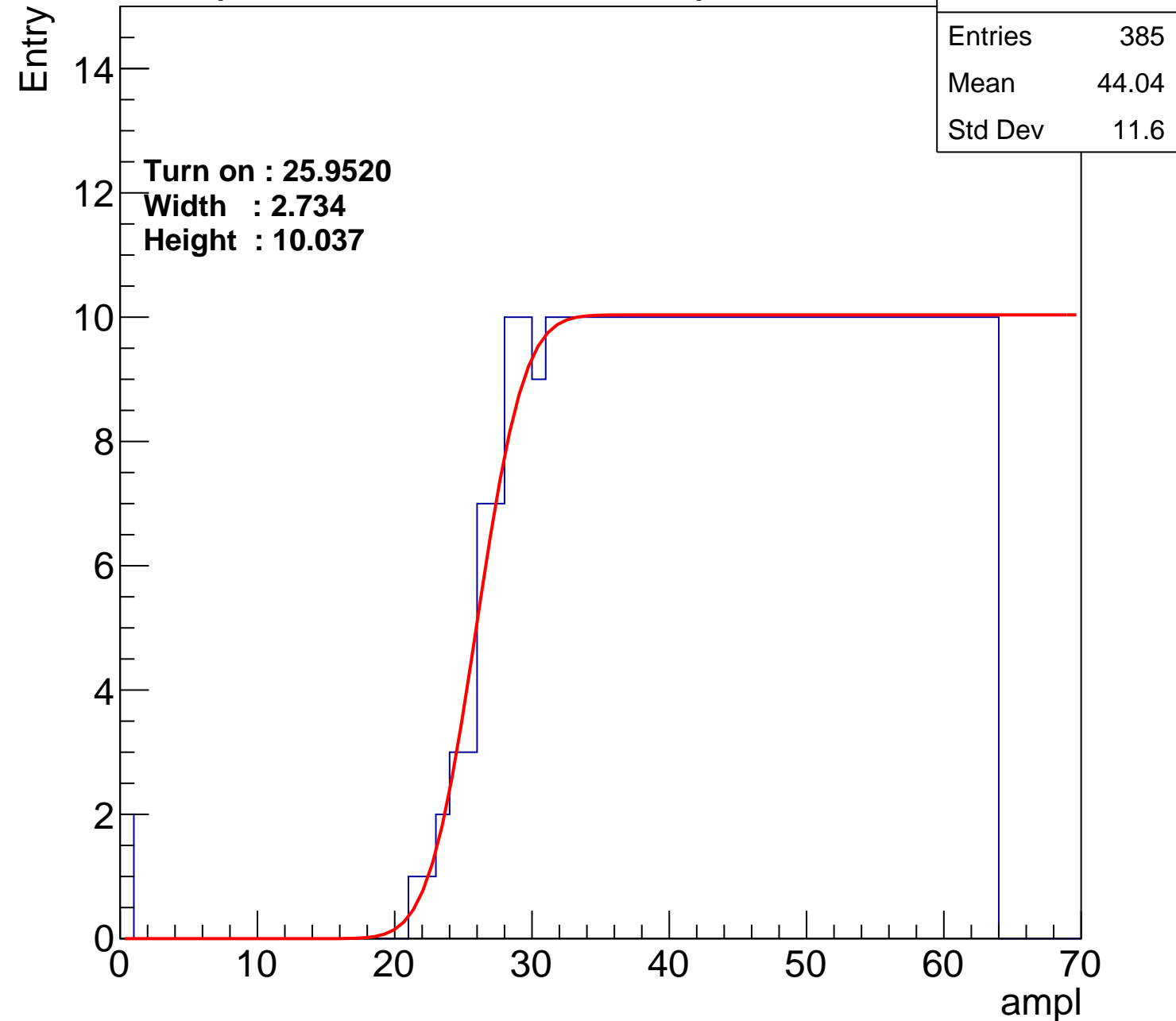
Width : 2.734

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch61

calib_packv5_042523_0143.root, FC#4, port A2

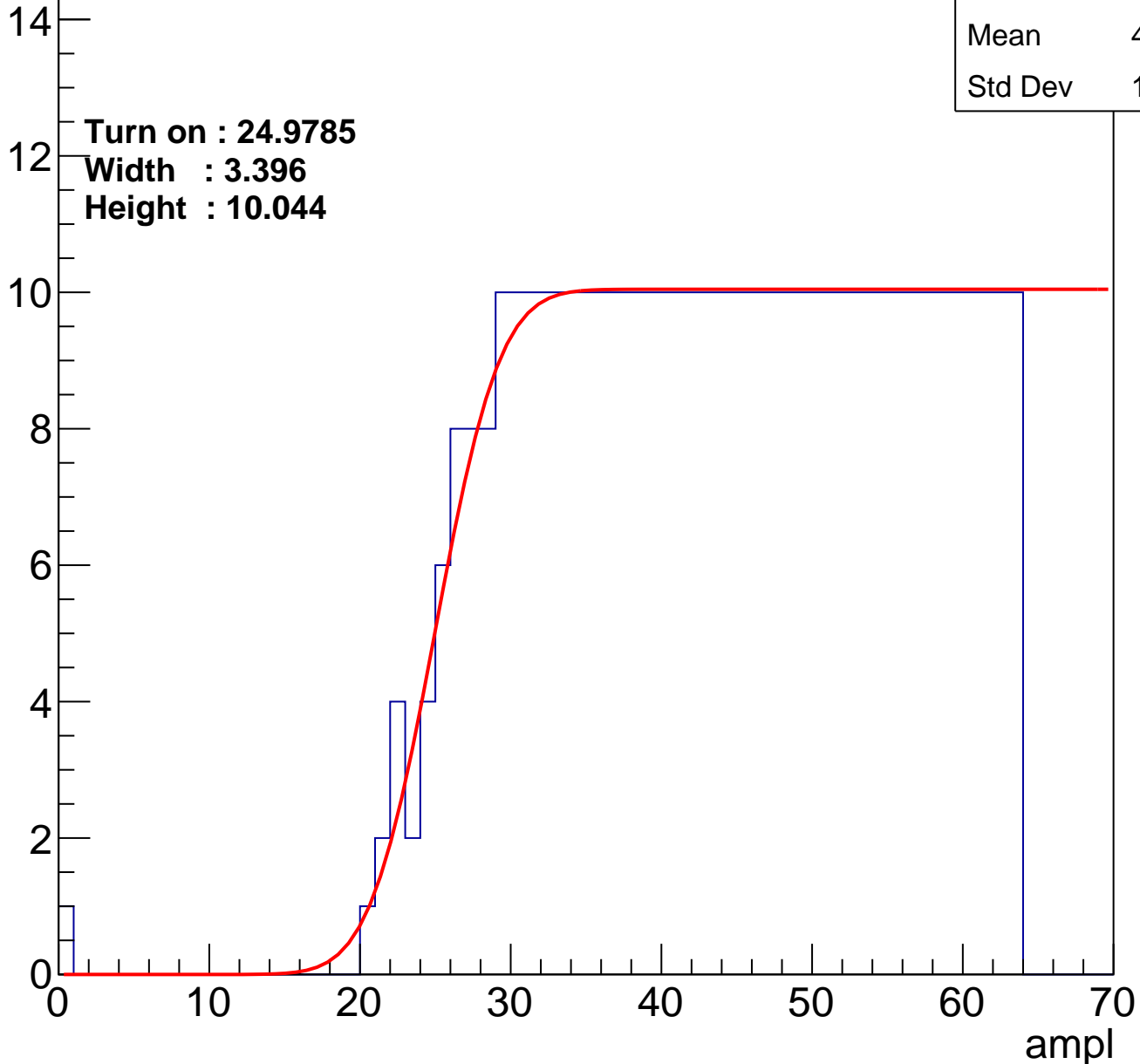
| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.63 |
| Std Dev | 11.72 |

Turn on : 24.9785

Width : 3.396

Height : 10.044

Entry



B1L100S, U1-ch62

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.8 |
| Std Dev | 11.67 |

Turn on : 25.6778

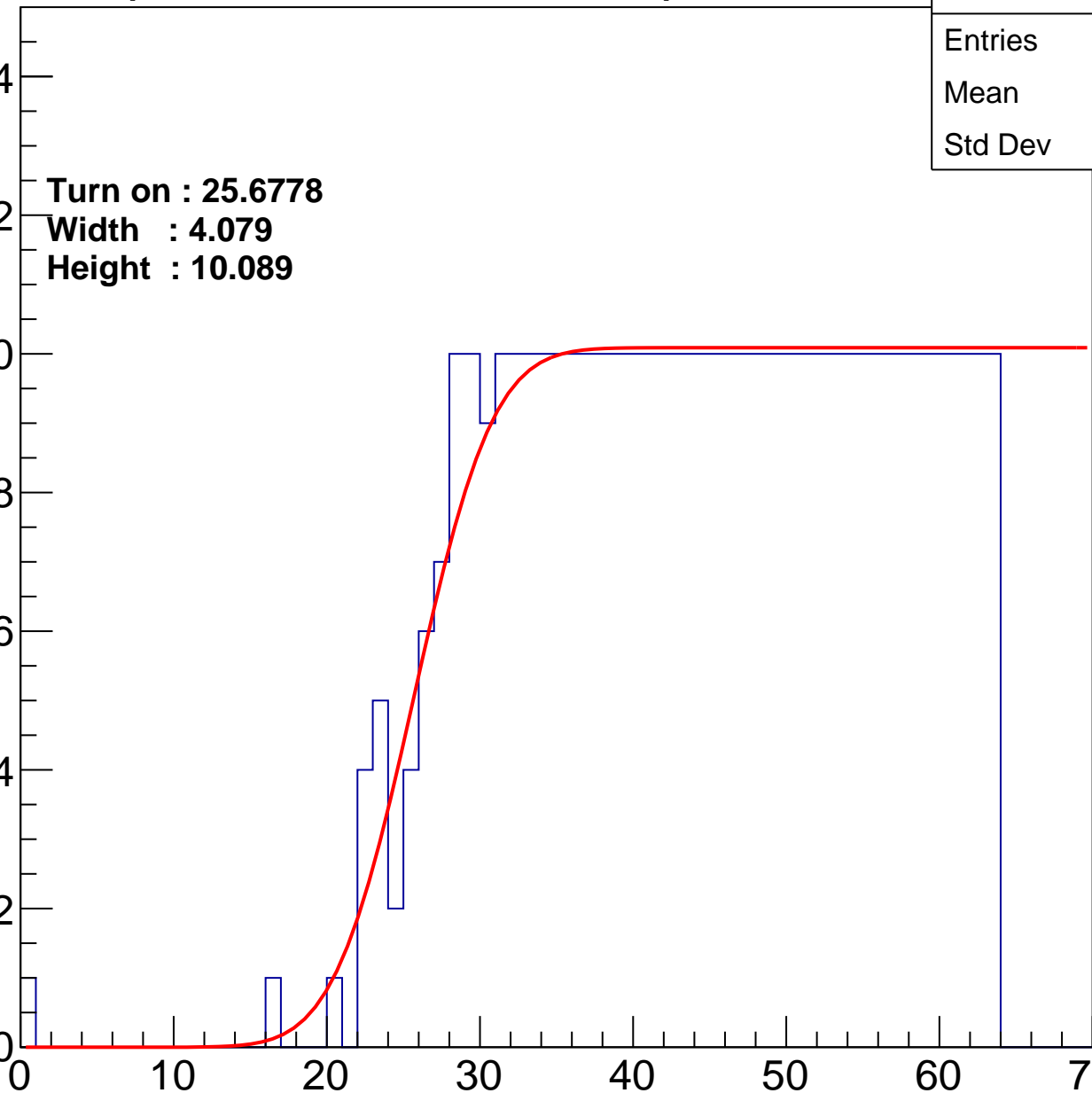
Width : 4.079

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch63

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.64 |
| Std Dev | 11.71 |

Turn on : 24.7380

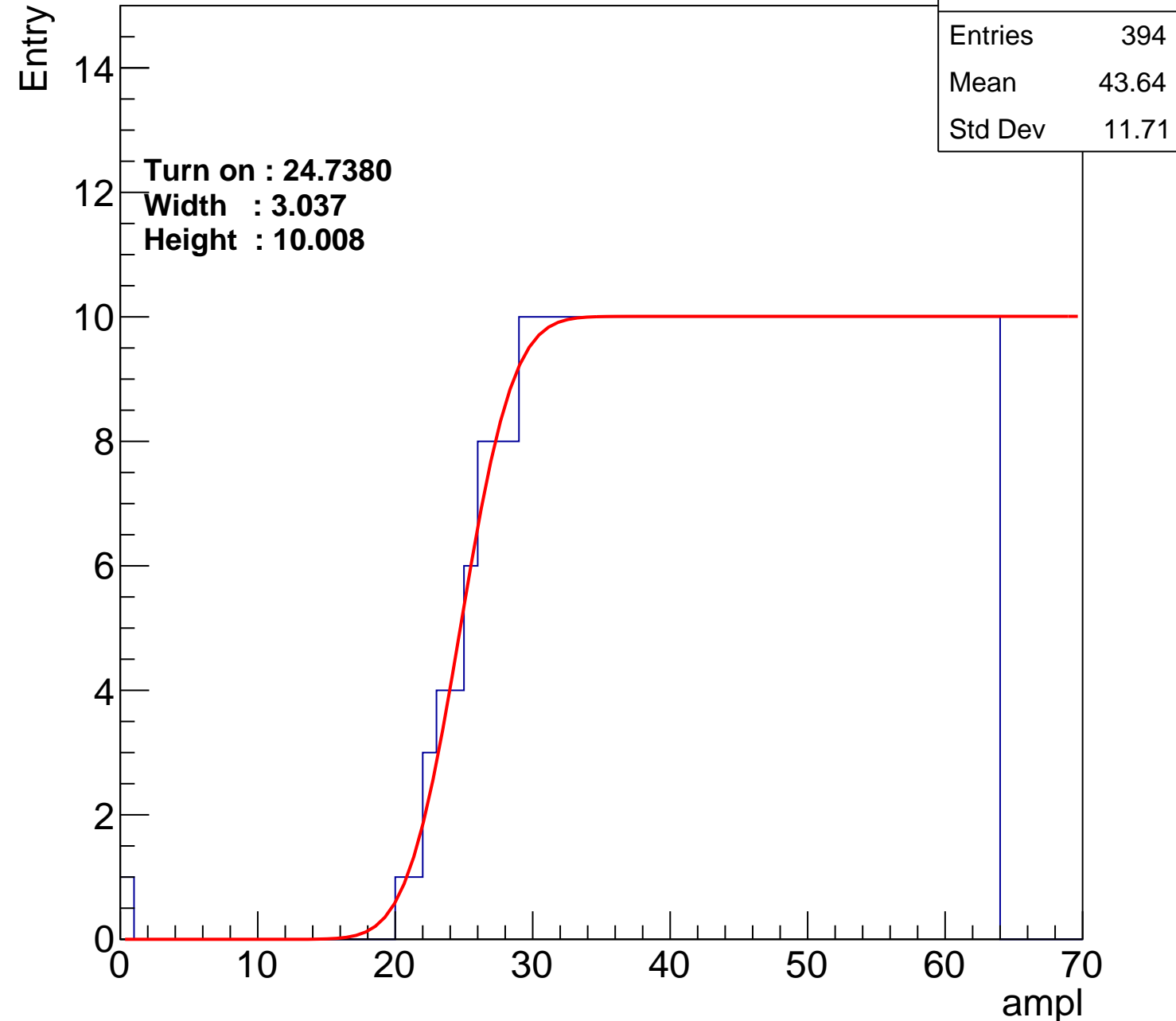
Width : 3.037

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch64

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.45 |
| Std Dev | 12.14 |

Turn on : 25.4878

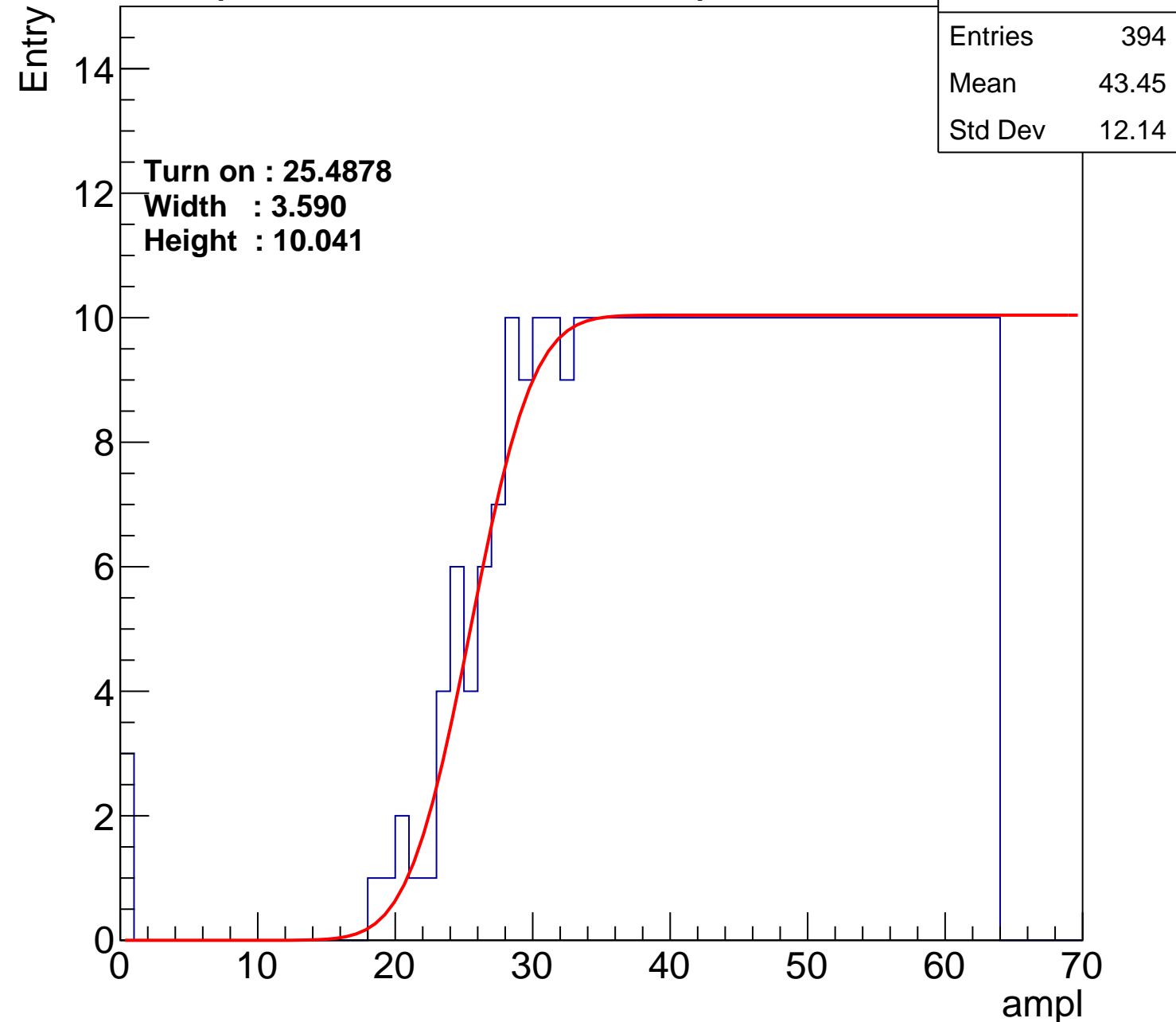
Width : 3.590

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch65

calib_packv5_042523_0143.root, FC#4, port A2

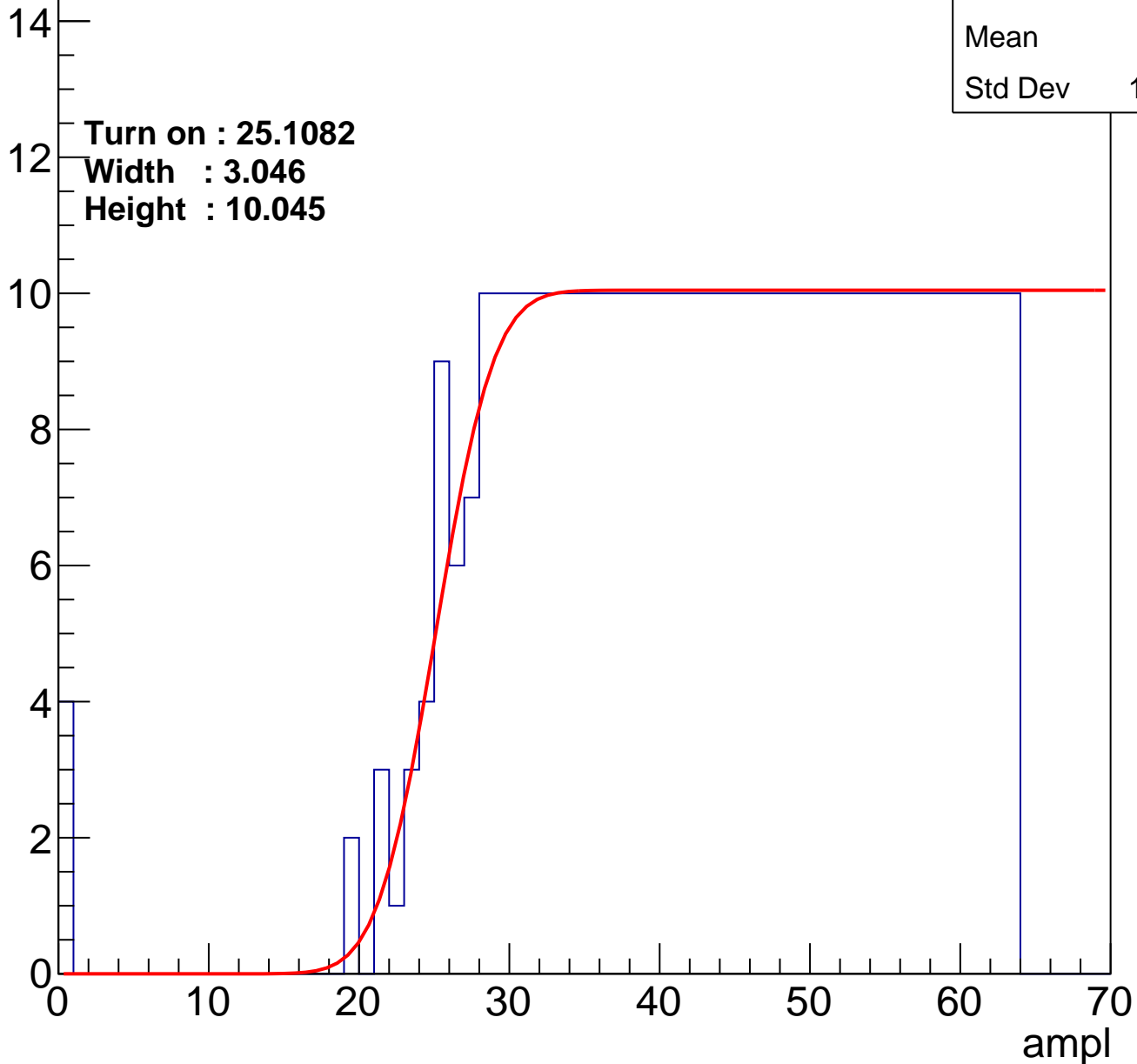
| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.2 |
| Std Dev | 12.33 |

Turn on : 25.1082

Width : 3.046

Height : 10.045

Entry



B1L100S, U1-ch66

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 383 |
| Mean | 43.95 |
| Std Dev | 12 |

Turn on : 26.3550

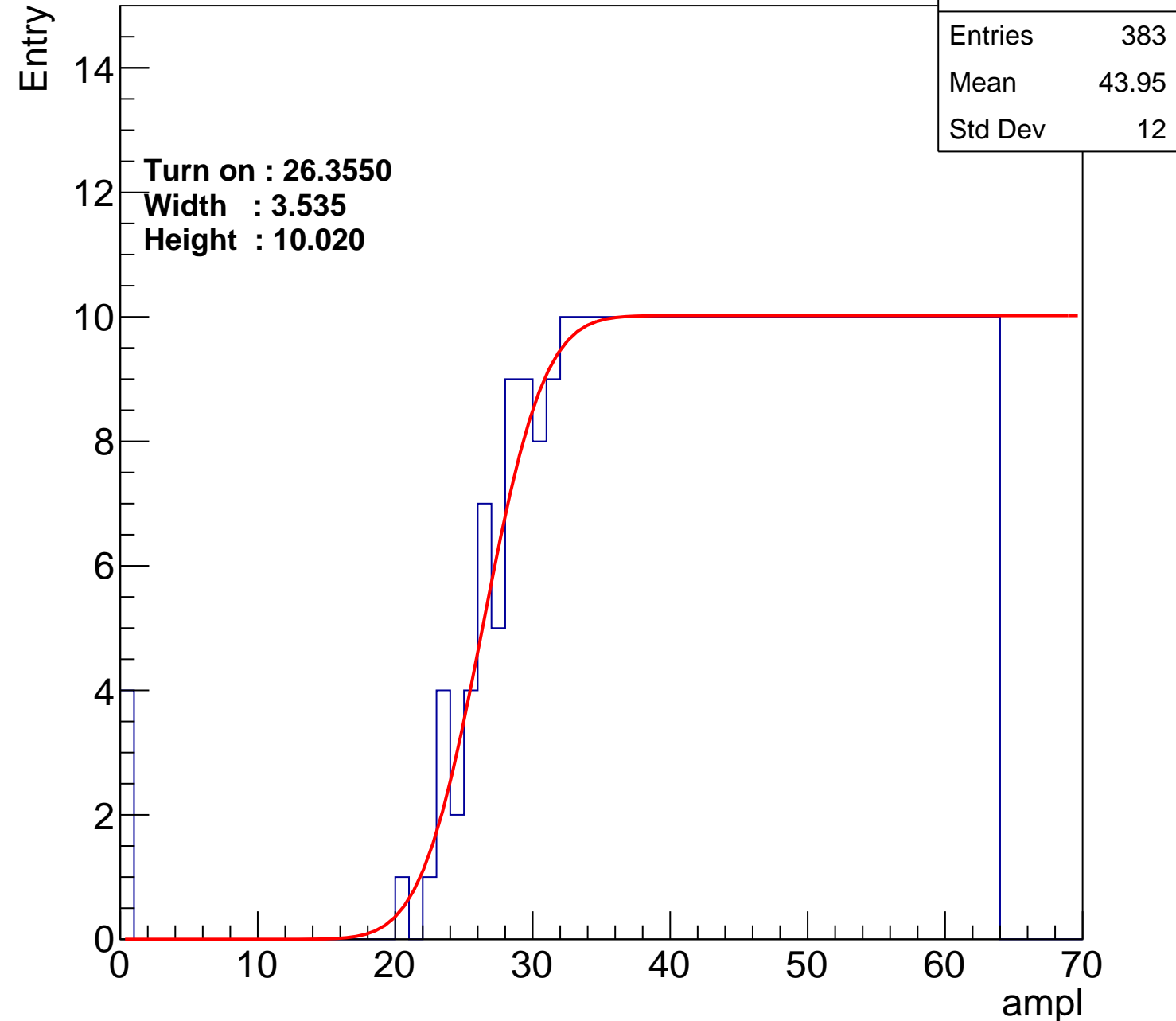
Width : 3.535

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch67

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.03 |
| Std Dev | 11.13 |

Turn on : 27.7012

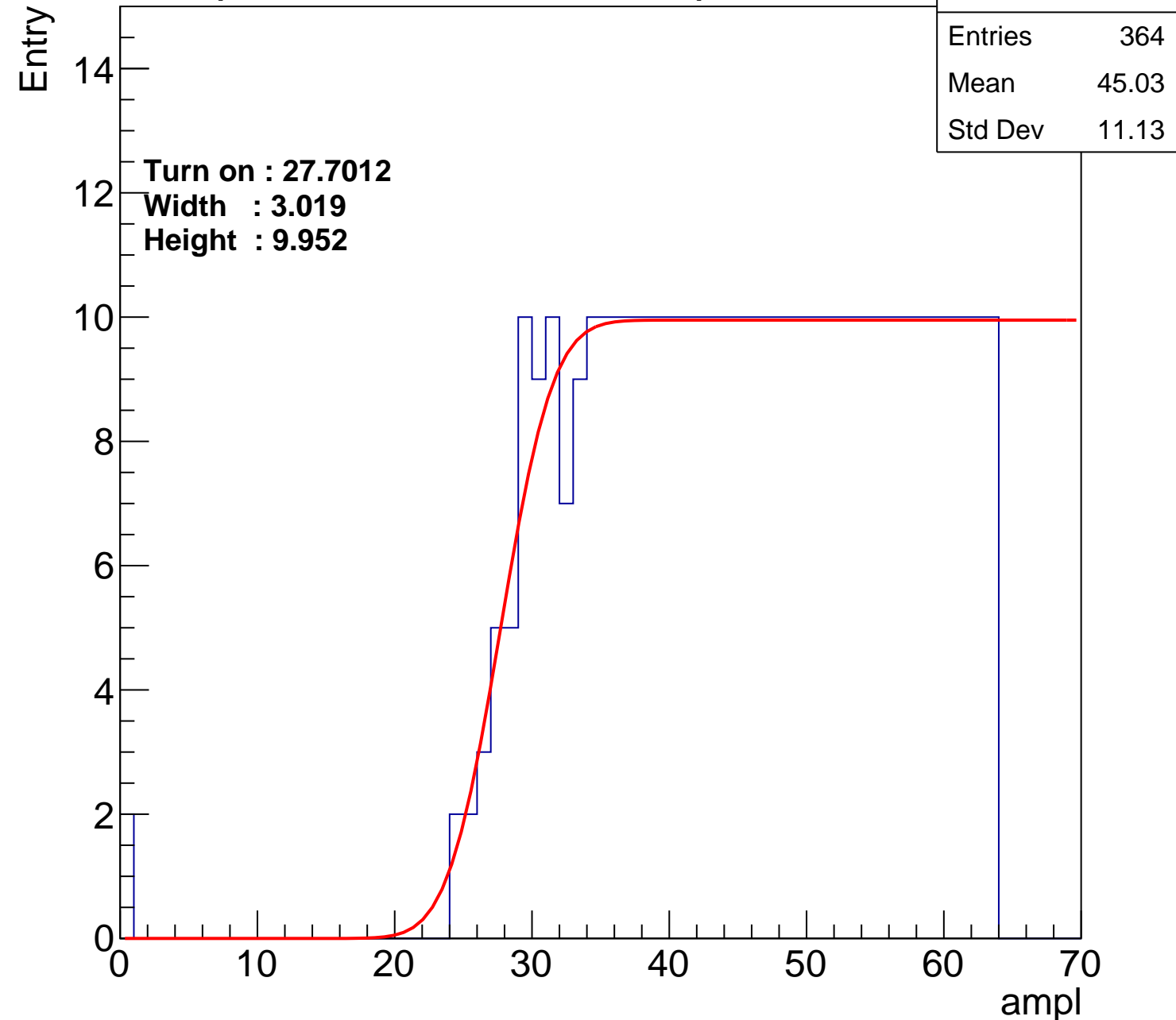
Width : 3.019

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch68

calib_packv5_042523_0143.root, FC#4, port A2

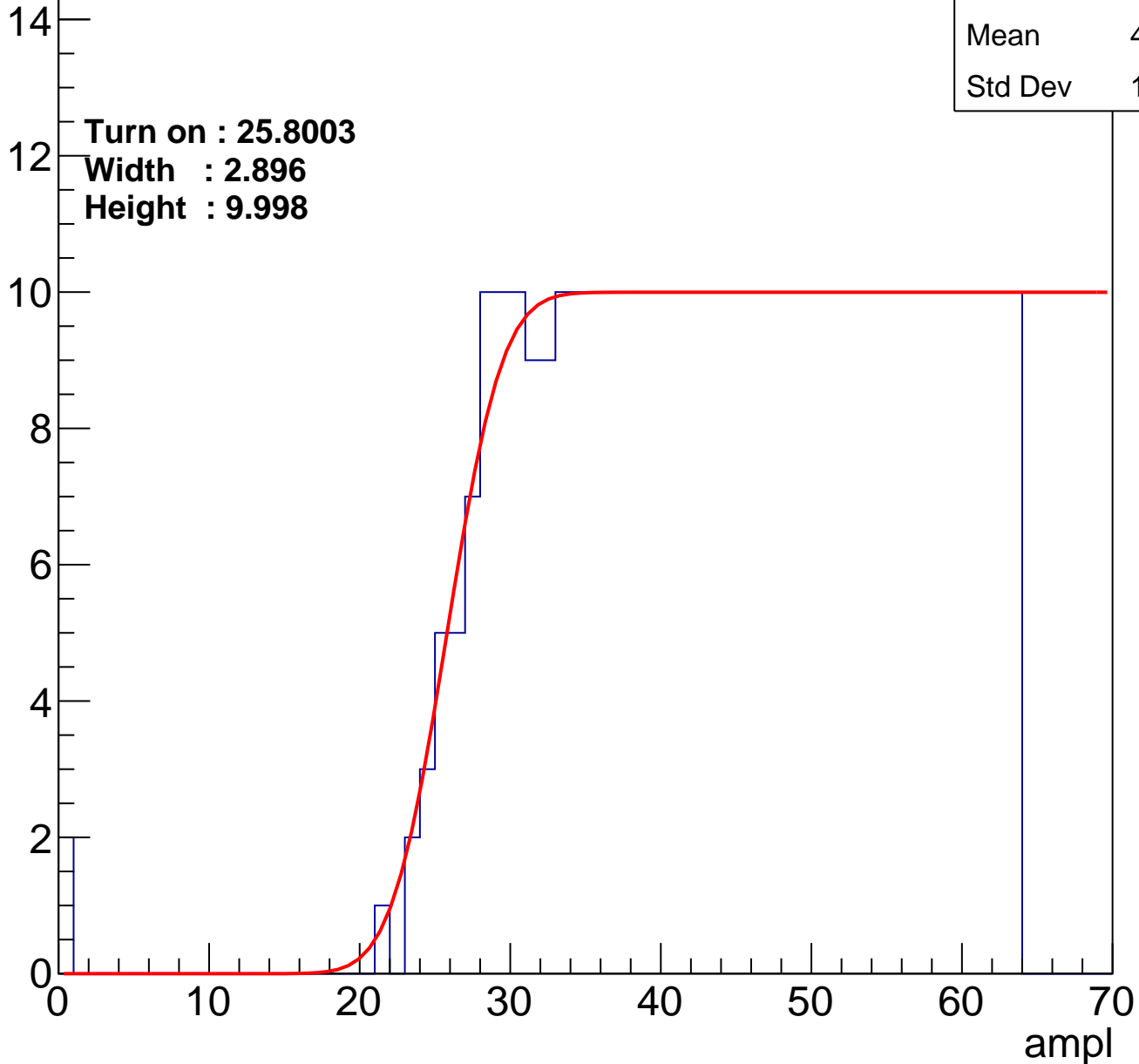
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.13 |
| Std Dev | 11.57 |

Turn on : 25.8003

Width : 2.896

Height : 9.998

Entry



B1L100S, U1-ch69

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.49 |
| Std Dev | 11.79 |

Turn on : 24.7375

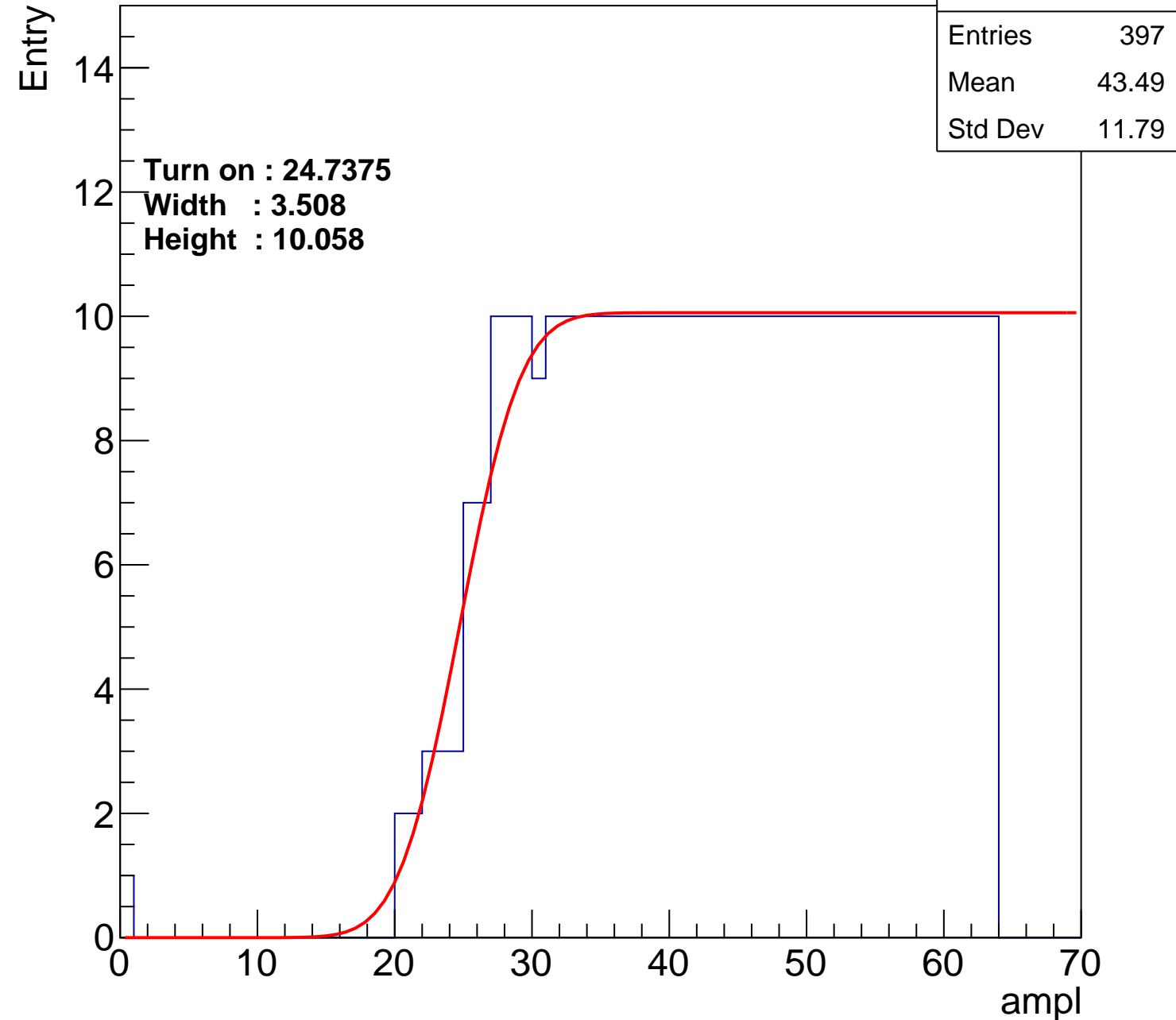
Width : 3.508

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch70

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.36 |
| Std Dev | 12.14 |

Turn on : 27.6645

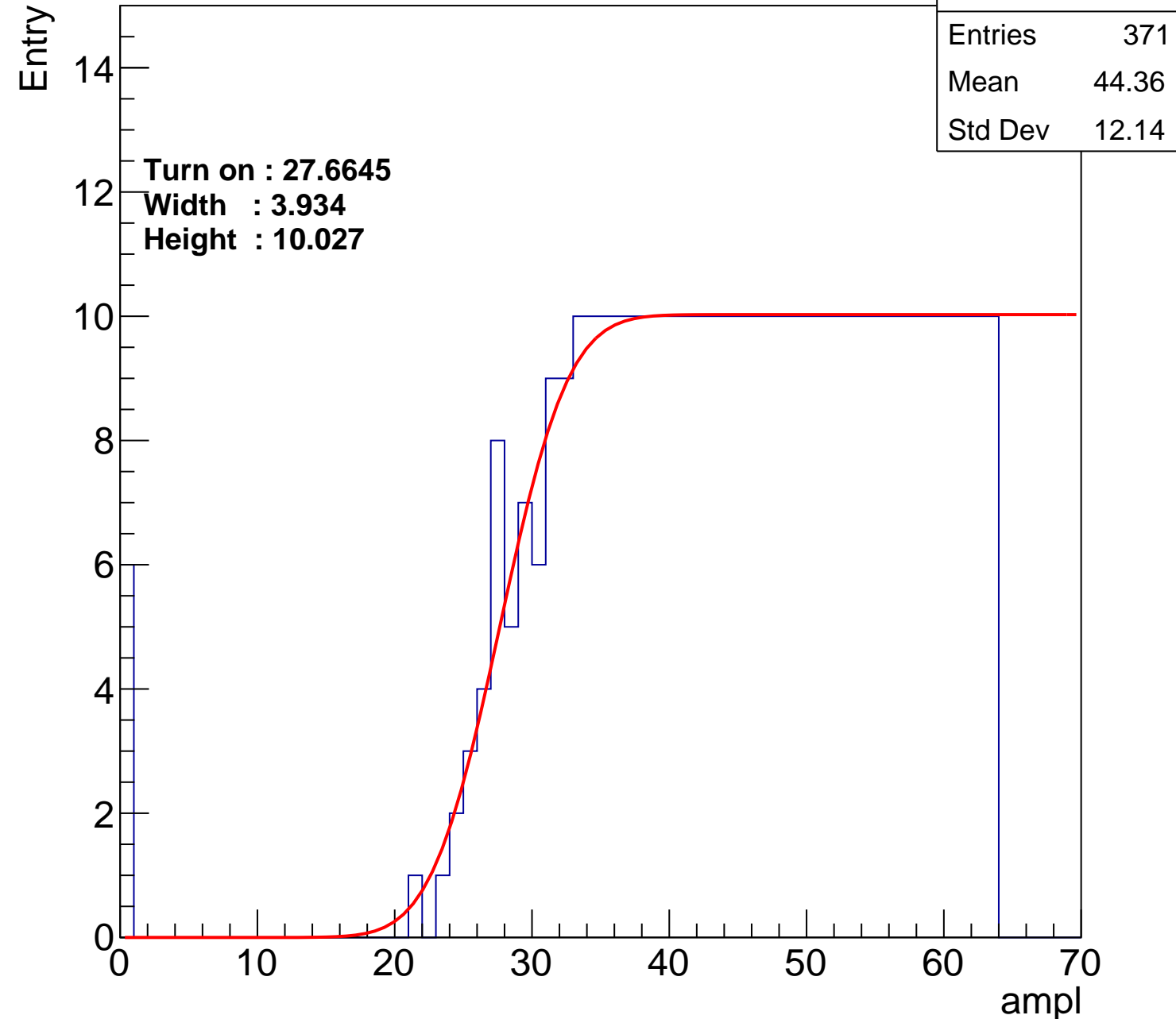
Width : 3.934

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch71

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.55 |
| Std Dev | 11.37 |

Turn on : 26.7054

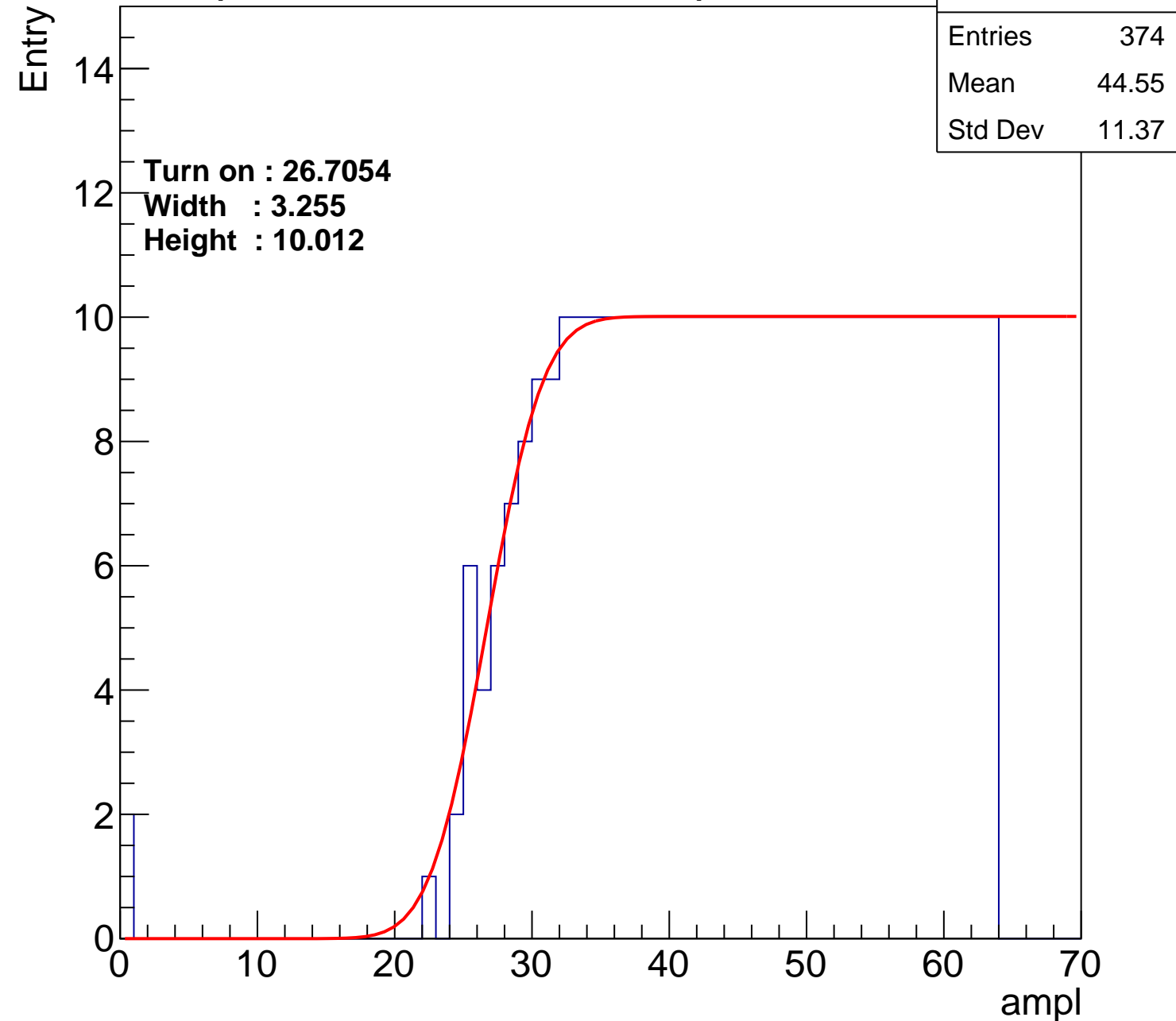
Width : 3.255

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch72

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.35 |
| Std Dev | 11.49 |

Turn on : 26.9427

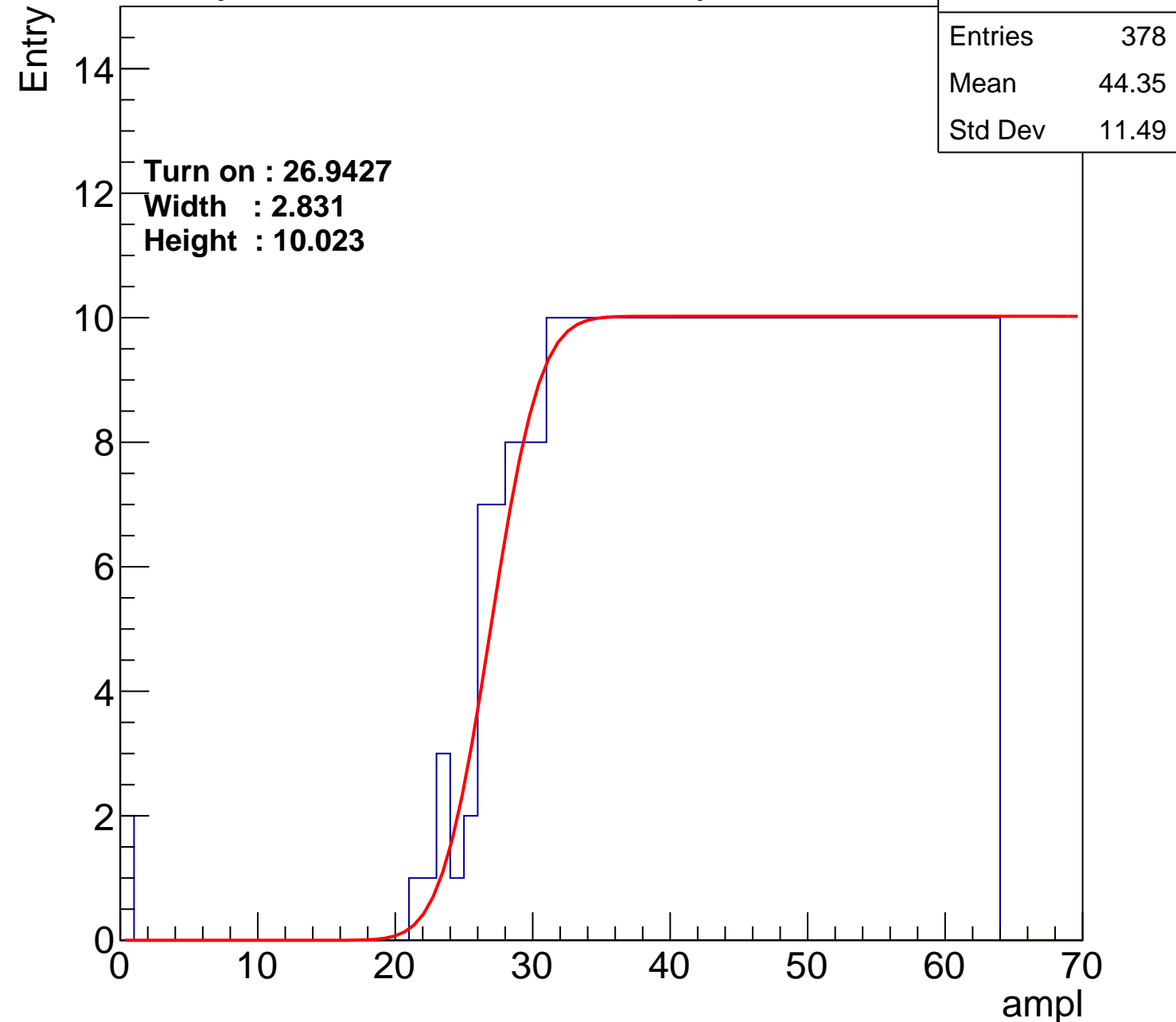
Width : 2.831

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch73

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.57 |
| Std Dev | 11.89 |

Turn on : 25.1908

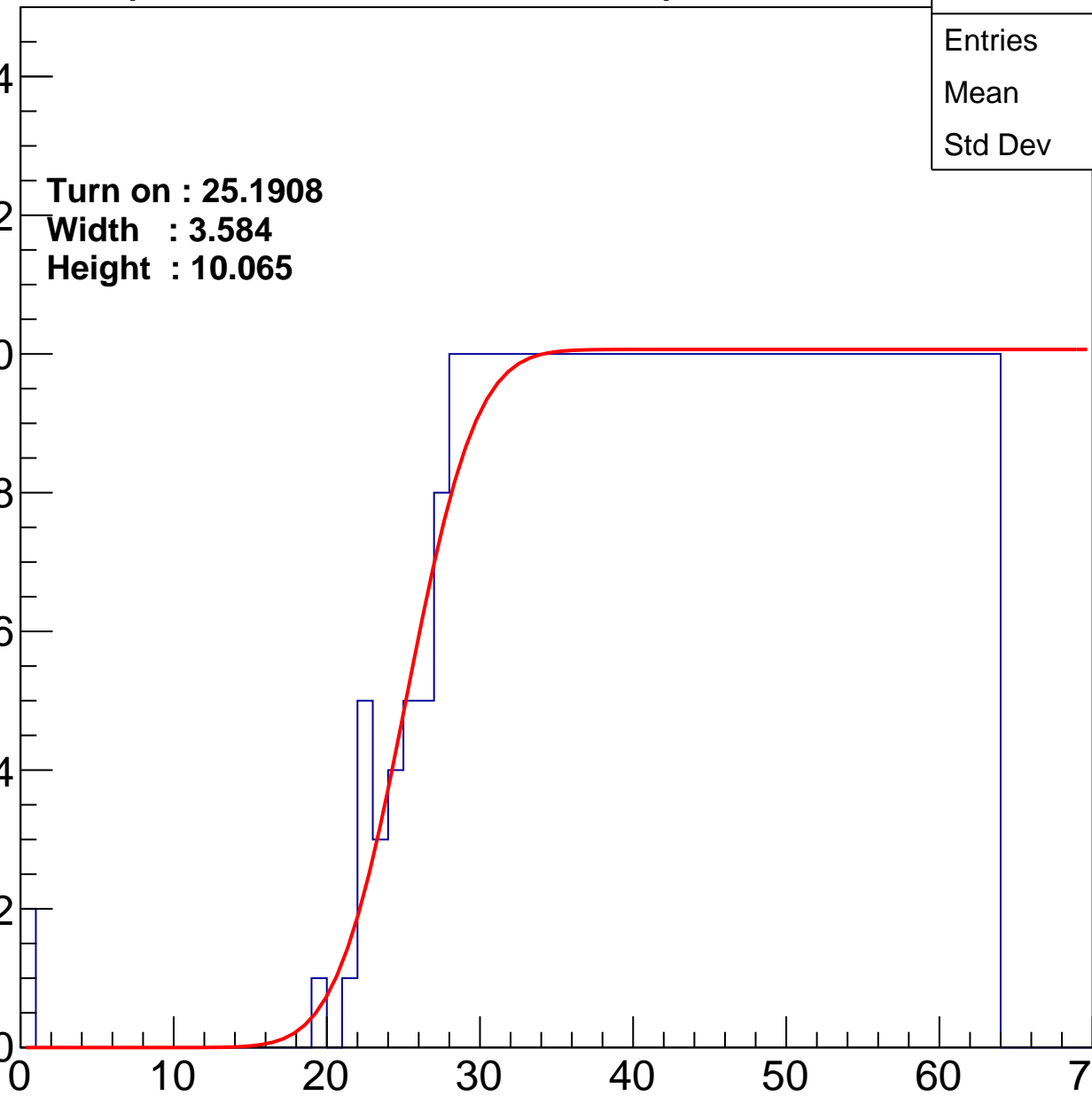
Width : 3.584

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch74

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.58 |
| Std Dev | 11.51 |

Turn on : 27.8018

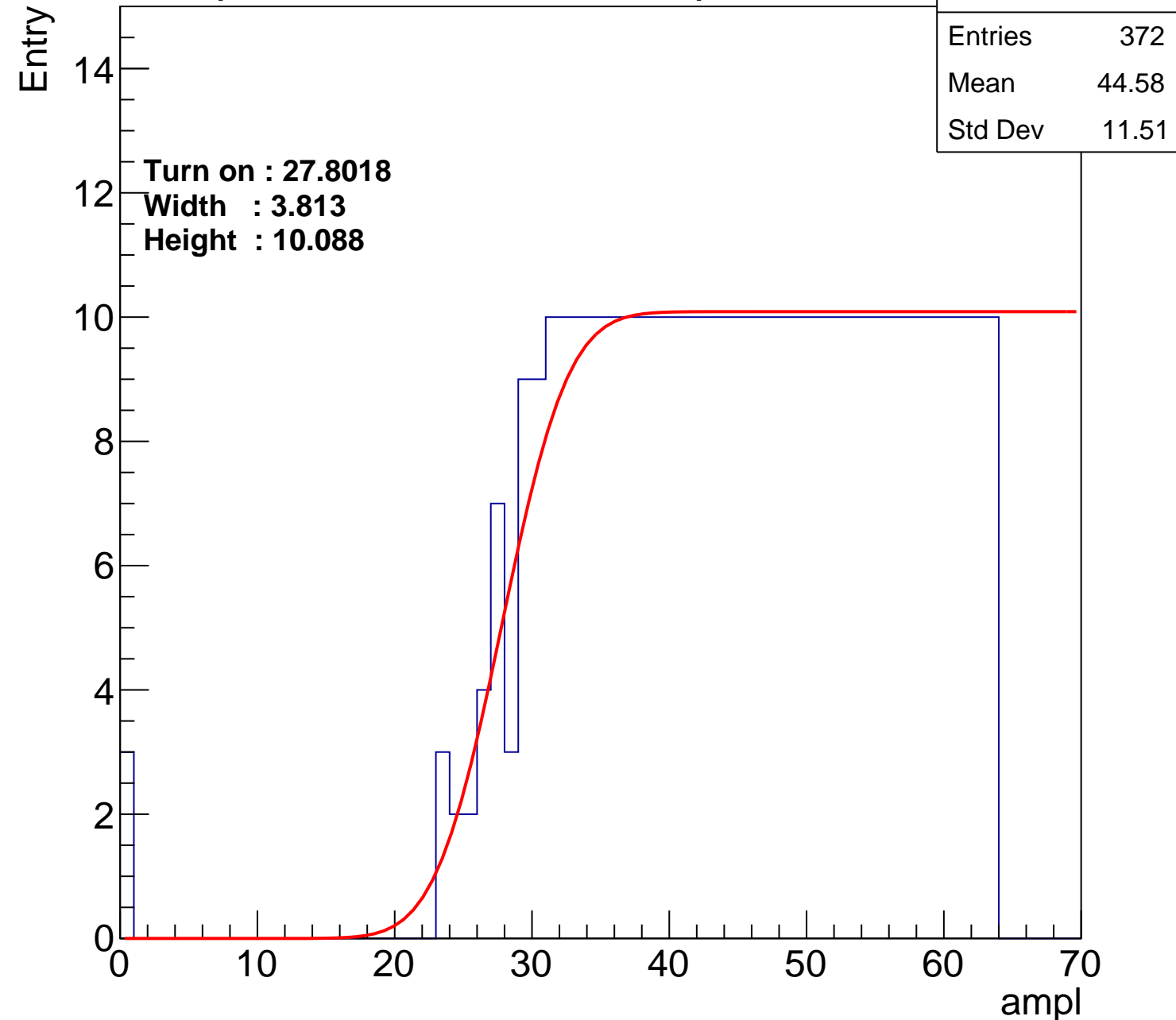
Width : 3.813

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch75

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.61 |
| Std Dev | 11.32 |

Turn on : 26.7357

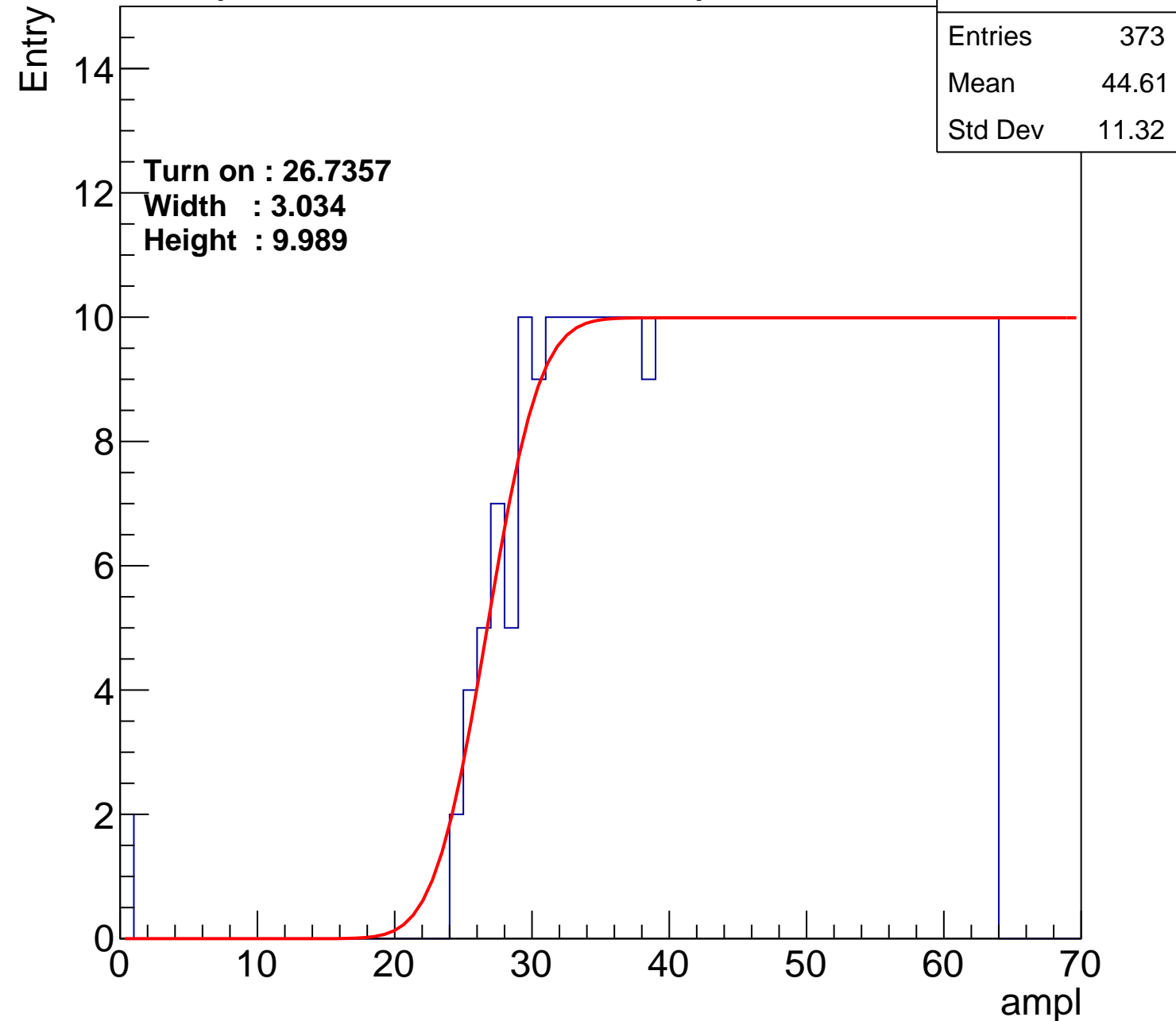
Width : 3.034

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch76

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.82 |
| Std Dev | 12.04 |

Turn on : 25.8267

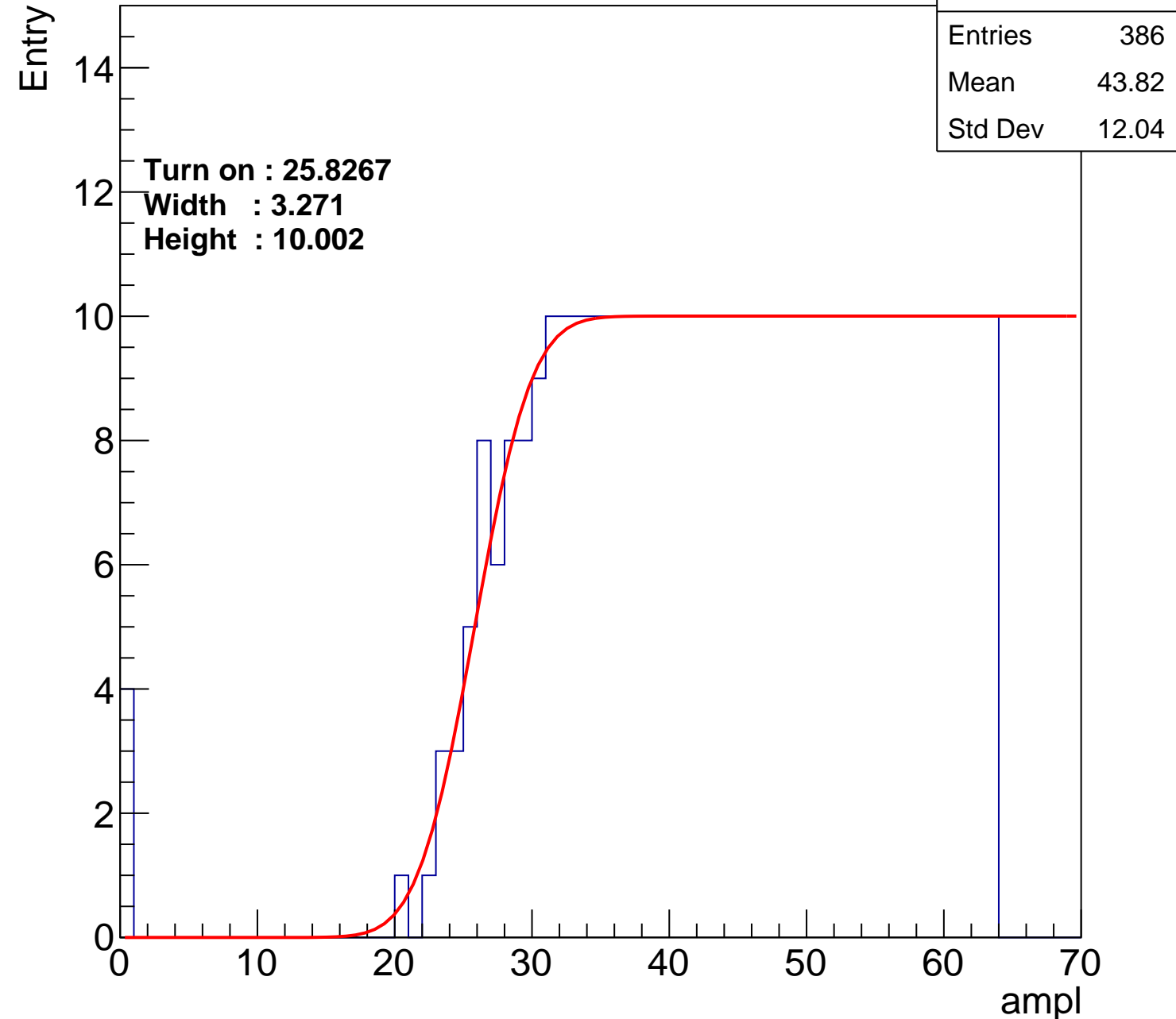
Width : 3.271

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch77

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.39 |
| Std Dev | 11.95 |

Turn on : 24.5930

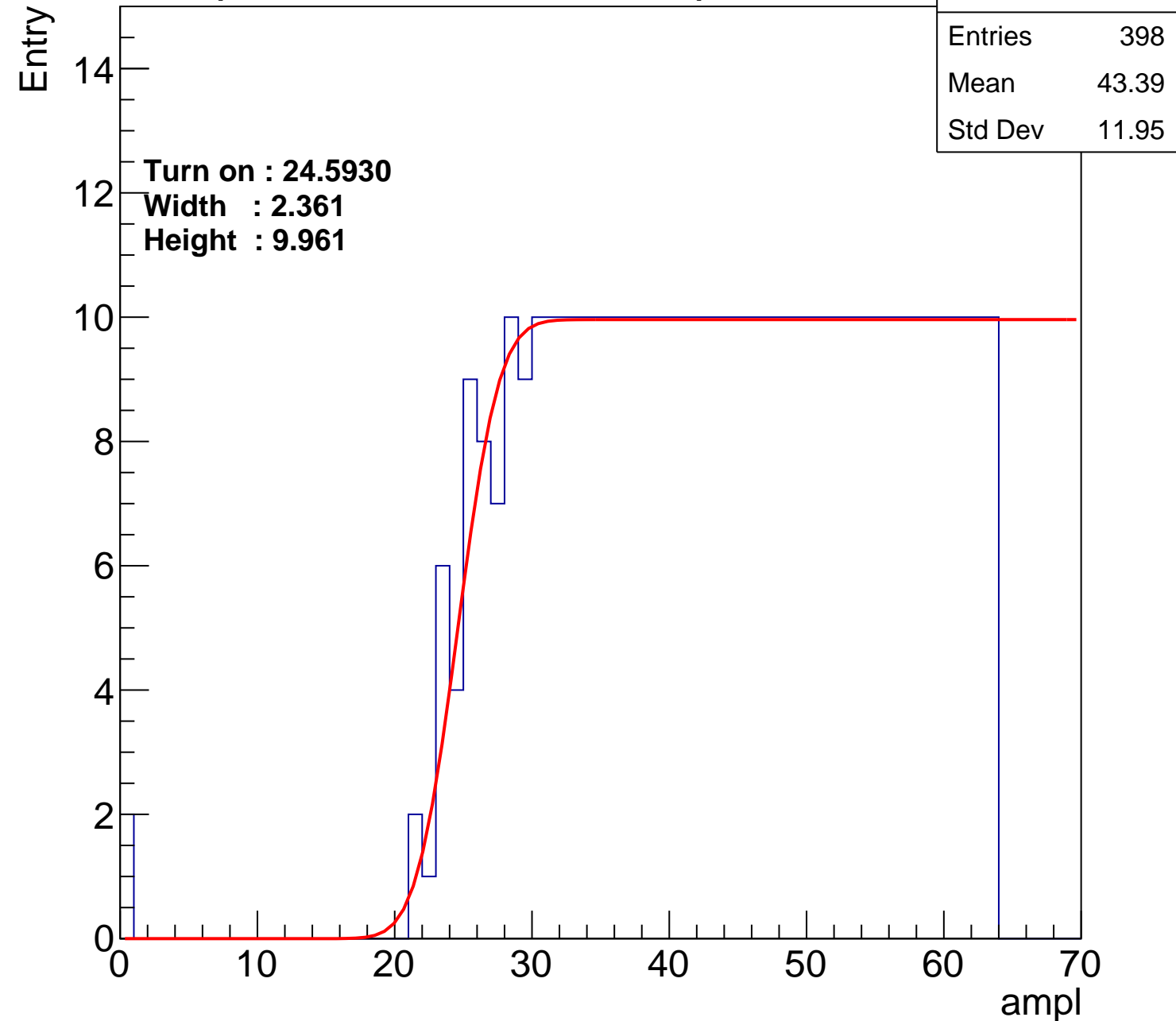
Width : 2.361

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch78

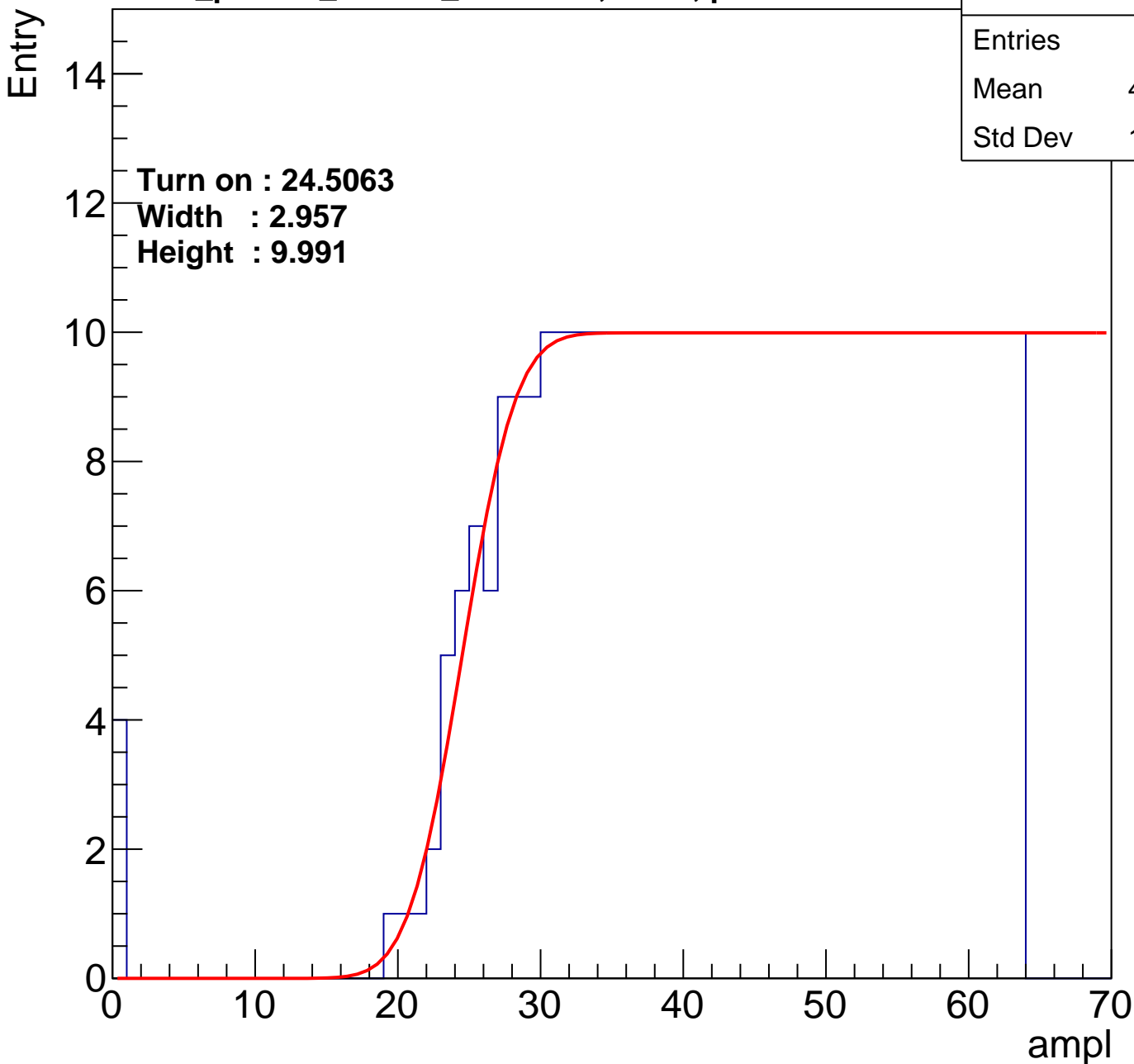
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.15 |
| Std Dev | 12.35 |

Turn on : 24.5063

Width : 2.957

Height : 9.991



B1L100S, U1-ch79

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.97 |
| Std Dev | 11.66 |

Turn on : 25.7627

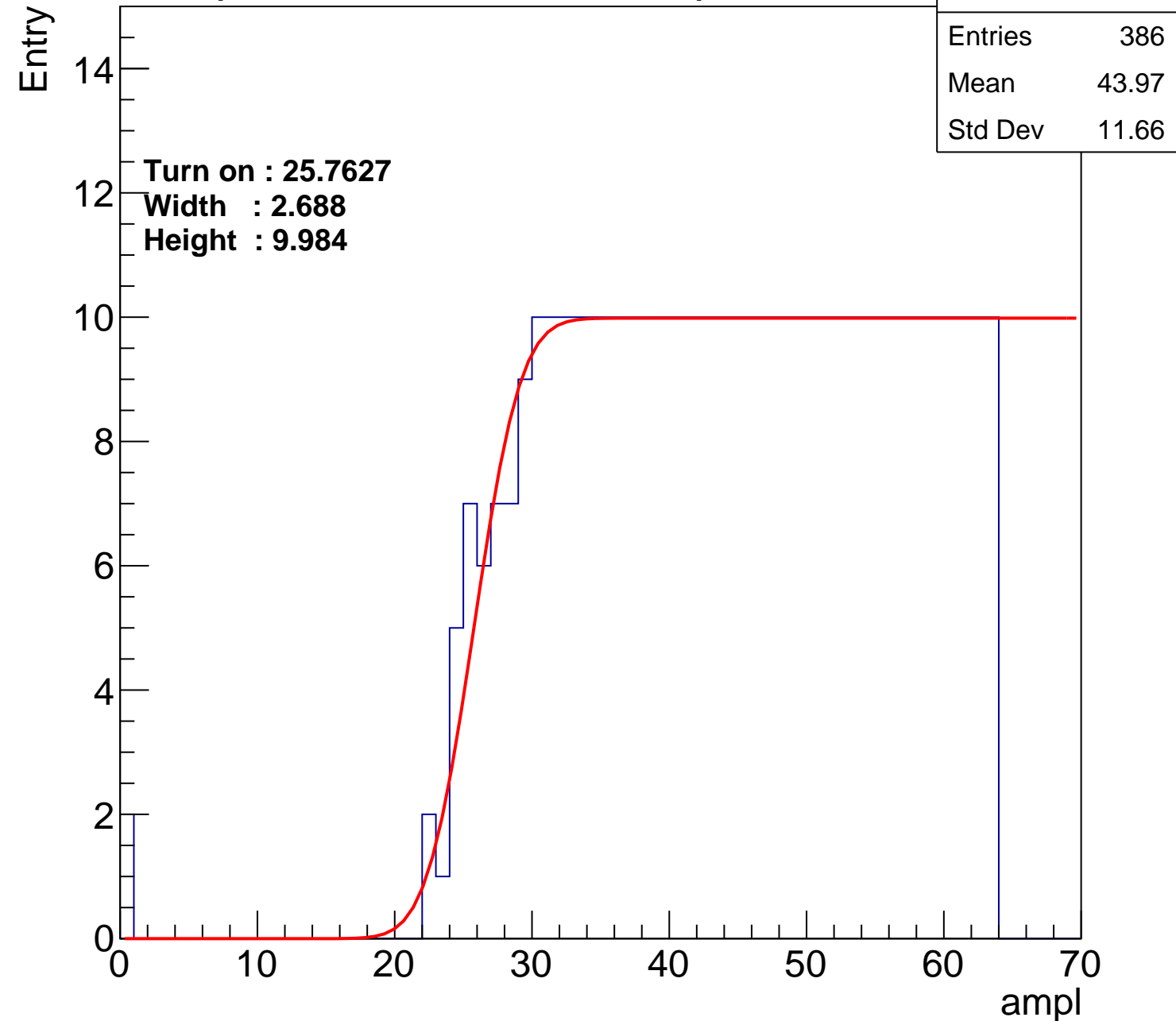
Width : 2.688

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch80

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 409 |
| Mean | 42.52 |
| Std Dev | 13.06 |

Turn on : 24.2883

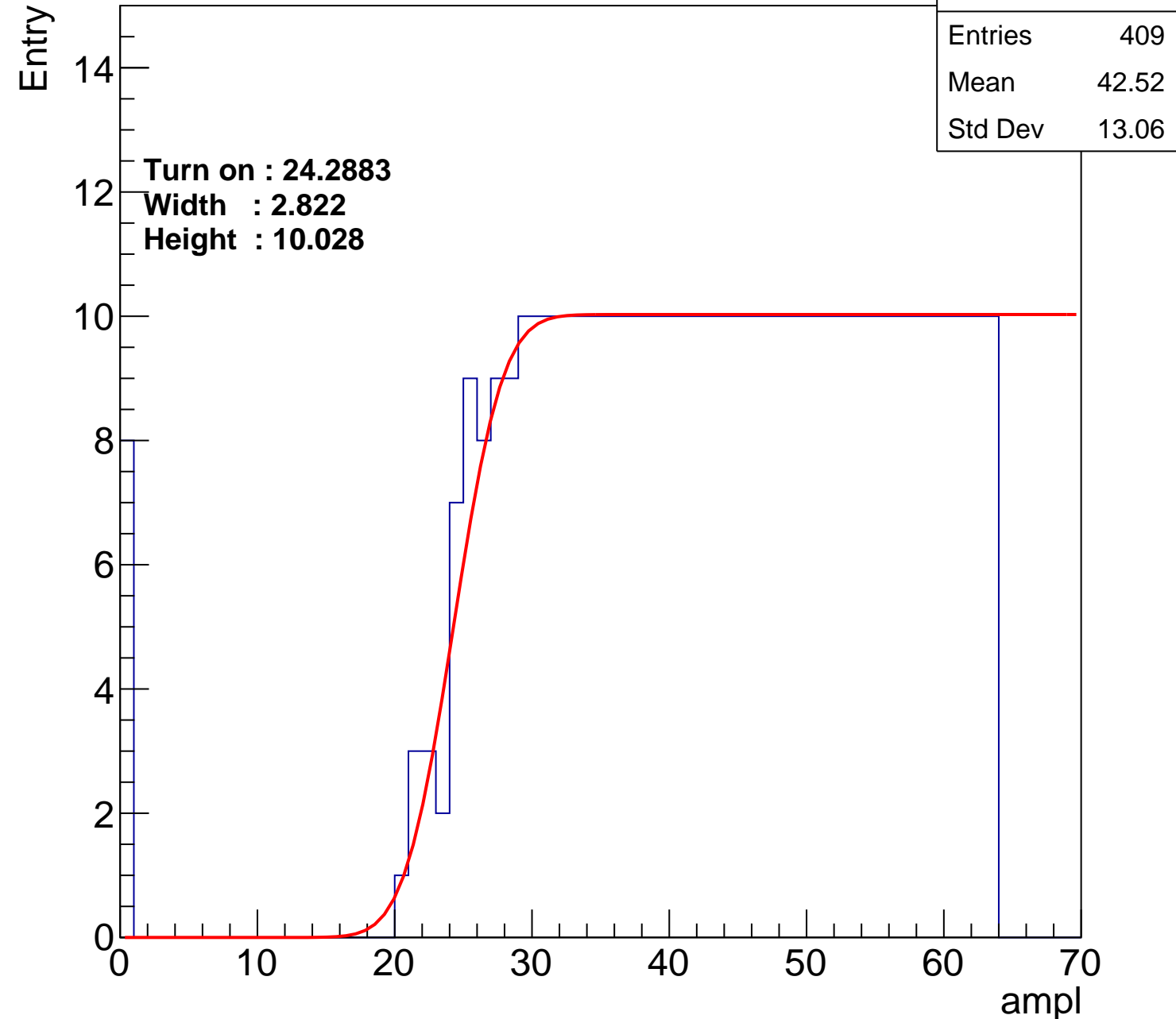
Width : 2.822

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch81

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.2 |
| Std Dev | 11.74 |

Turn on : 27.1202

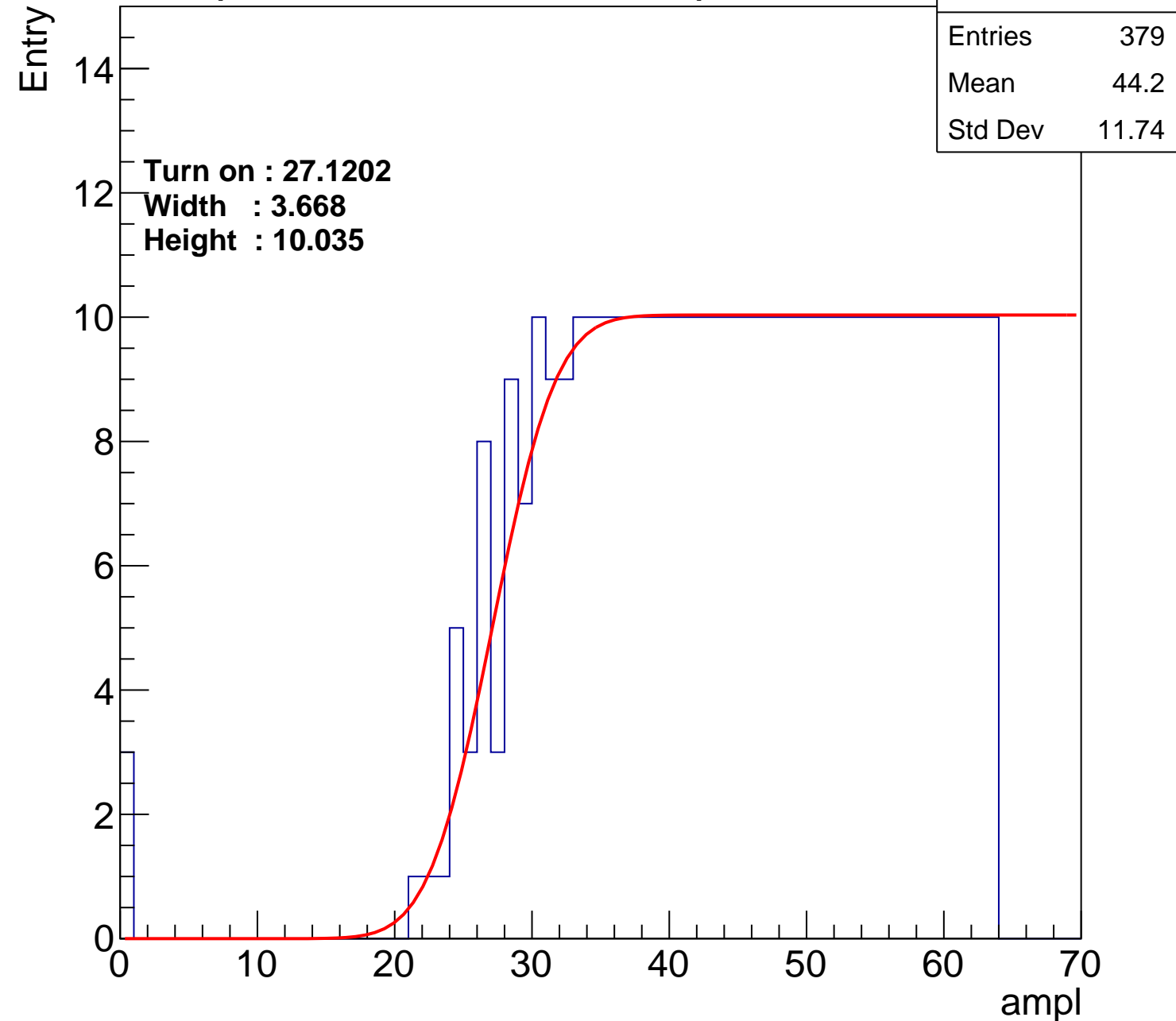
Width : 3.668

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch82

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.72 |
| Std Dev | 11.93 |

Turn on : 25.7596

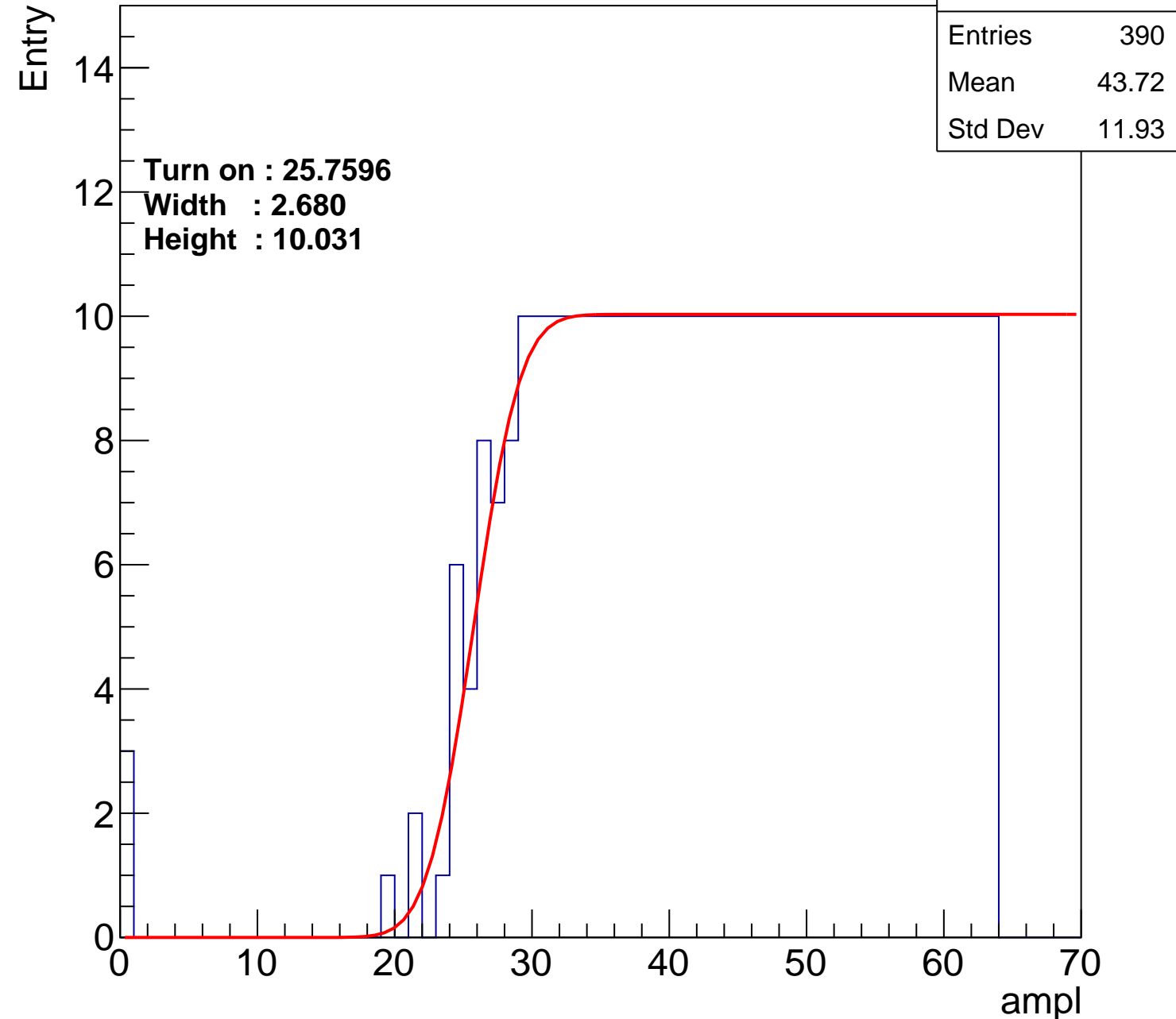
Width : 2.680

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch83

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.85 |
| Std Dev | 11.35 |

Turn on : 28.0391

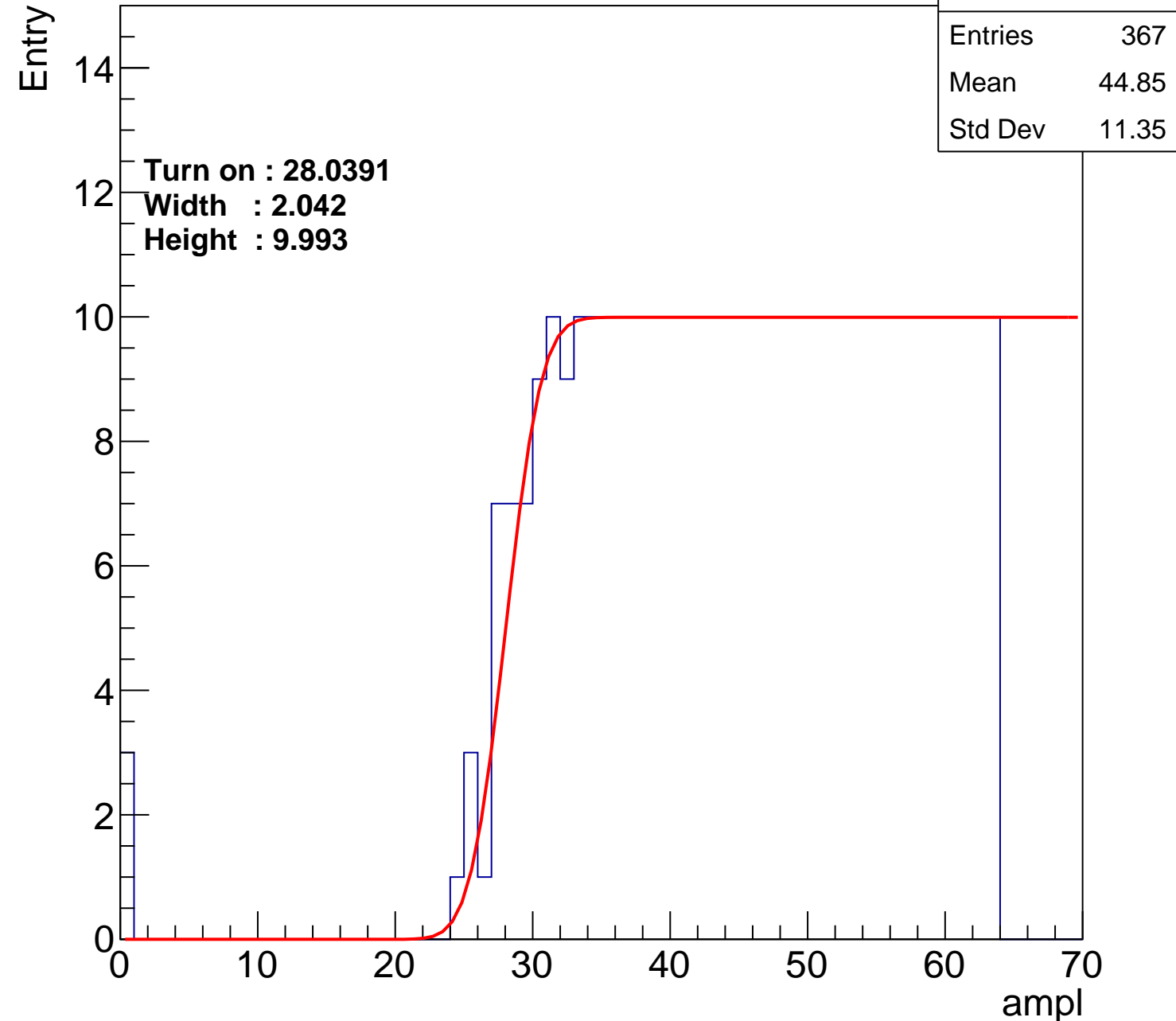
Width : 2.042

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch84

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.1 |
| Std Dev | 11.48 |

Turn on : 25.7343

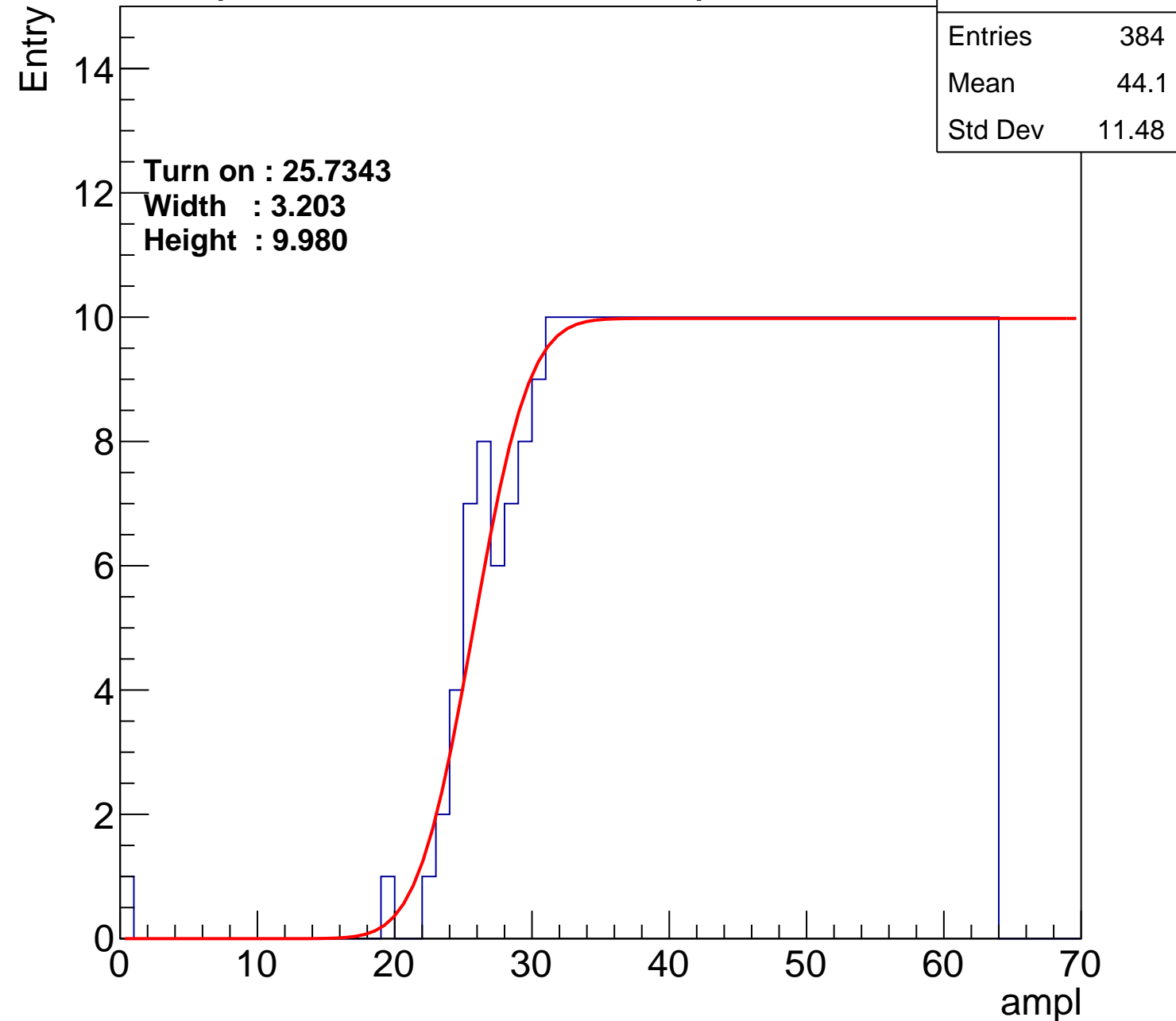
Width : 3.203

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch85

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.87 |
| Std Dev | 11.84 |

Turn on : 25.7062

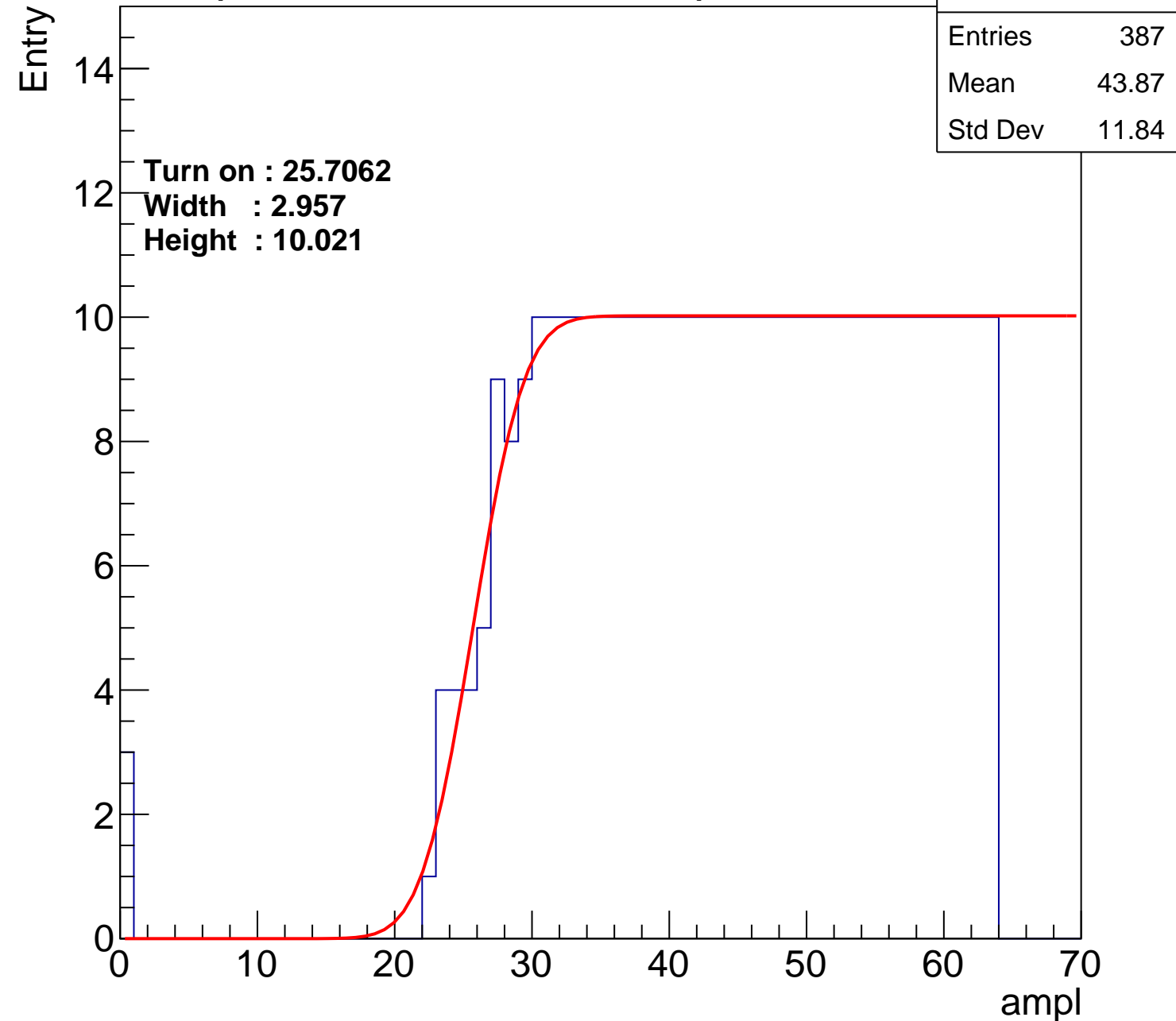
Width : 2.957

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch86

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.82 |
| Std Dev | 11.75 |

Turn on : 26.1838

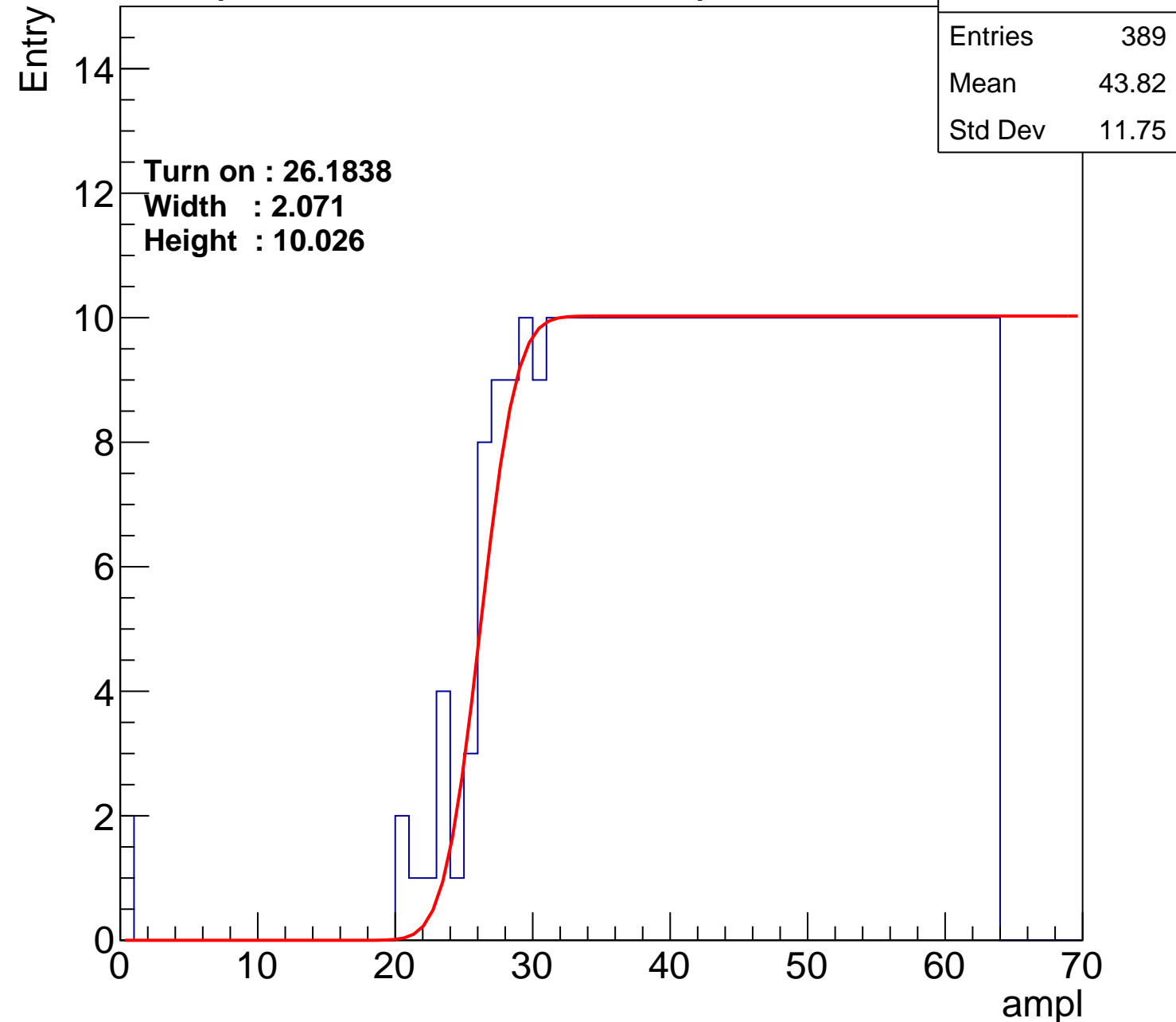
Width : 2.071

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch87

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.5 |
| Std Dev | 11.39 |

Turn on : 27.4262

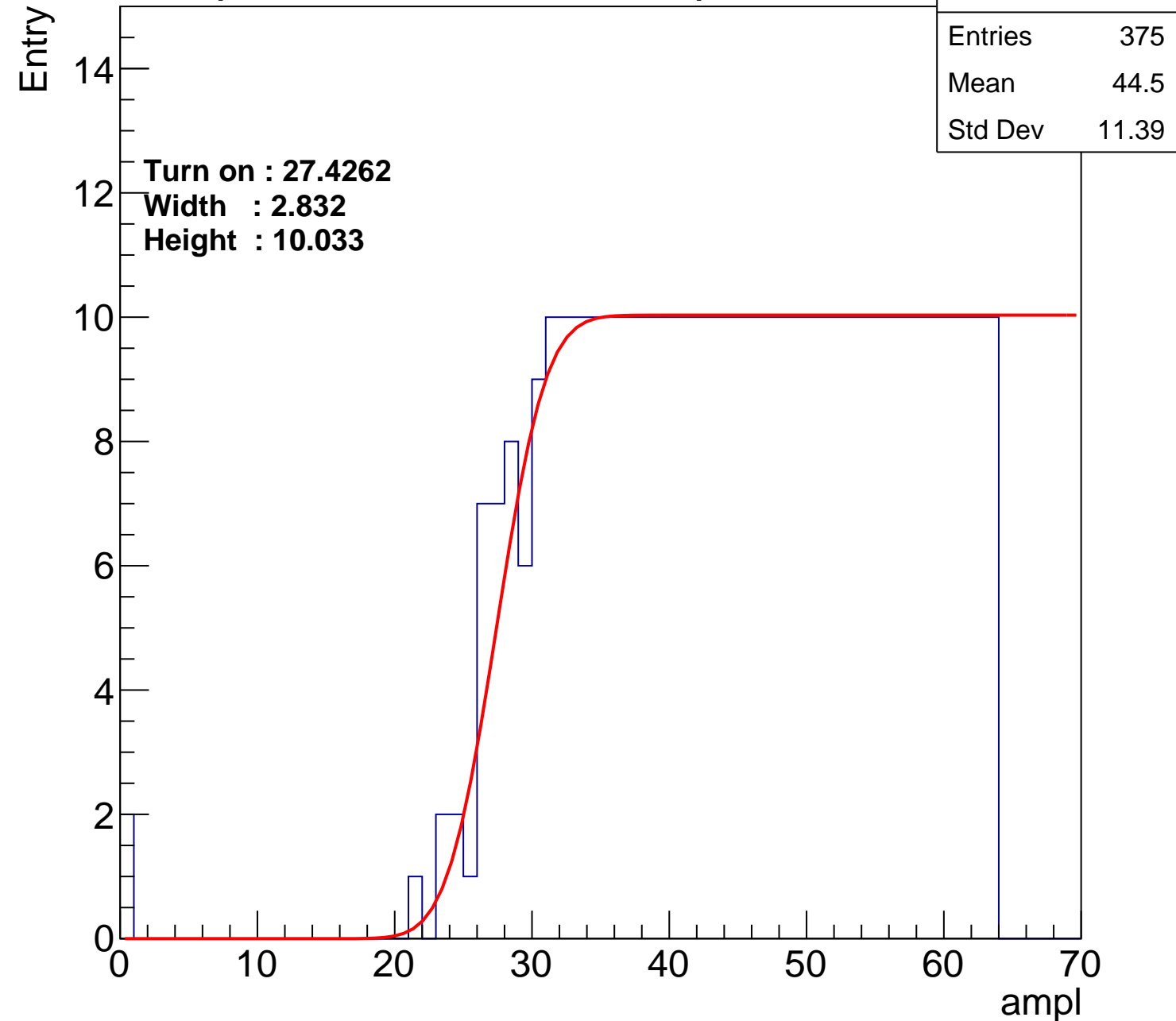
Width : 2.832

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch88

calib_packv5_042523_0143.root, FC#4, port A2

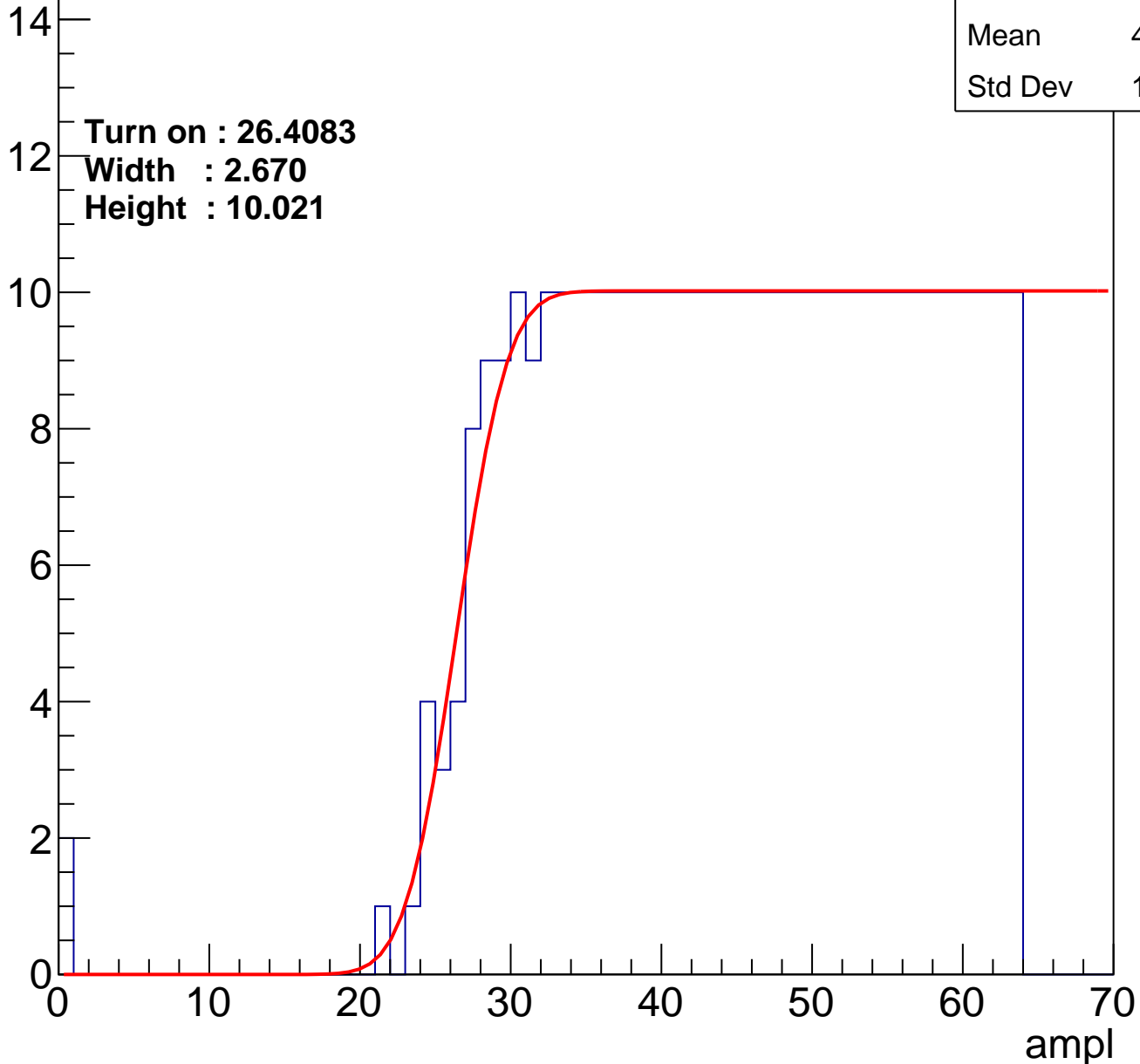
| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.28 |
| Std Dev | 11.48 |

Turn on : 26.4083

Width : 2.670

Height : 10.021

Entry



B1L100S, U1-ch89

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.22 |
| Std Dev | 11.41 |

Turn on : 26.3138

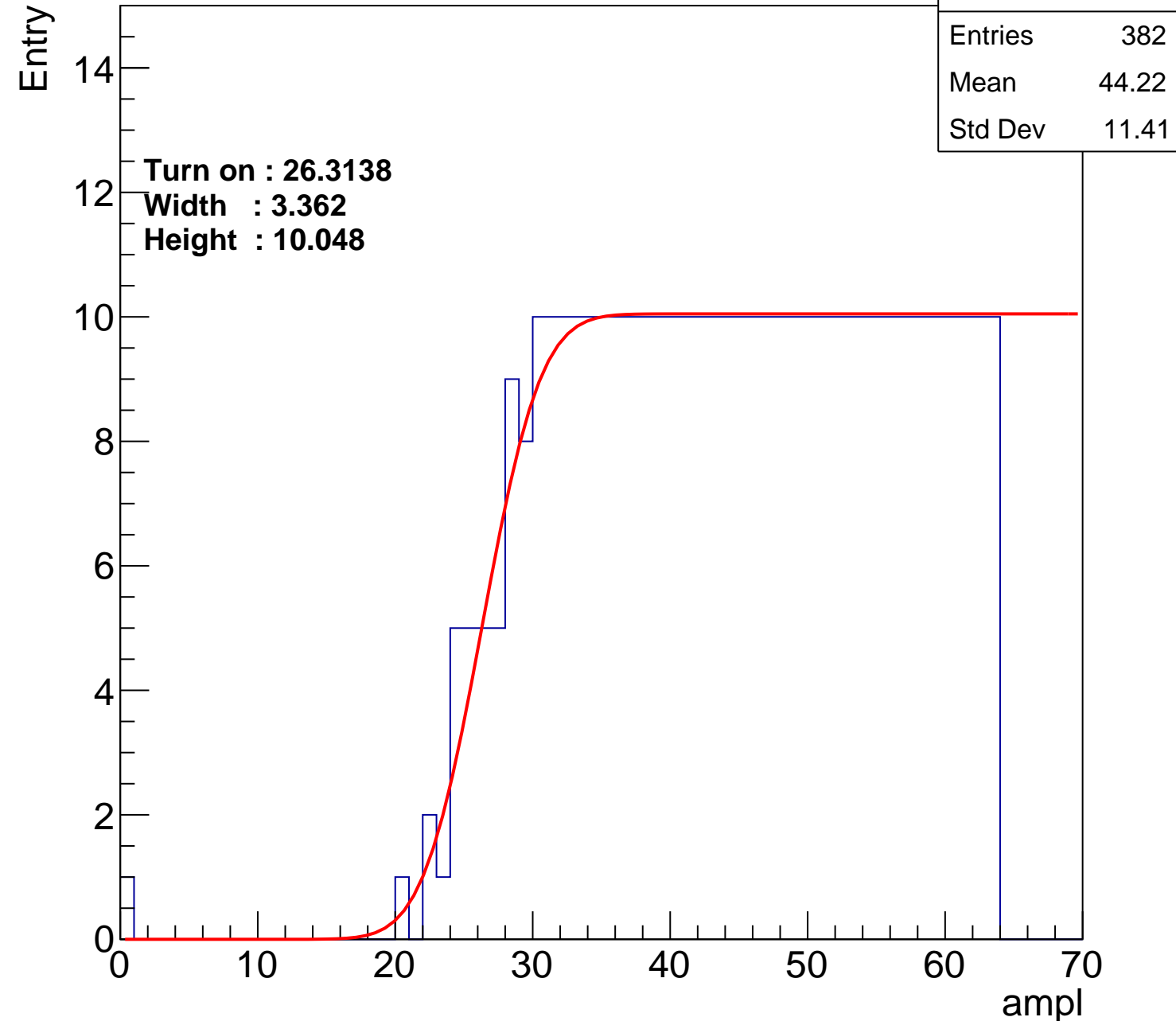
Width : 3.362

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch90

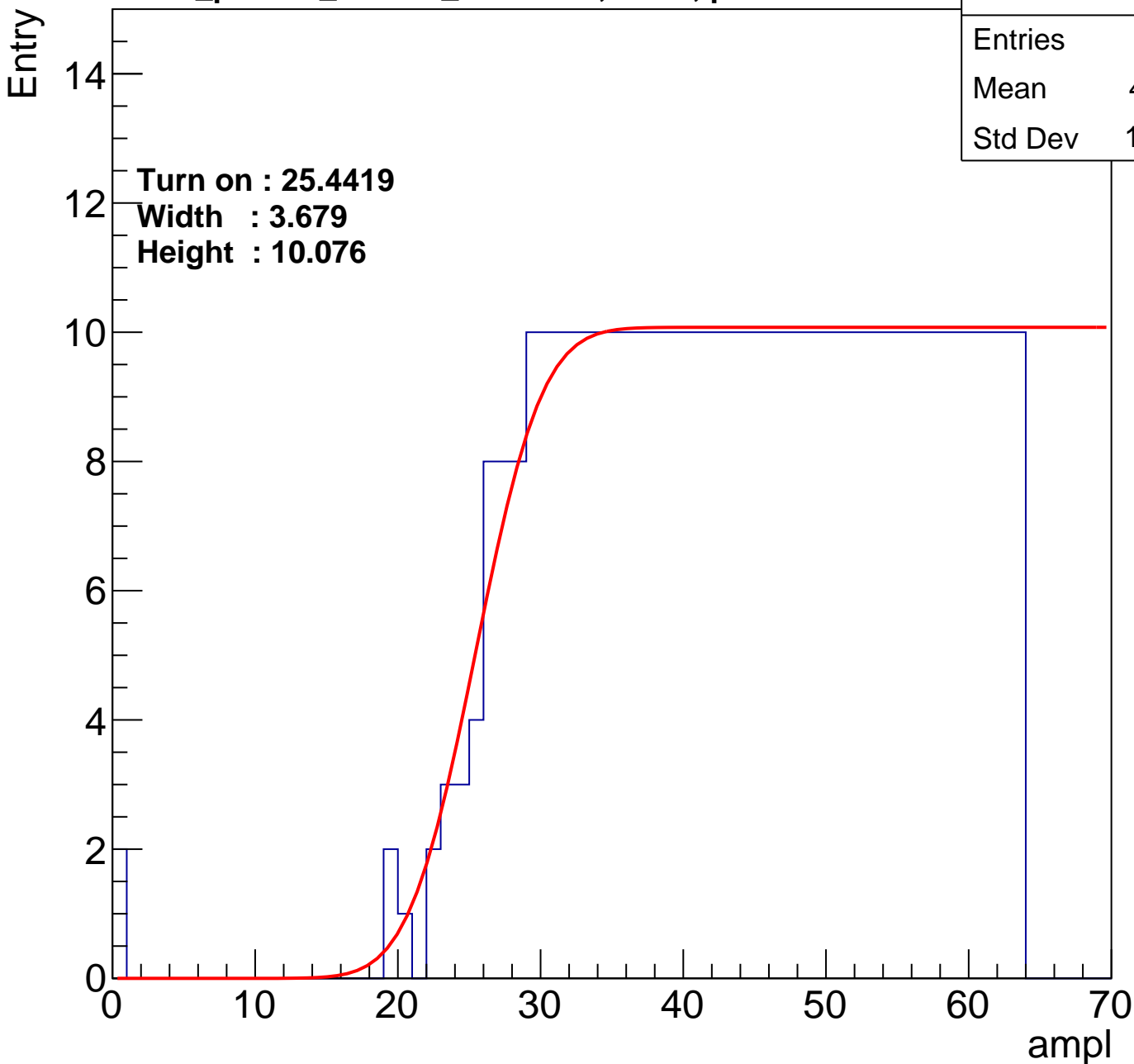
calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.71 |
| Std Dev | 11.82 |

Turn on : 25.4419

Width : 3.679

Height : 10.076



B1L100S, U1-ch91

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.56 |
| Std Dev | 11.49 |

Turn on : 26.9189

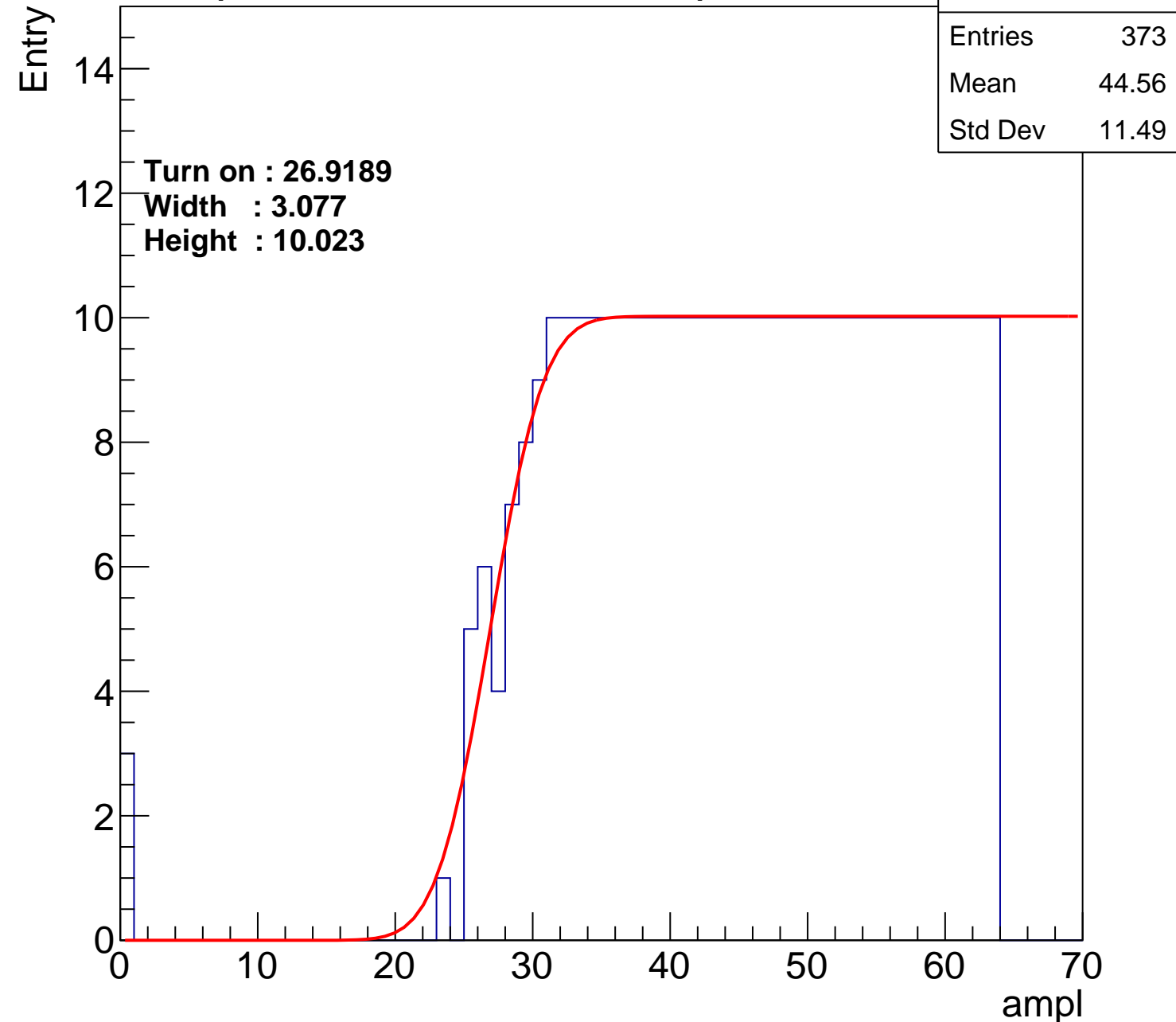
Width : 3.077

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch92

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.7 |
| Std Dev | 11.95 |

Turn on : 25.6445

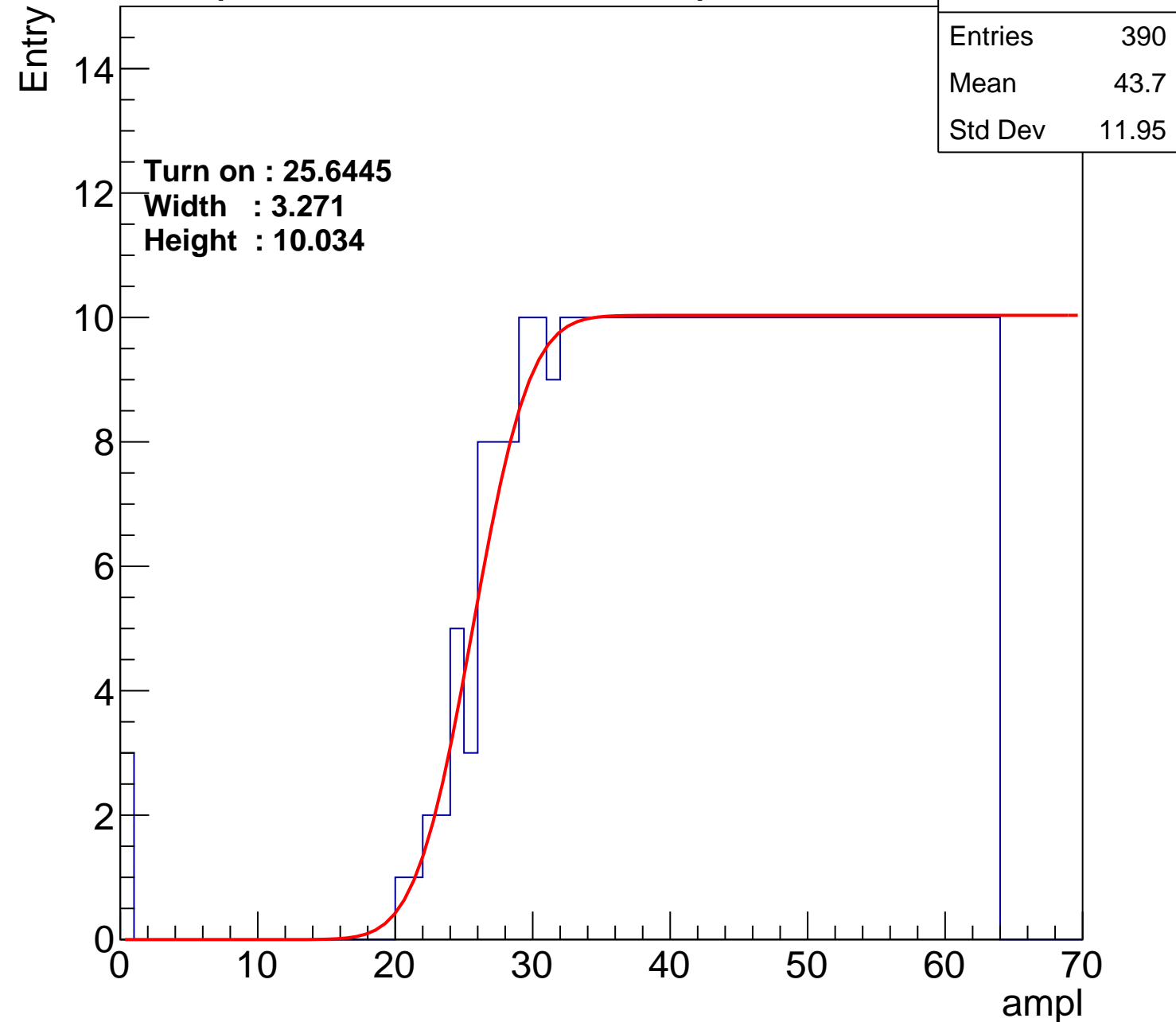
Width : 3.271

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch93

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.55 |
| Std Dev | 12.04 |

Turn on : 25.5646

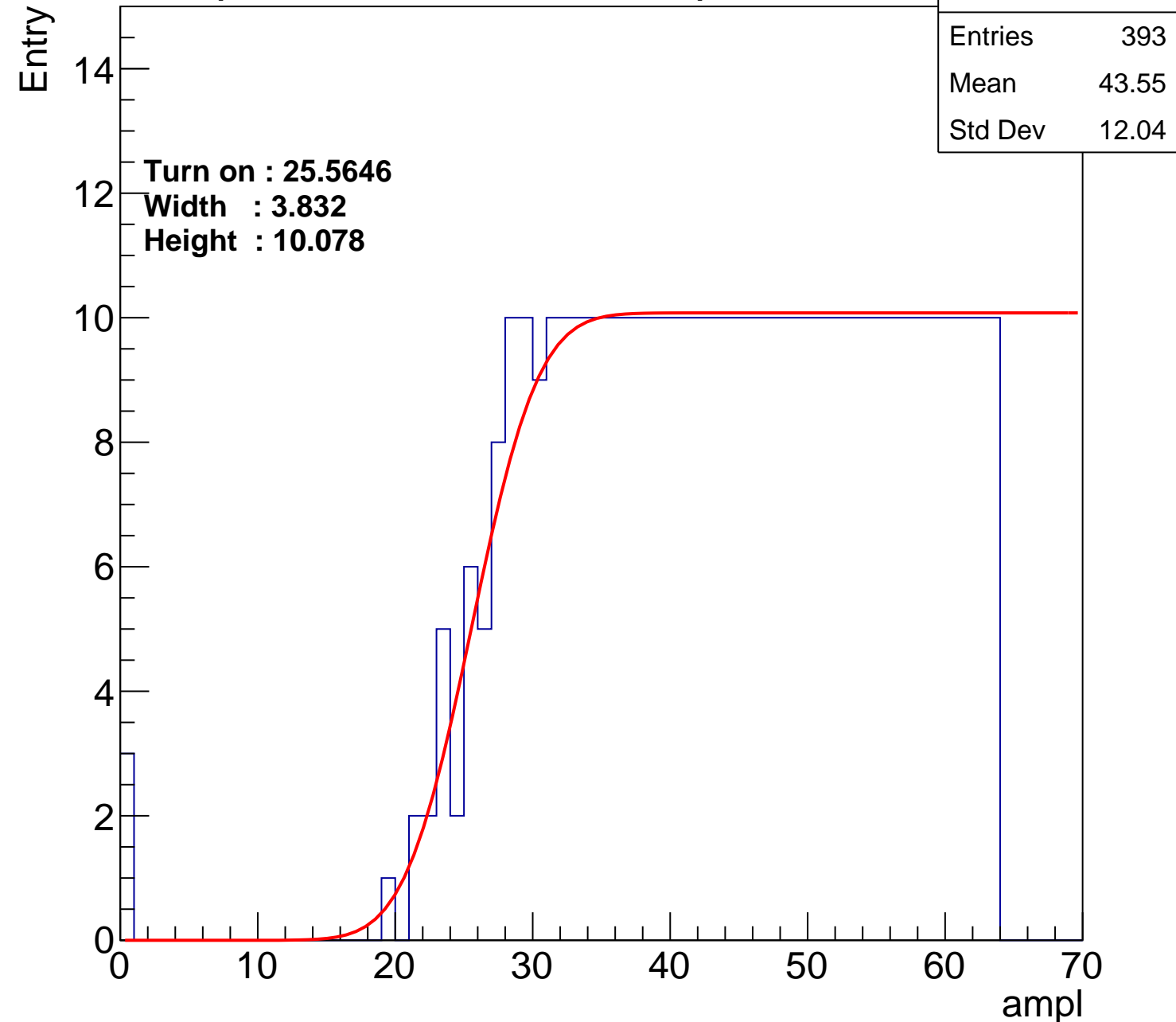
Width : 3.832

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch94

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.21 |
| Std Dev | 11.73 |

Turn on : 26.4750

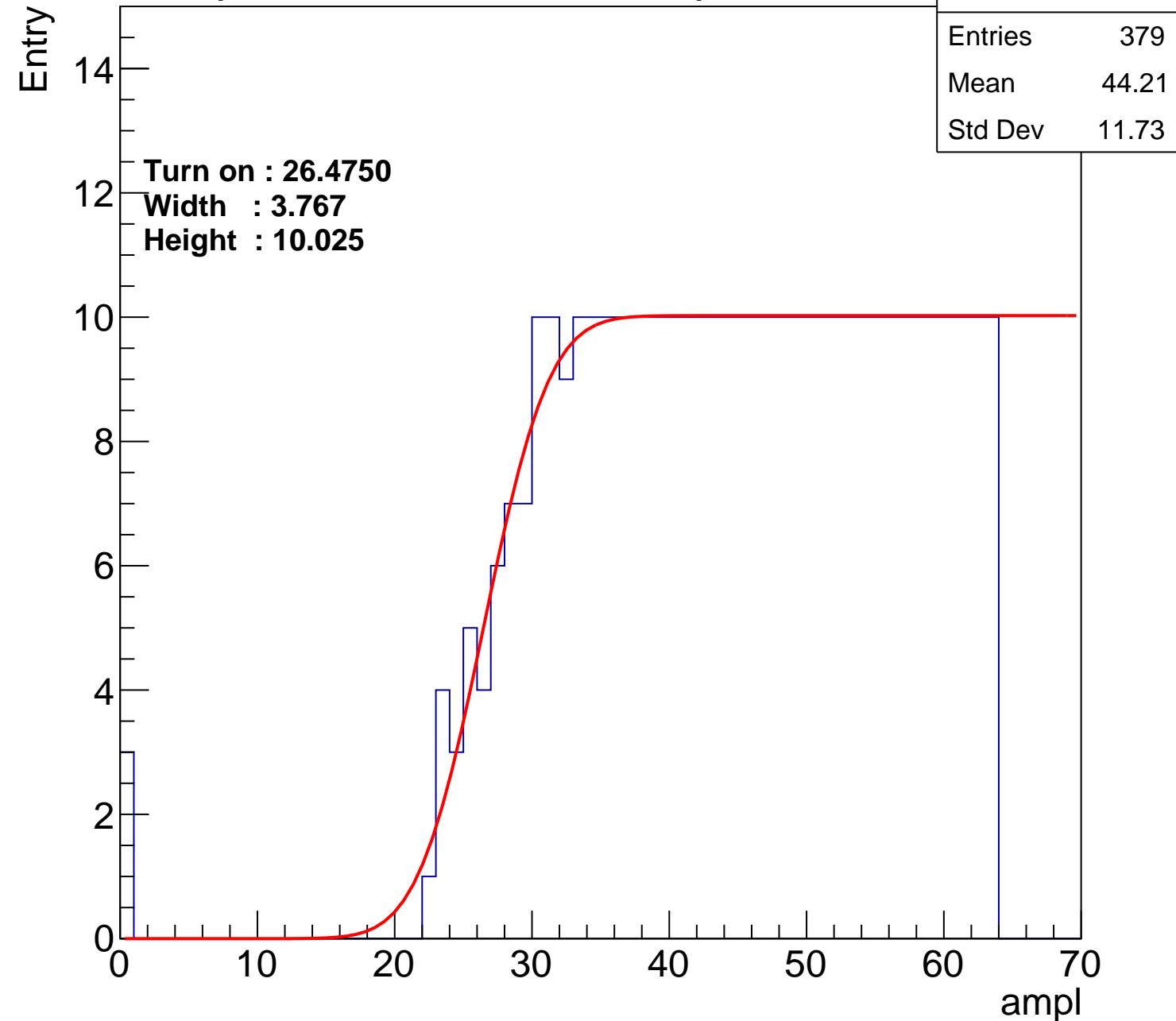
Width : 3.767

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch95

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.51 |
| Std Dev | 11.45 |

Turn on : 27.7655

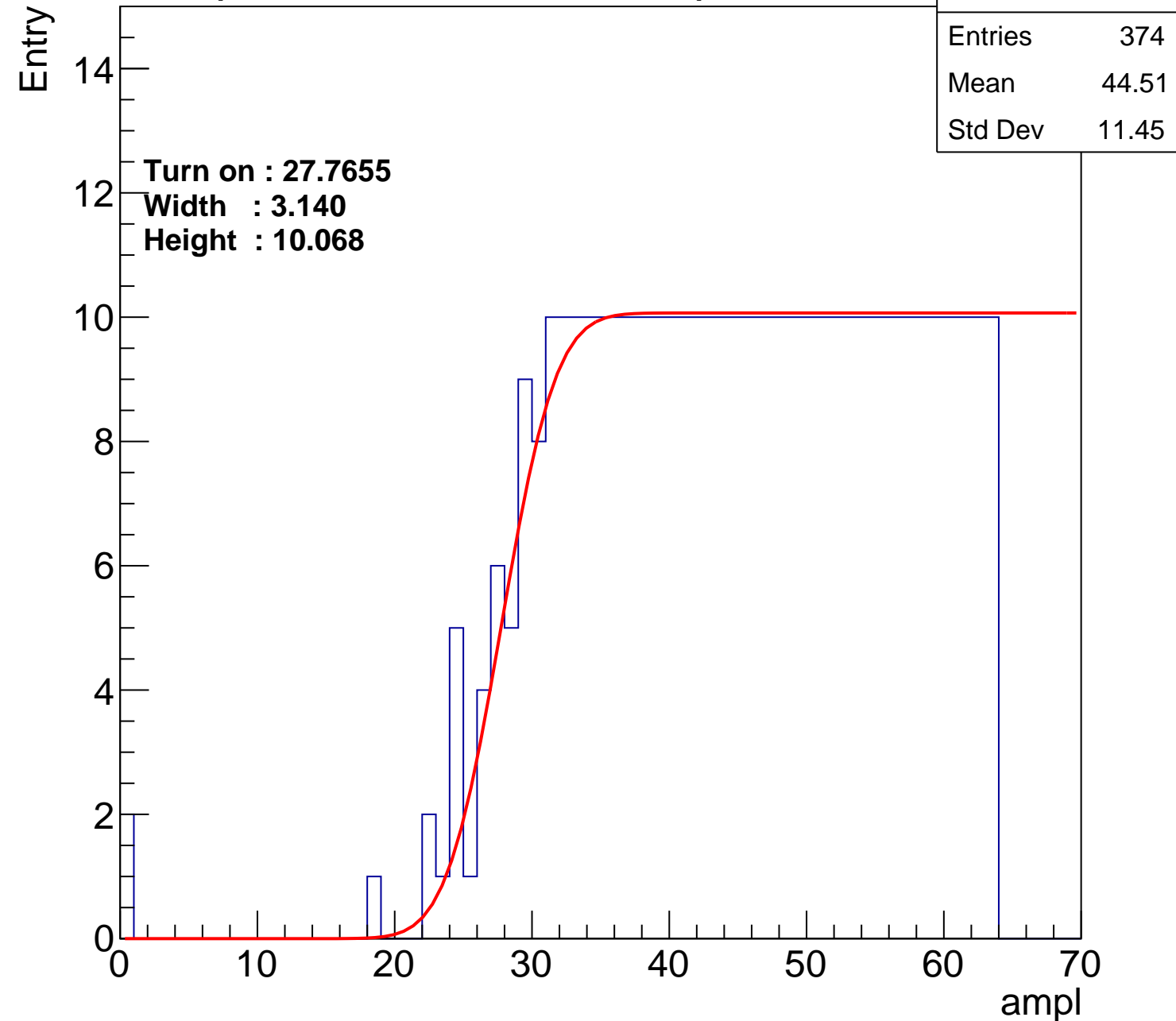
Width : 3.140

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch96

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.77 |
| Std Dev | 11.24 |

Turn on : 27.6409

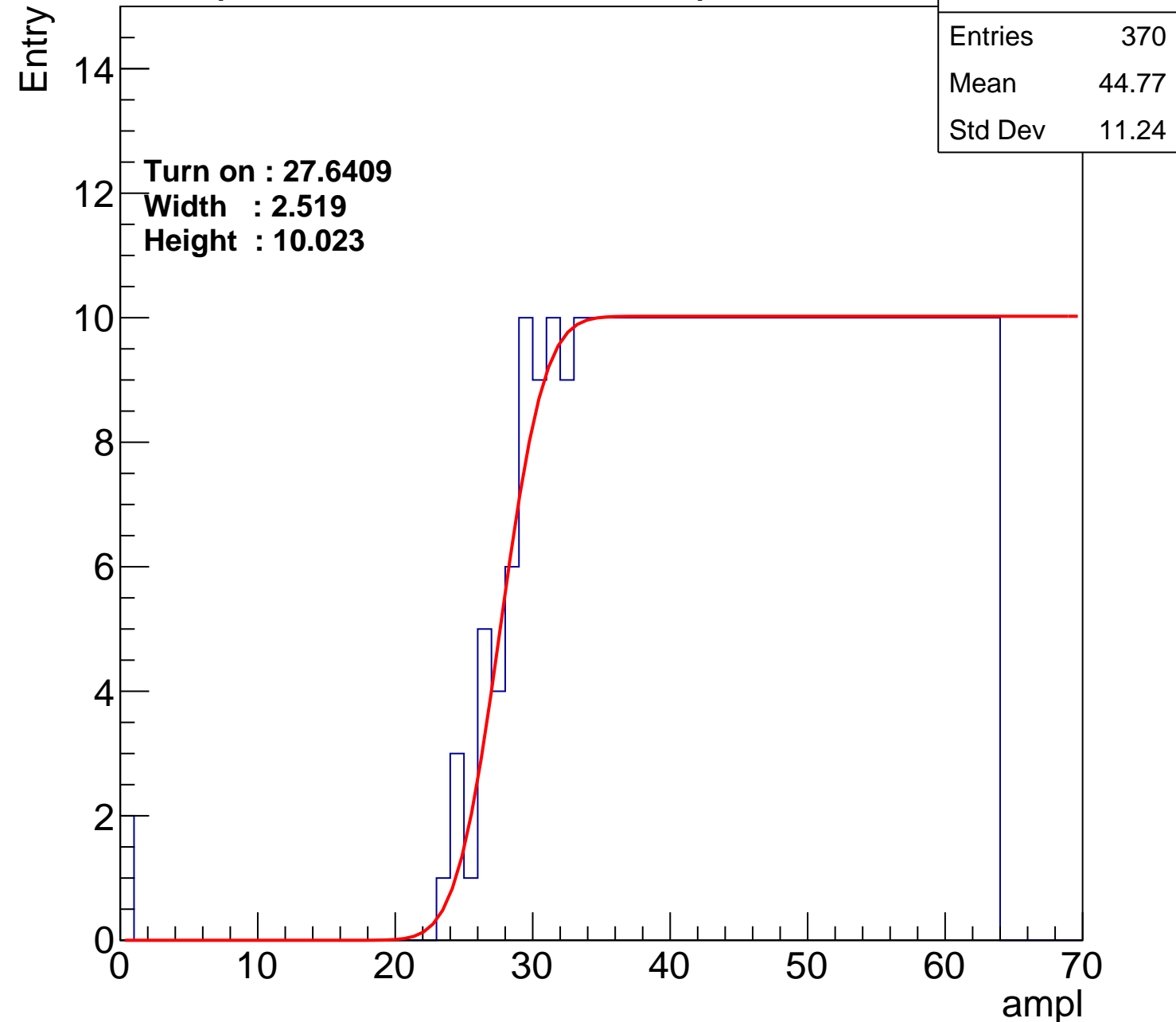
Width : 2.519

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch97

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.27 |
| Std Dev | 11.37 |

Turn on : 26.3144

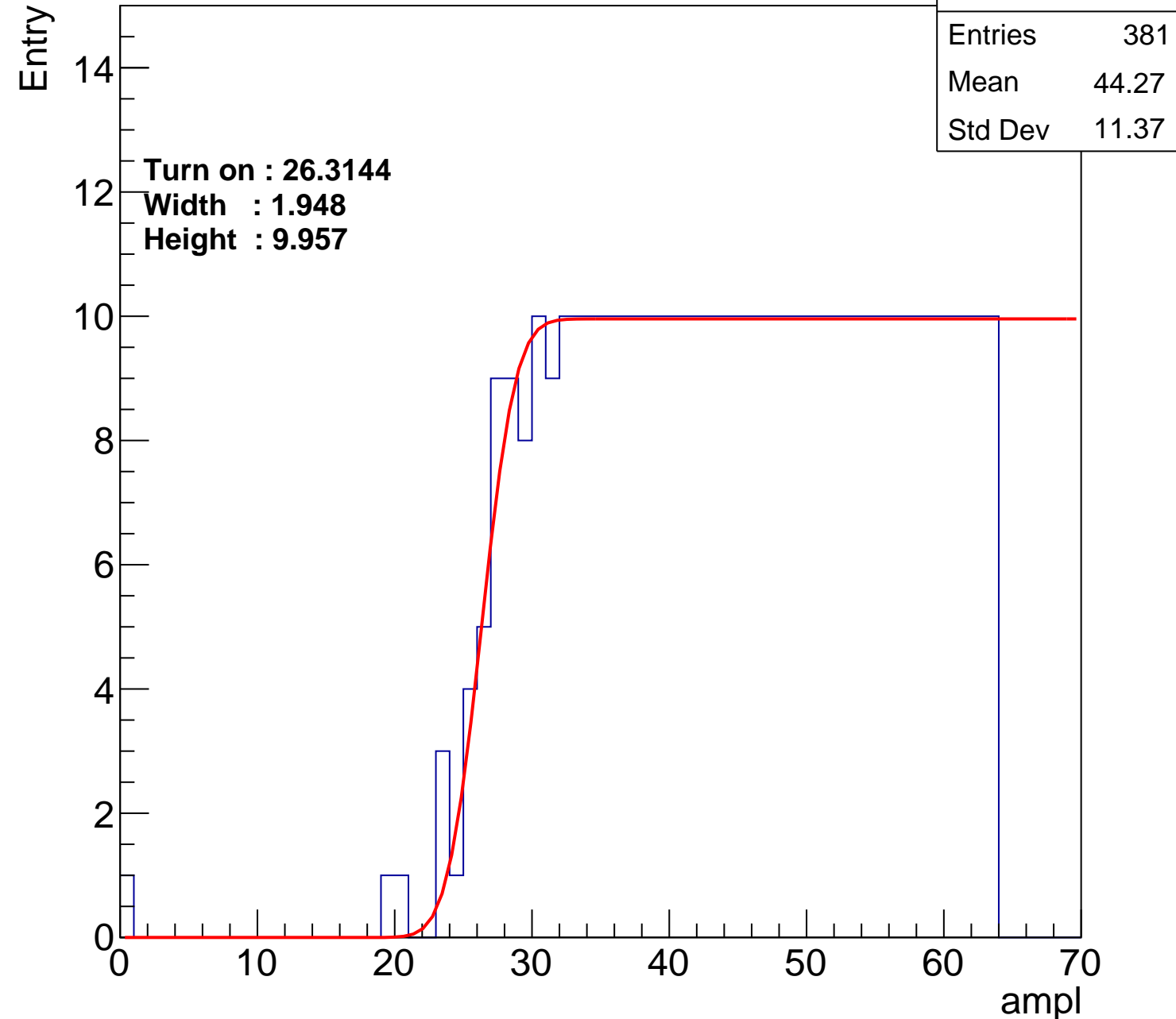
Width : 1.948

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch98

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.3 |
| Std Dev | 12.28 |

Turn on : 25.3647

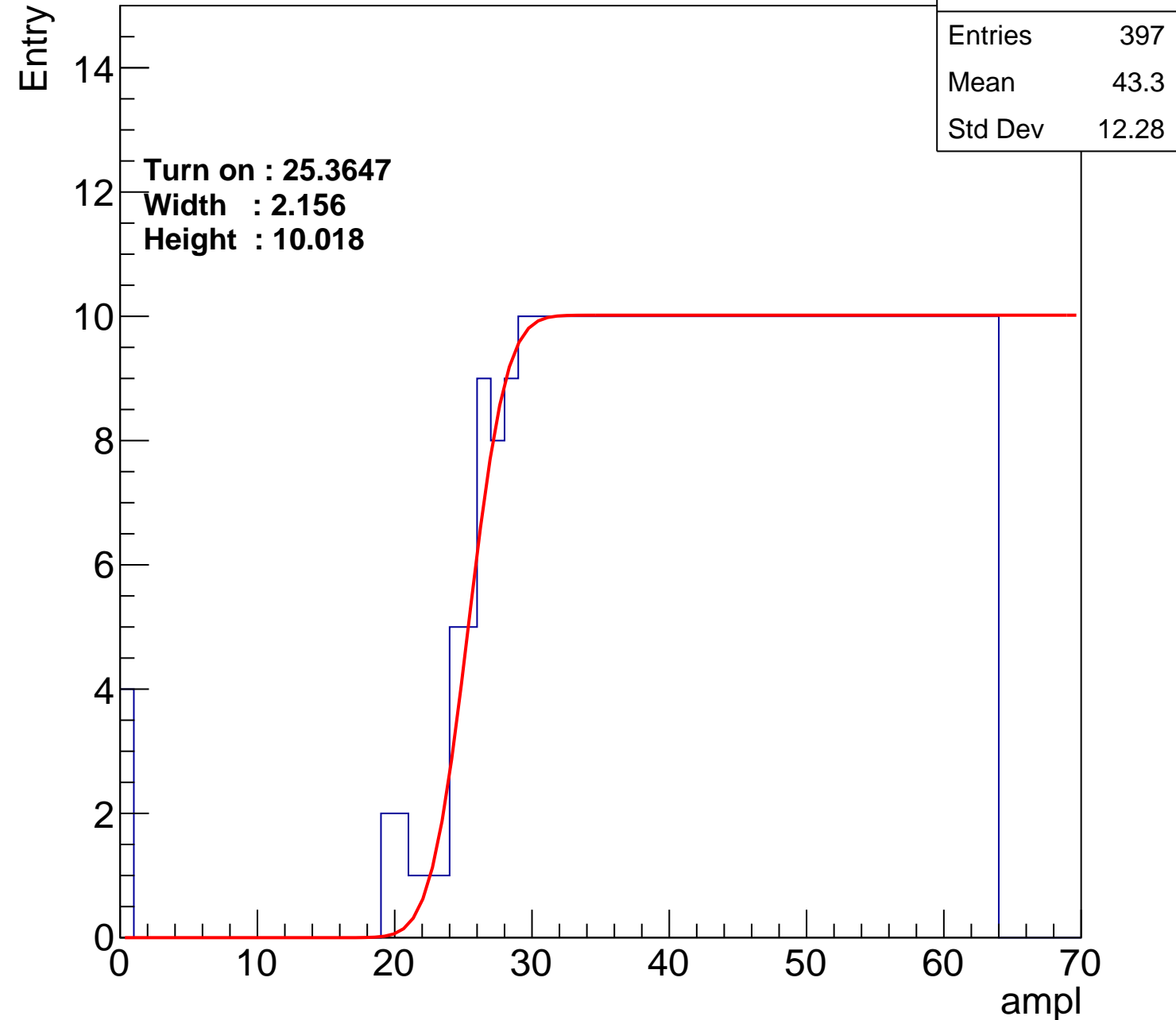
Width : 2.156

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch99

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.21 |
| Std Dev | 11.54 |

Turn on : 26.2483

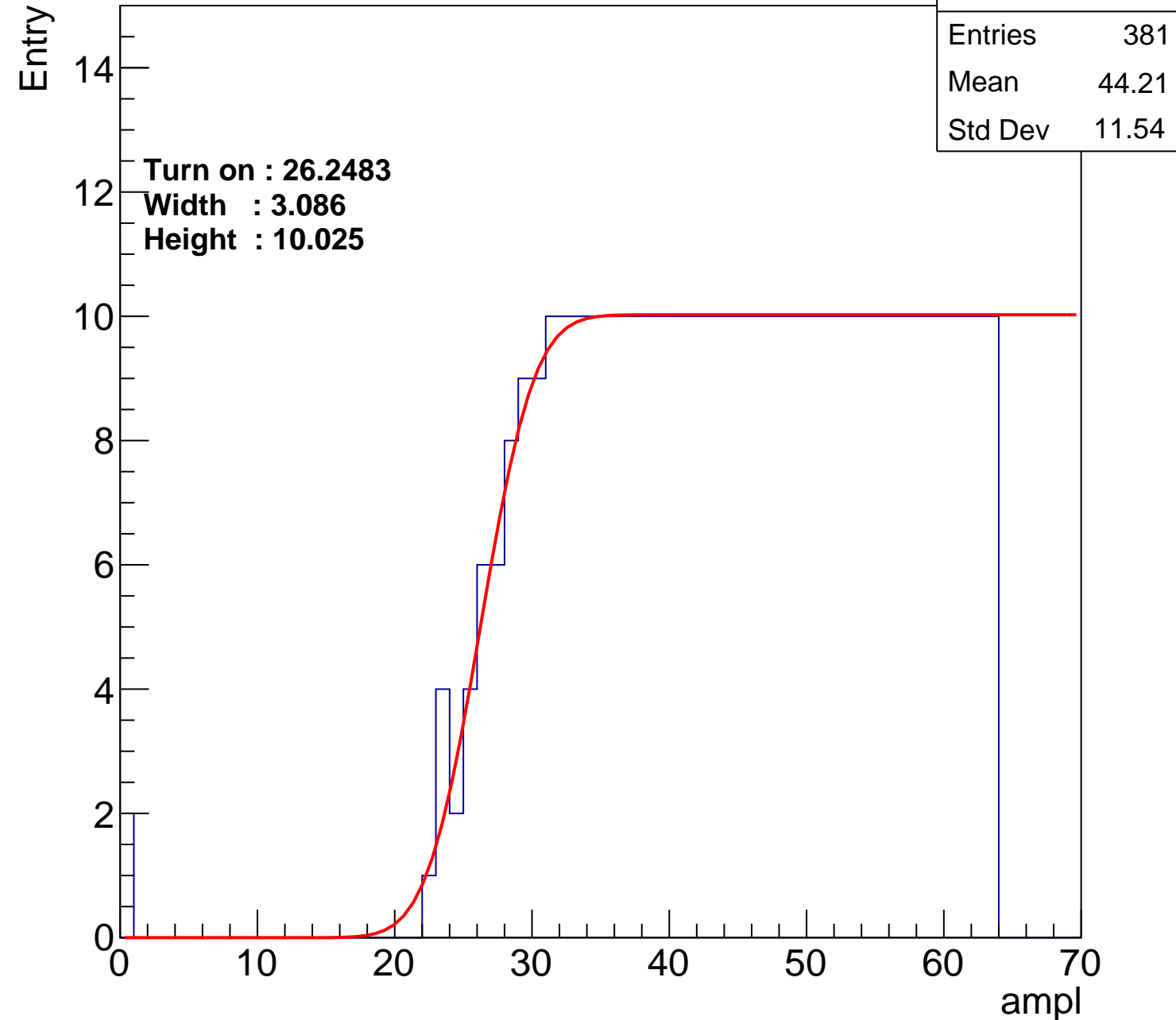
Width : 3.086

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch100

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.53 |
| Std Dev | 11.58 |

Turn on : 27.3308

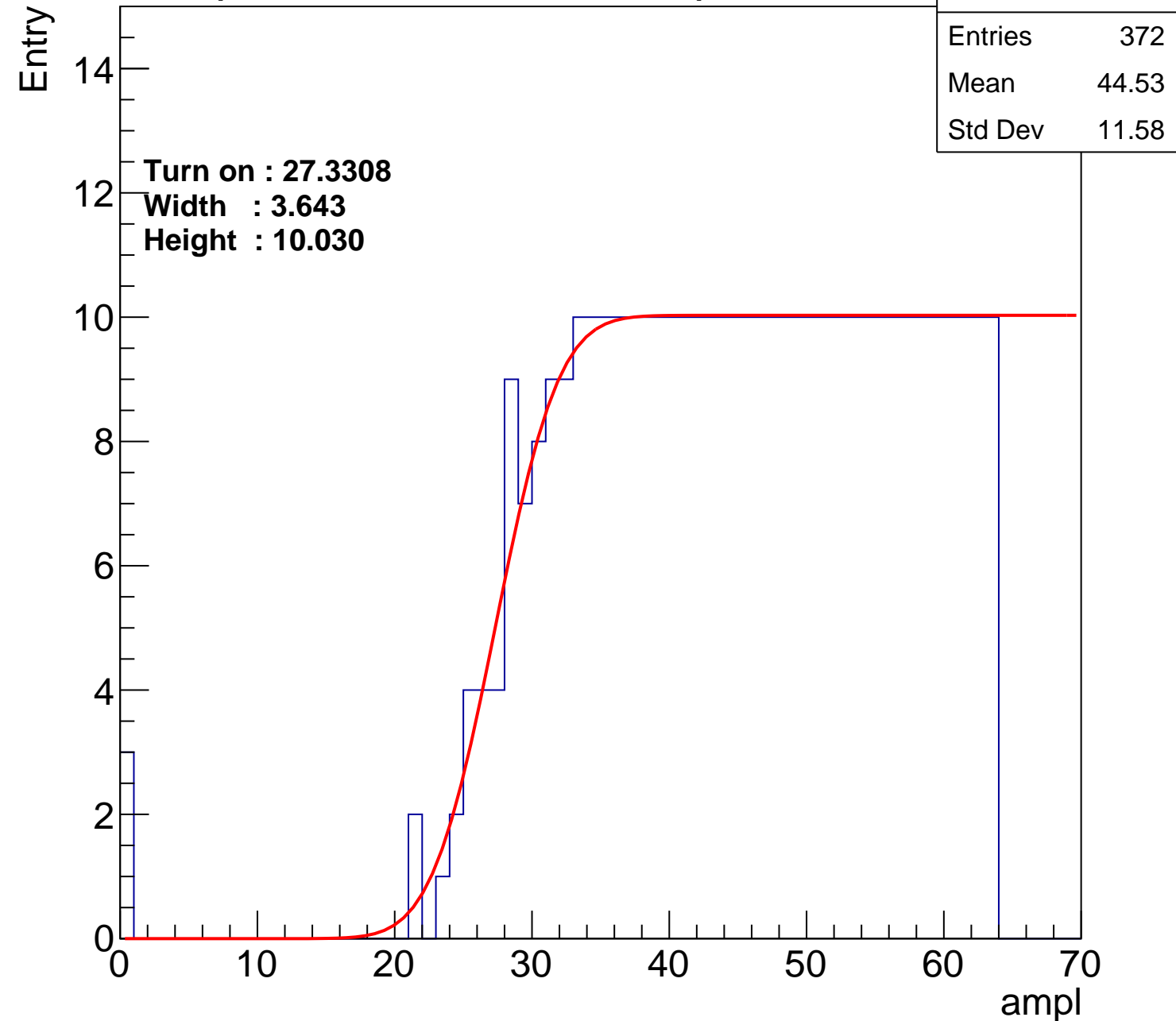
Width : 3.643

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch101

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.68 |
| Std Dev | 11.46 |

Turn on : 27.4713

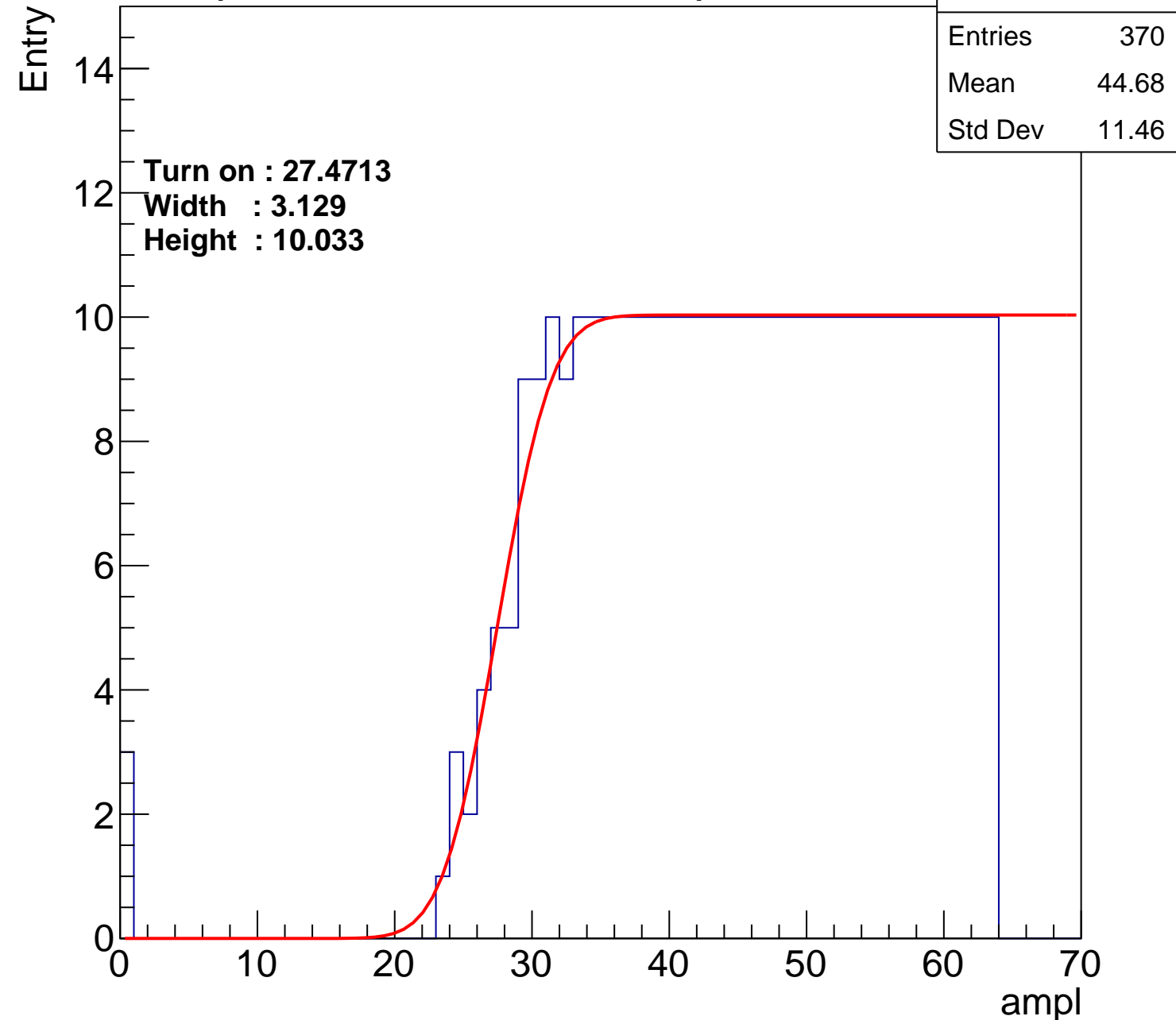
Width : 3.129

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch102

calib_packv5_042523_0143.root, FC#4, port A2

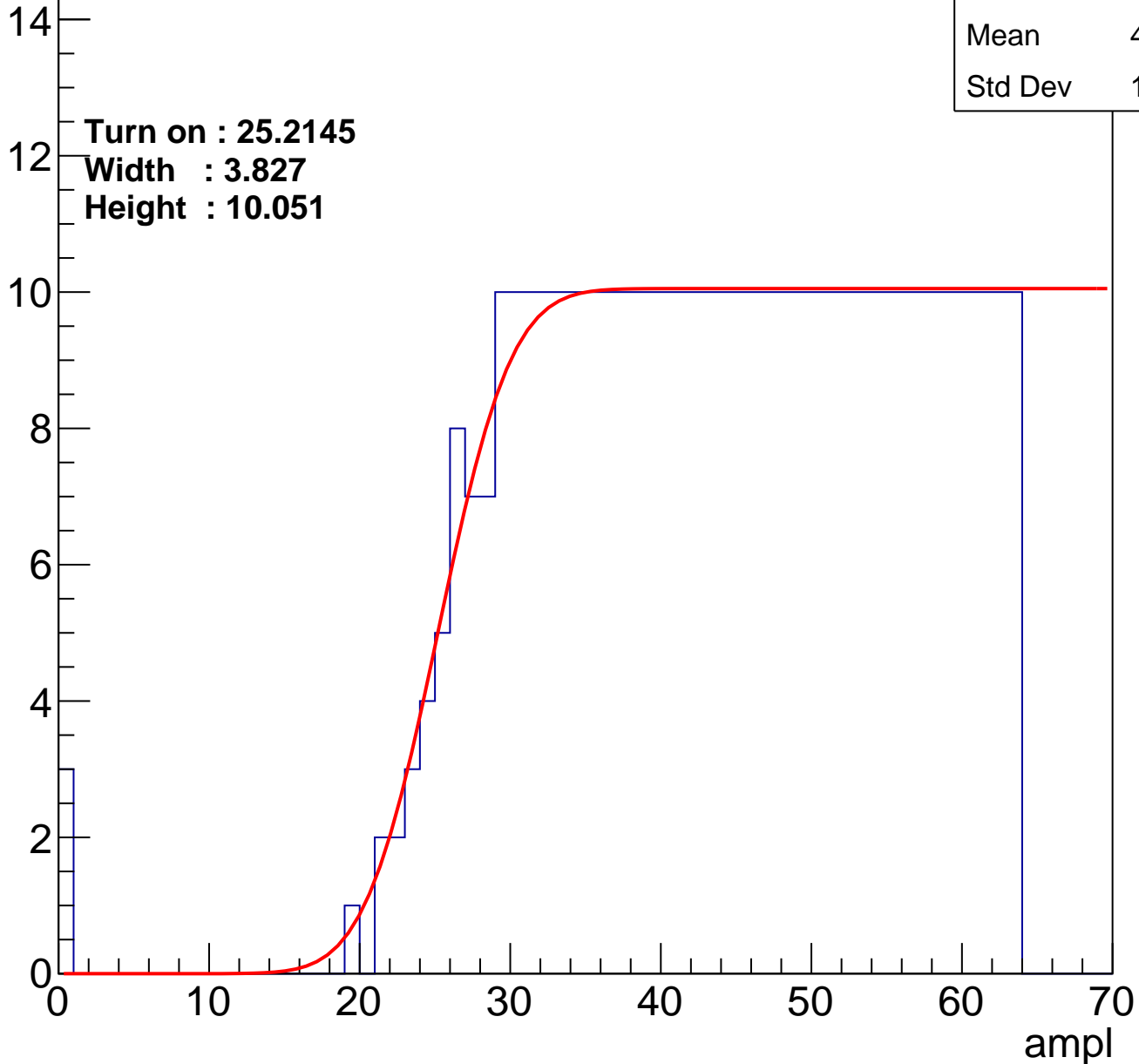
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.59 |
| Std Dev | 12.02 |

Turn on : 25.2145

Width : 3.827

Height : 10.051

Entry



B1L100S, U1-ch103

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.56 |
| Std Dev | 11.36 |

Turn on : 27.1663

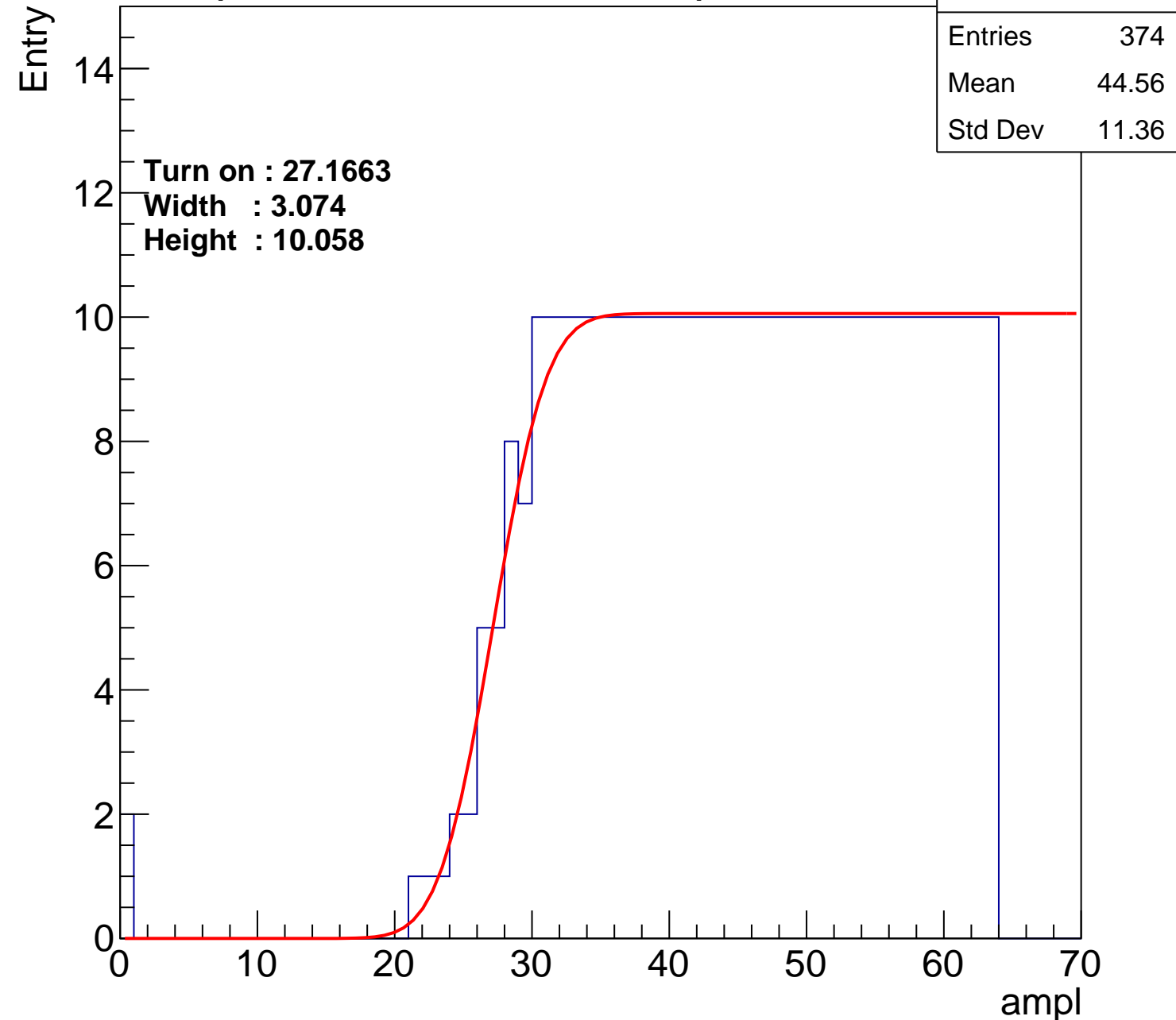
Width : 3.074

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch104

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.22 |
| Std Dev | 11.67 |

Turn on : 26.7733

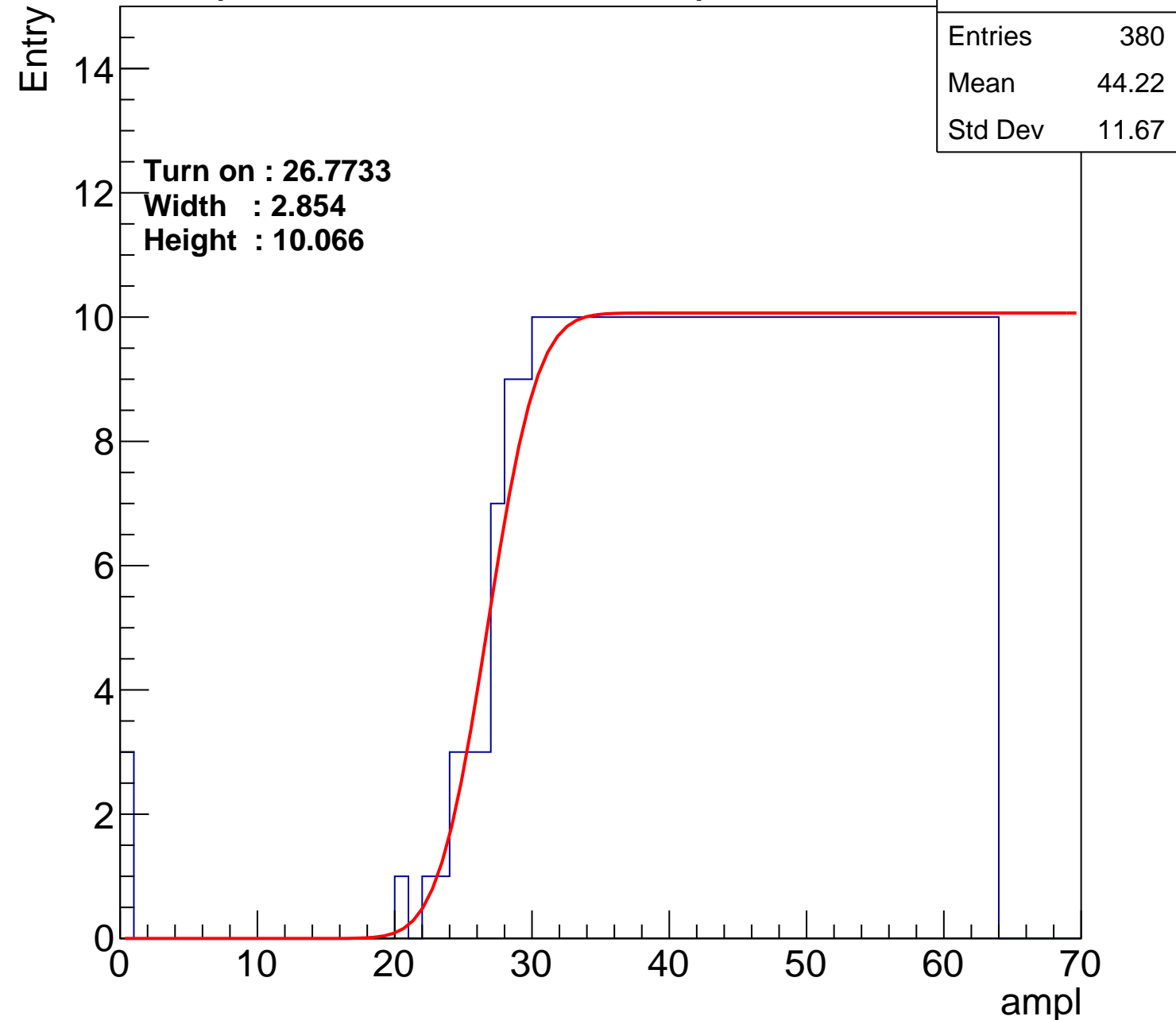
Width : 2.854

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch105

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.97 |
| Std Dev | 10.99 |

Turn on : 28.1078

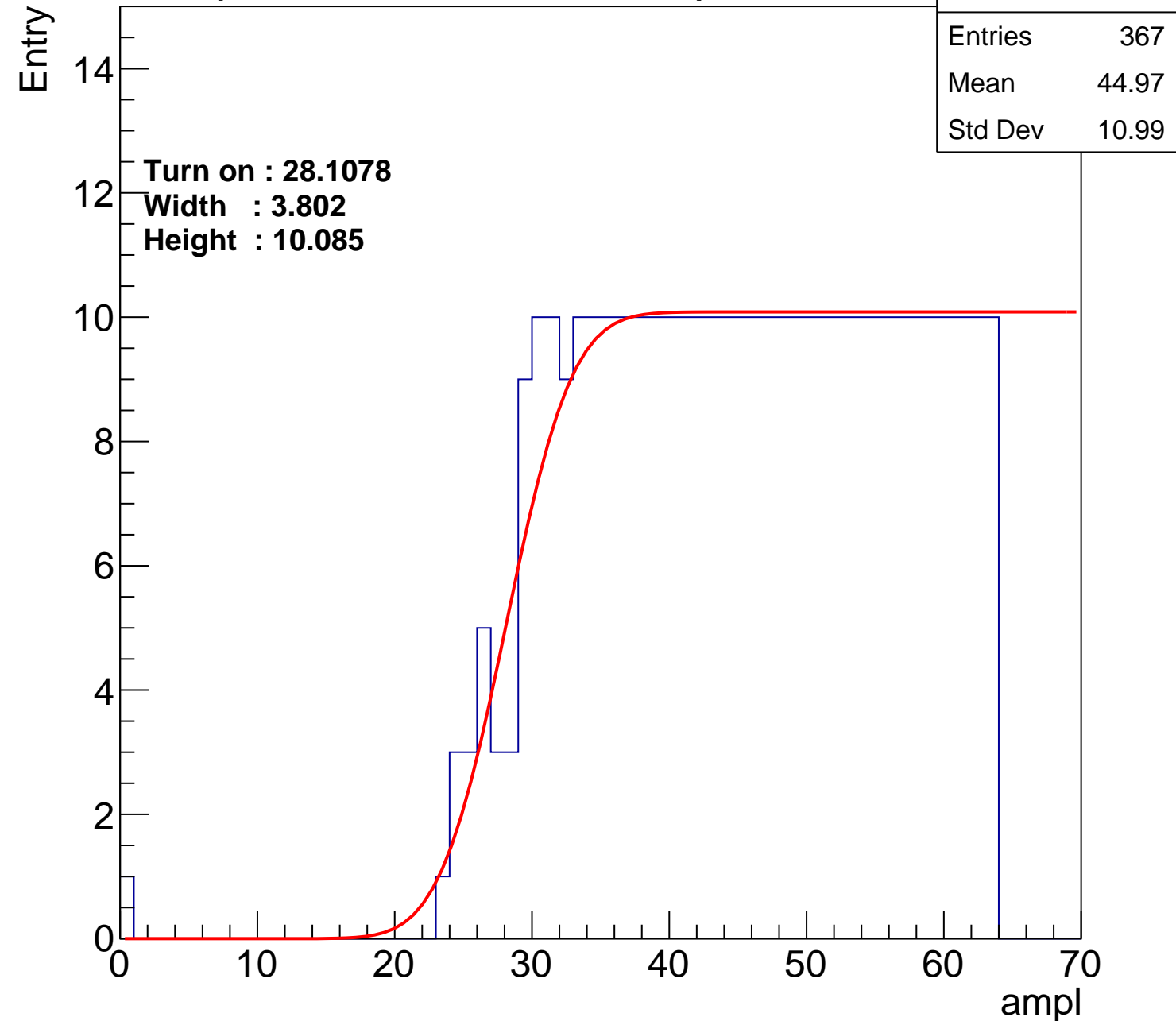
Width : 3.802

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch106

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.44 |
| Std Dev | 11.99 |

Turn on : 24.4042

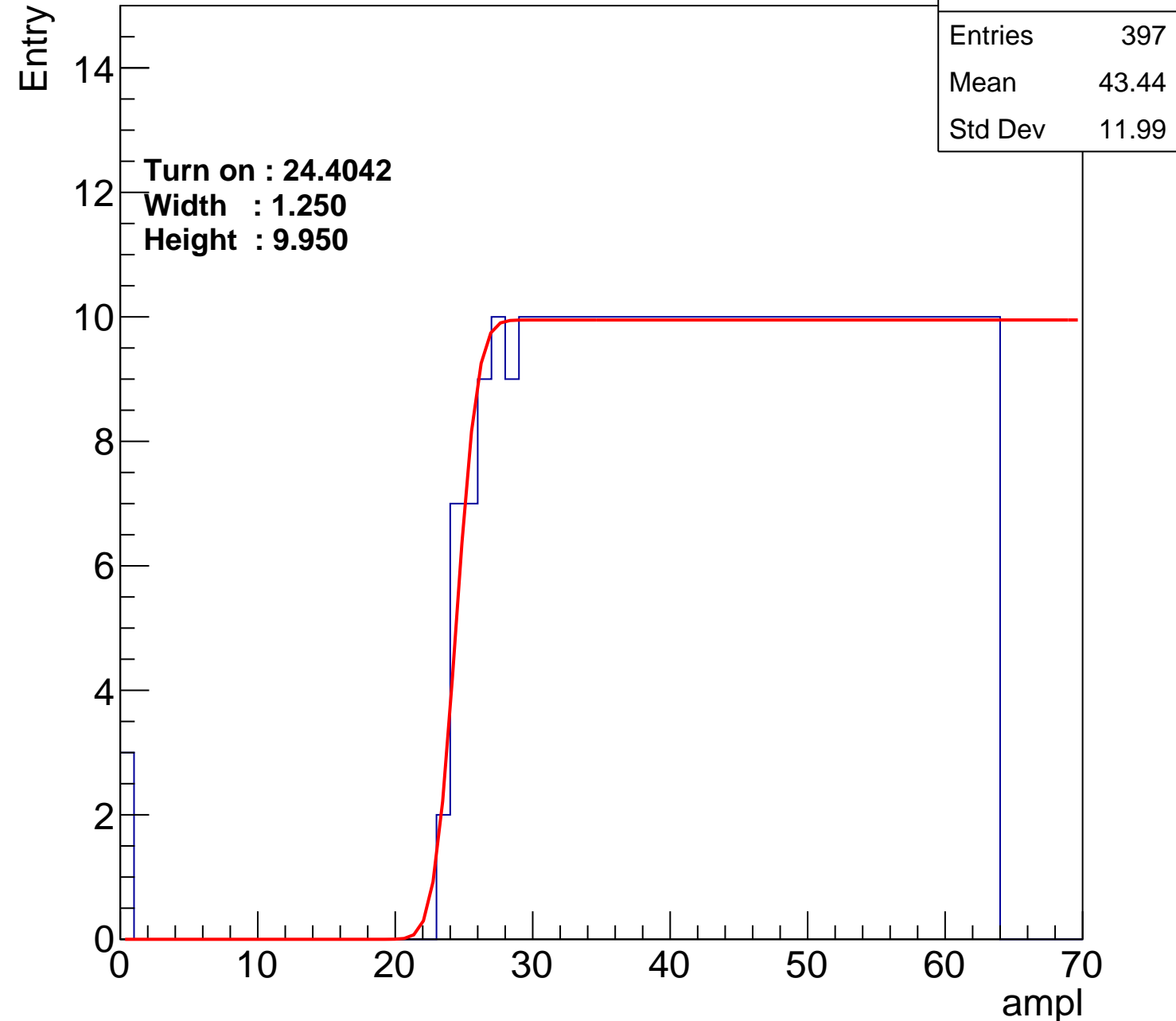
Width : 1.250

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch107

calib_packv5_042523_0143.root, FC#4, port A2

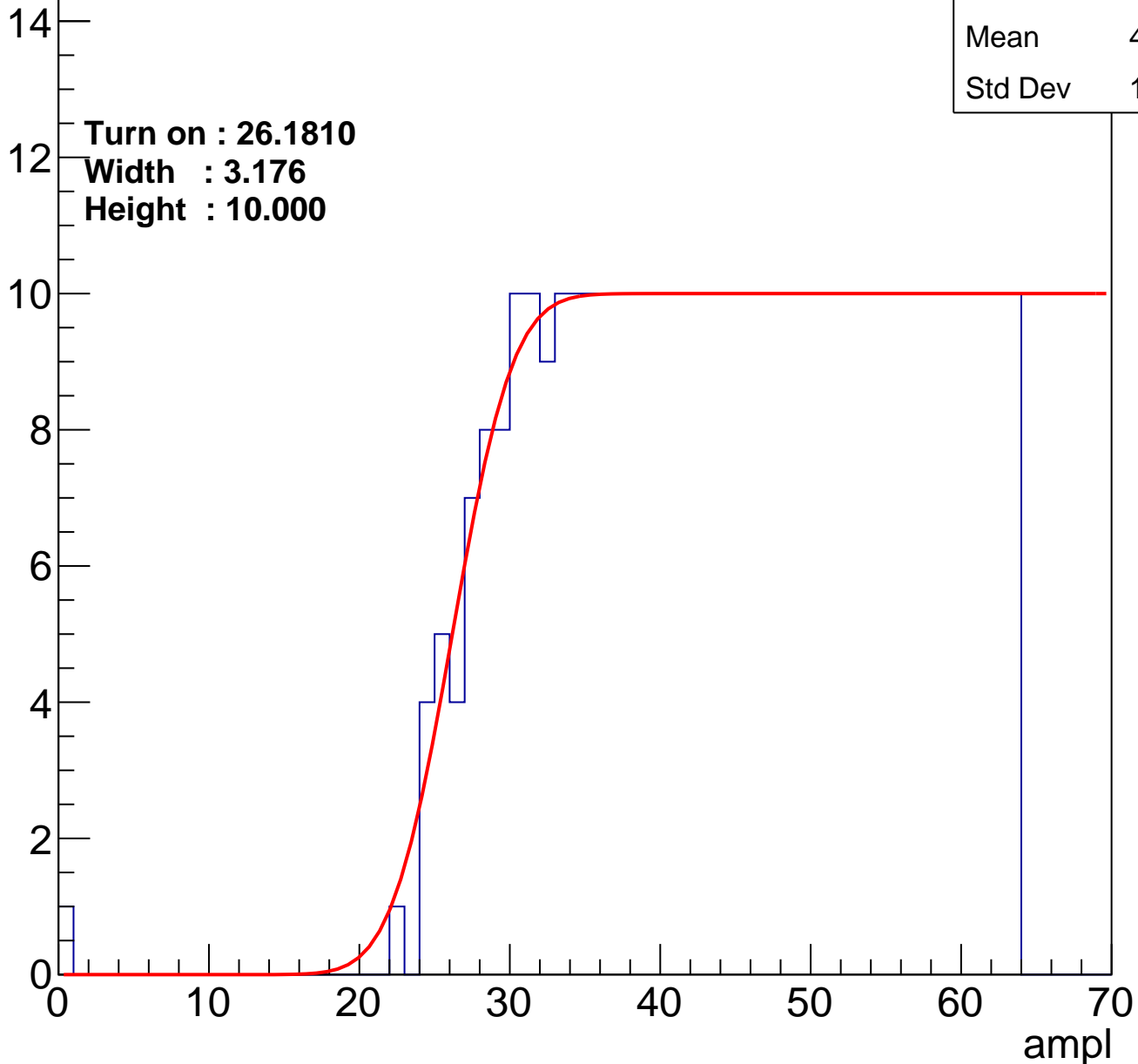
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.48 |
| Std Dev | 11.24 |

Turn on : 26.1810

Width : 3.176

Height : 10.000

Entry



B1L100S, U1-ch108

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.72 |
| Std Dev | 11.93 |

Turn on : 26.0786

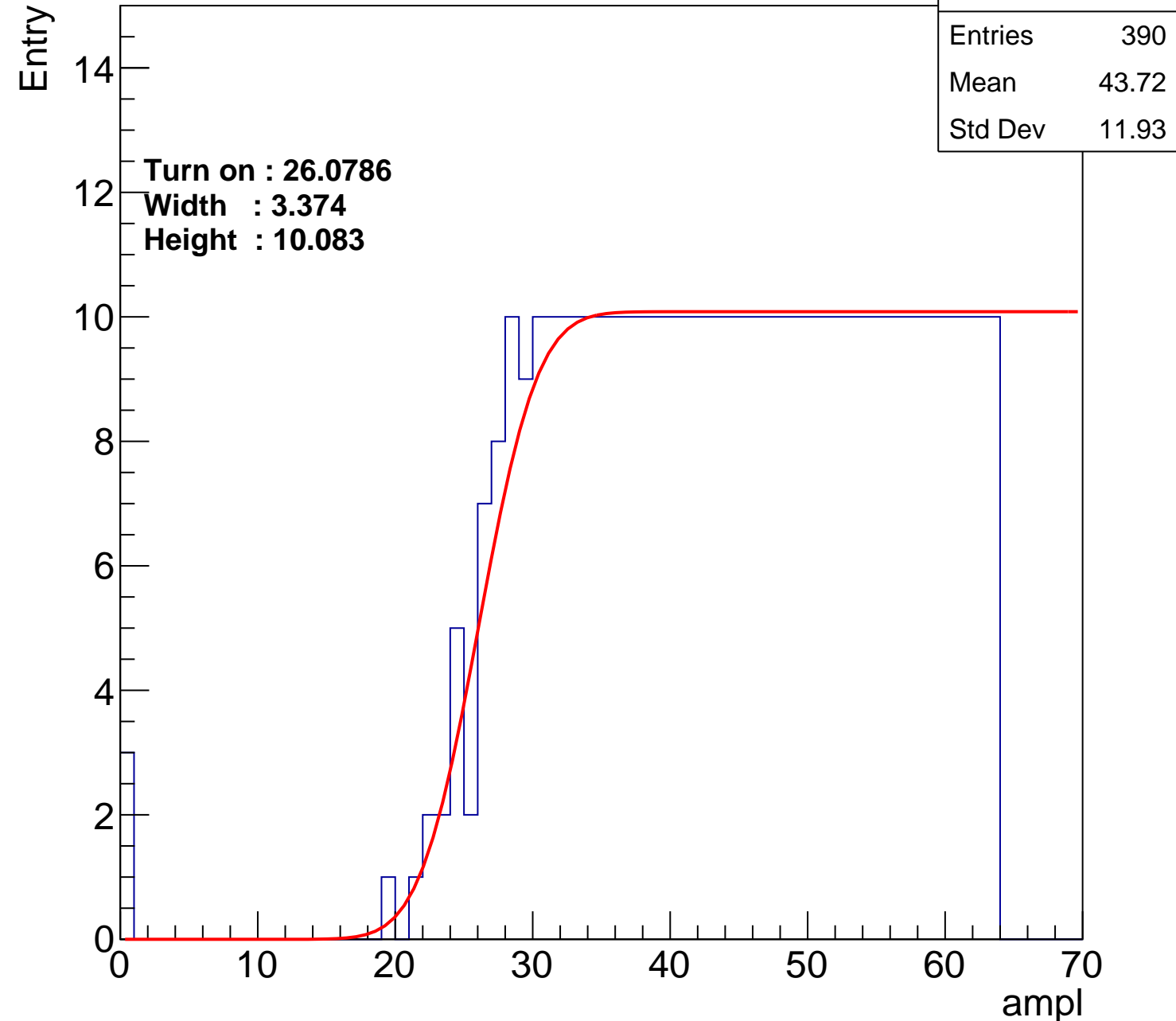
Width : 3.374

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch109

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.54 |
| Std Dev | 10.65 |

Turn on : 28.5056

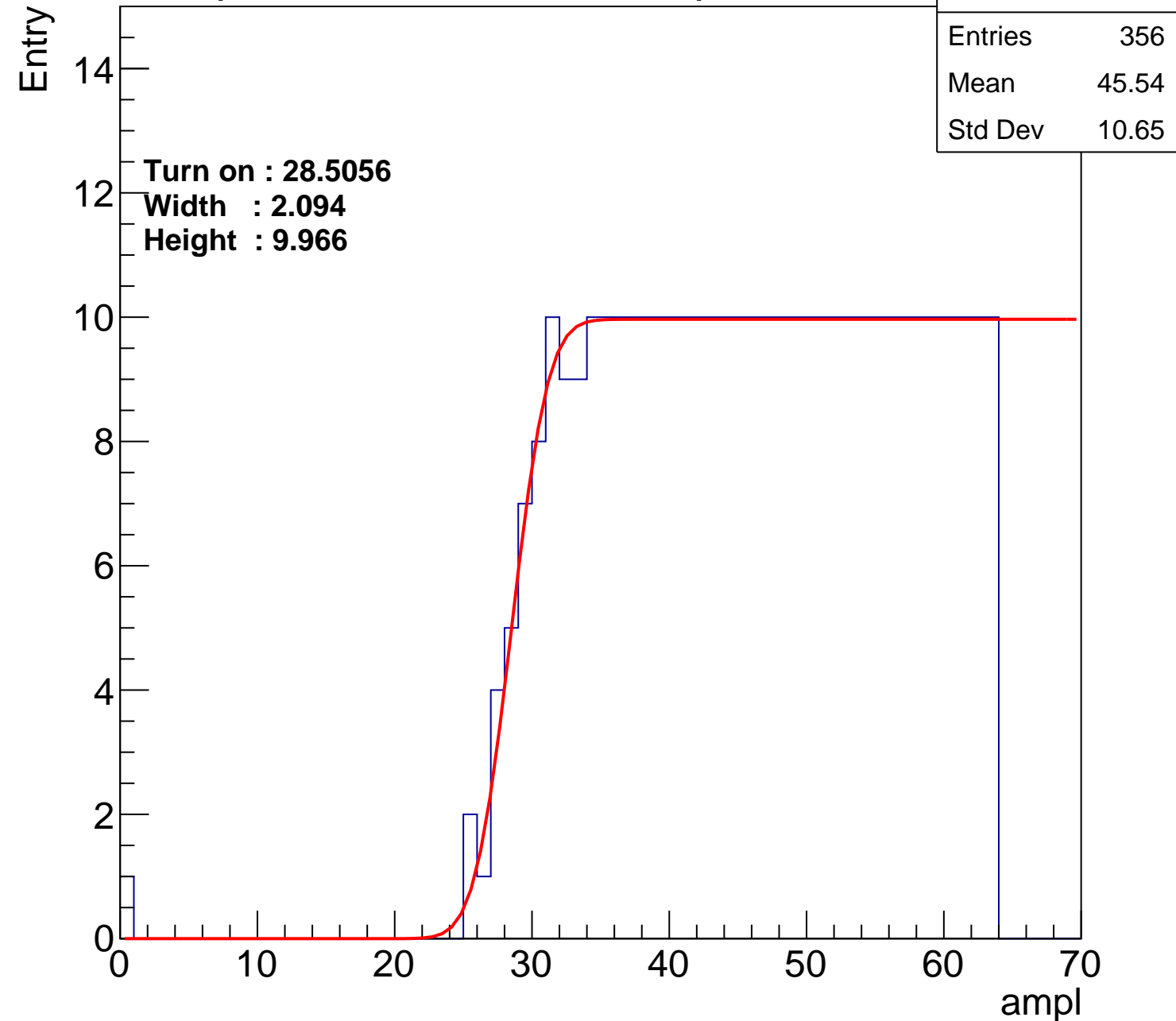
Width : 2.094

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch110

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.26 |
| Std Dev | 11.34 |

Turn on : 25.9961

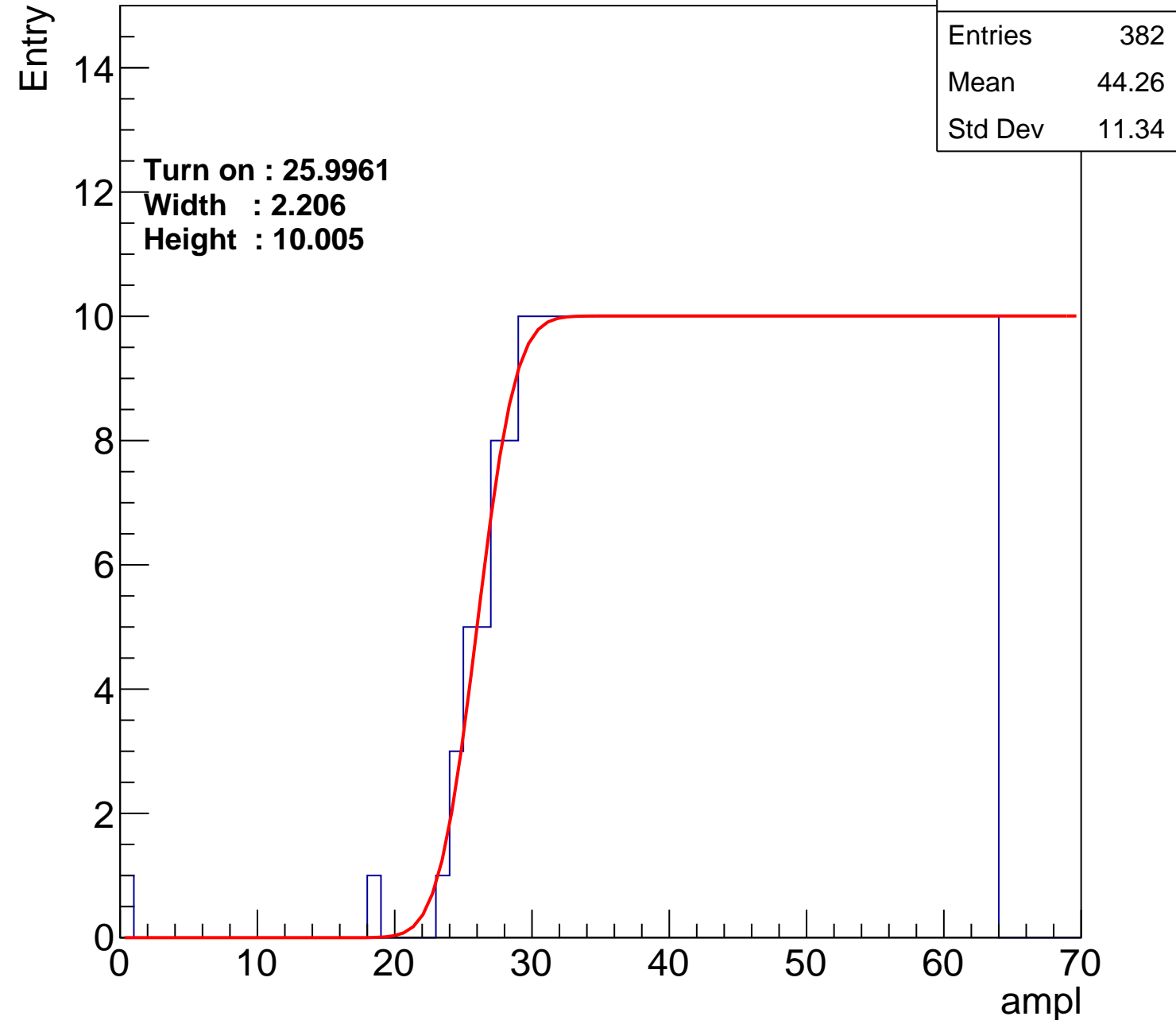
Width : 2.206

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch111

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.88 |
| Std Dev | 11.59 |

Turn on : 25.8937

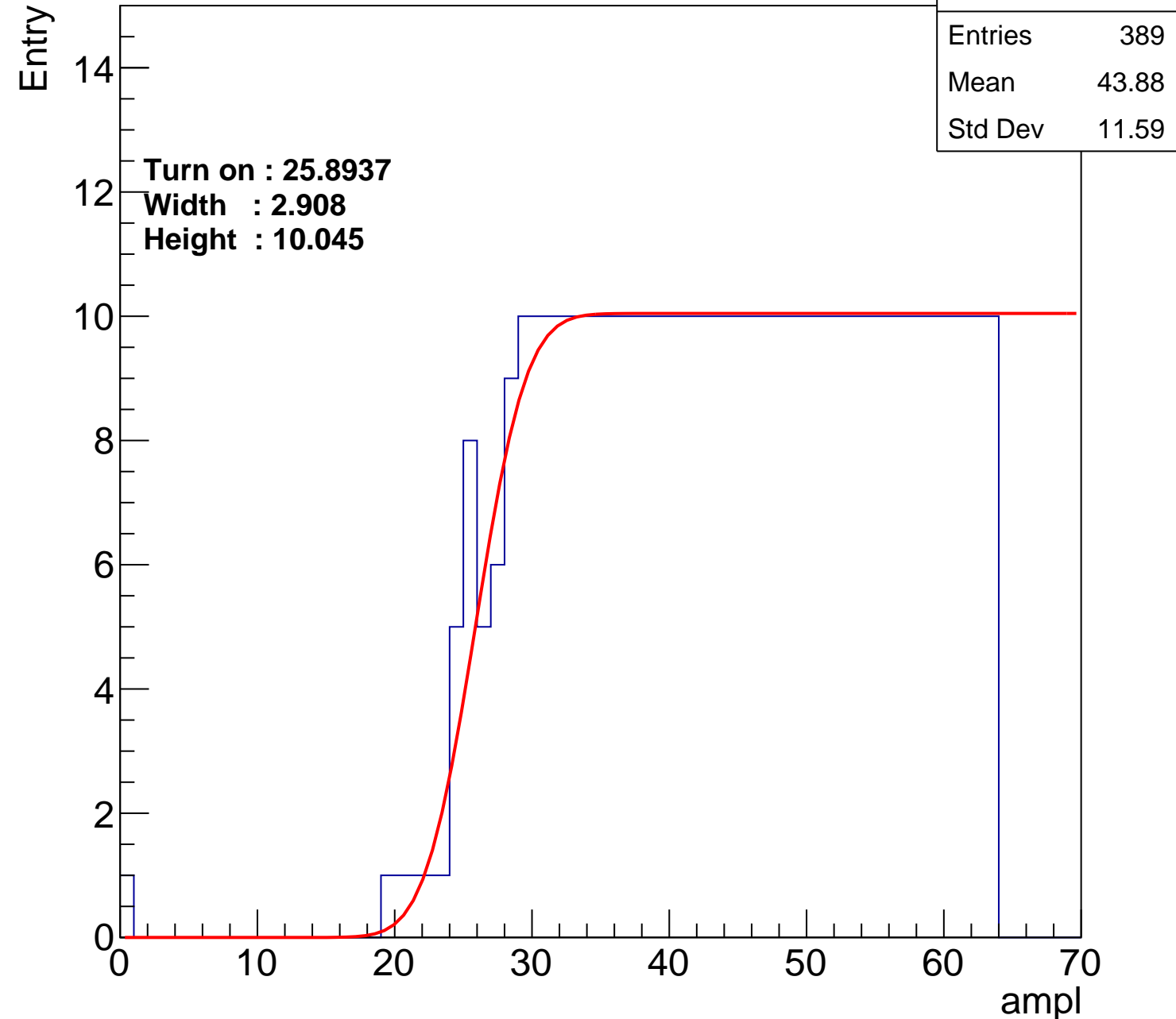
Width : 2.908

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch112

calib_packv5_042523_0143.root, FC#4, port A2

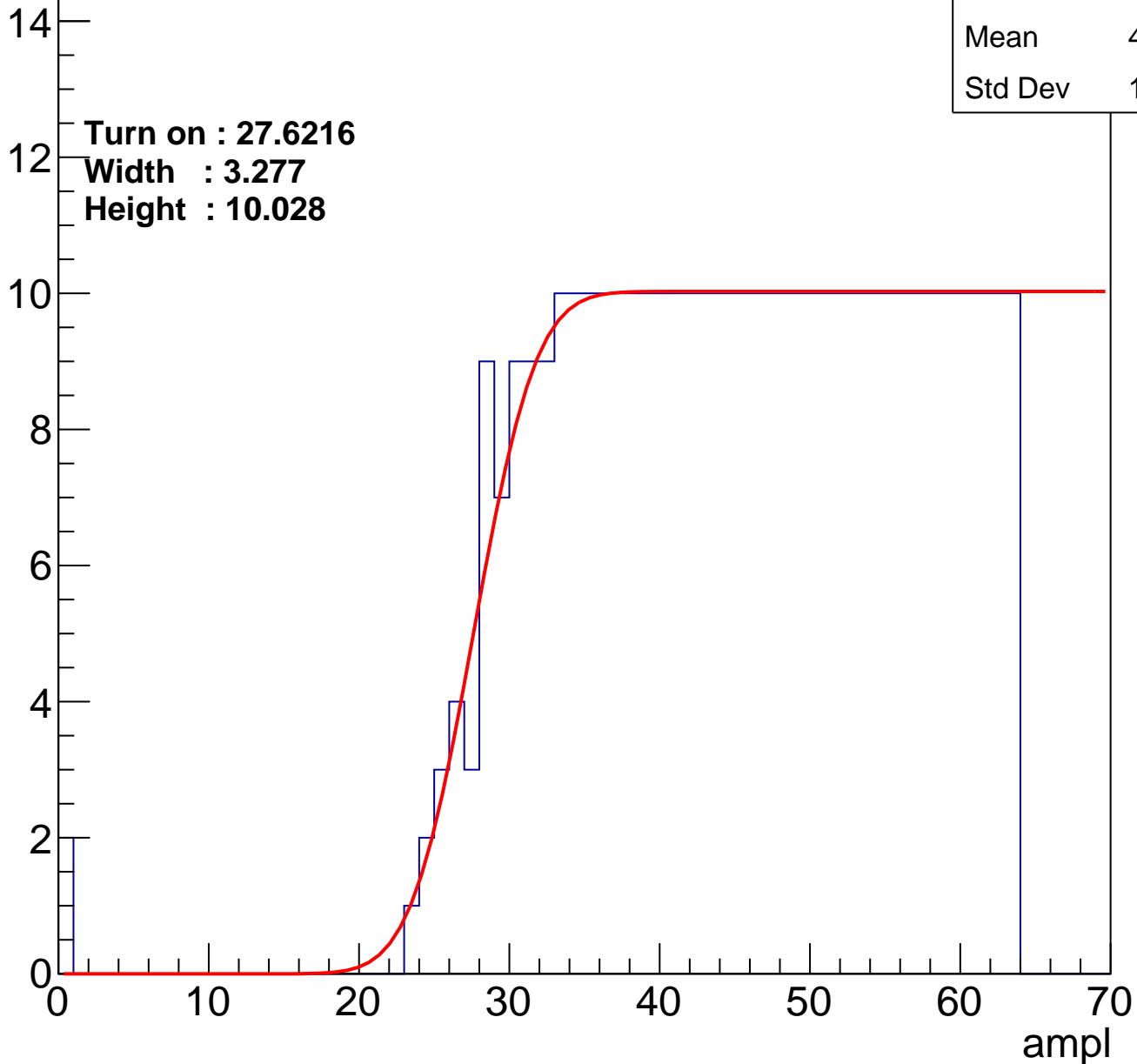
| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.85 |
| Std Dev | 11.22 |

Turn on : 27.6216

Width : 3.277

Height : 10.028

Entry



B1L100S, U1-ch113

calib_packv5_042523_0143.root, FC#4, port A2

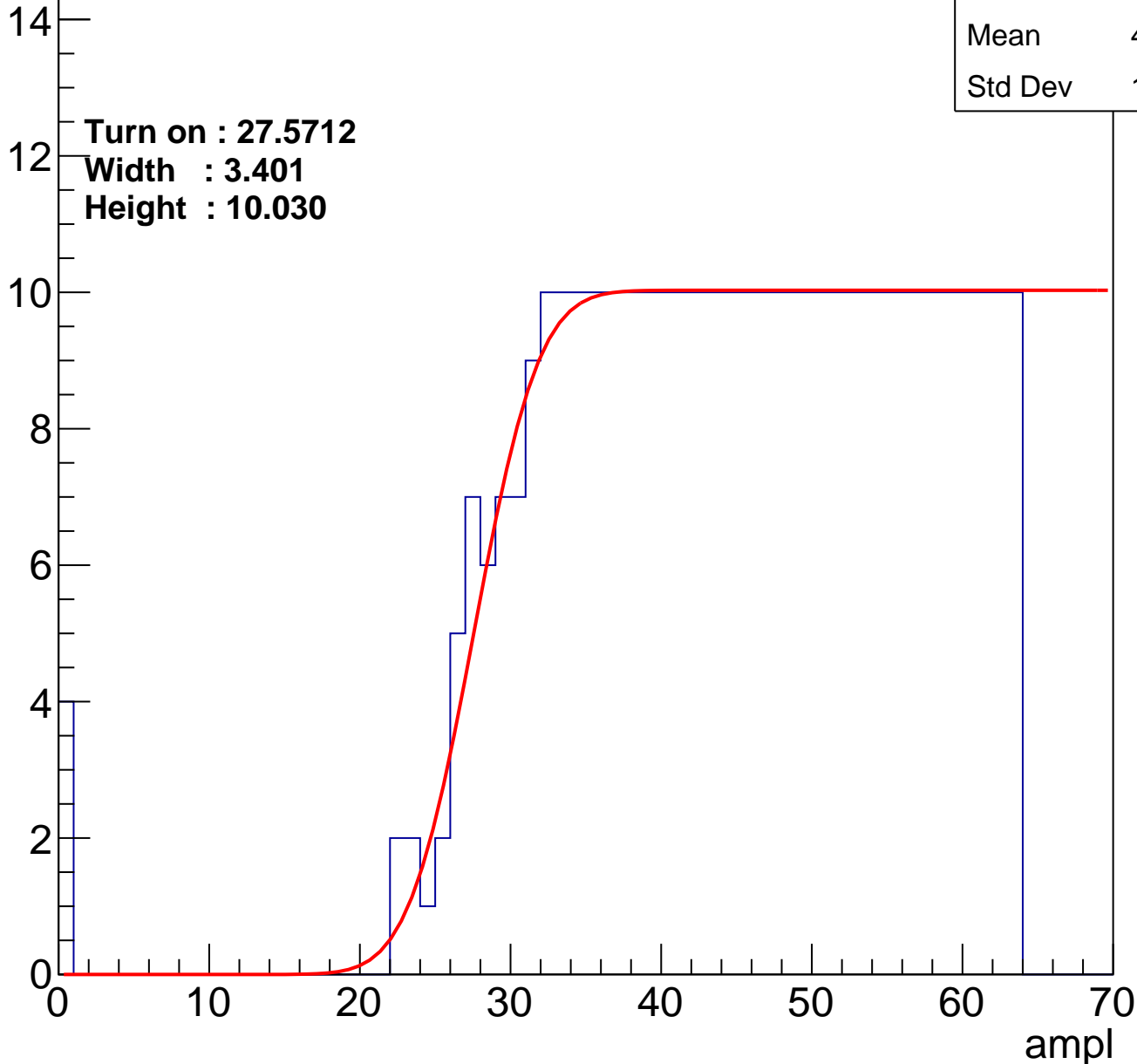
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.47 |
| Std Dev | 11.76 |

Turn on : 27.5712

Width : 3.401

Height : 10.030

Entry



B1L100S, U1-ch114

calib_packv5_042523_0143.root, FC#4, port A2

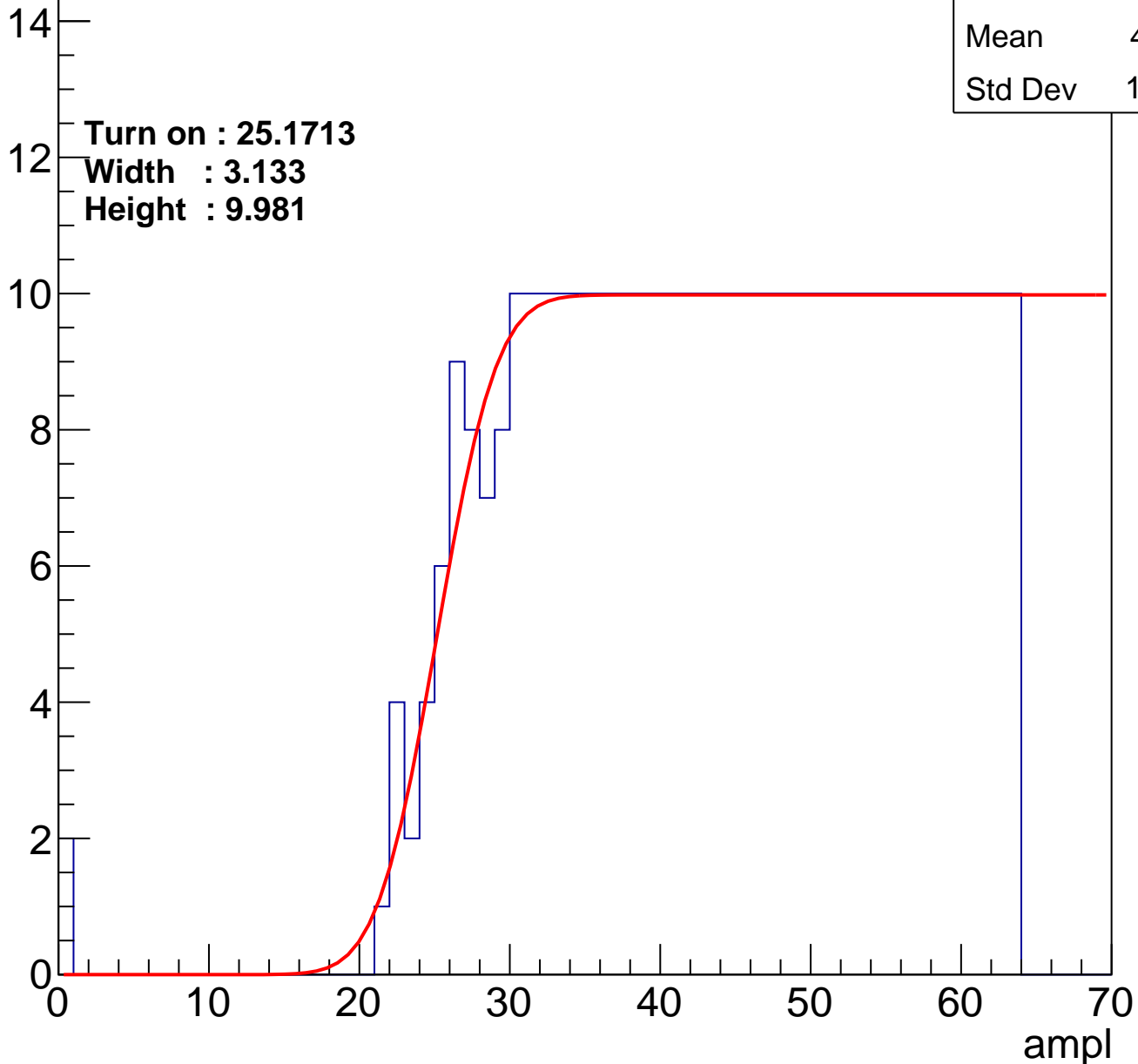
| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.71 |
| Std Dev | 11.82 |

Turn on : 25.1713

Width : 3.133

Height : 9.981

Entry



B1L100S, U1-ch115

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.44 |
| Std Dev | 11.36 |

Turn on : 26.8898

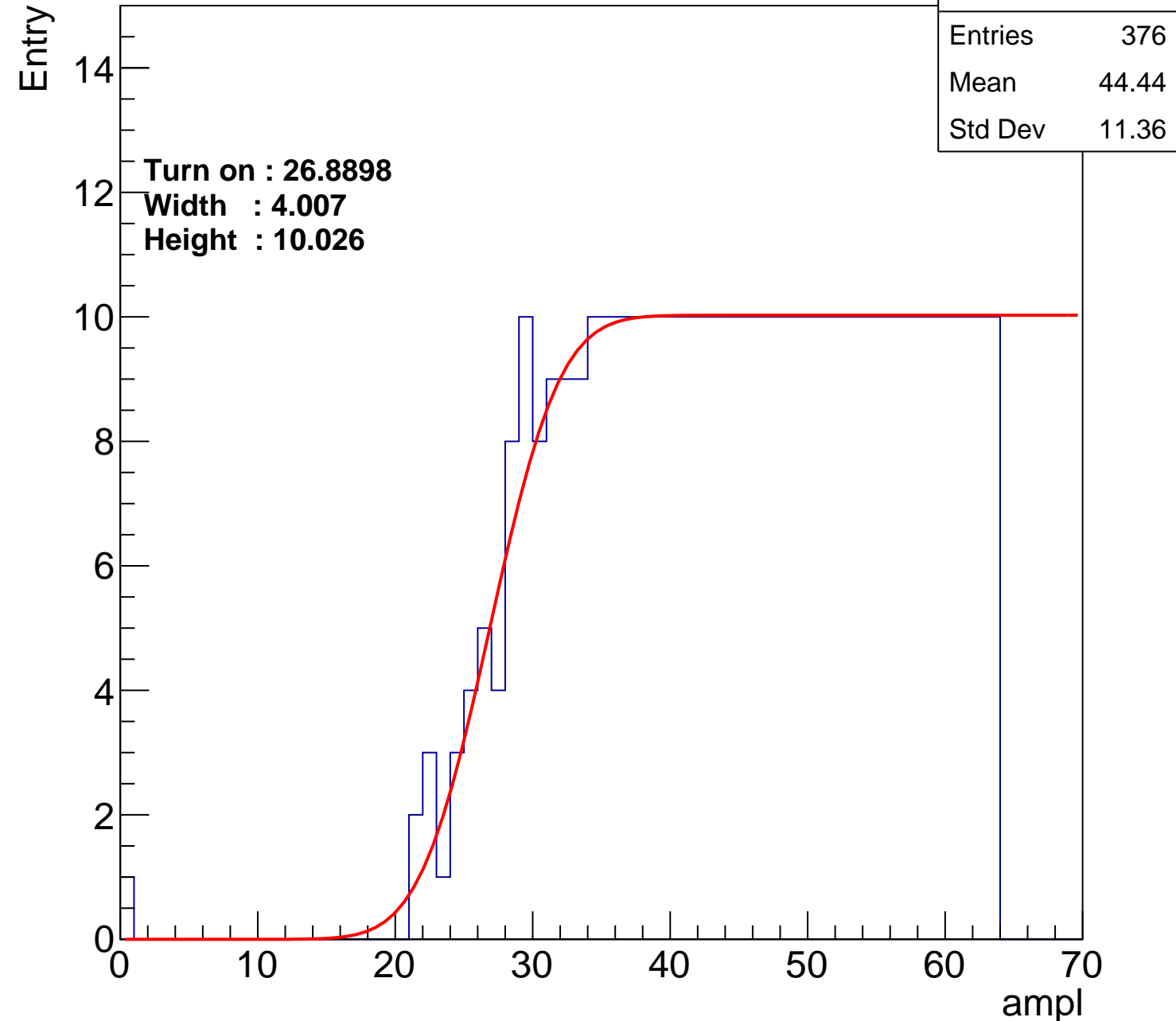
Width : 4.007

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch116

calib_packv5_042523_0143.root, FC#4, port A2

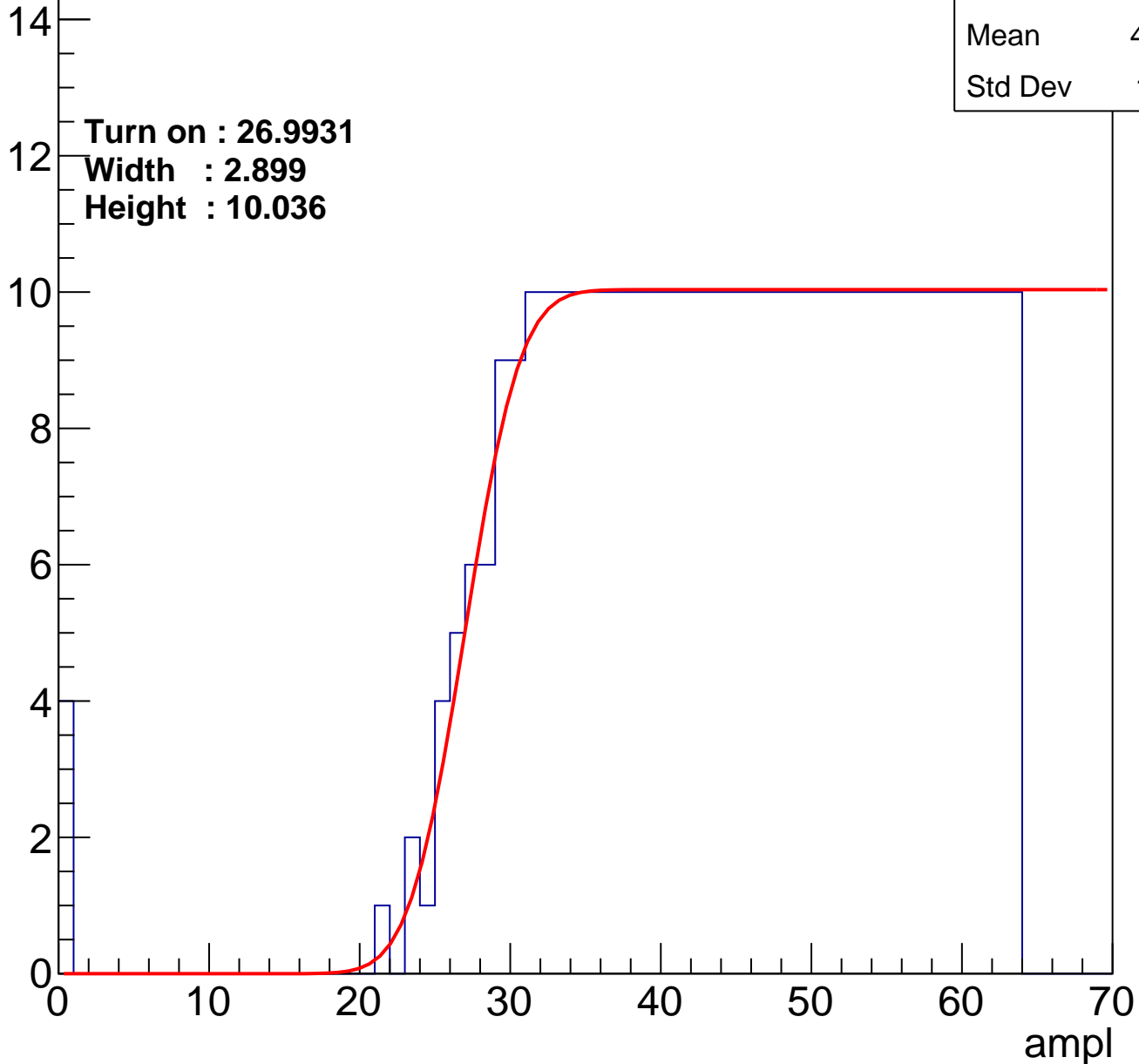
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.28 |
| Std Dev | 11.81 |

Turn on : 26.9931

Width : 2.899

Height : 10.036

Entry



B1L100S, U1-ch117

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 44.97 |
| Std Dev | 11.53 |

Turn on : 28.5616

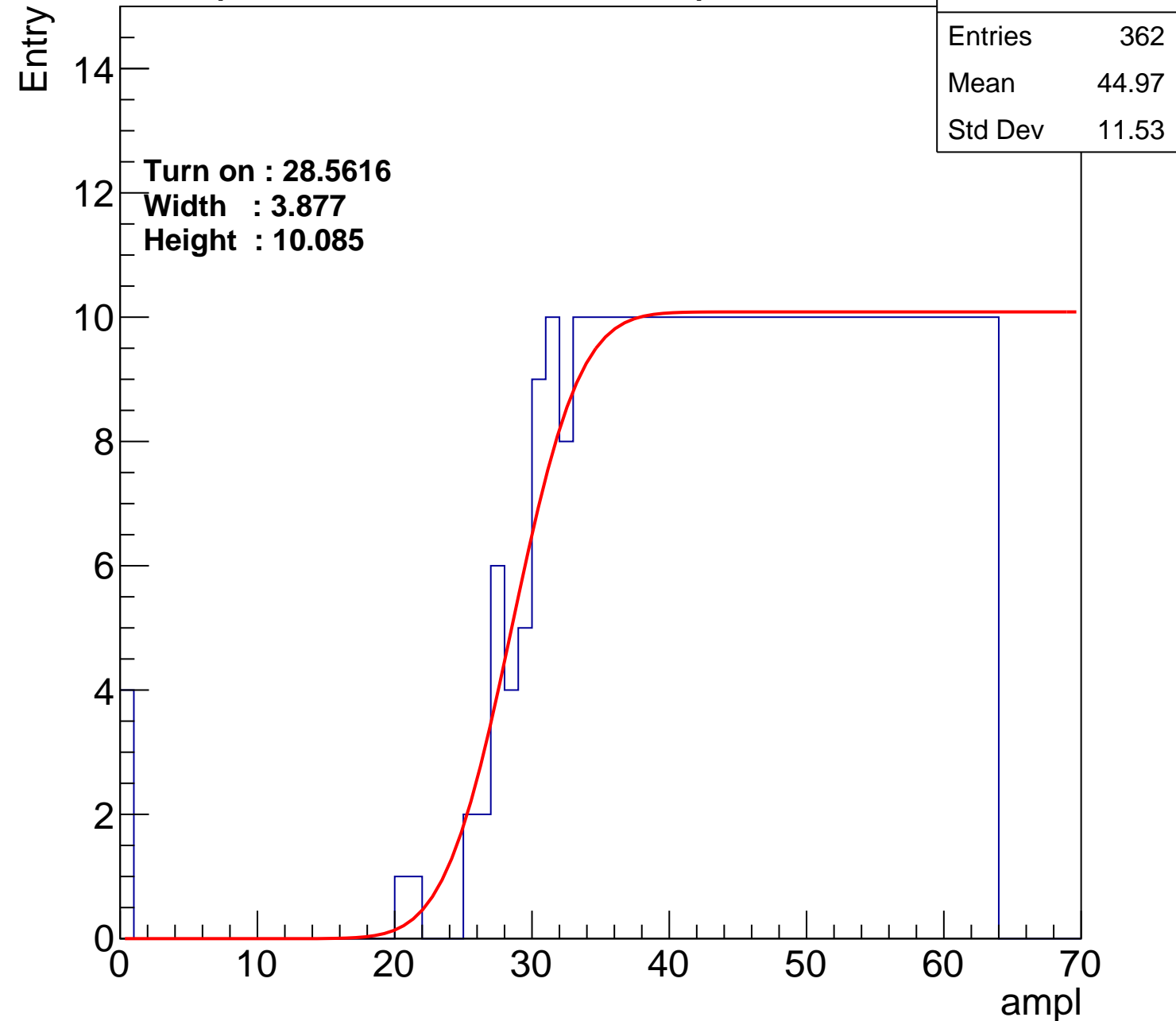
Width : 3.877

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch118

calib_packv5_042523_0143.root, FC#4, port A2

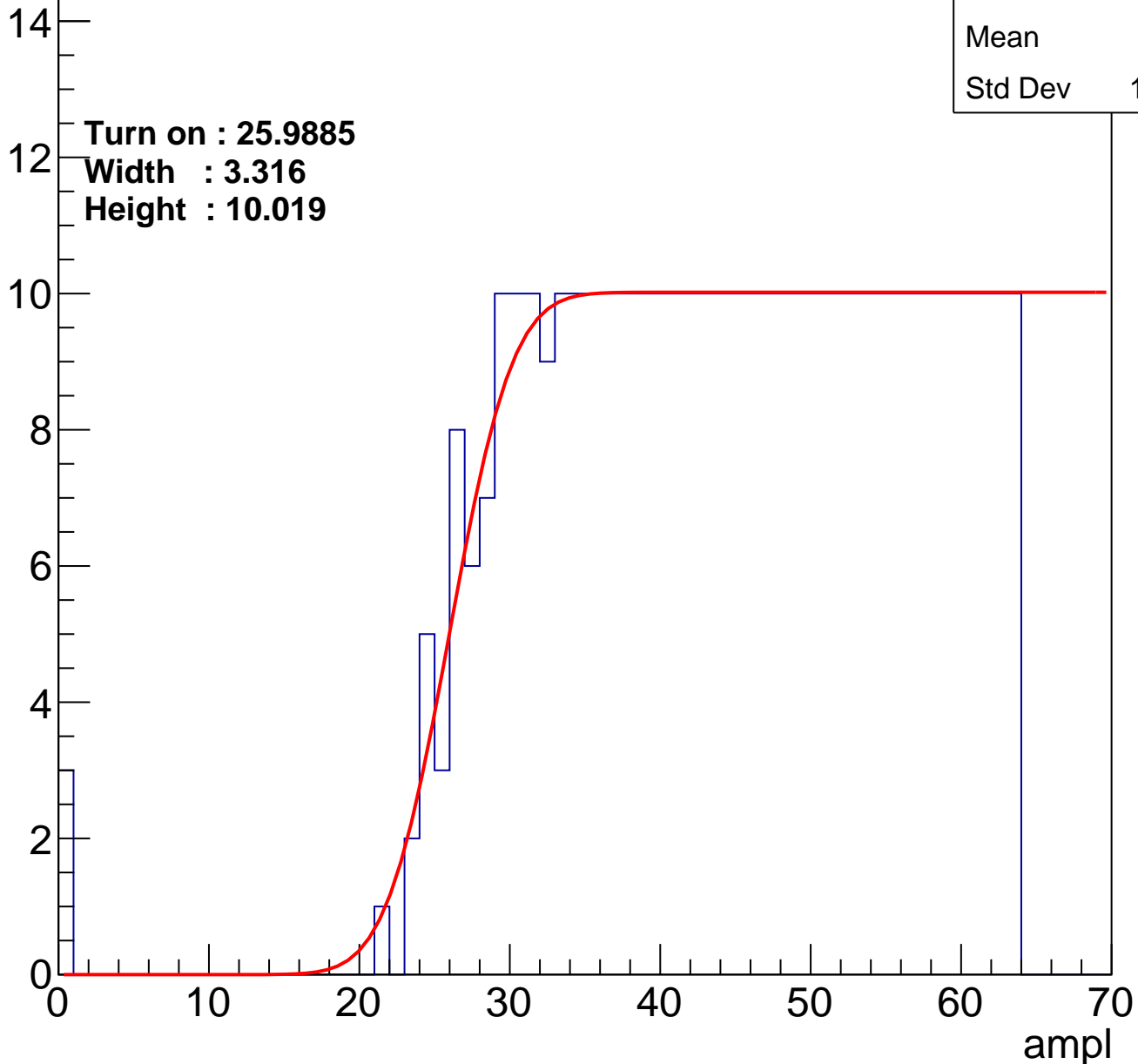
| | |
|---------|-------|
| Entries | 384 |
| Mean | 44 |
| Std Dev | 11.79 |

Turn on : 25.9885

Width : 3.316

Height : 10.019

Entry



B1L100S, U1-ch119

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.17 |
| Std Dev | 11.23 |

Turn on : 28.9960

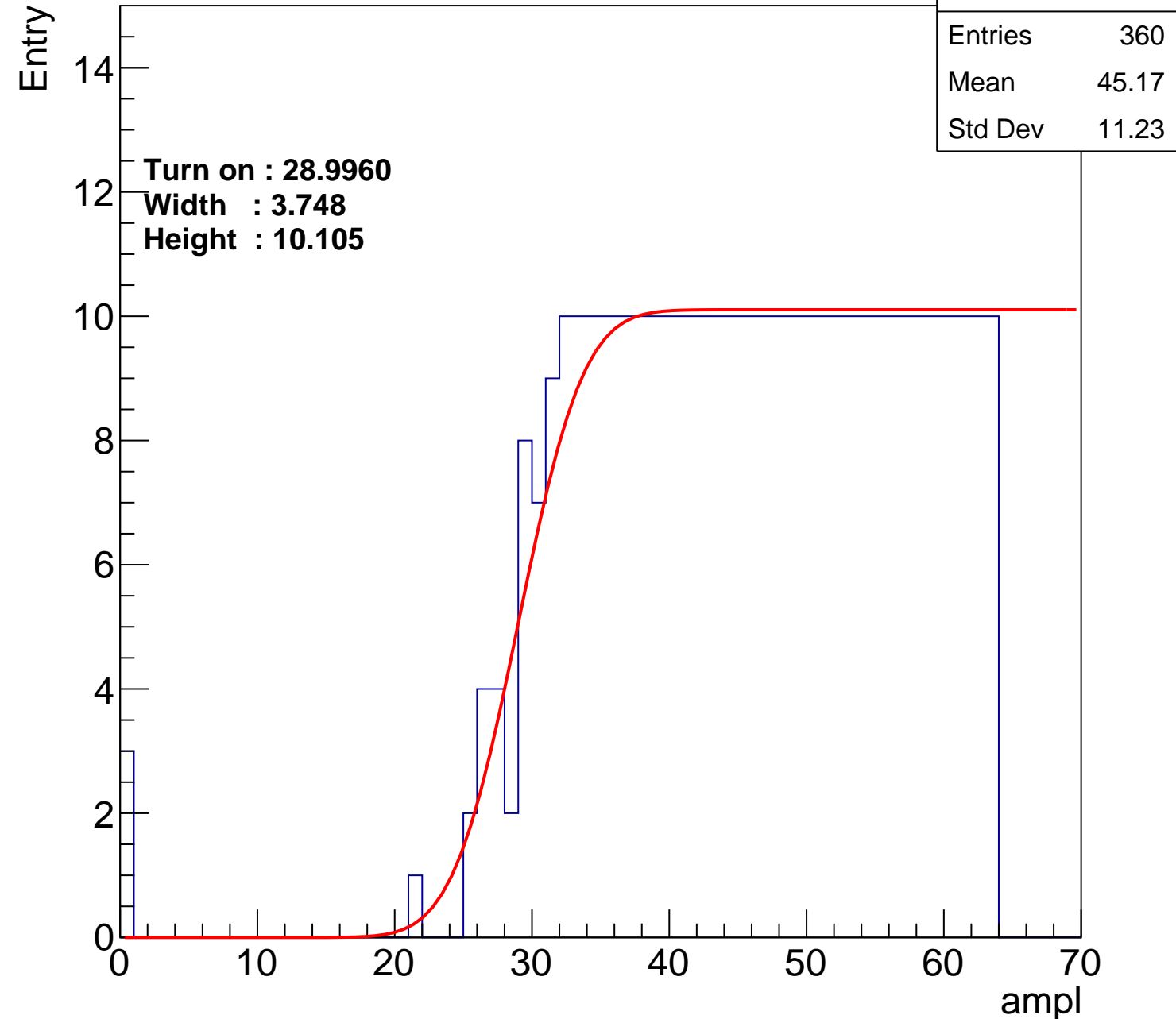
Width : 3.748

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch120

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.92 |
| Std Dev | 11.63 |

Turn on : 25.5554

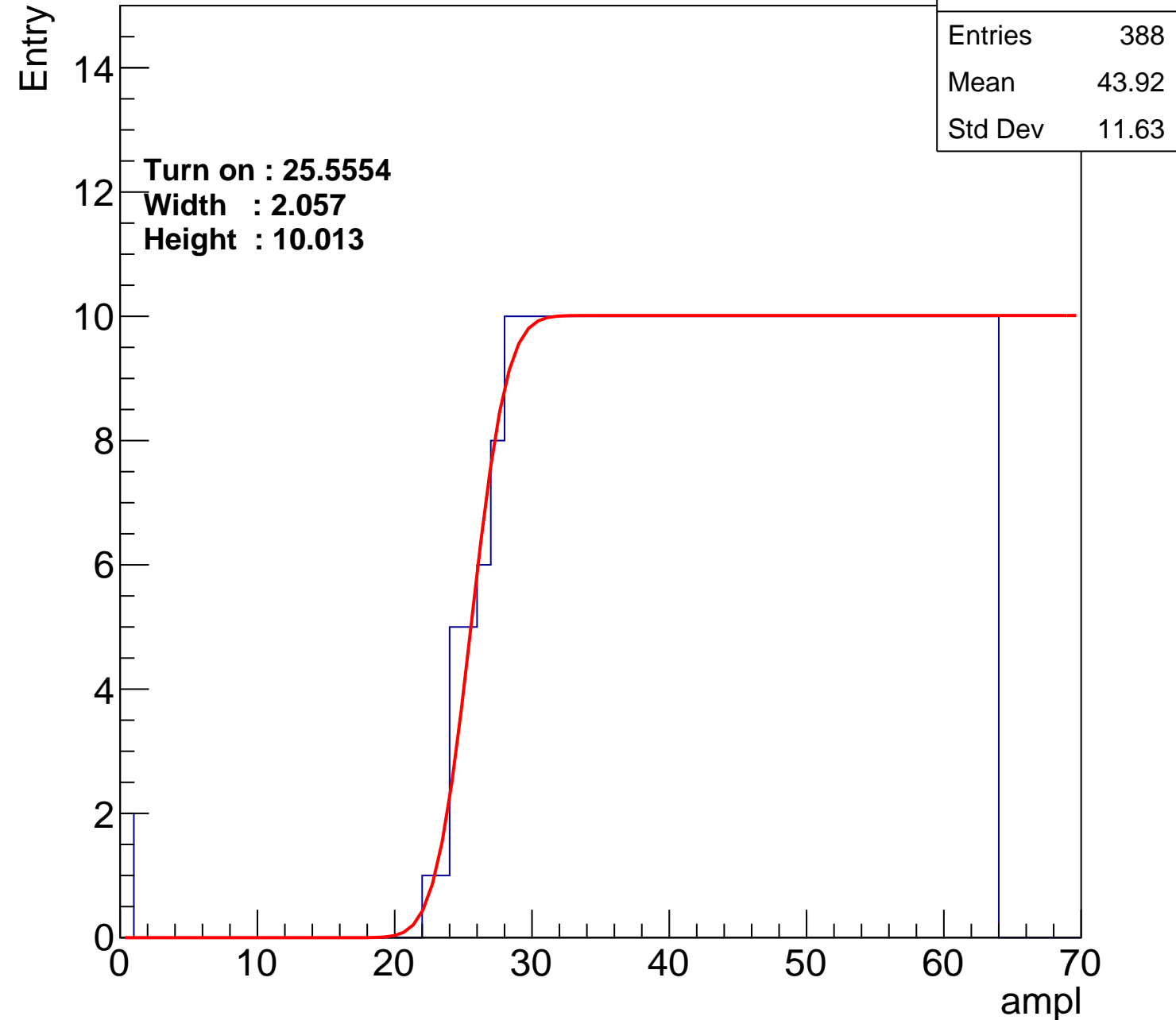
Width : 2.057

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch121

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.87 |
| Std Dev | 11.78 |

Turn on : 25.7462

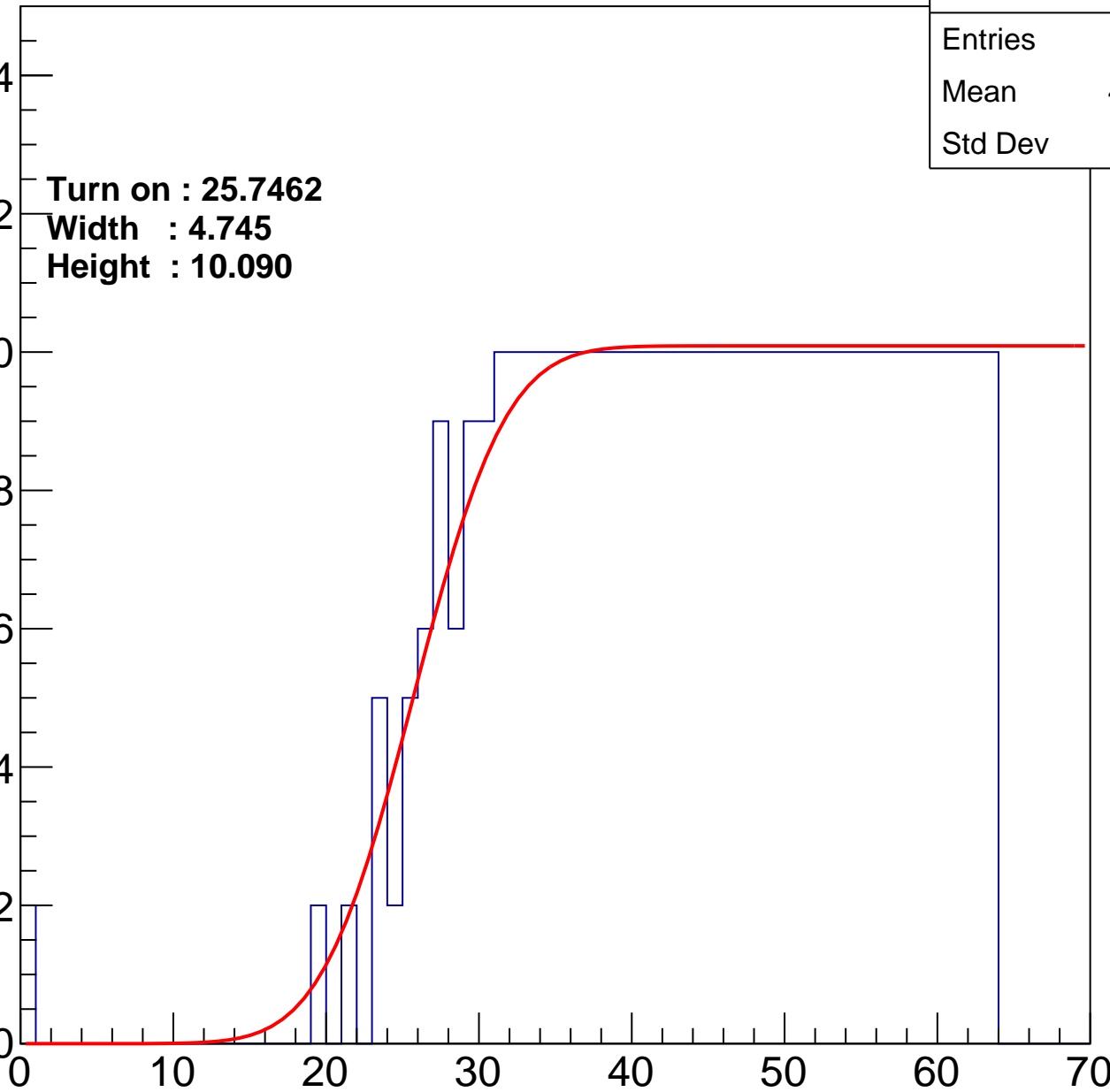
Width : 4.745

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch122

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.14 |
| Std Dev | 12.7 |

Turn on : 25.0290

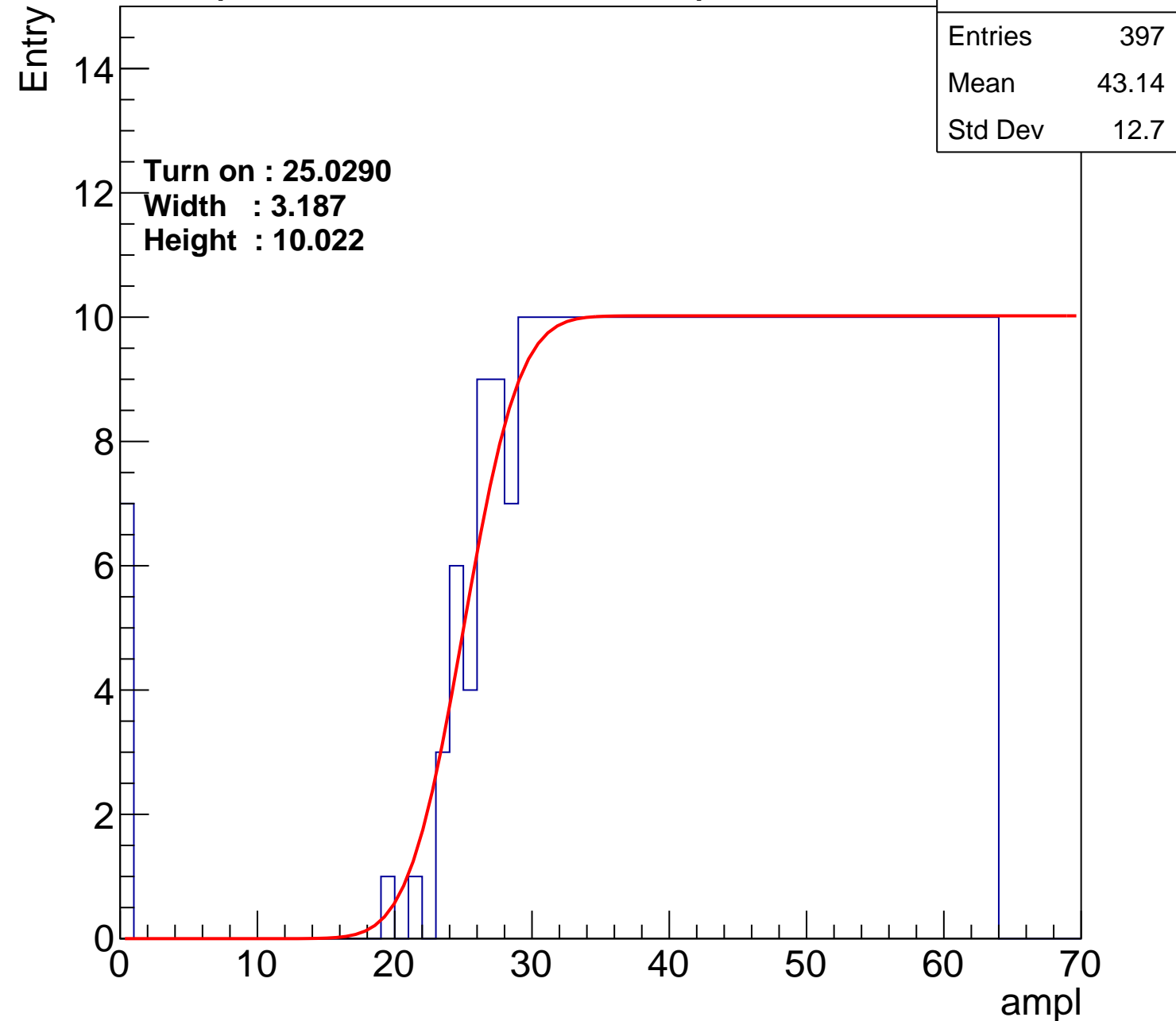
Width : 3.187

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch123

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.87 |
| Std Dev | 11.57 |

Turn on : 28.3257

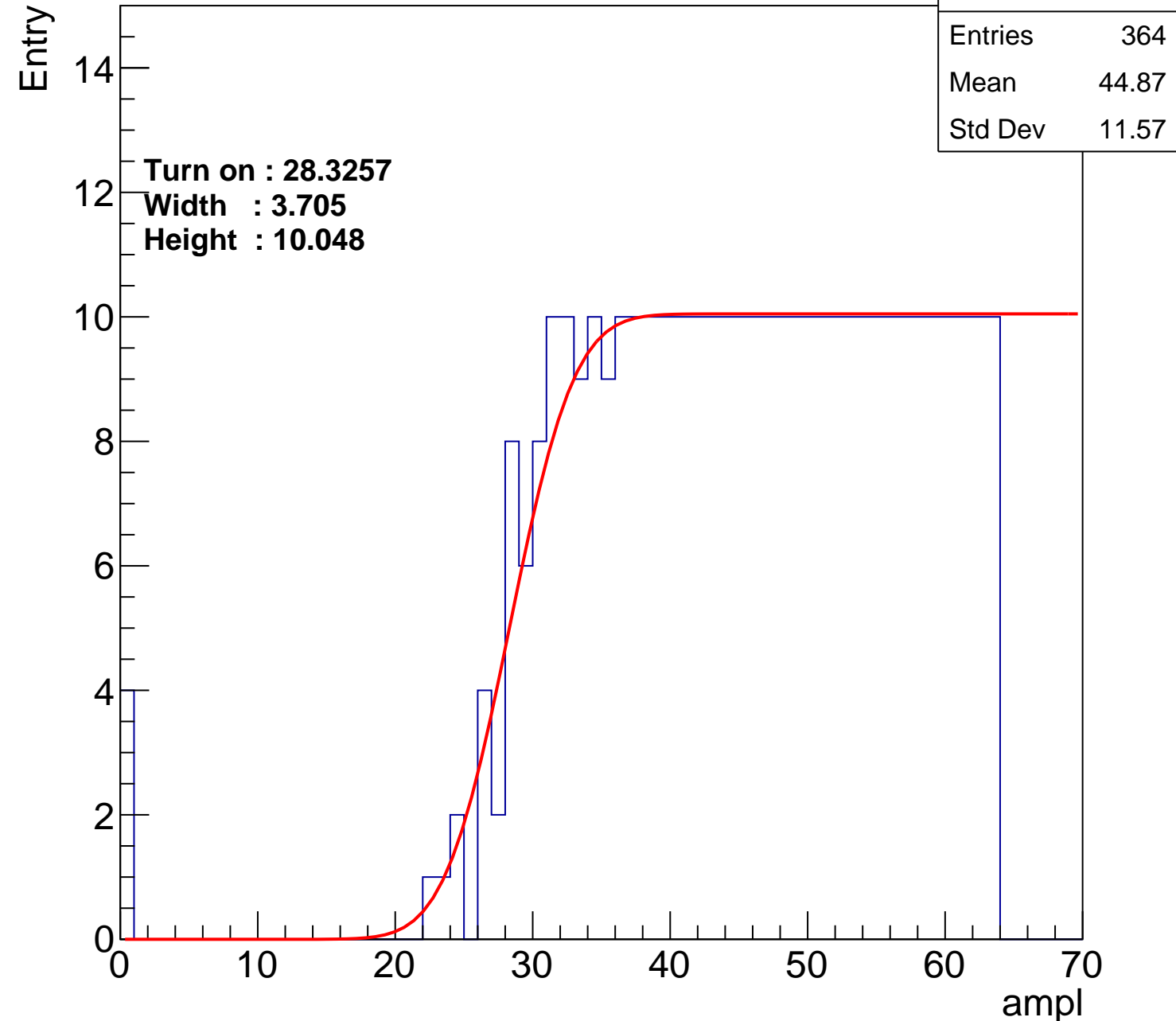
Width : 3.705

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch124

calib_packv5_042523_0143.root, FC#4, port A2

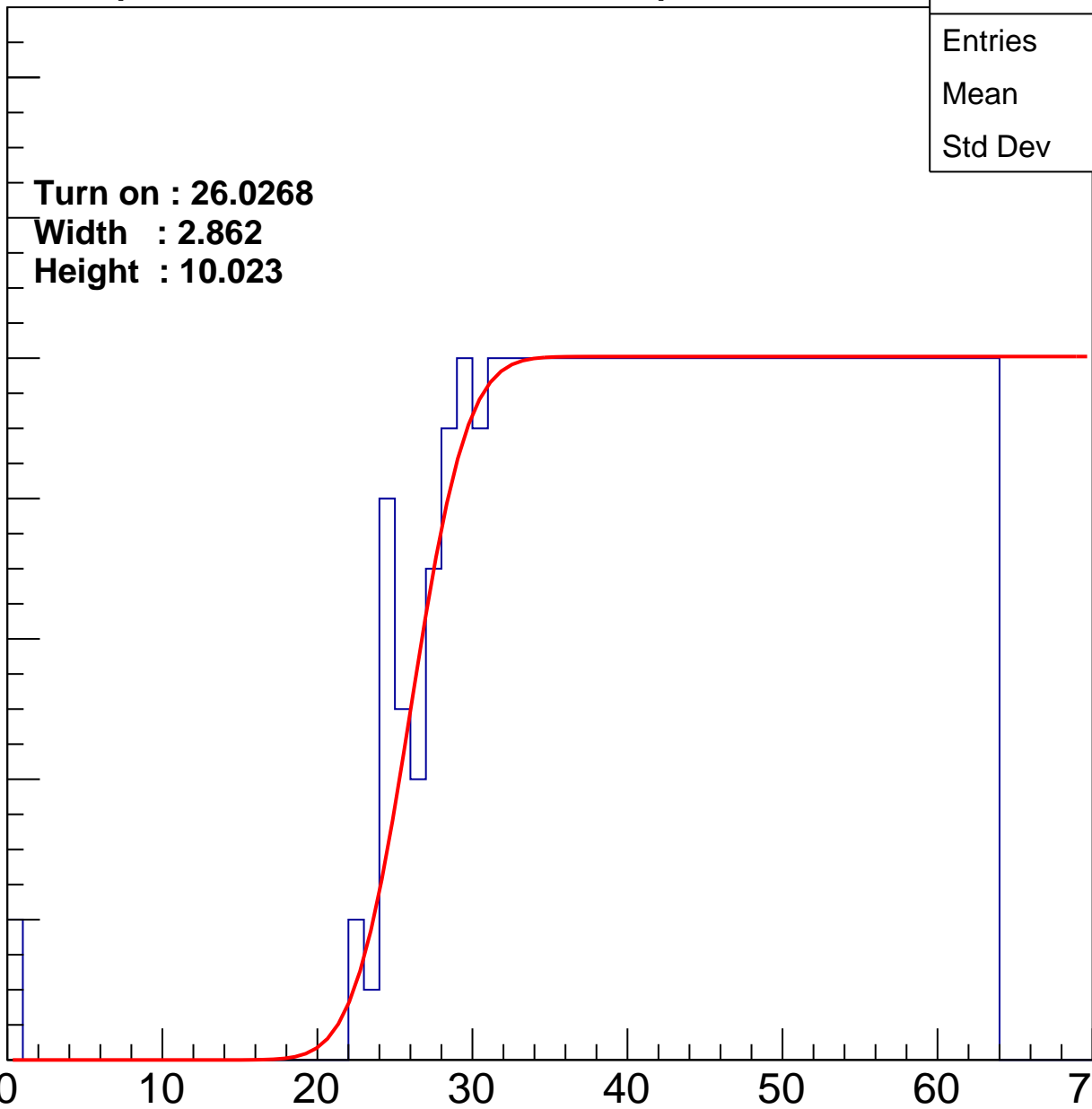
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0268
Width : 2.862
Height : 10.023

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.93 |
| Std Dev | 11.68 |

ampl



B1L100S, U1-ch125

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.31 |
| Std Dev | 12.03 |

Turn on : 24.6322

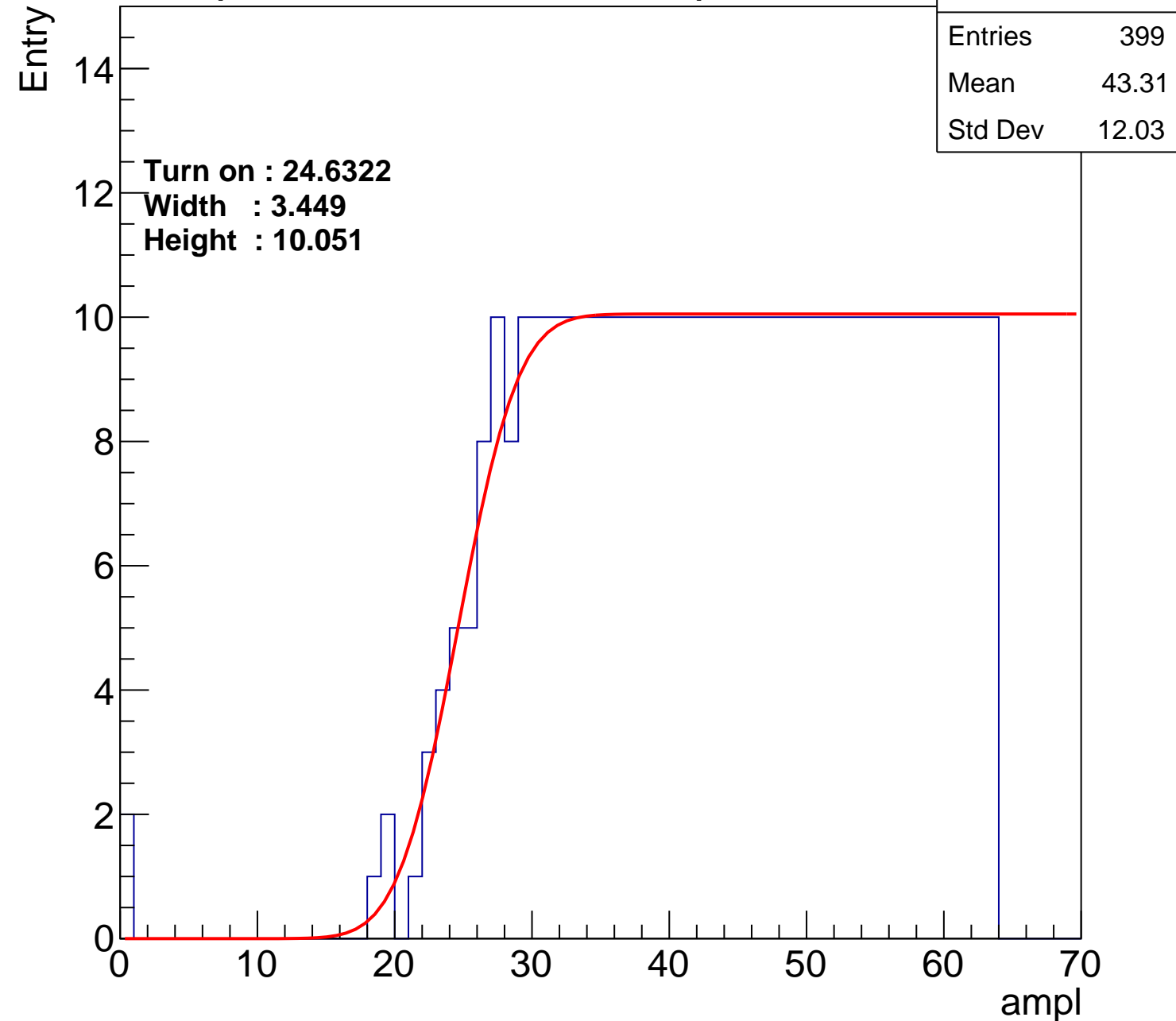
Width : 3.449

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U1-ch126

calib_packv5_042523_0143.root, FC#4, port A2

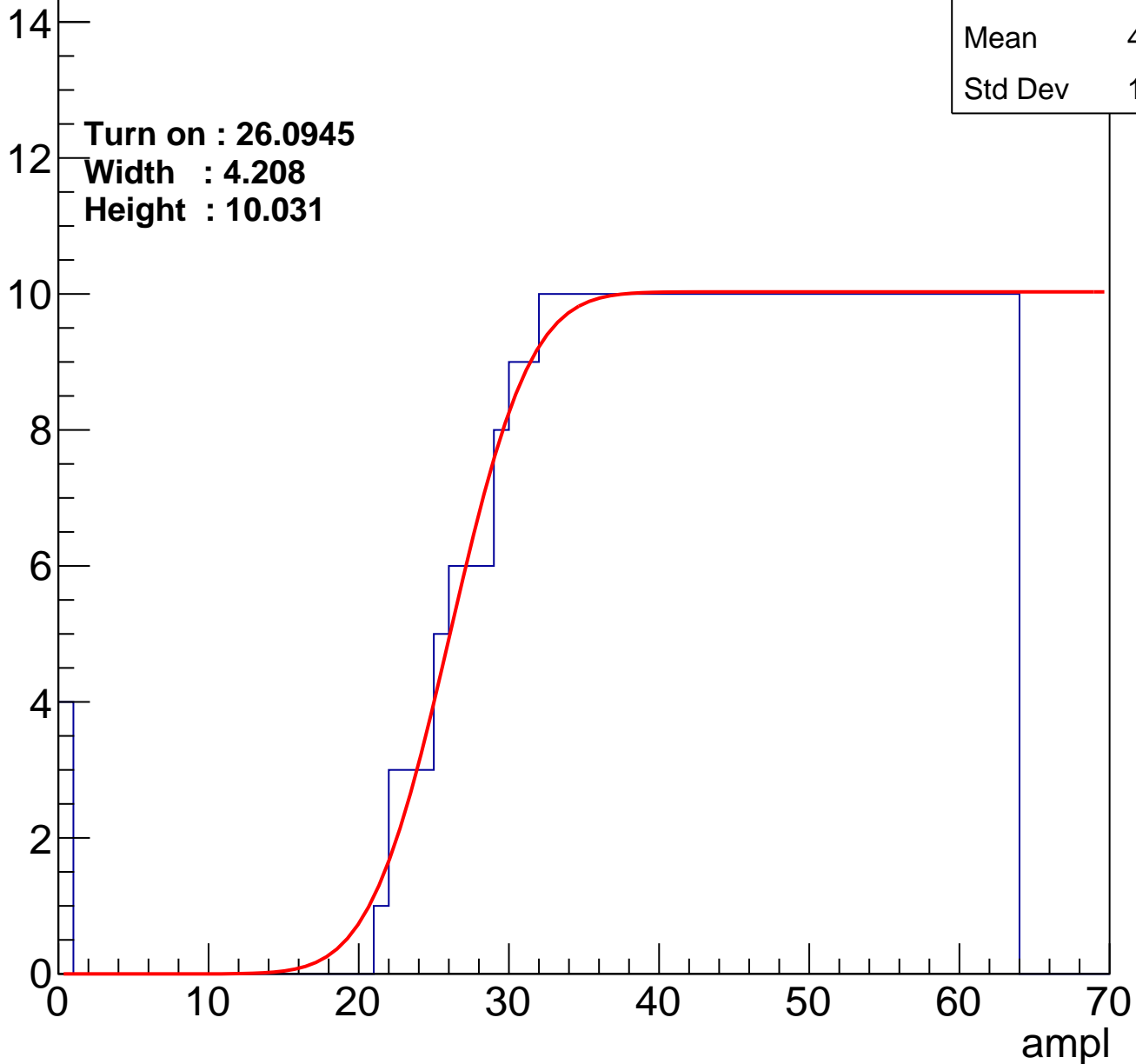
| | |
|---------|-------|
| Entries | 383 |
| Mean | 43.92 |
| Std Dev | 12.04 |

Turn on : 26.0945

Width : 4.208

Height : 10.031

Entry



B1L100S, U1-ch127

calib_packv5_042523_0143.root, FC#4, port A2

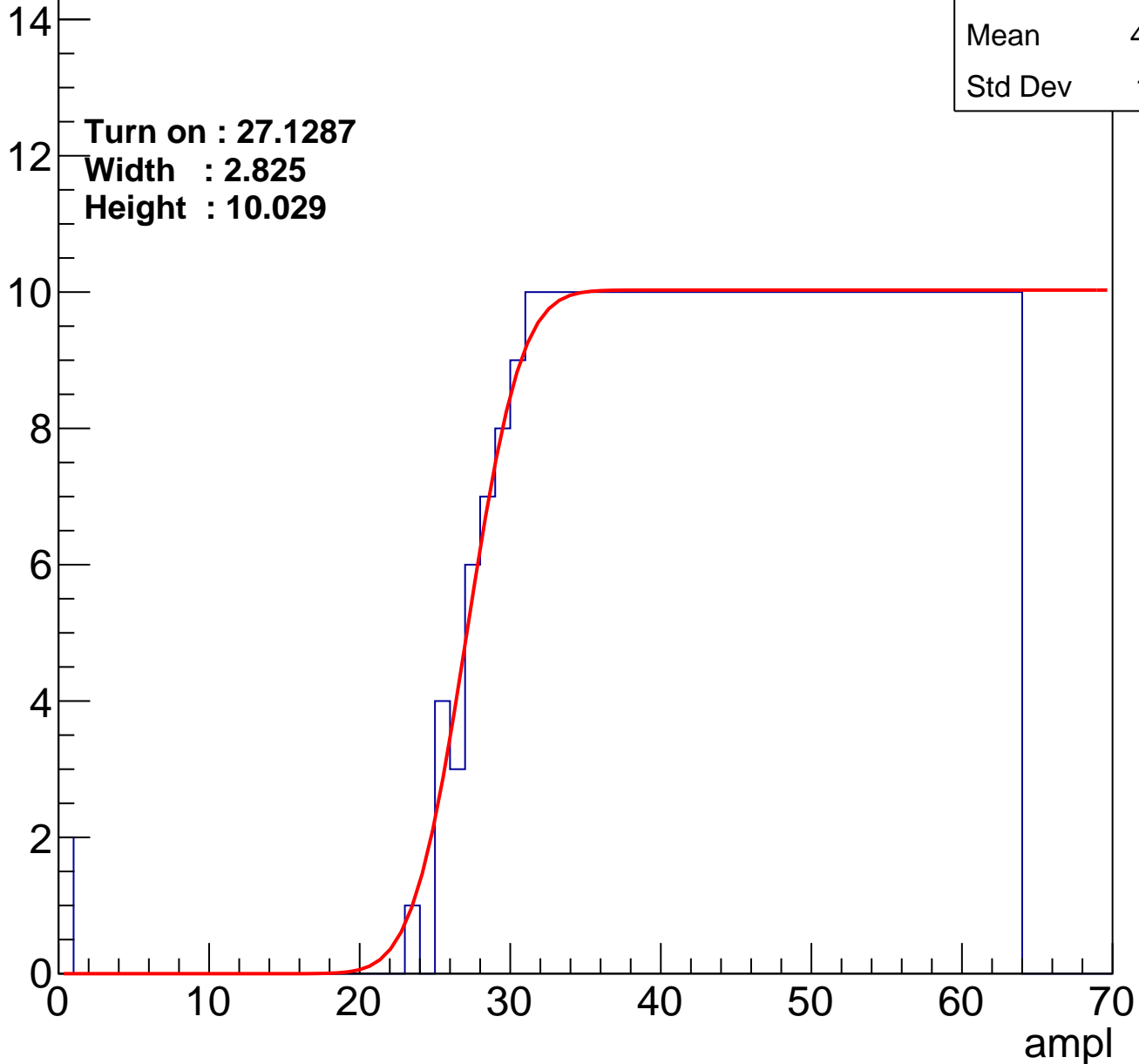
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.79 |
| Std Dev | 11.21 |

Turn on : 27.1287

Width : 2.825

Height : 10.029

Entry



B1L100S, U1-ch127

calib_packv5_042523_0143.root, FC#4, port A2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.79 |
| Std Dev | 11.21 |

Turn on : 27.1287

Width : 2.825

Height : 10.029

Entry

