

B1L103S, U24-ch0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	42.93
Std Dev	12.76

Turn on : 25.4163

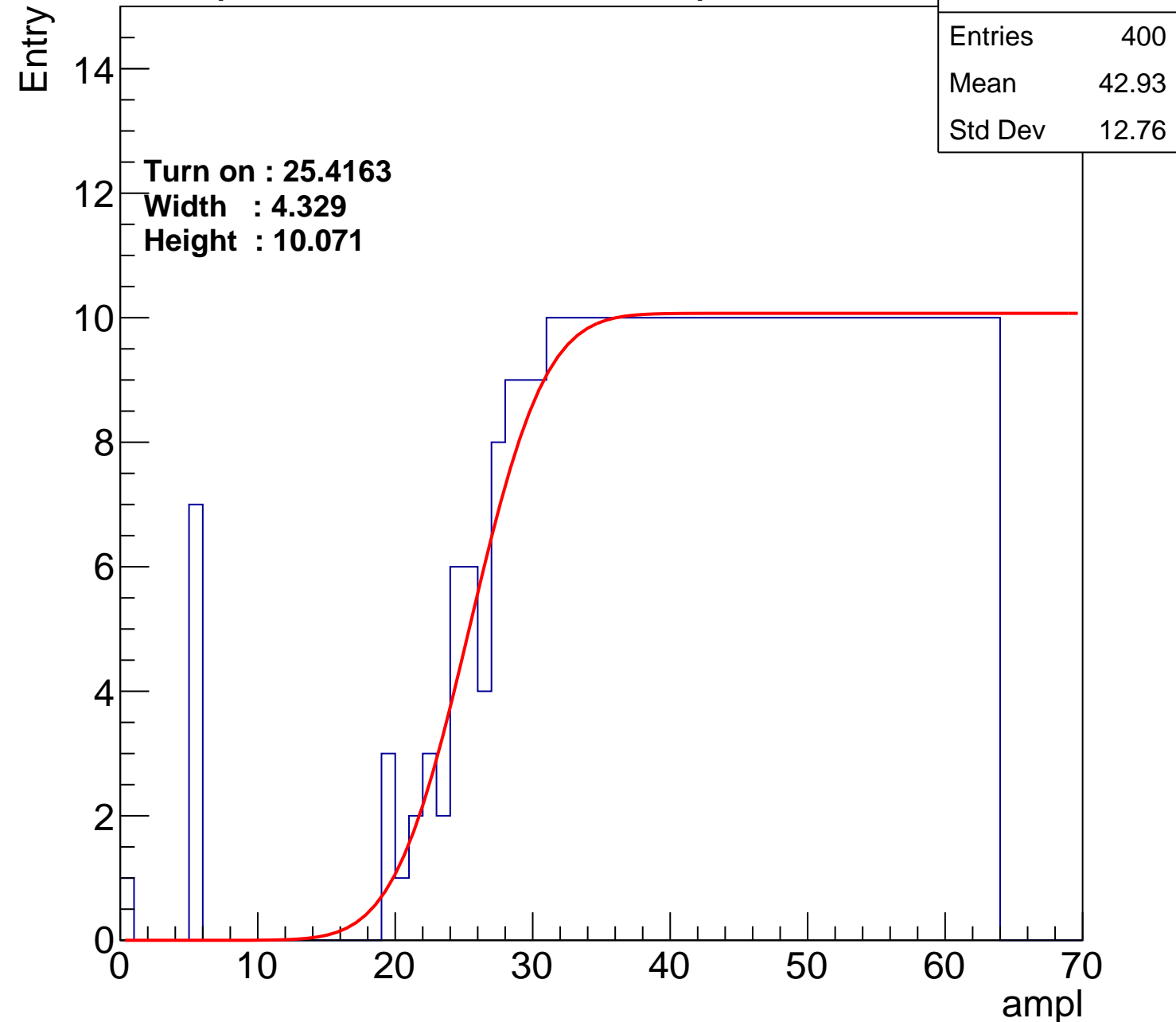
Width : 4.329

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	354
Mean	45.53
Std Dev	10.87

Turn on : 29.0604

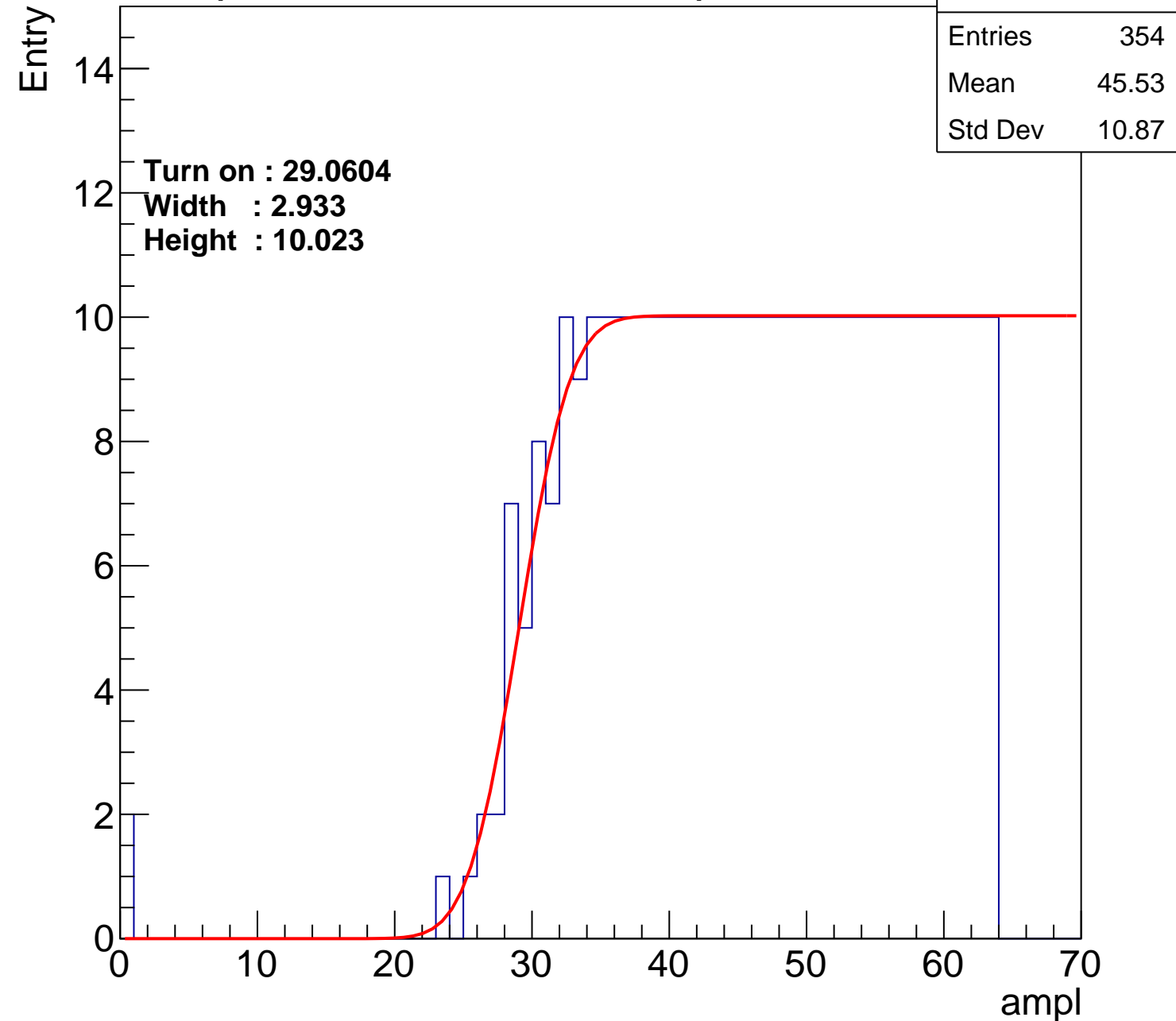
Width : 2.933

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch2

calib_packv5_042523_0143.root, FC#7, port C2

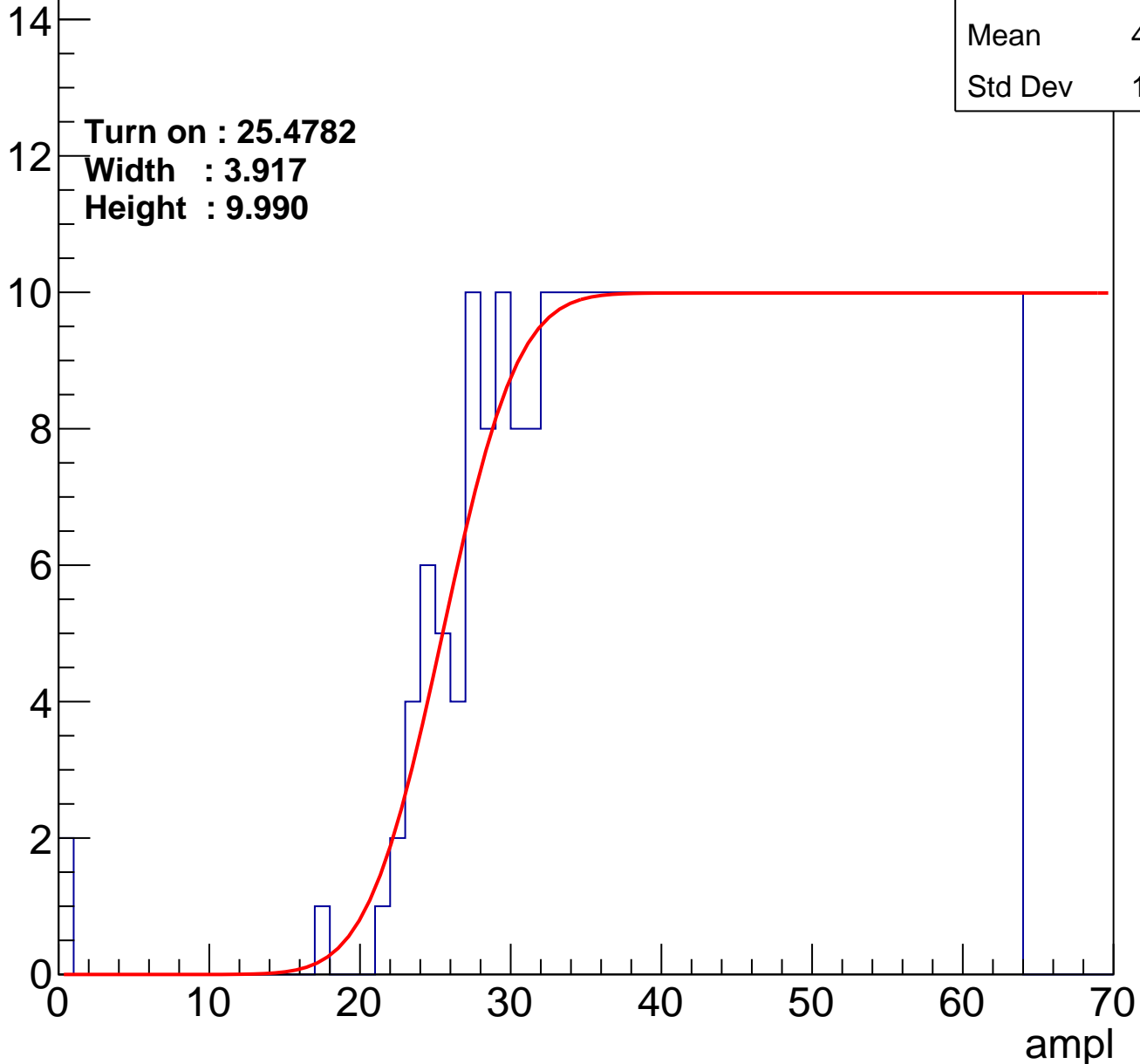
Entries	389
Mean	43.75
Std Dev	11.85

Turn on : 25.4782

Width : 3.917

Height : 9.990

Entry



B1L103S, U24-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.48
Std Dev	10.9

Turn on : 28.8816

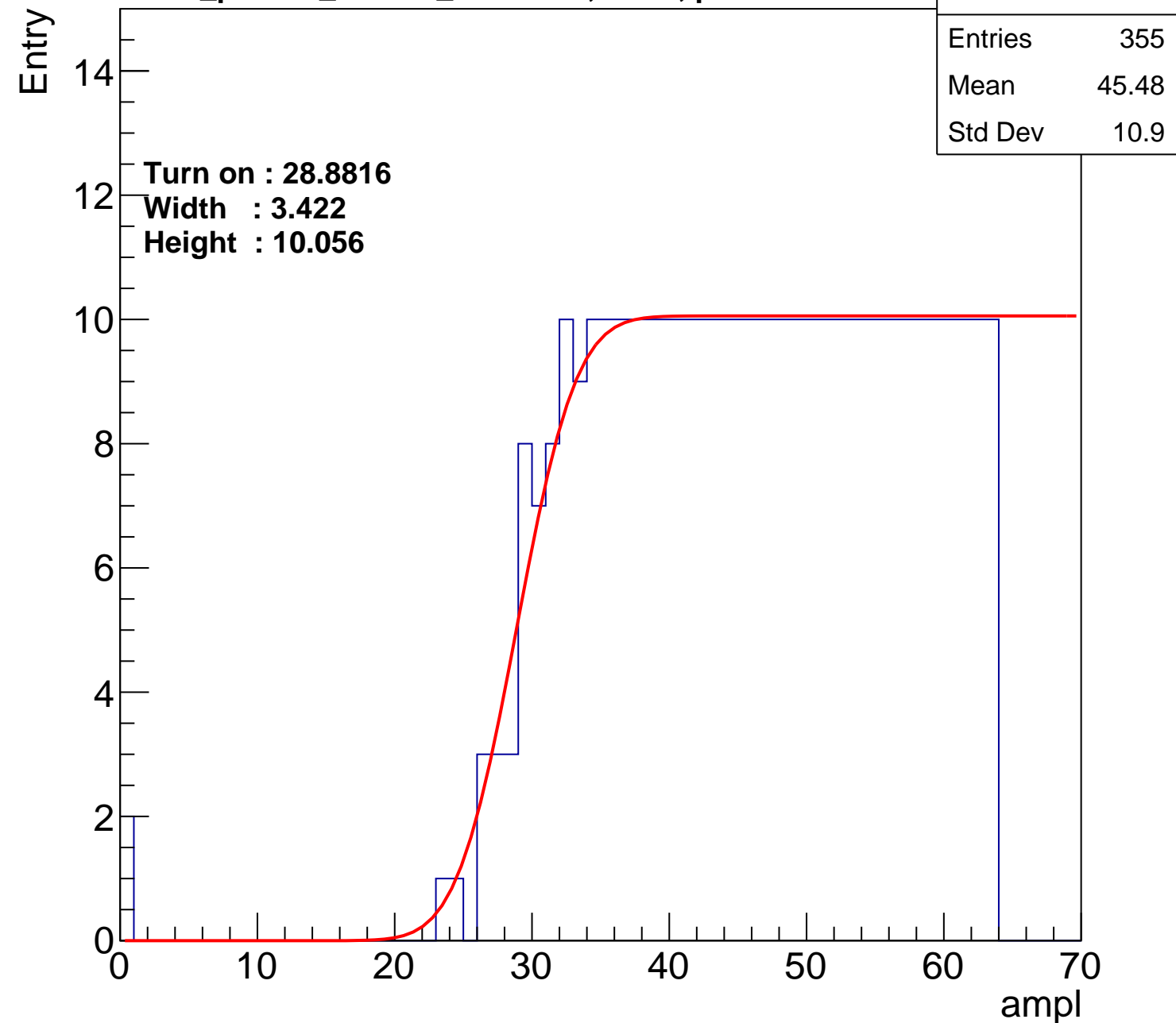
Width : 3.422

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.22
Std Dev	11.39

Turn on : 26.1839

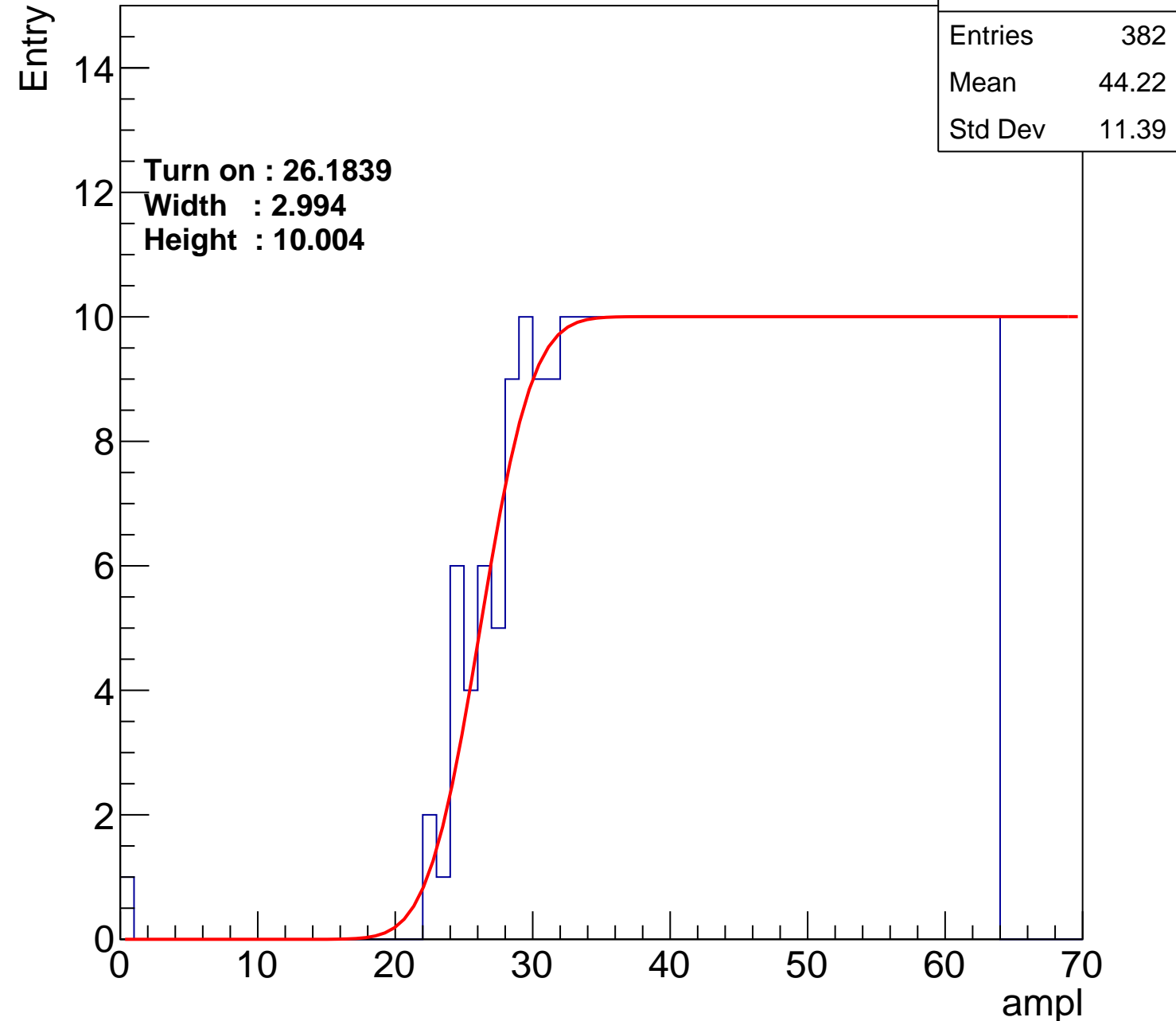
Width : 2.994

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.8134

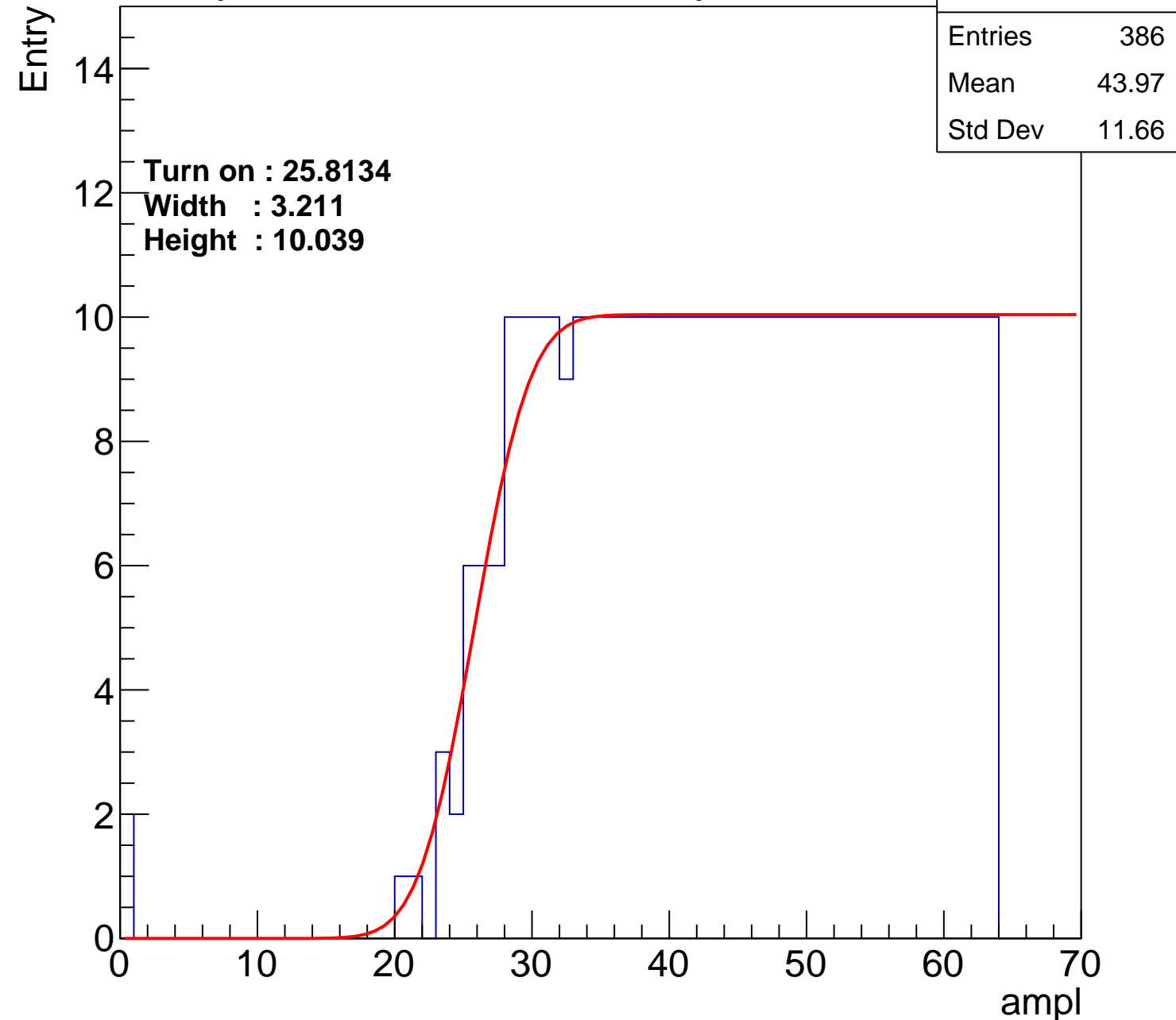
Width : 3.211

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 26.1857

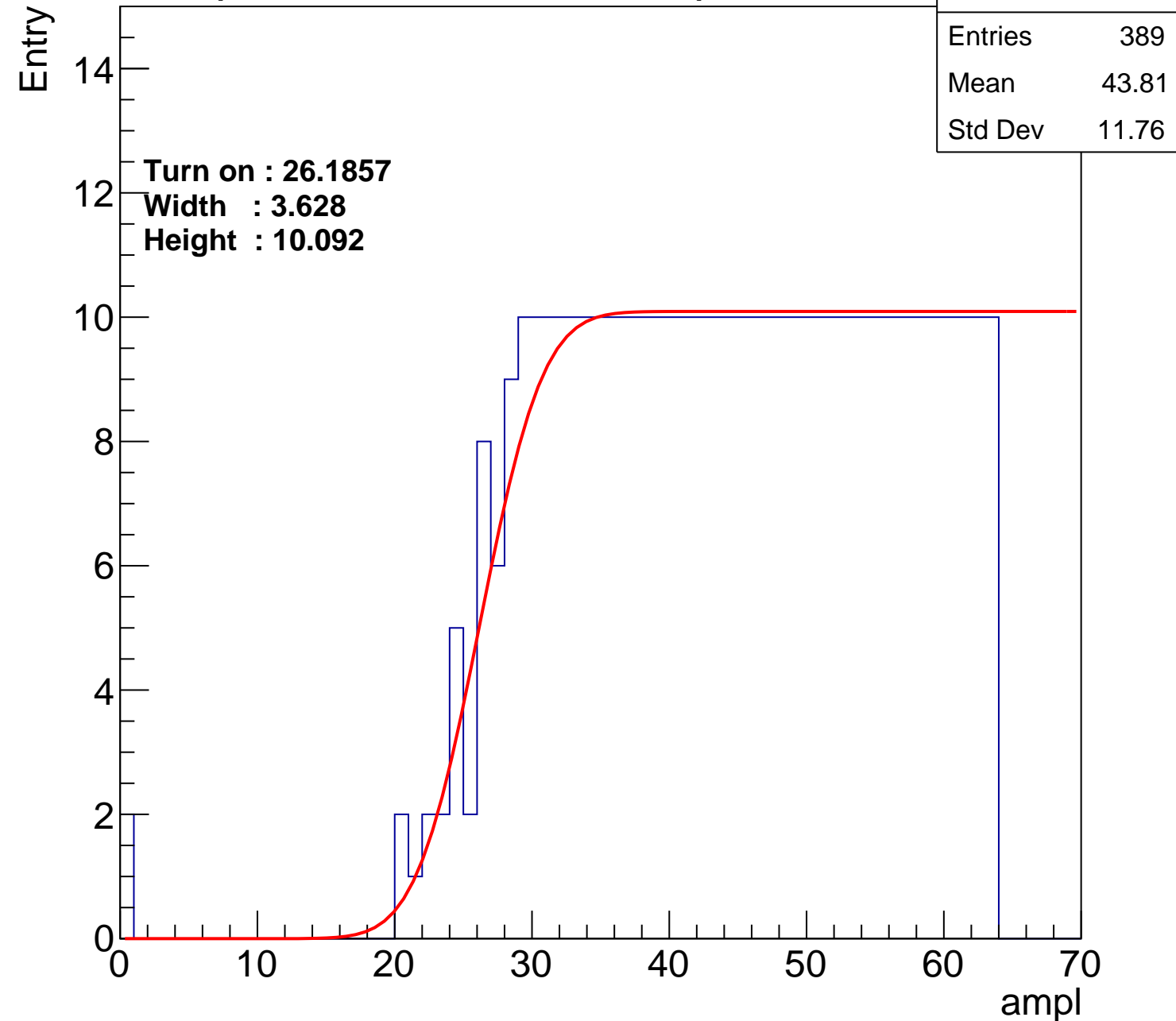
Width : 3.628

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	340
Mean	46.22
Std Dev	10.4

Turn on : 30.3944

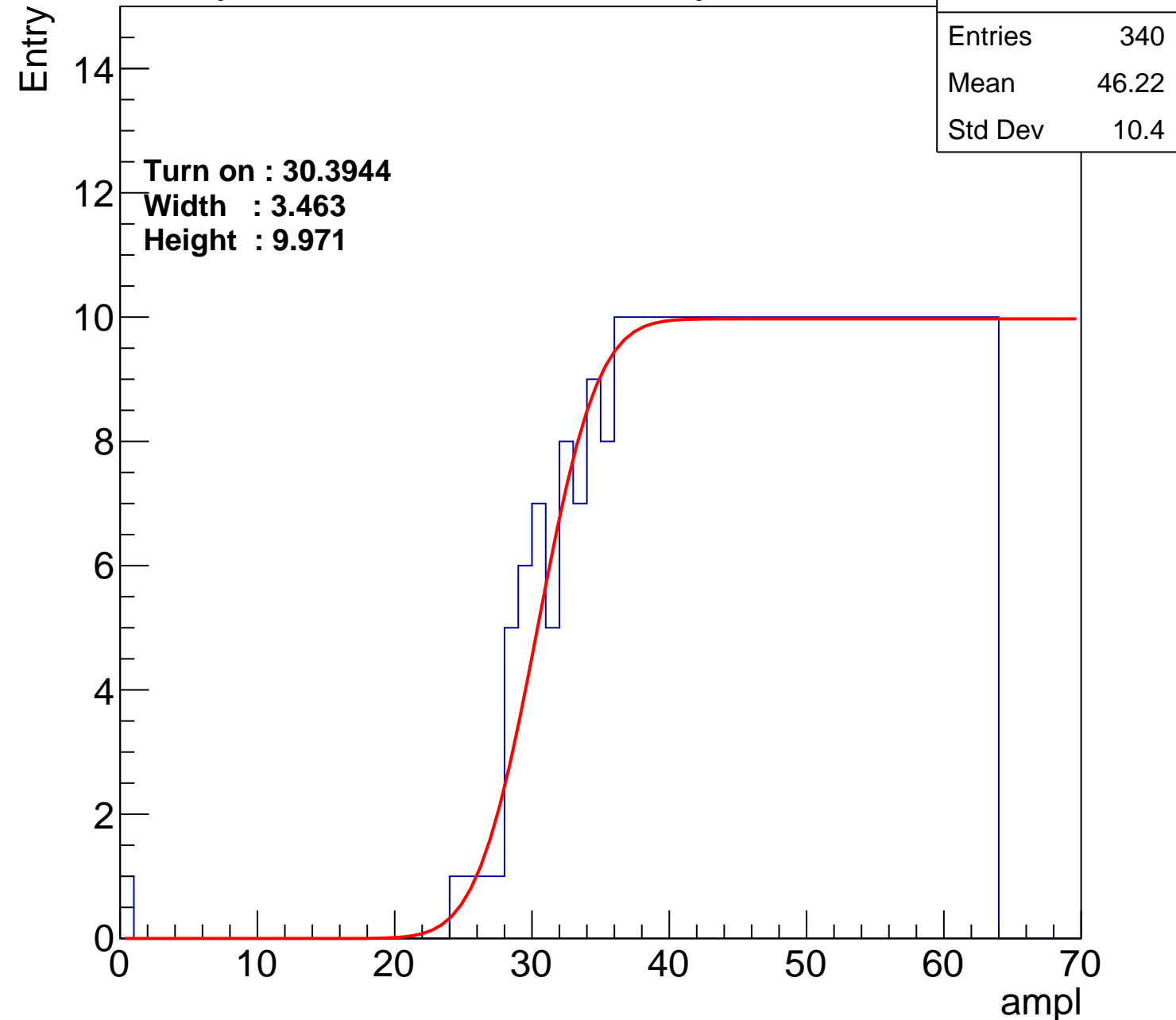
Width : 3.463

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.02
Std Dev	11.81

Turn on : 26.2661

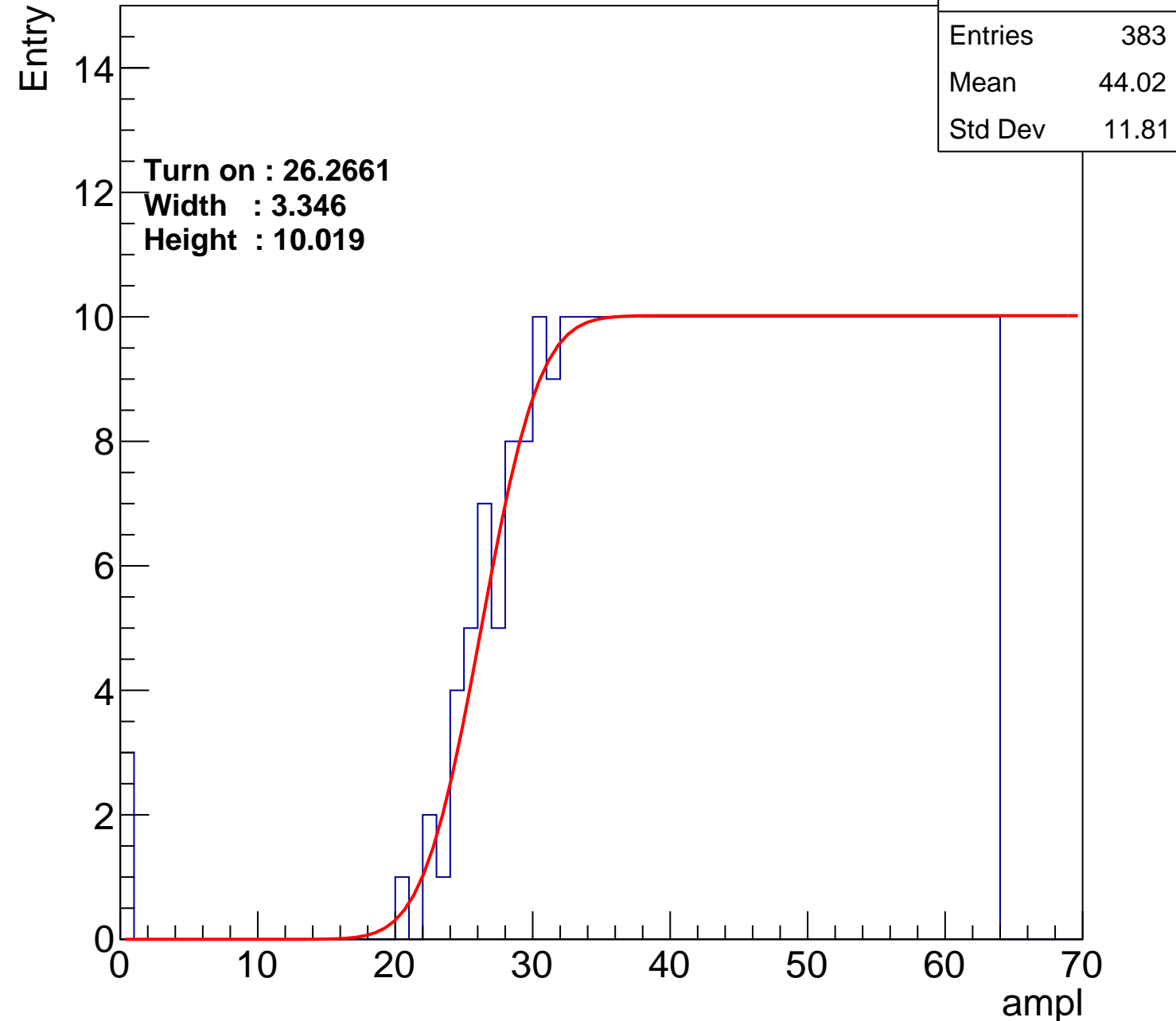
Width : 3.346

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch9

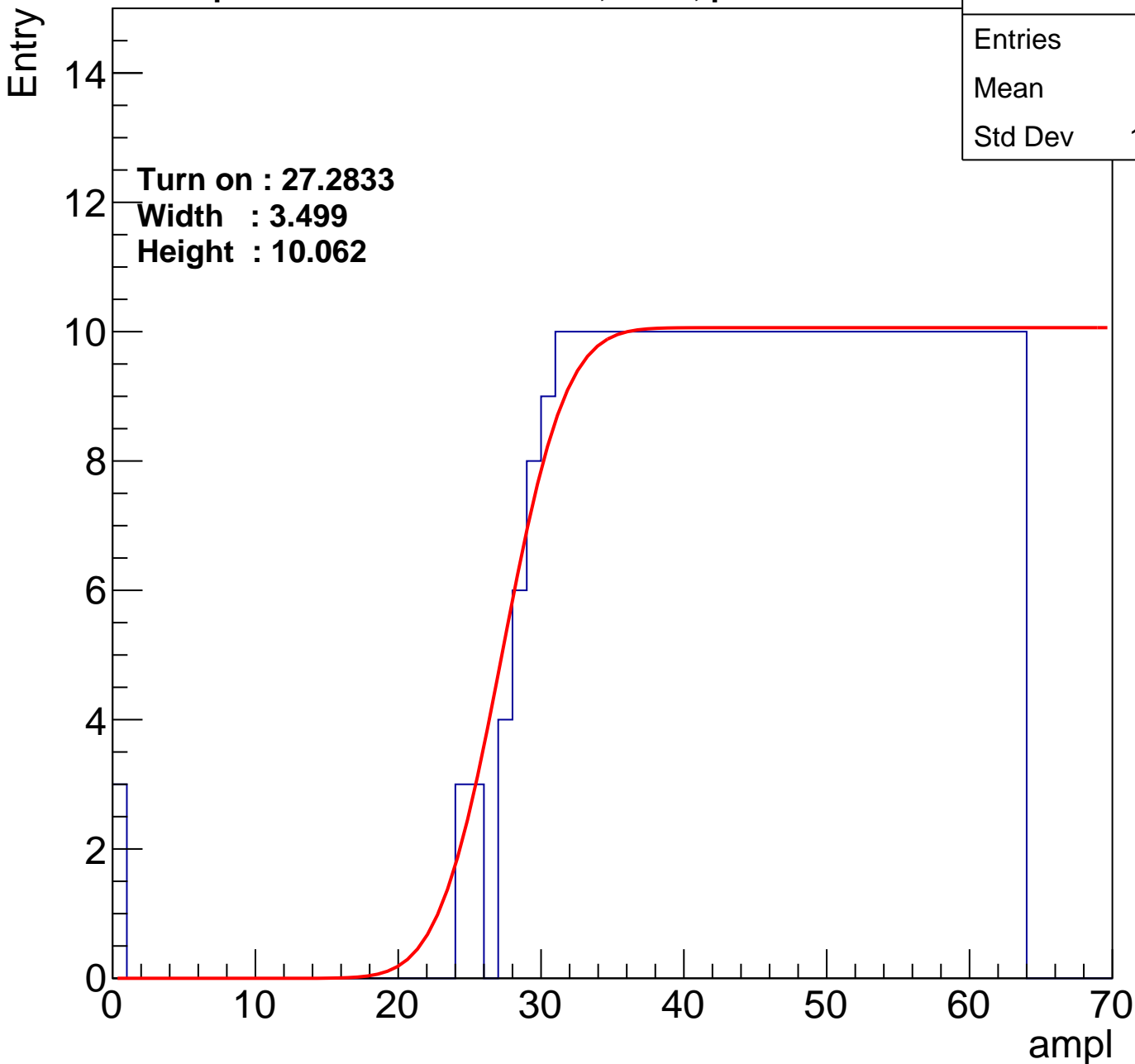
calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.9
Std Dev	11.32

Turn on : 27.2833

Width : 3.499

Height : 10.062



B1L103S, U24-ch10

calib_packv5_042523_0143.root, FC#7, port C2

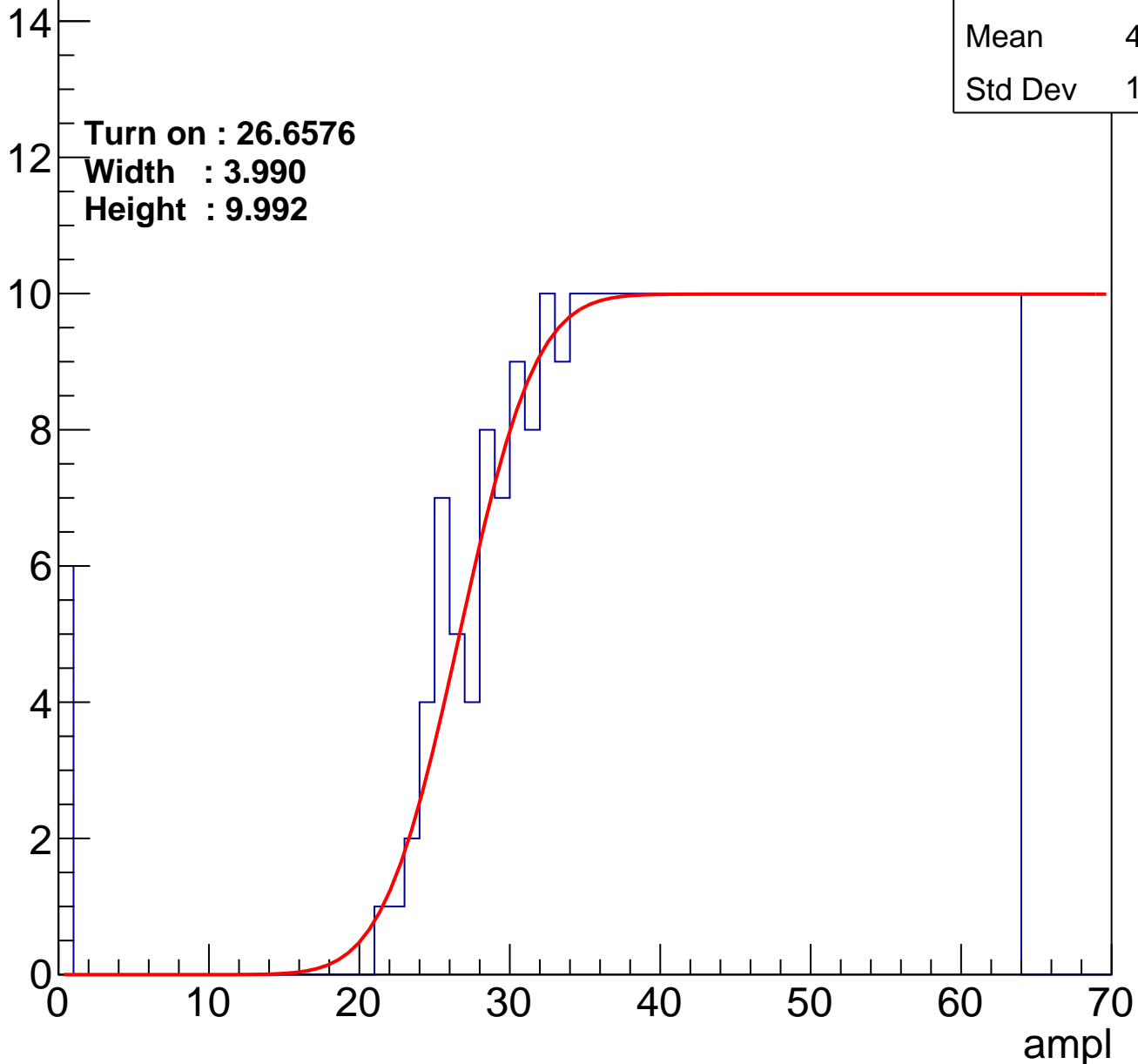
Entries	381
Mean	43.86
Std Dev	12.36

Turn on : 26.6576

Width : 3.990

Height : 9.992

Entry



B1L103S, U24-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	353
Mean	45.64
Std Dev	10.64

Turn on : 29.4502

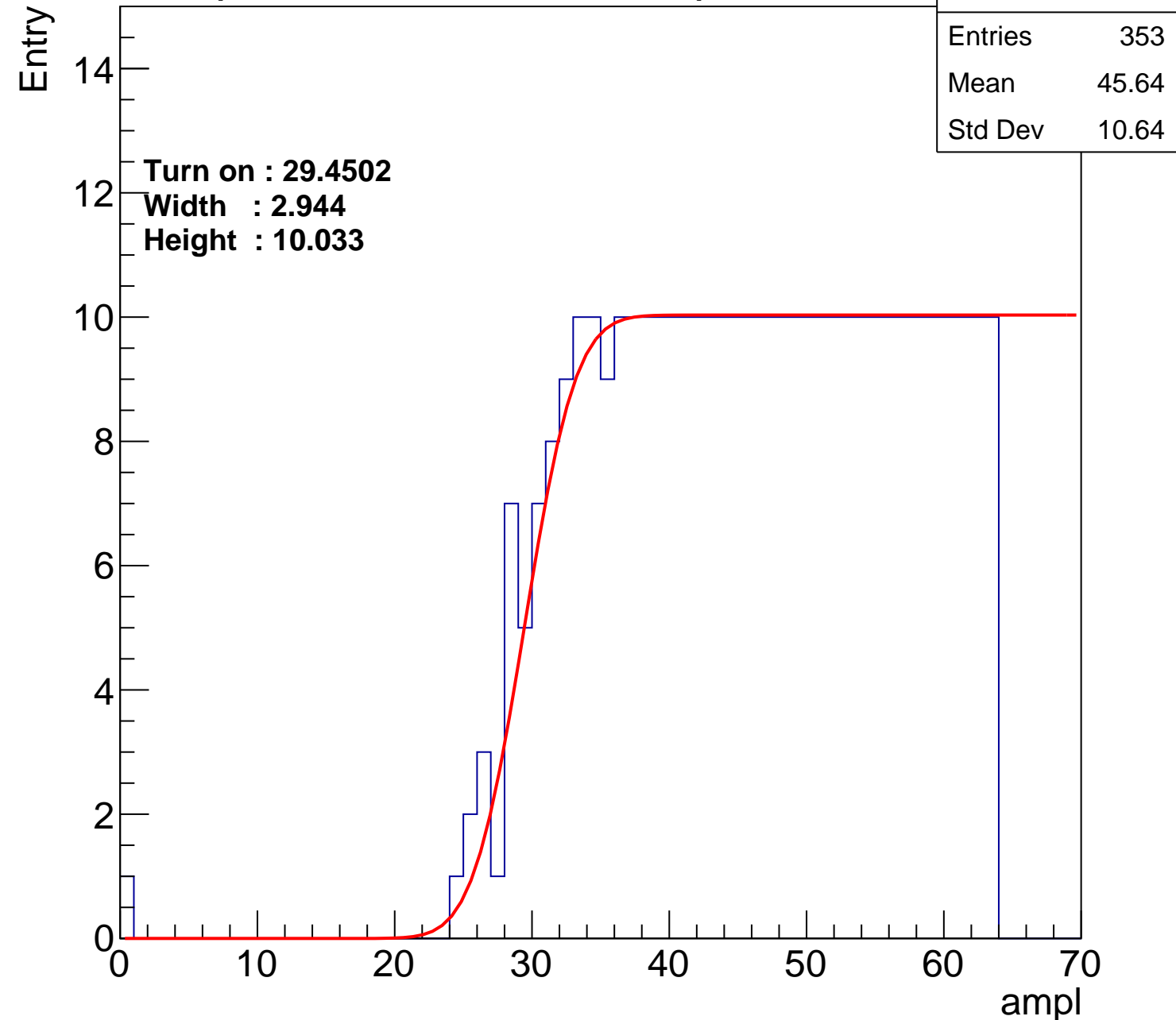
Width : 2.944

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch12

calib_packv5_042523_0143.root, FC#7, port C2

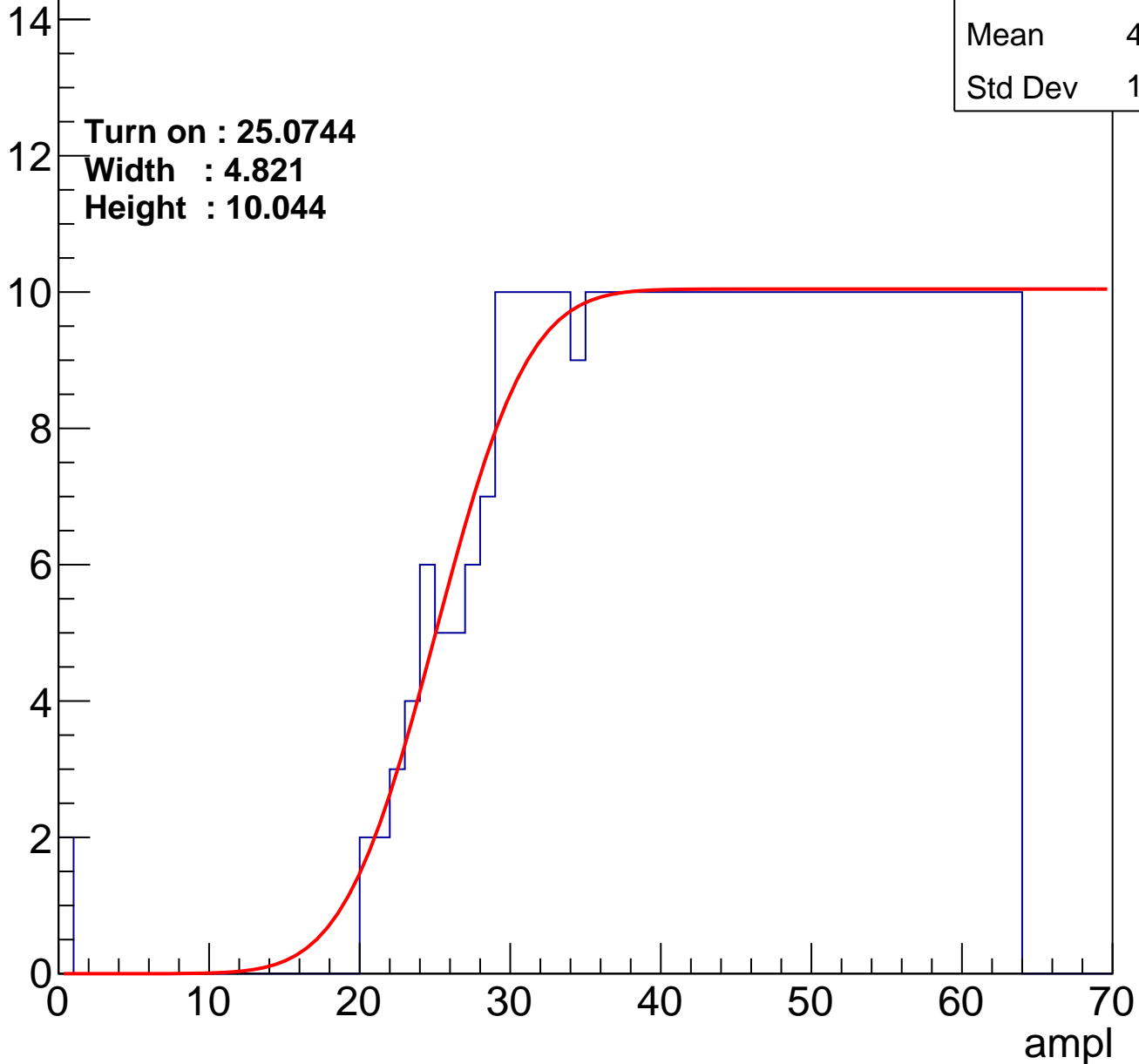
Entries	391
Mean	43.64
Std Dev	11.92

Turn on : 25.0744

Width : 4.821

Height : 10.044

Entry



B1L103S, U24-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 27.0451

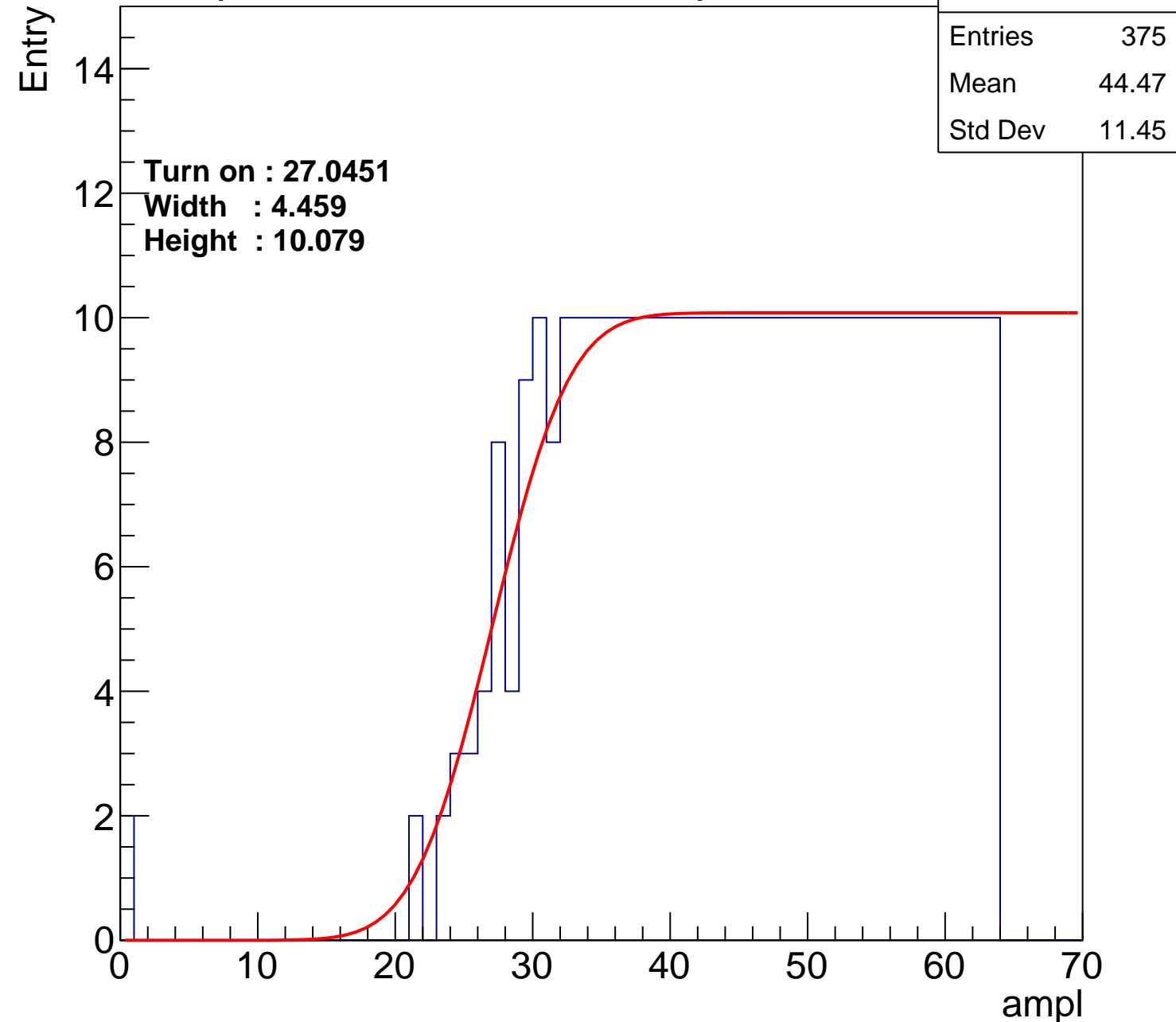
Width : 4.459

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.16
Std Dev	11.62

Turn on : 26.7685

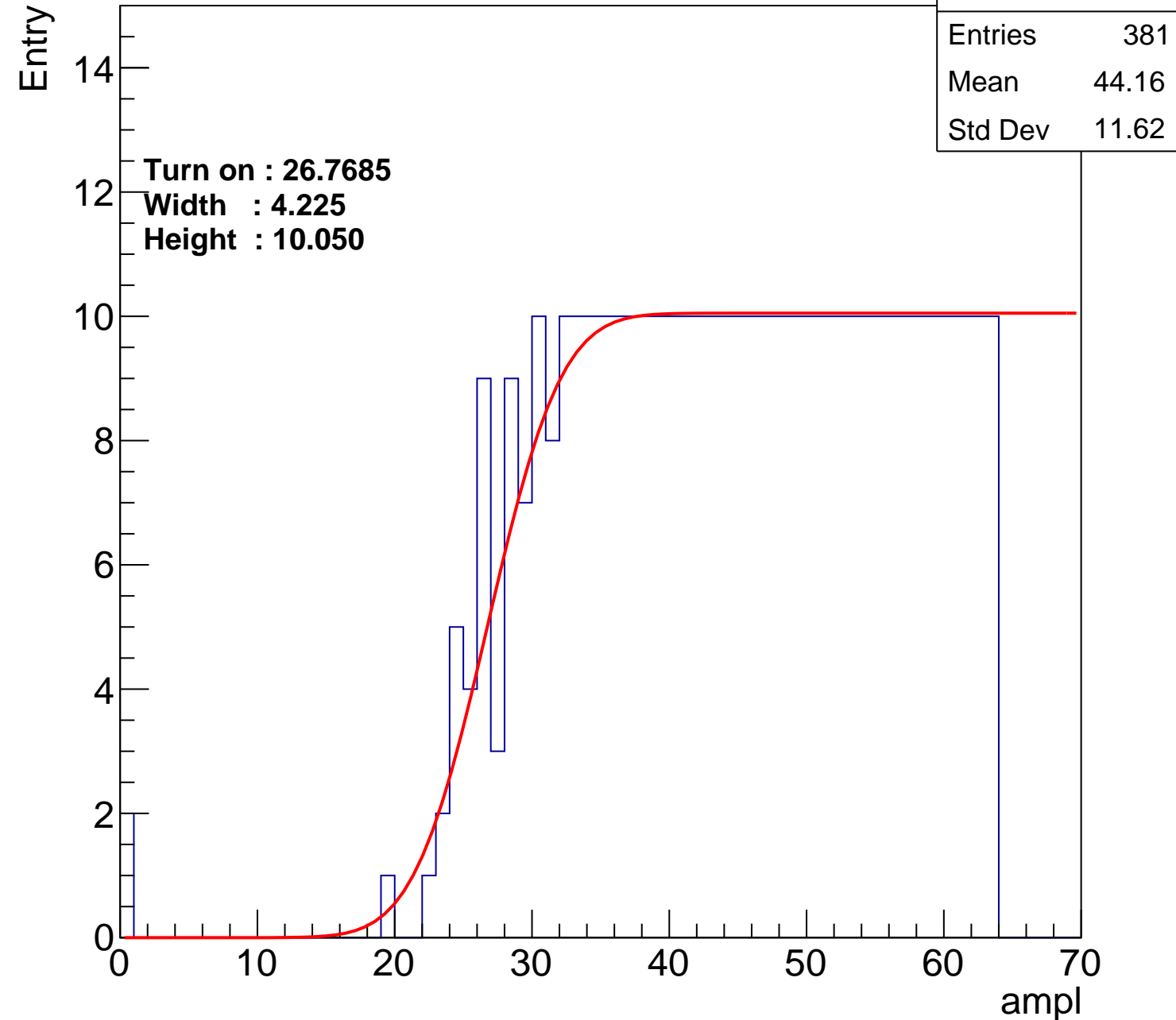
Width : 4.225

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.01
Std Dev	11.19

Turn on : 28.1868

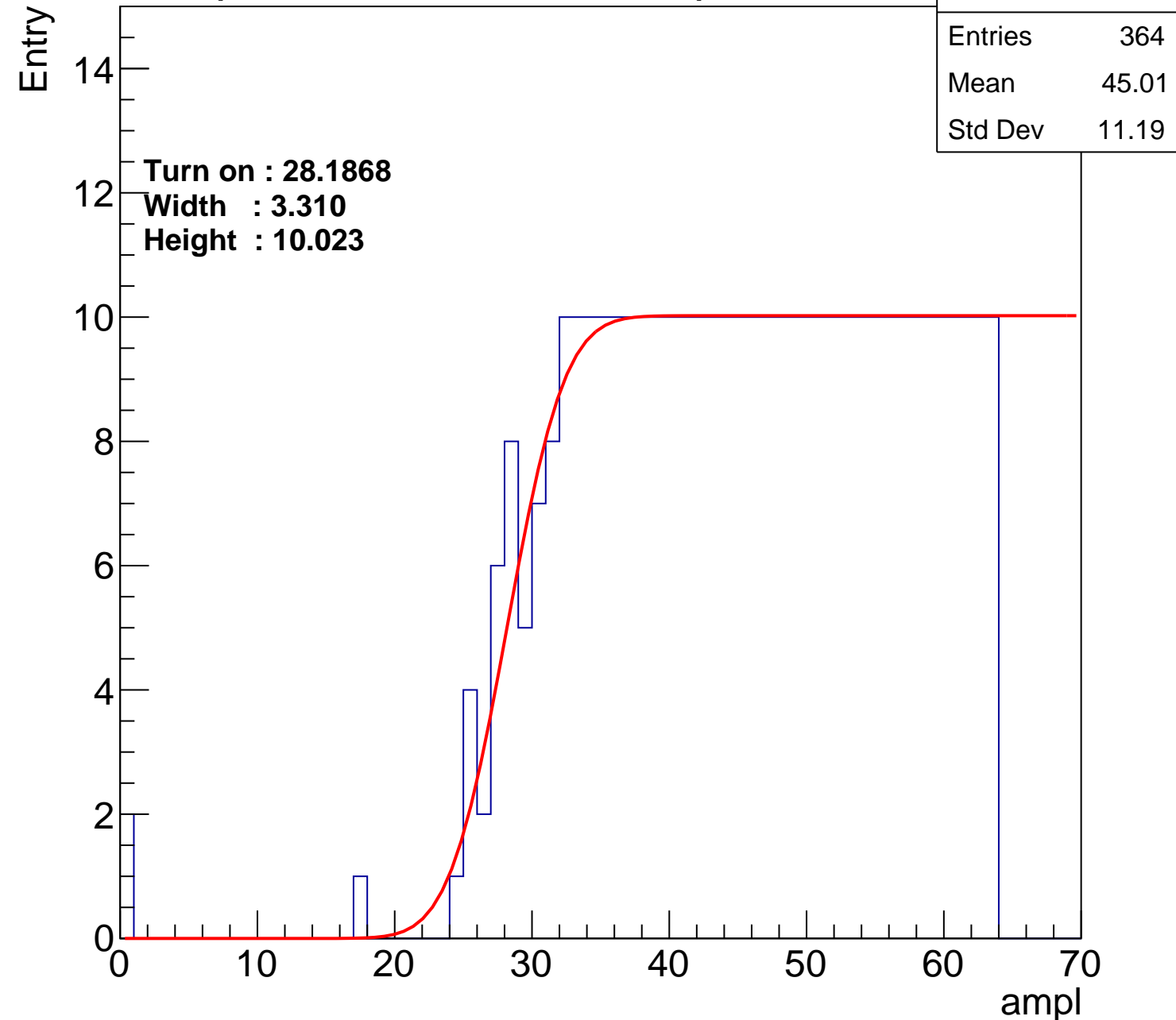
Width : 3.310

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch16

calib_packv5_042523_0143.root, FC#7, port C2

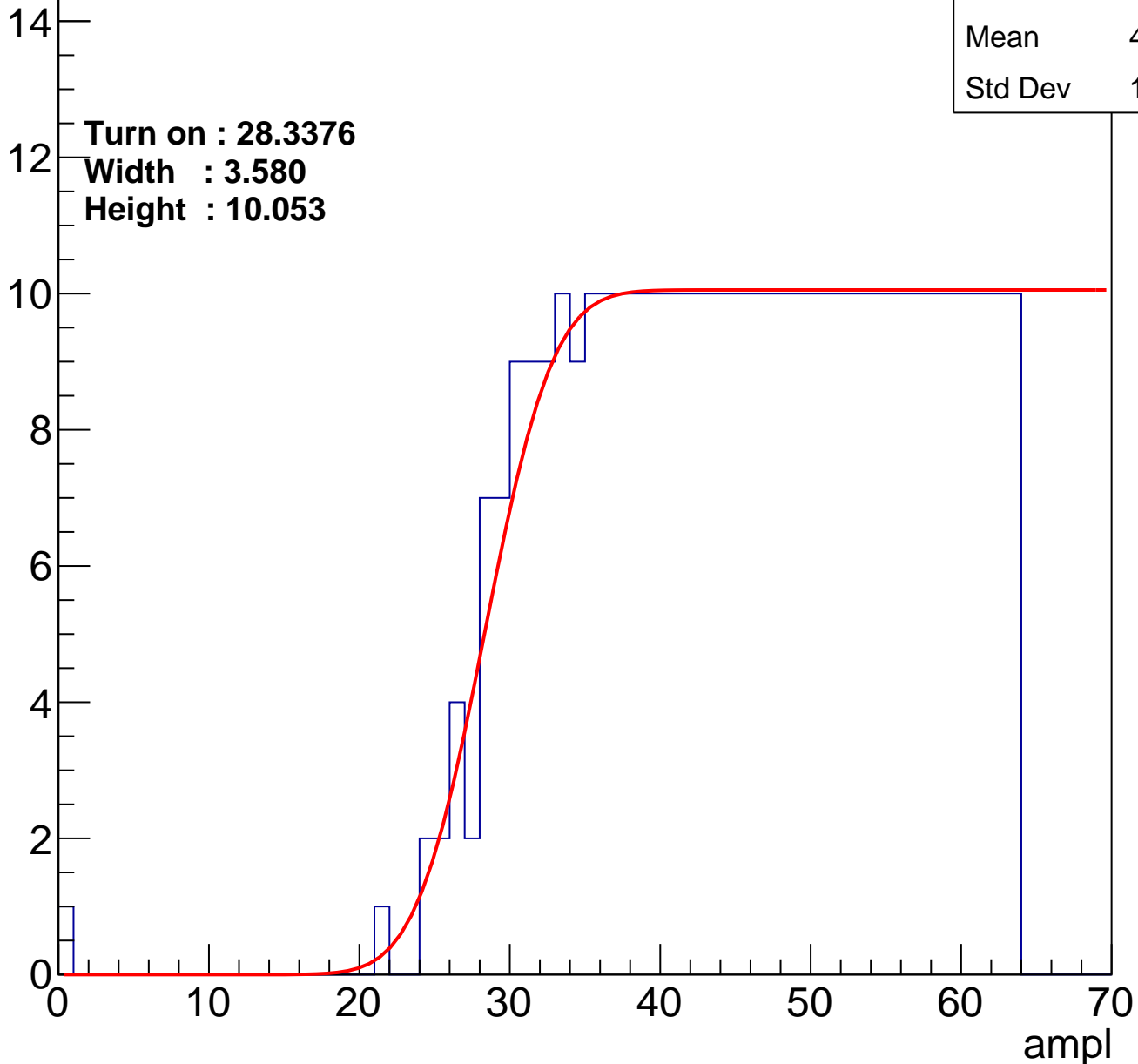
Entries	362
Mean	45.19
Std Dev	10.89

Turn on : 28.3376

Width : 3.580

Height : 10.053

Entry



B1L103S, U24-ch17

calib_packv5_042523_0143.root, FC#7, port C2

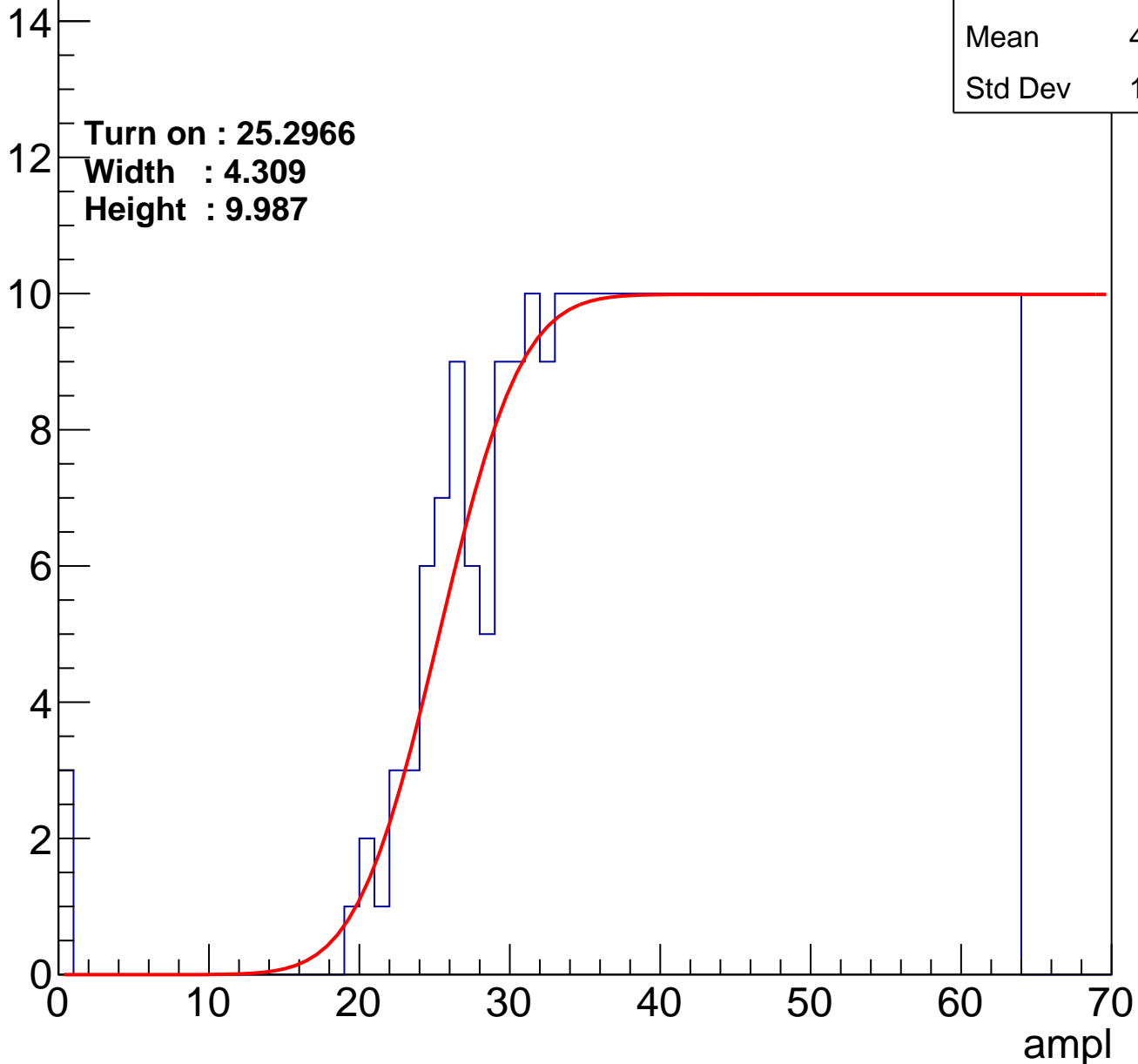
Entries	393
Mean	43.46
Std Dev	12.17

Turn on : 25.2966

Width : 4.309

Height : 9.987

Entry



B1L103S, U24-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	43.99
Std Dev	12.03

Turn on : 27.2472

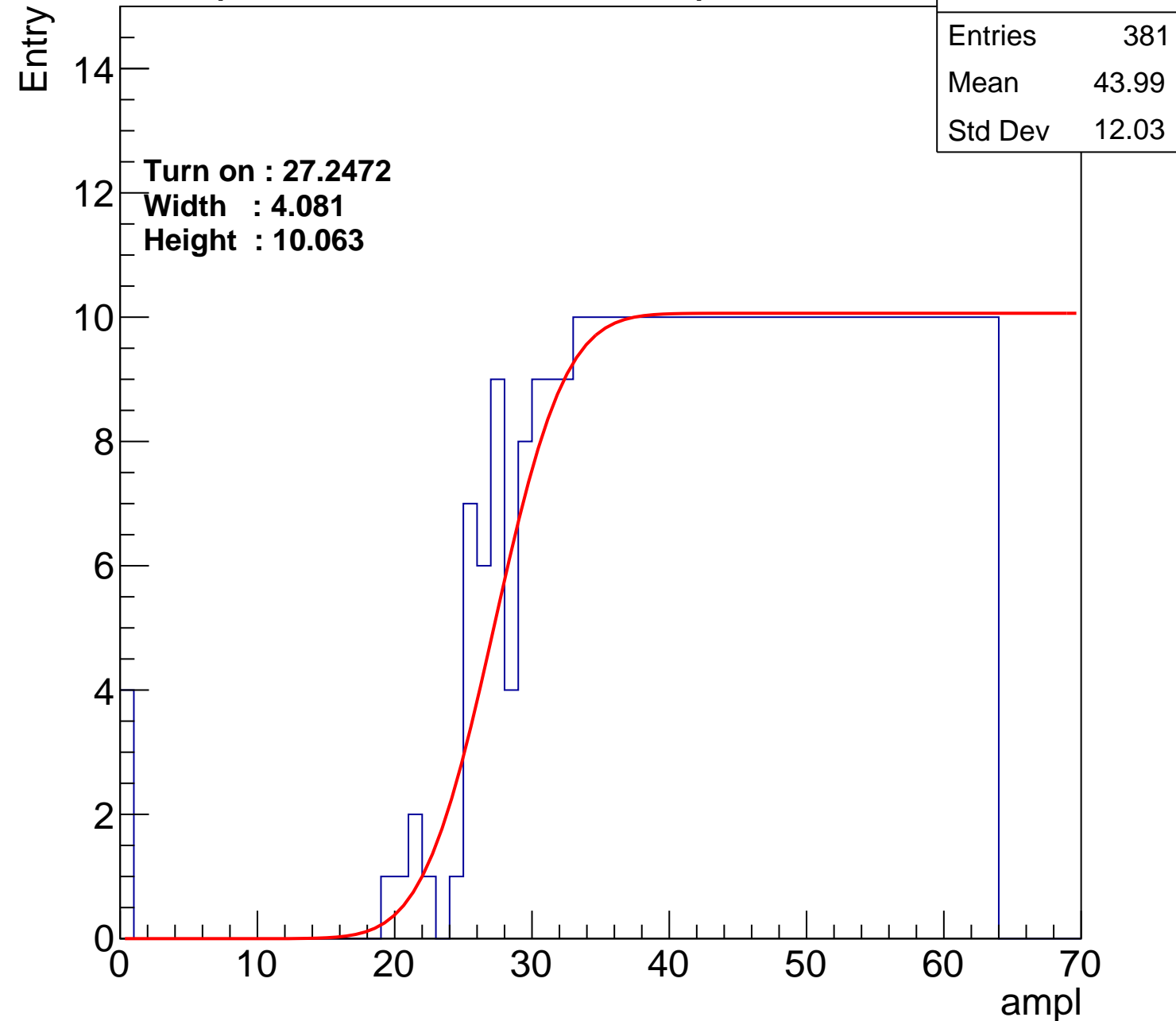
Width : 4.081

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.29
Std Dev	11.57

Turn on : 26.4096

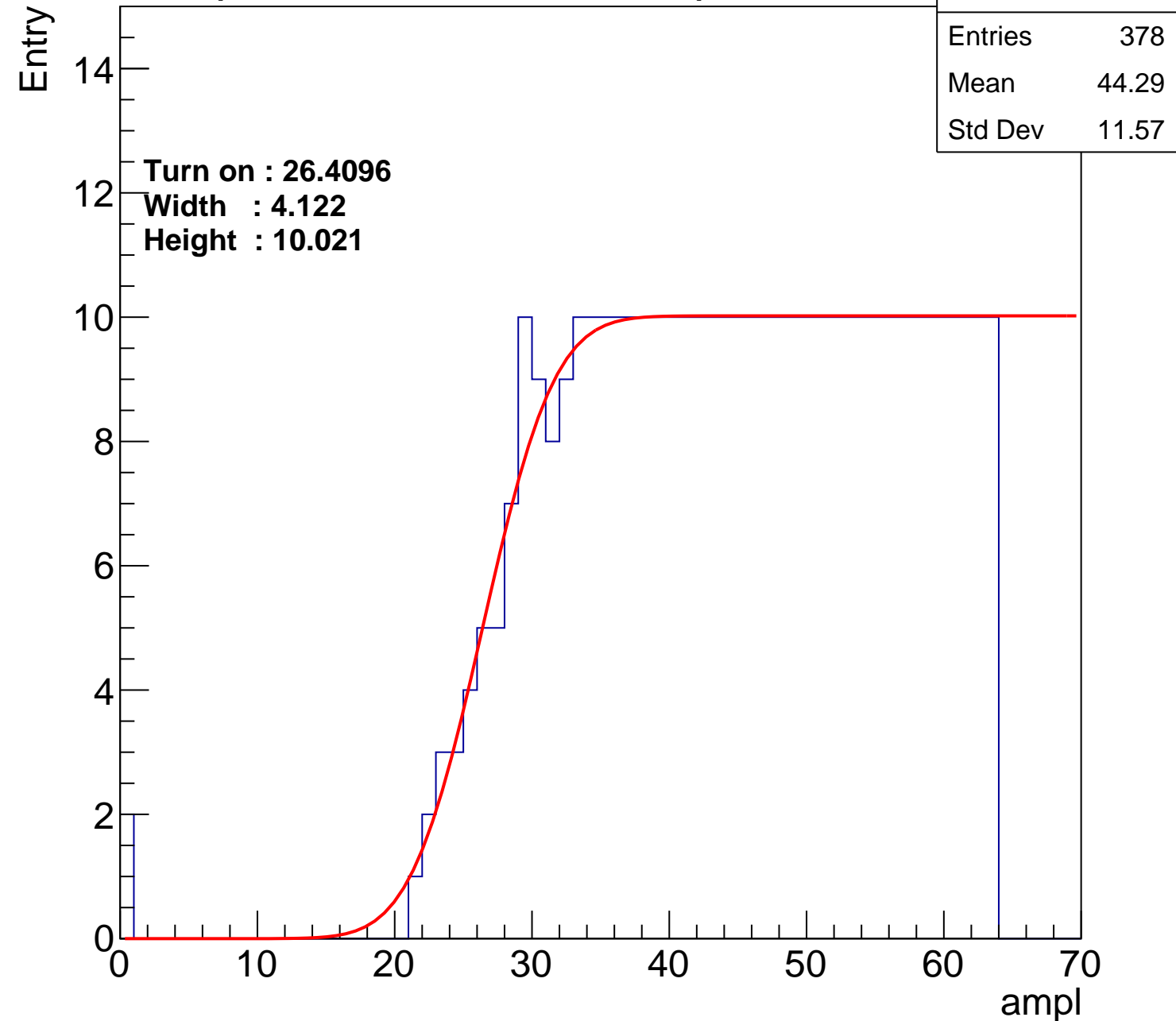
Width : 4.122

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch20

calib_packv5_042523_0143.root, FC#7, port C2

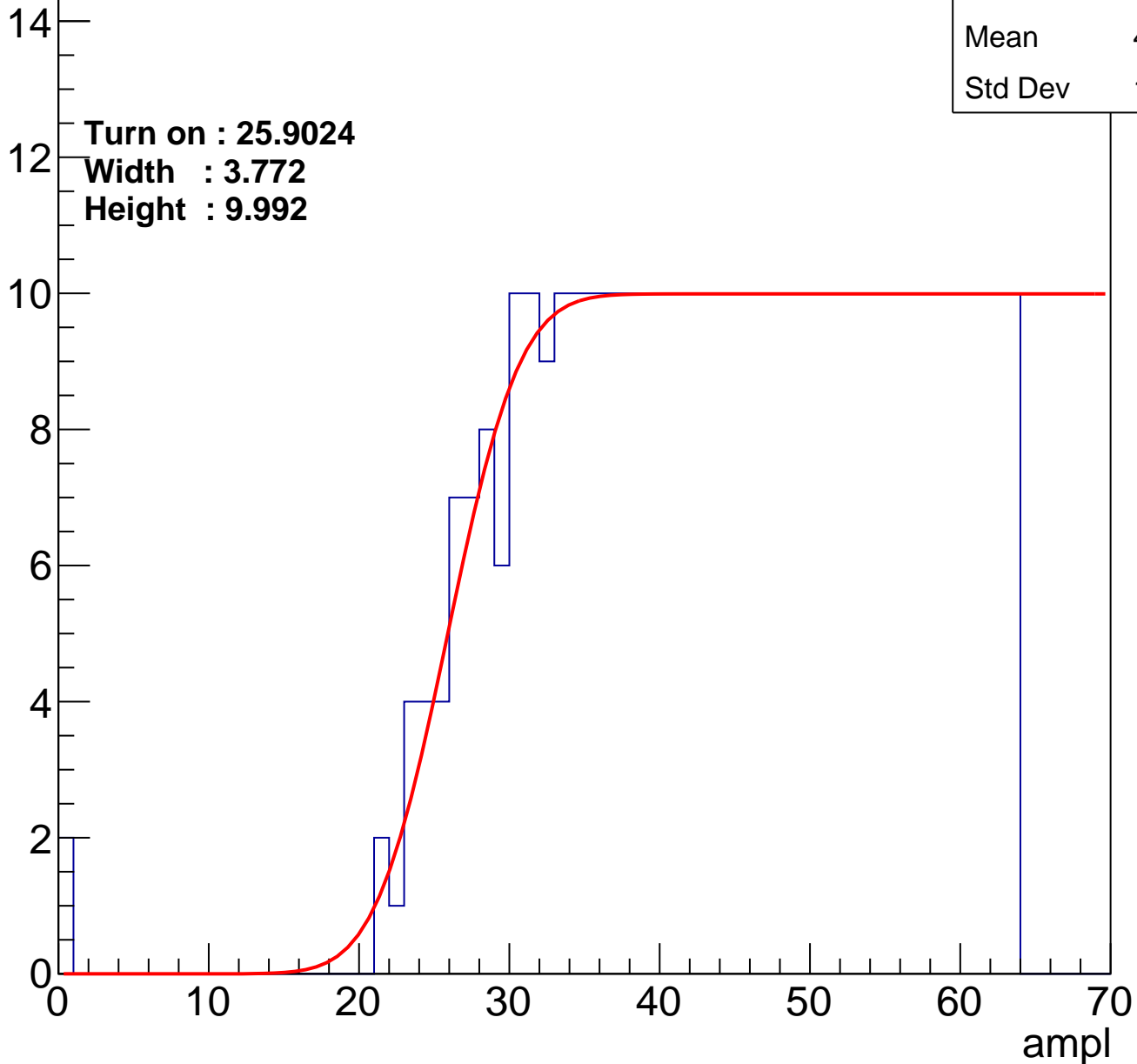
Entries	384
Mean	44.01
Std Dev	11.71

Turn on : 25.9024

Width : 3.772

Height : 9.992

Entry



B1L103S, U24-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	45
Std Dev	10.97

Turn on : 26.9307

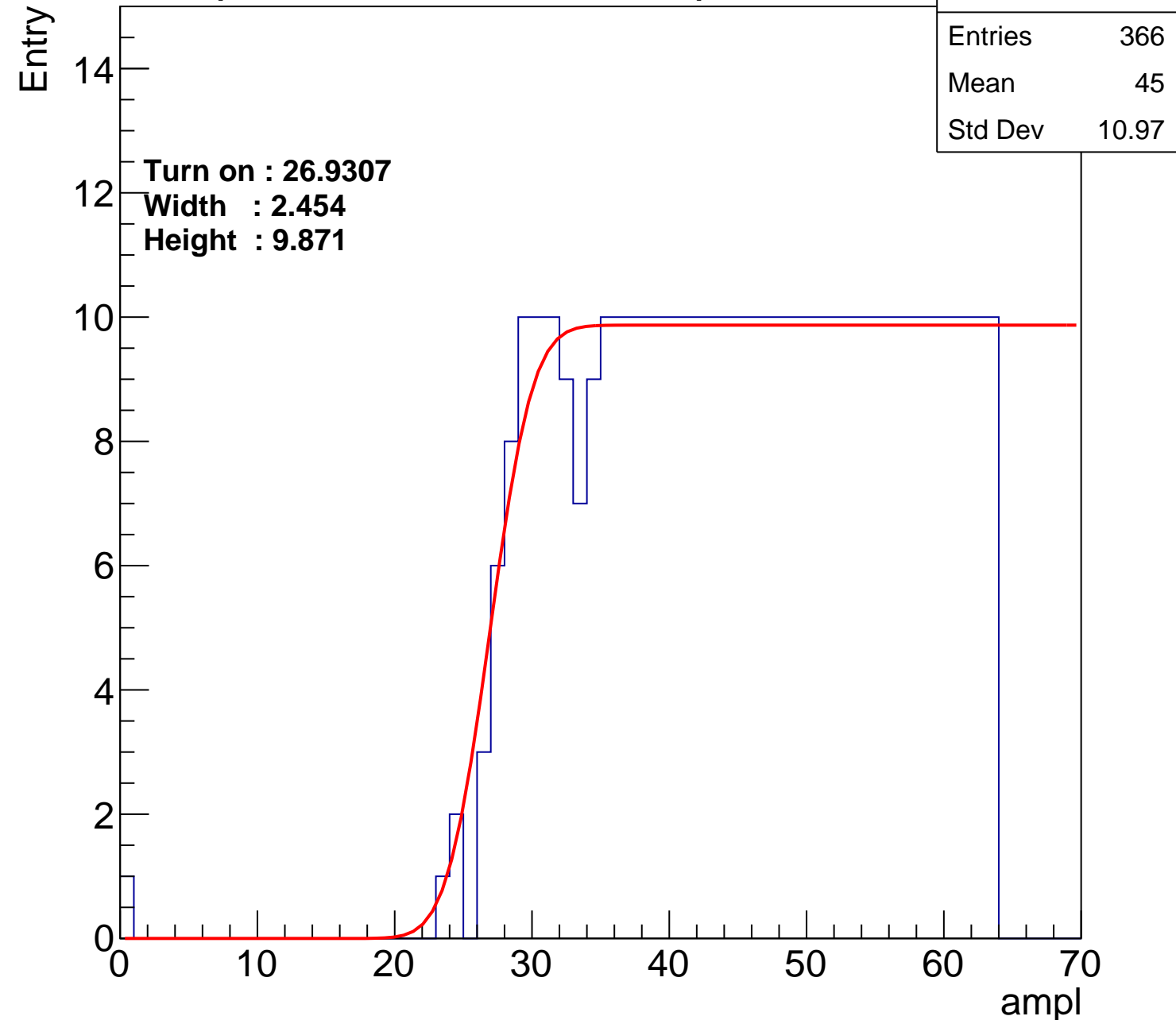
Width : 2.454

Height : 9.871

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch22

calib_packv5_042523_0143.root, FC#7, port C2

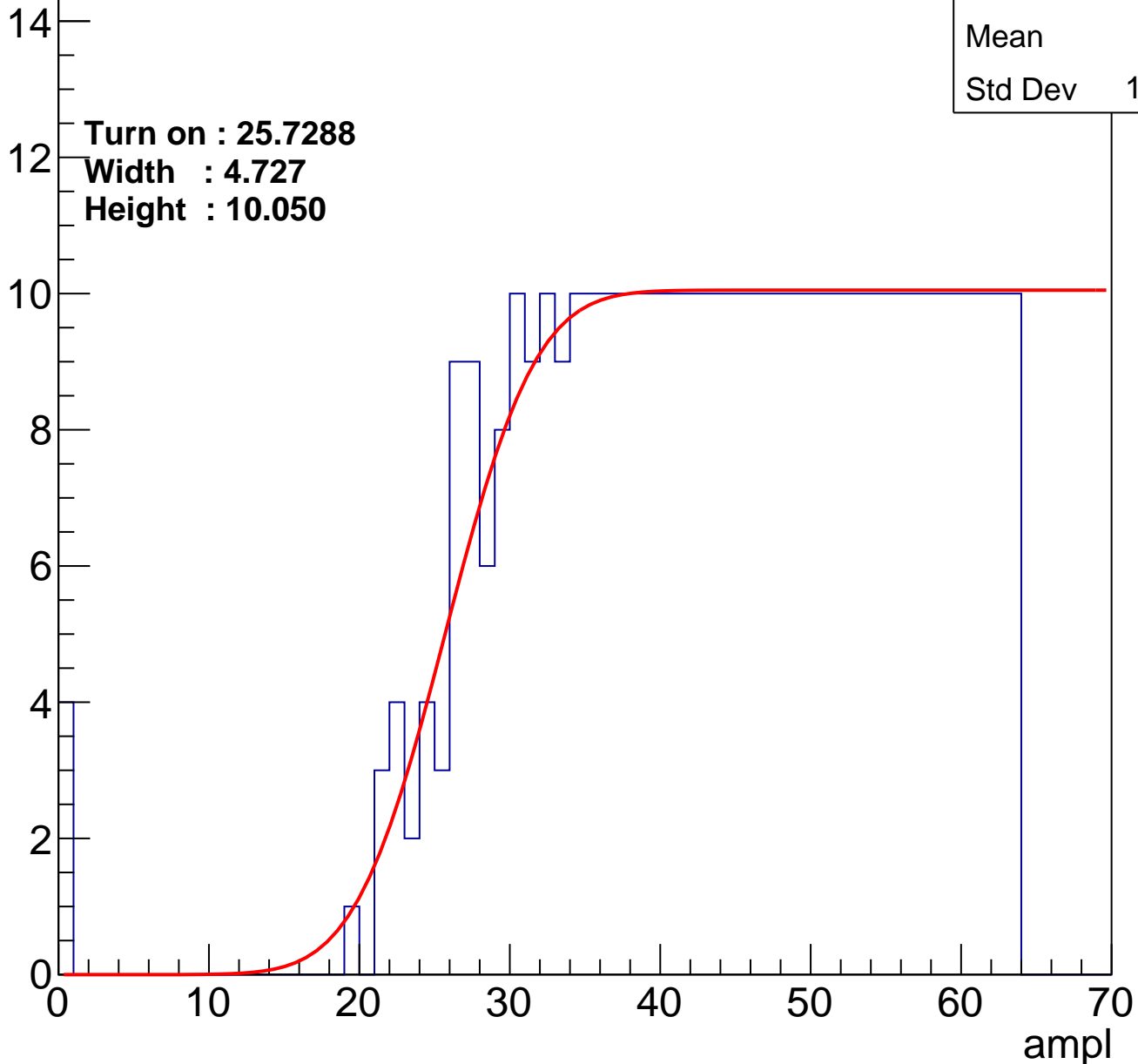
Entries	391
Mean	43.5
Std Dev	12.26

Turn on : 25.7288

Width : 4.727

Height : 10.050

Entry



B1L103S, U24-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.8881

Width : 2.965

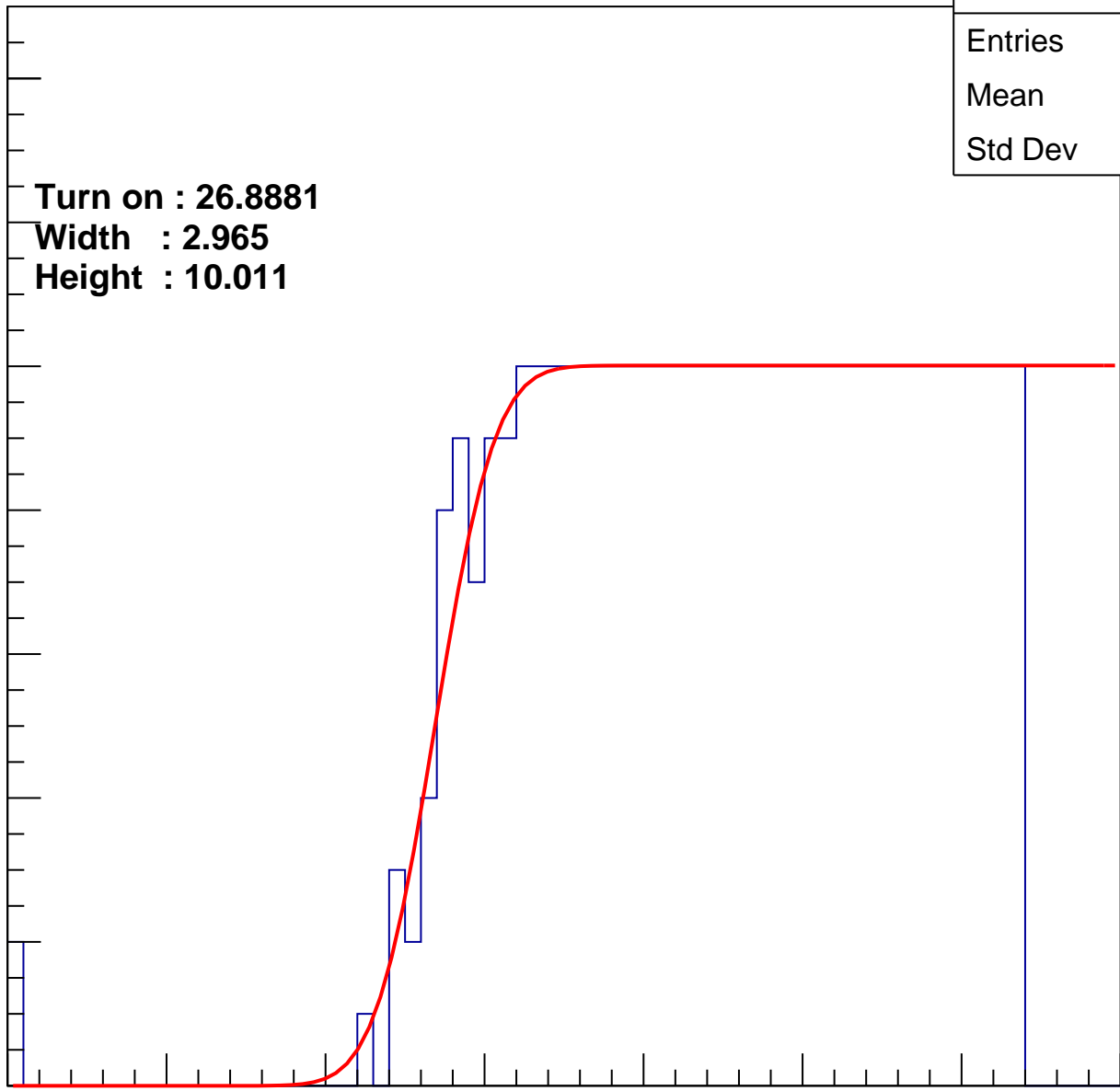
Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch24

calib_packv5_042523_0143.root, FC#7, port C2

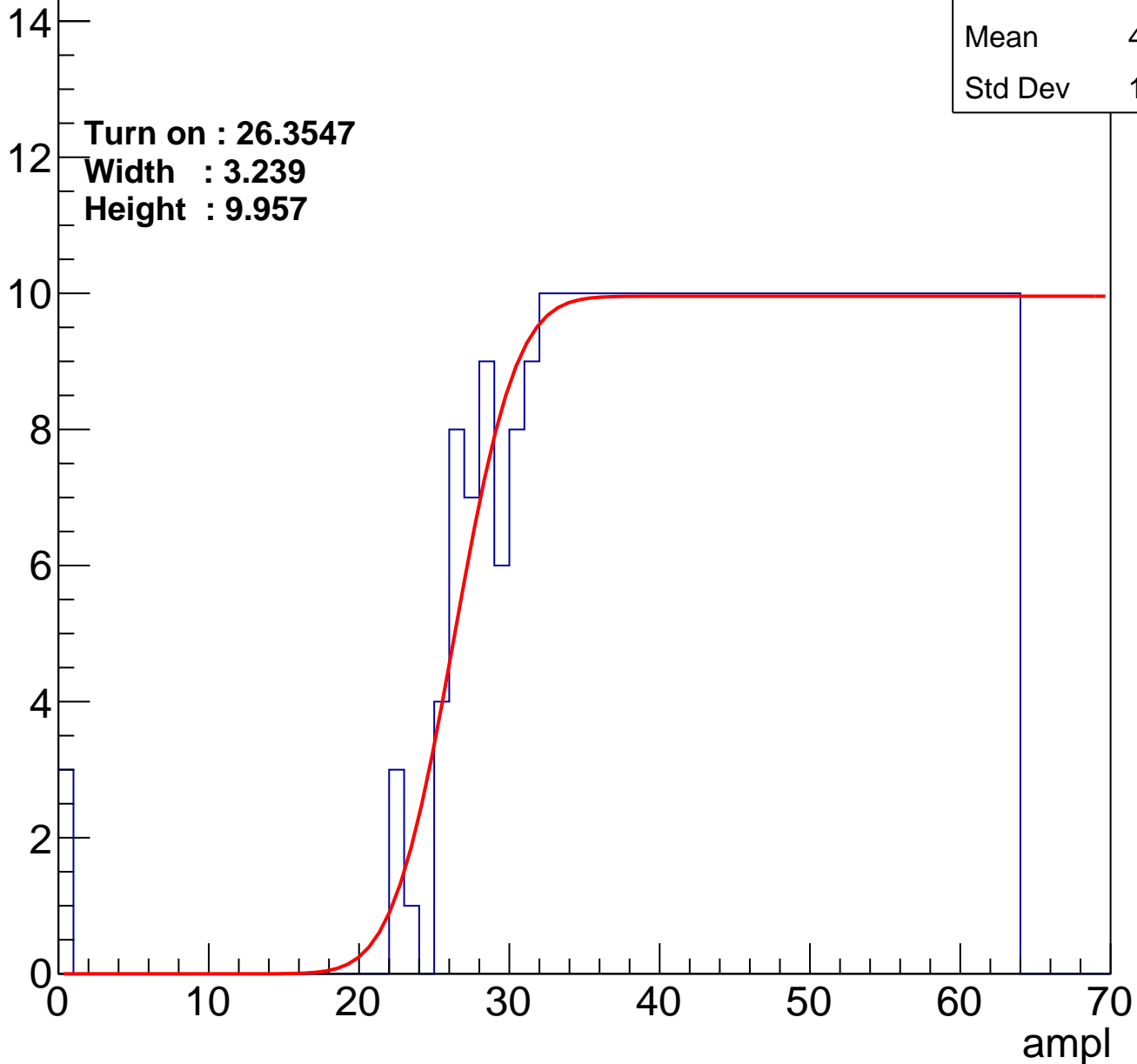
Entry

Entries	378
Mean	44.26
Std Dev	11.69

Turn on : 26.3547

Width : 3.239

Height : 9.957



B1L103S, U24-ch25

calib_packv5_042523_0143.root, FC#7, port C2

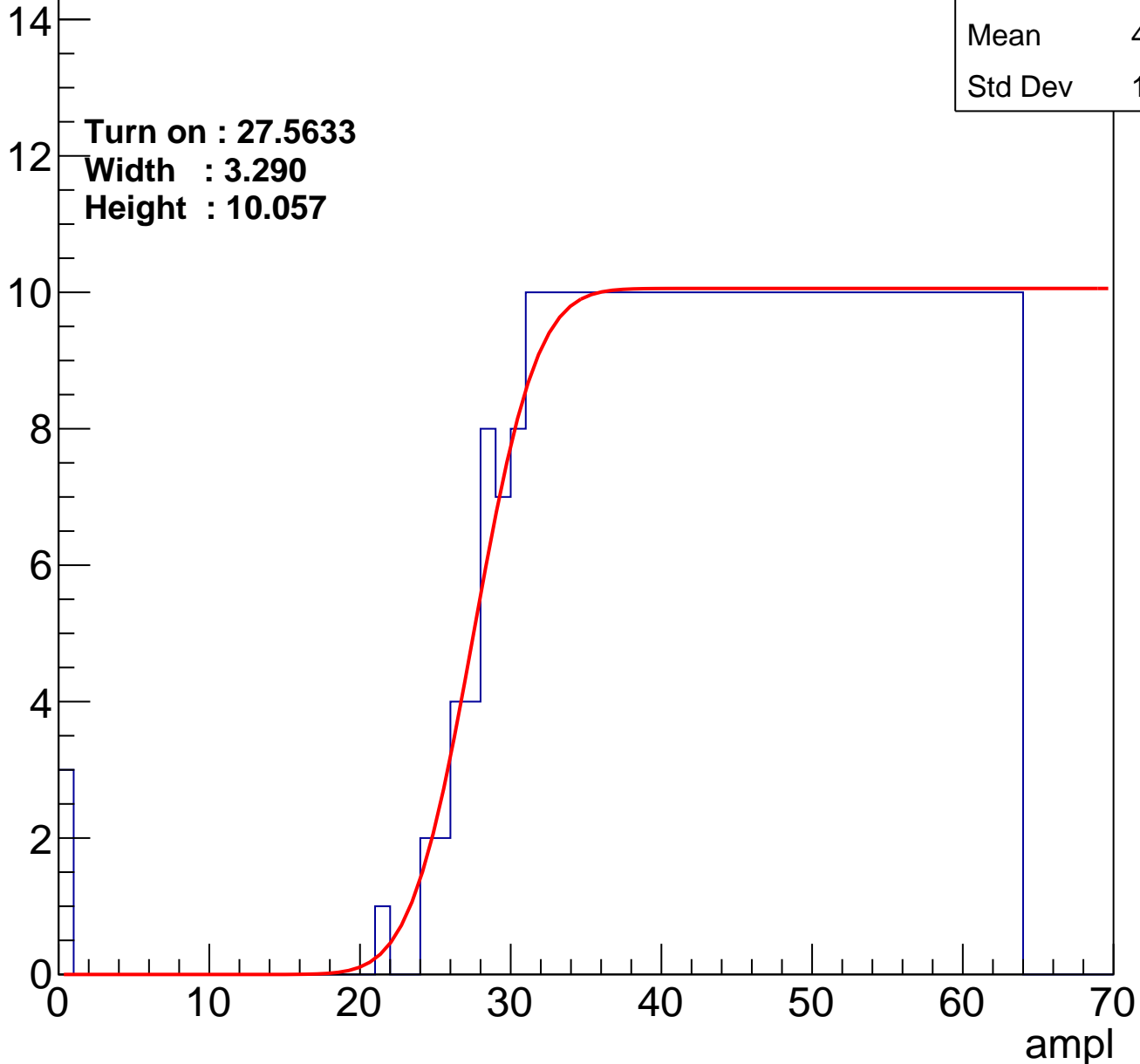
Entries	369
Mean	44.74
Std Dev	11.43

Turn on : 27.5633

Width : 3.290

Height : 10.057

Entry



B1L103S, U24-ch26

calib_packv5_042523_0143.root, FC#7, port C2

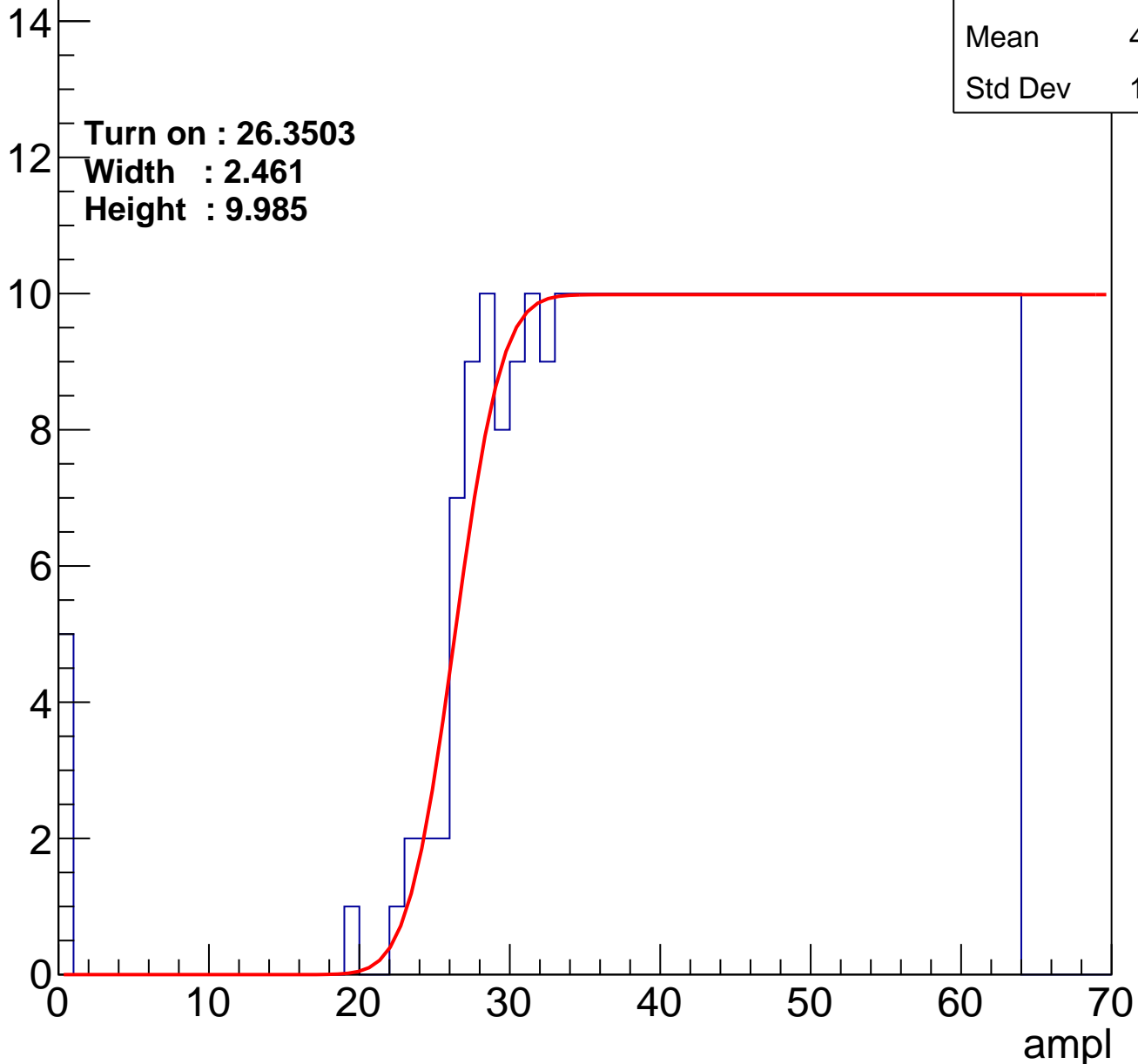
Entries	385
Mean	43.82
Std Dev	12.16

Turn on : 26.3503

Width : 2.461

Height : 9.985

Entry



B1L103S, U24-ch27

calib_packv5_042523_0143.root, FC#7, port C2

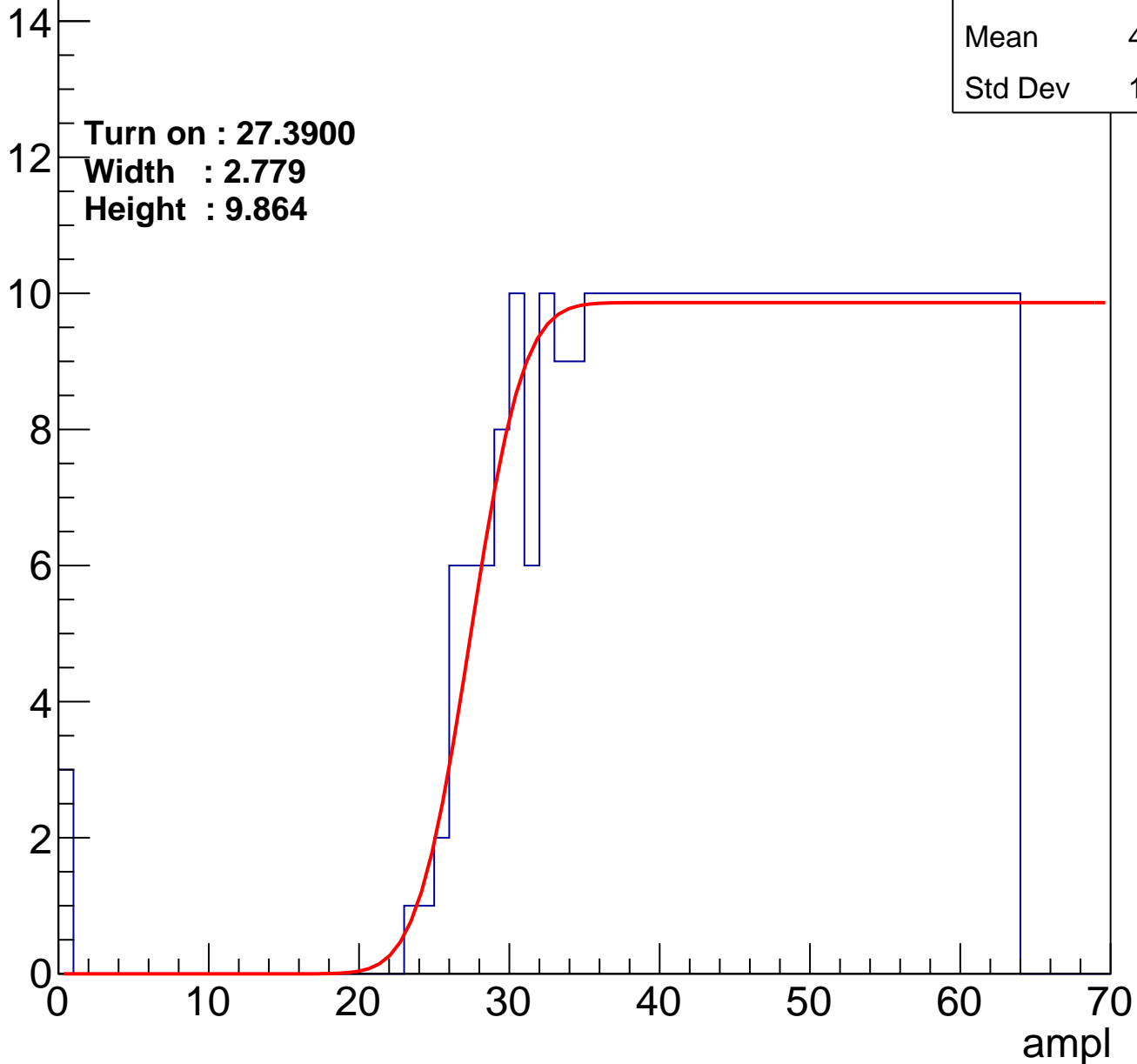
Entry

Entries	367
Mean	44.78
Std Dev	11.45

Turn on : 27.3900

Width : 2.779

Height : 9.864



B1L103S, U24-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.54
Std Dev	12.29

Turn on : 27.0069

Width : 2.945

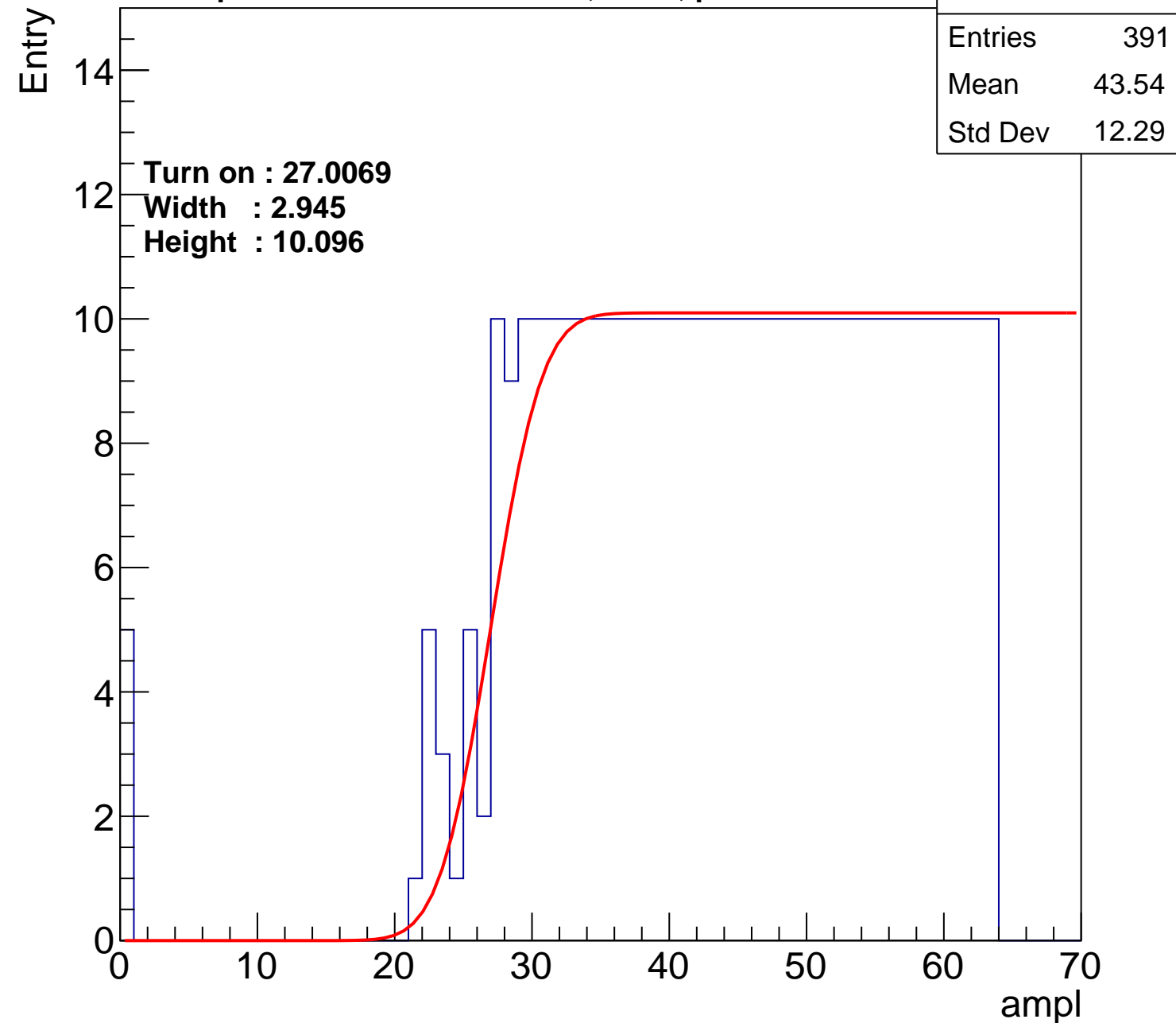
Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch29

calib_packv5_042523_0143.root, FC#7, port C2

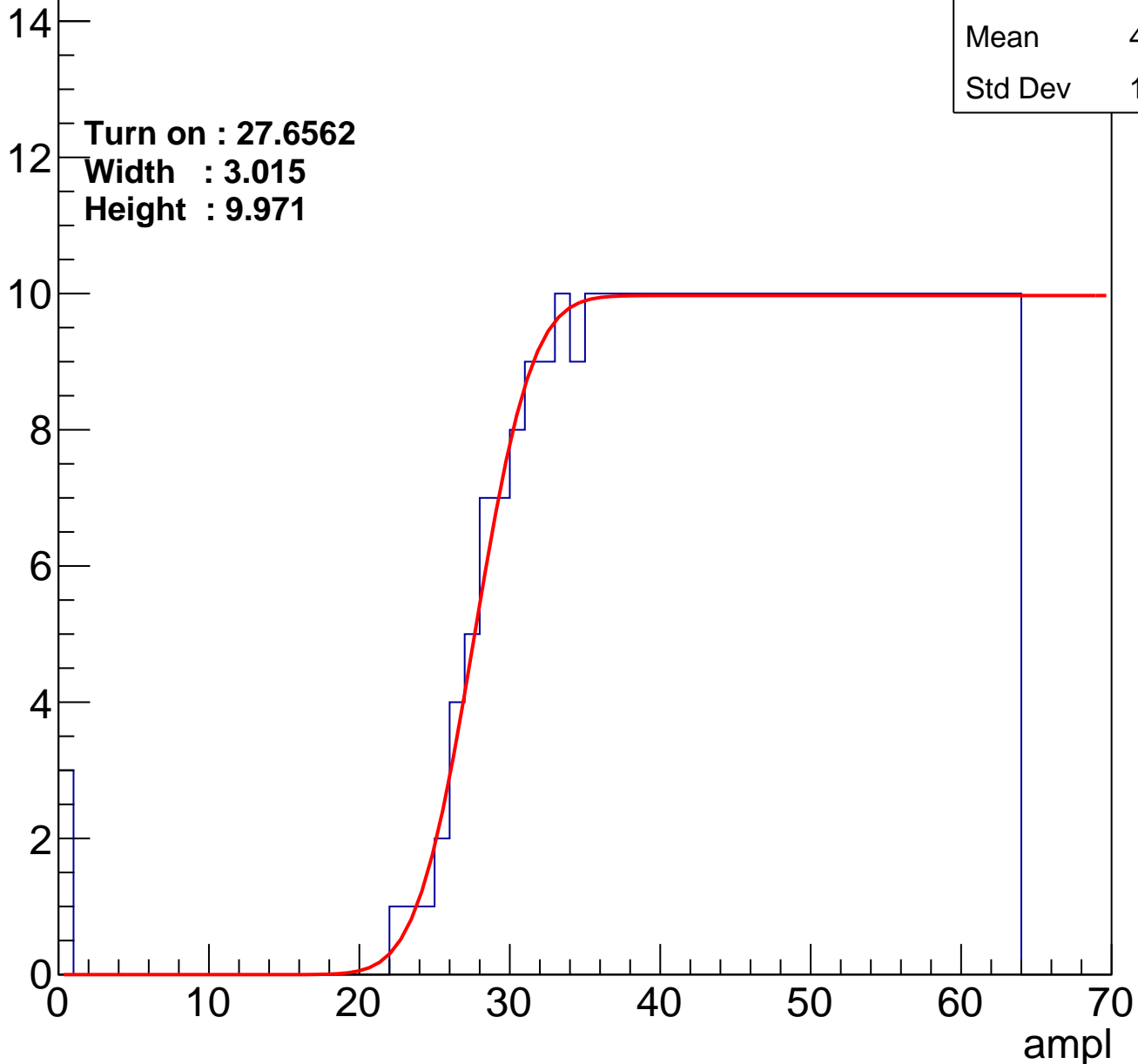
Entries	366
Mean	44.84
Std Dev	11.42

Turn on : 27.6562

Width : 3.015

Height : 9.971

Entry



B1L103S, U24-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	401
Mean	43.24
Std Dev	11.99

Turn on : 24.2660

Width : 3.872

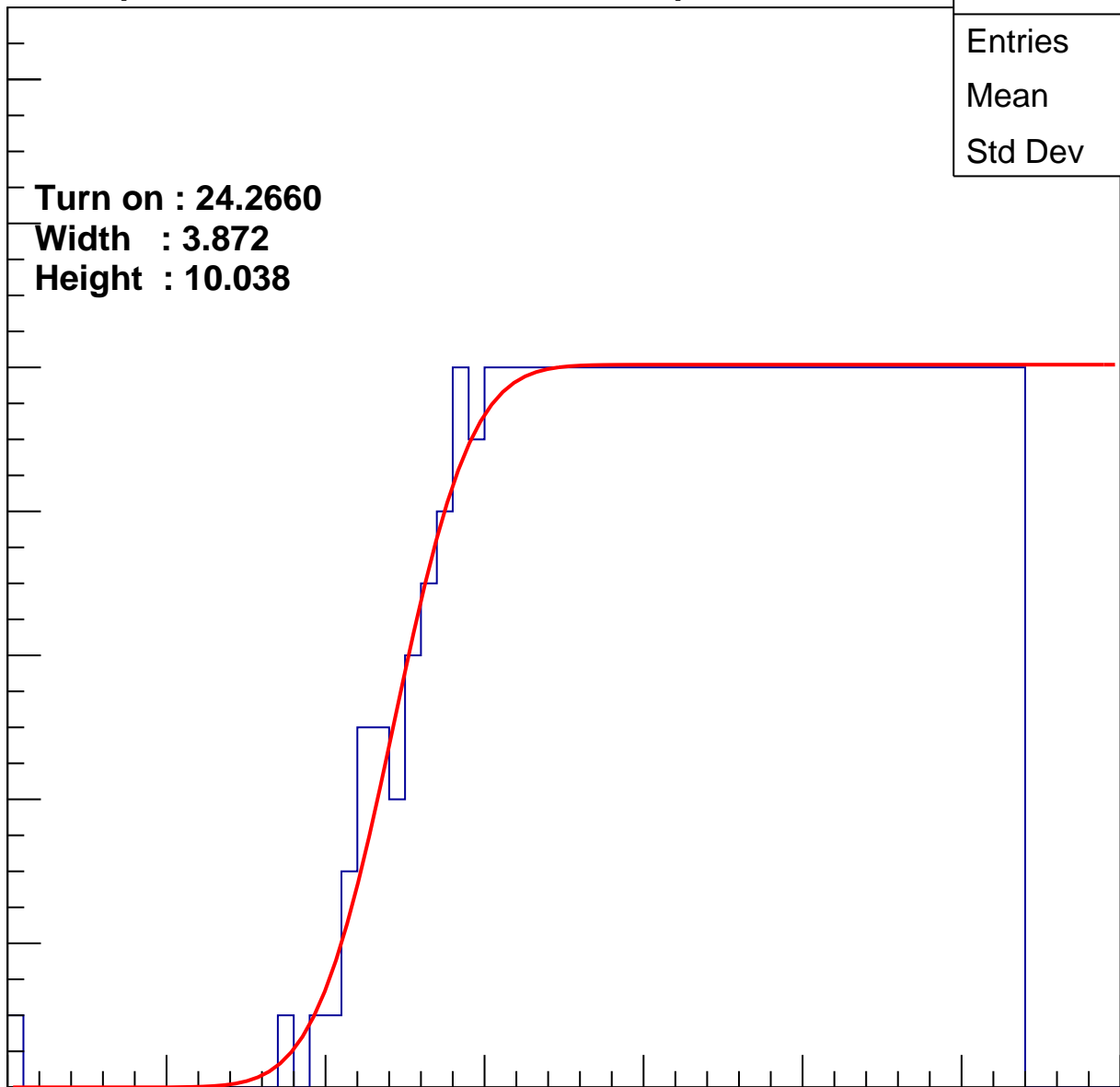
Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.37
Std Dev	12.26

Turn on : 24.7276

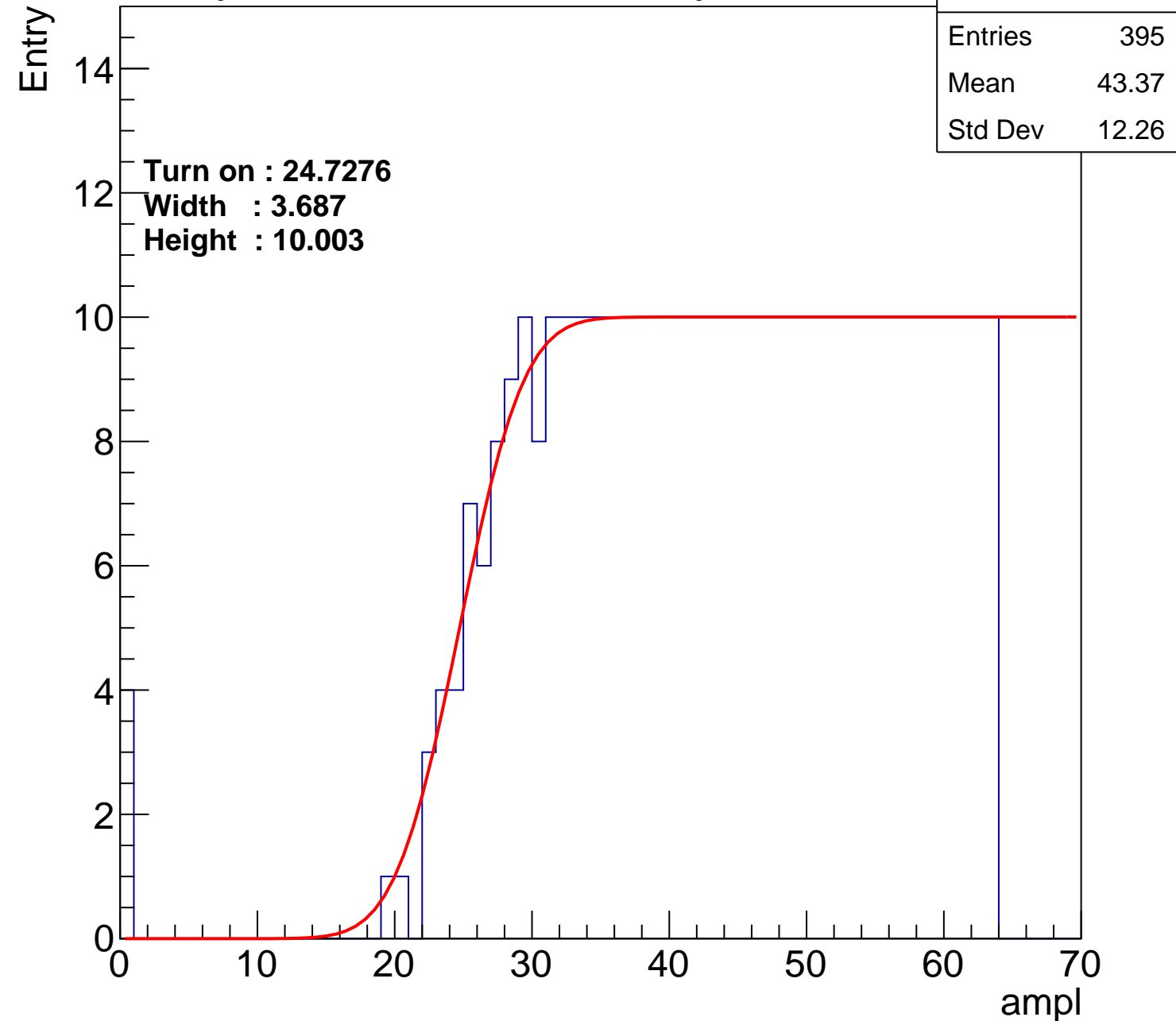
Width : 3.687

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.42
Std Dev	11.28

Turn on : 26.0955

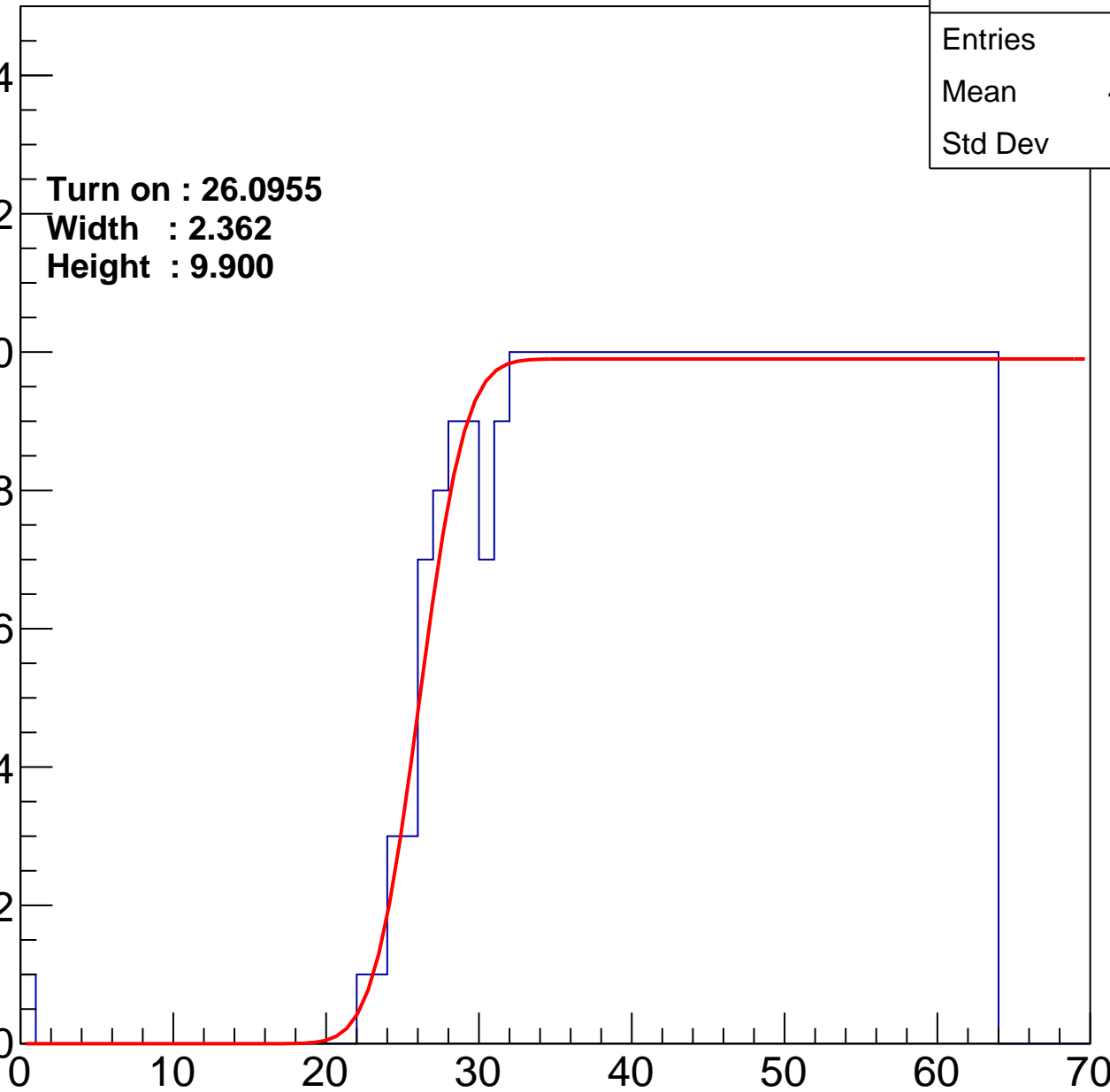
Width : 2.362

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 28.1249

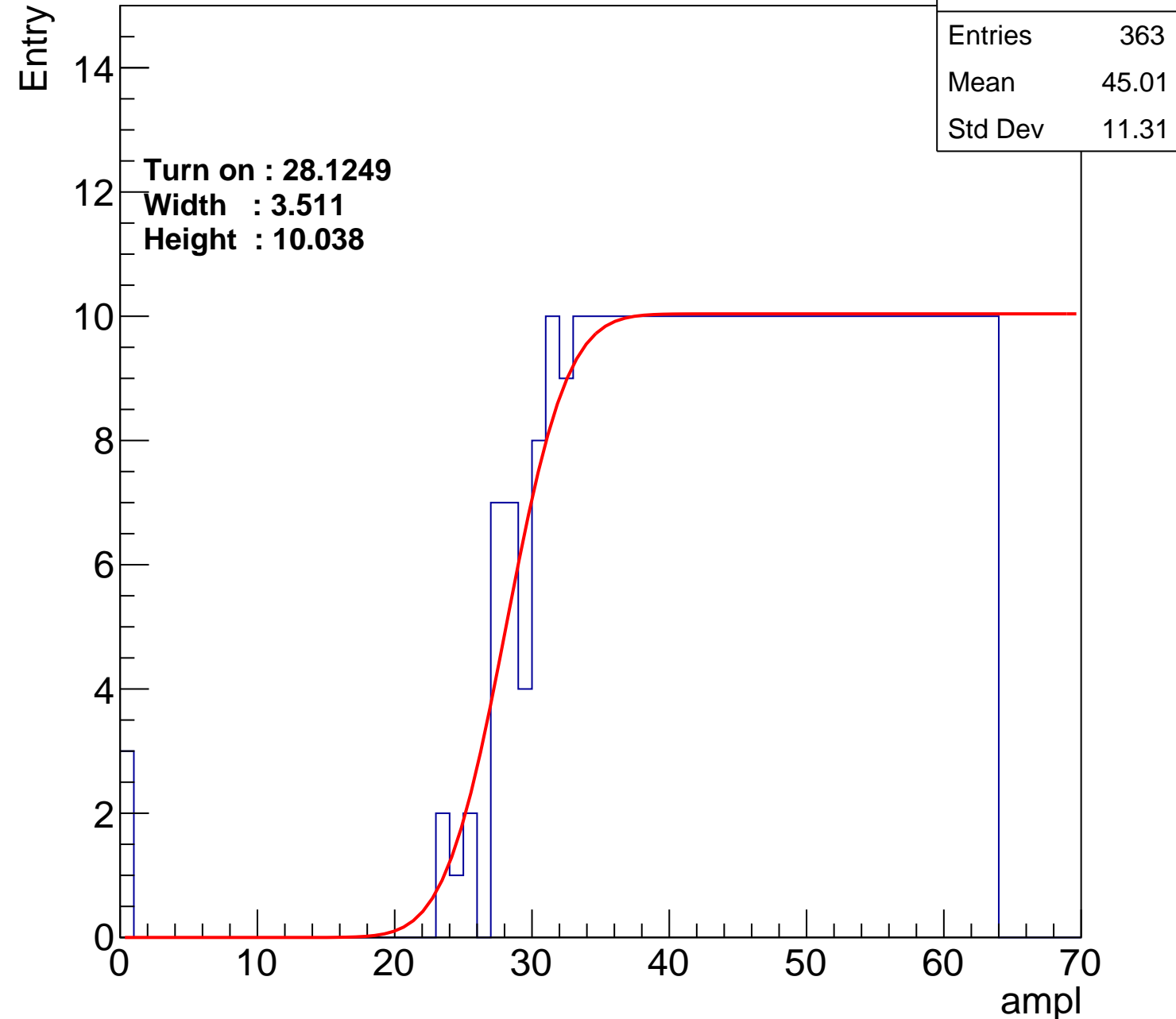
Width : 3.511

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.66
Std Dev	11.85

Turn on : 25.4982

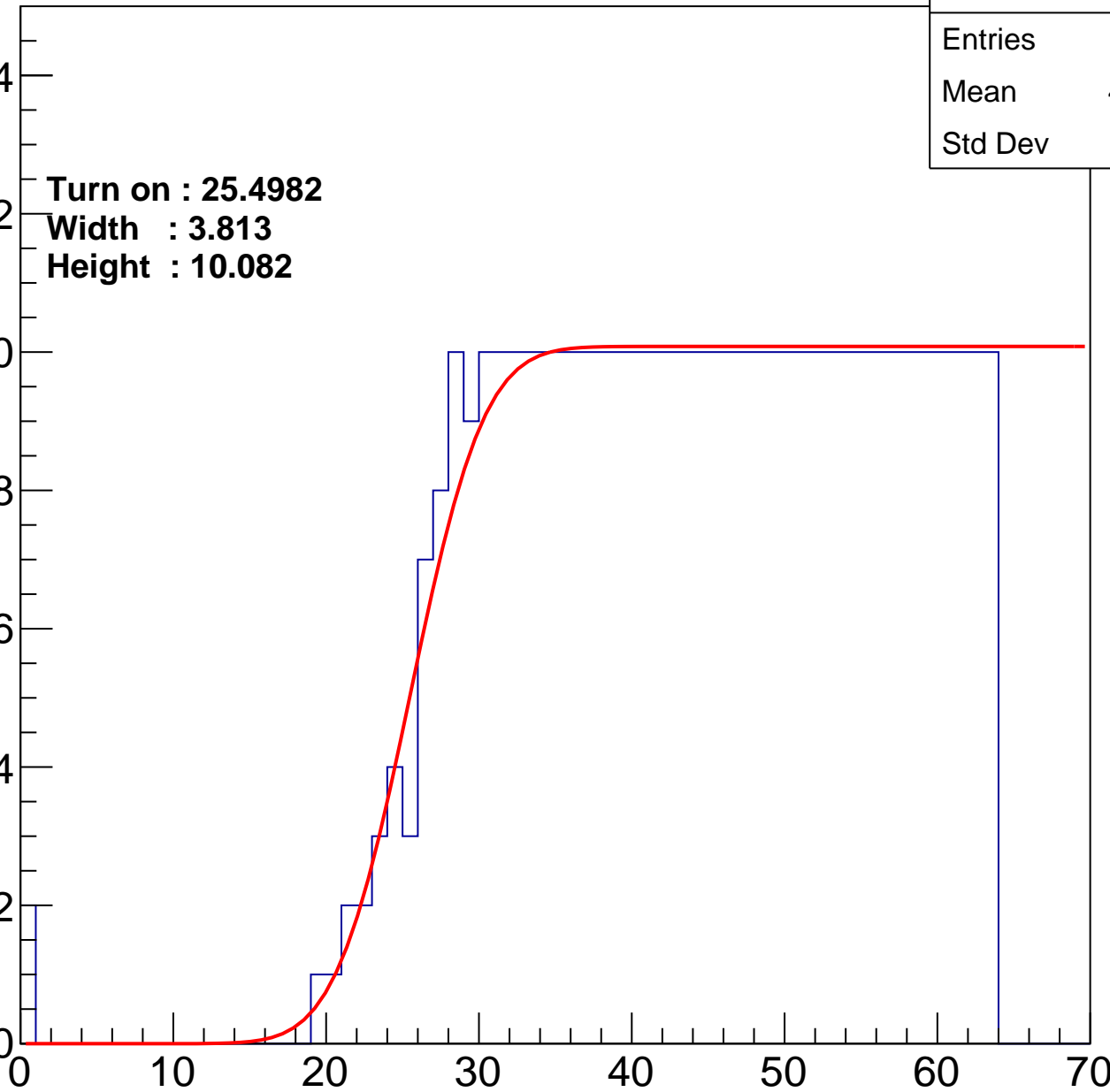
Width : 3.813

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.77
Std Dev	11.62

Turn on : 28.1199

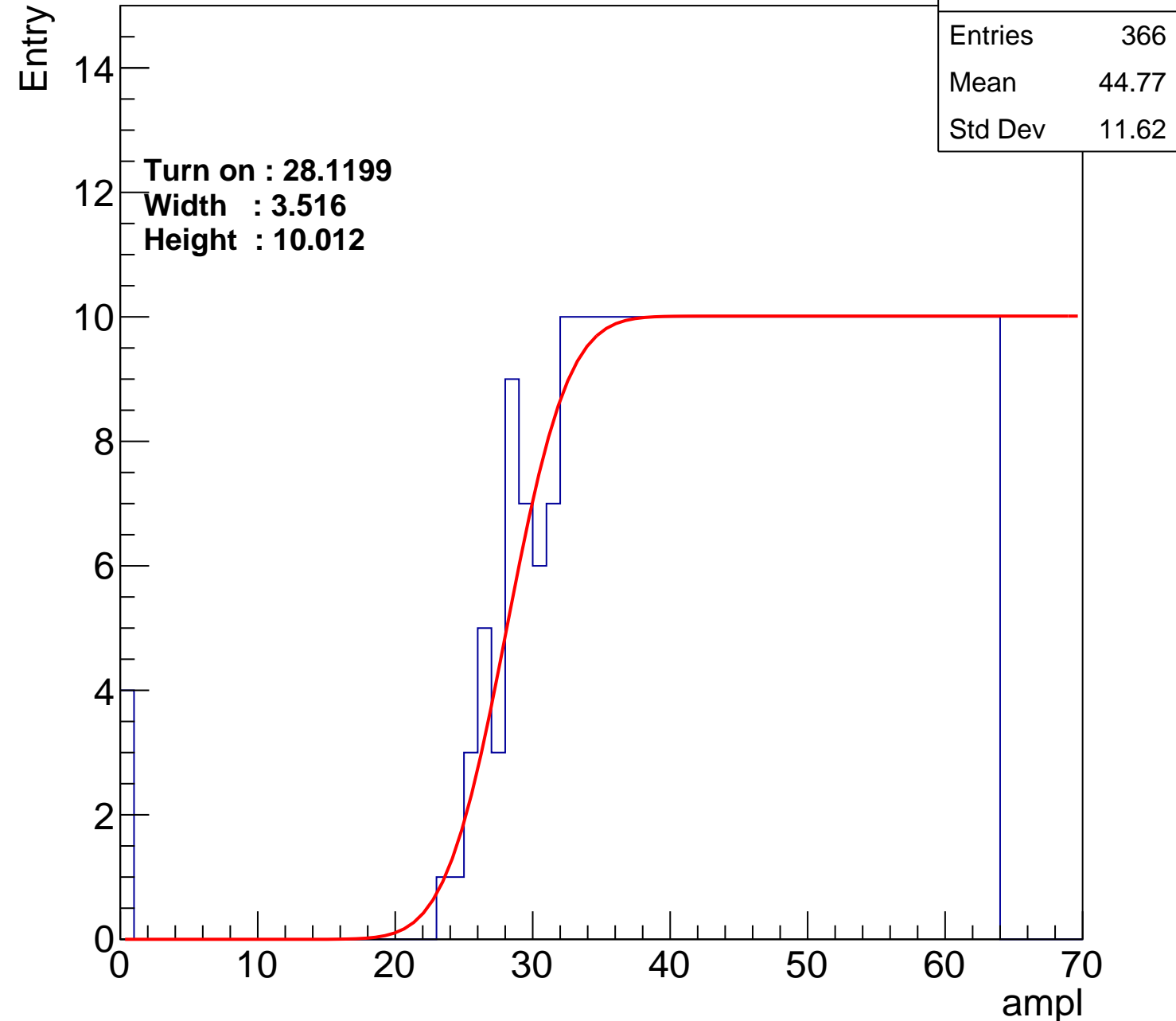
Width : 3.516

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch36

calib_packv5_042523_0143.root, FC#7, port C2

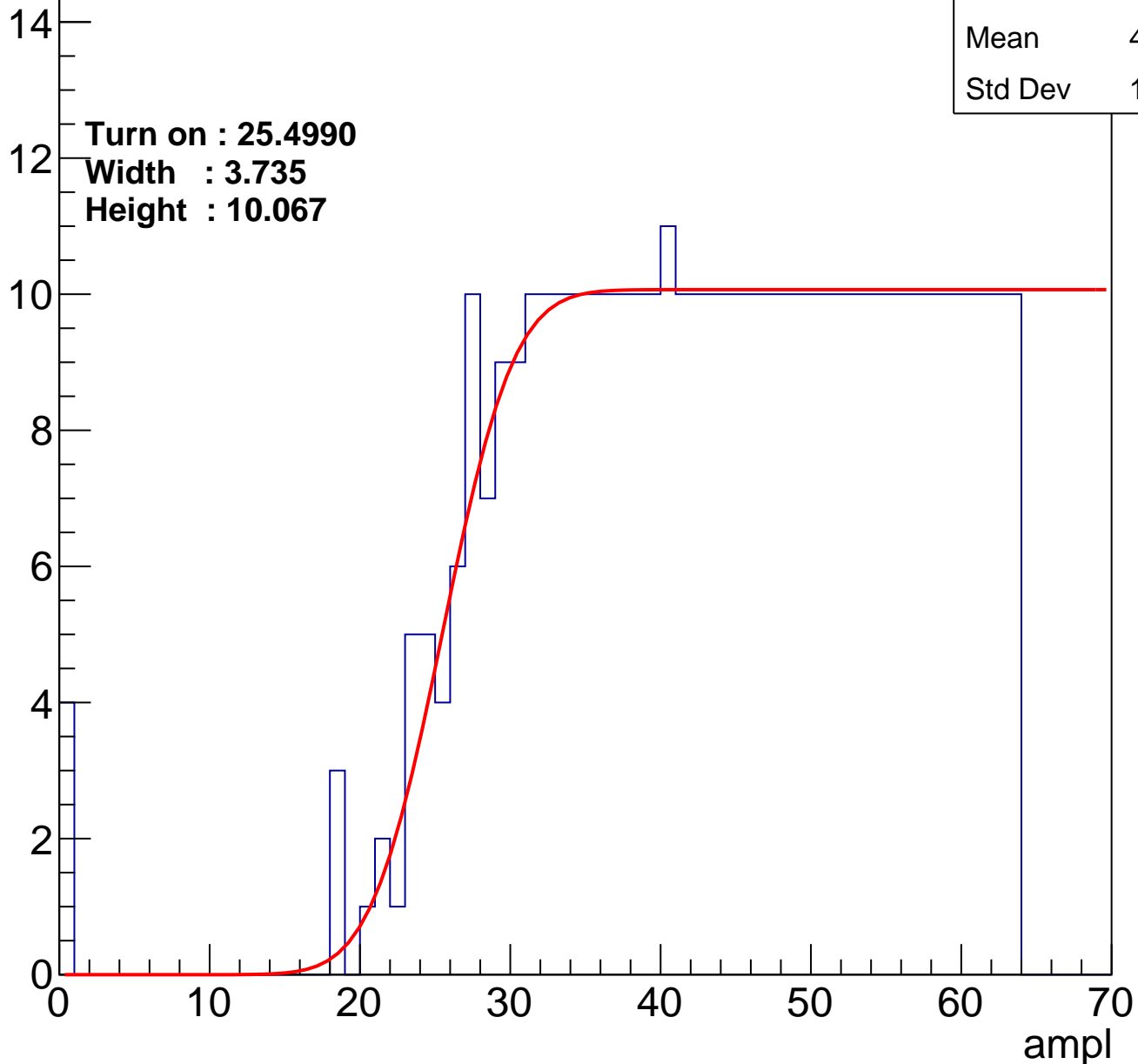
Entries	397
Mean	43.26
Std Dev	12.36

Turn on : 25.4990

Width : 3.735

Height : 10.067

Entry



B1L103S, U24-ch37

calib_packv5_042523_0143.root, FC#7, port C2

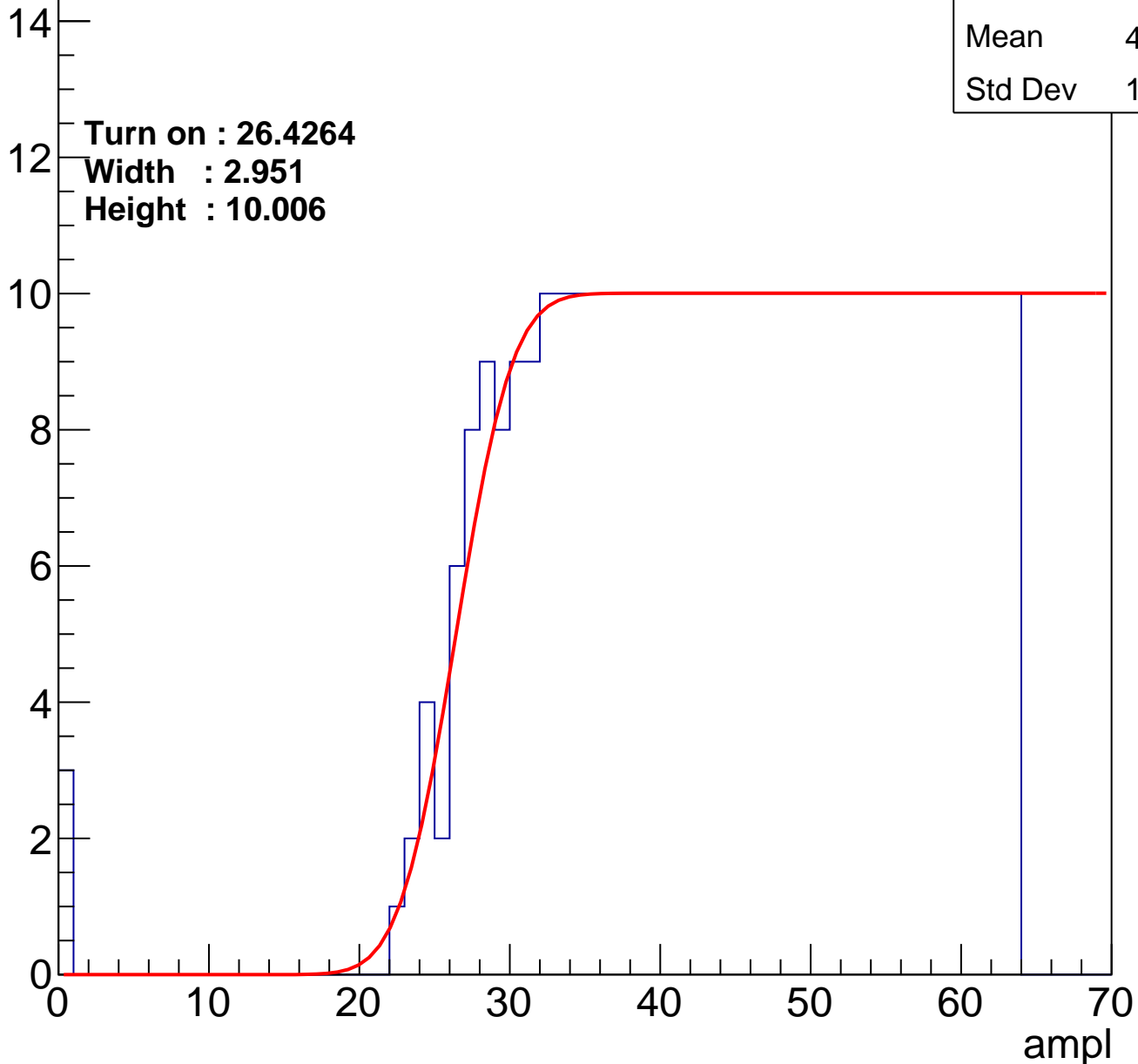
Entries	381
Mean	44.14
Std Dev	11.72

Turn on : 26.4264

Width : 2.951

Height : 10.006

Entry



B1L103S, U24-ch38

calib_packv5_042523_0143.root, FC#7, port C2

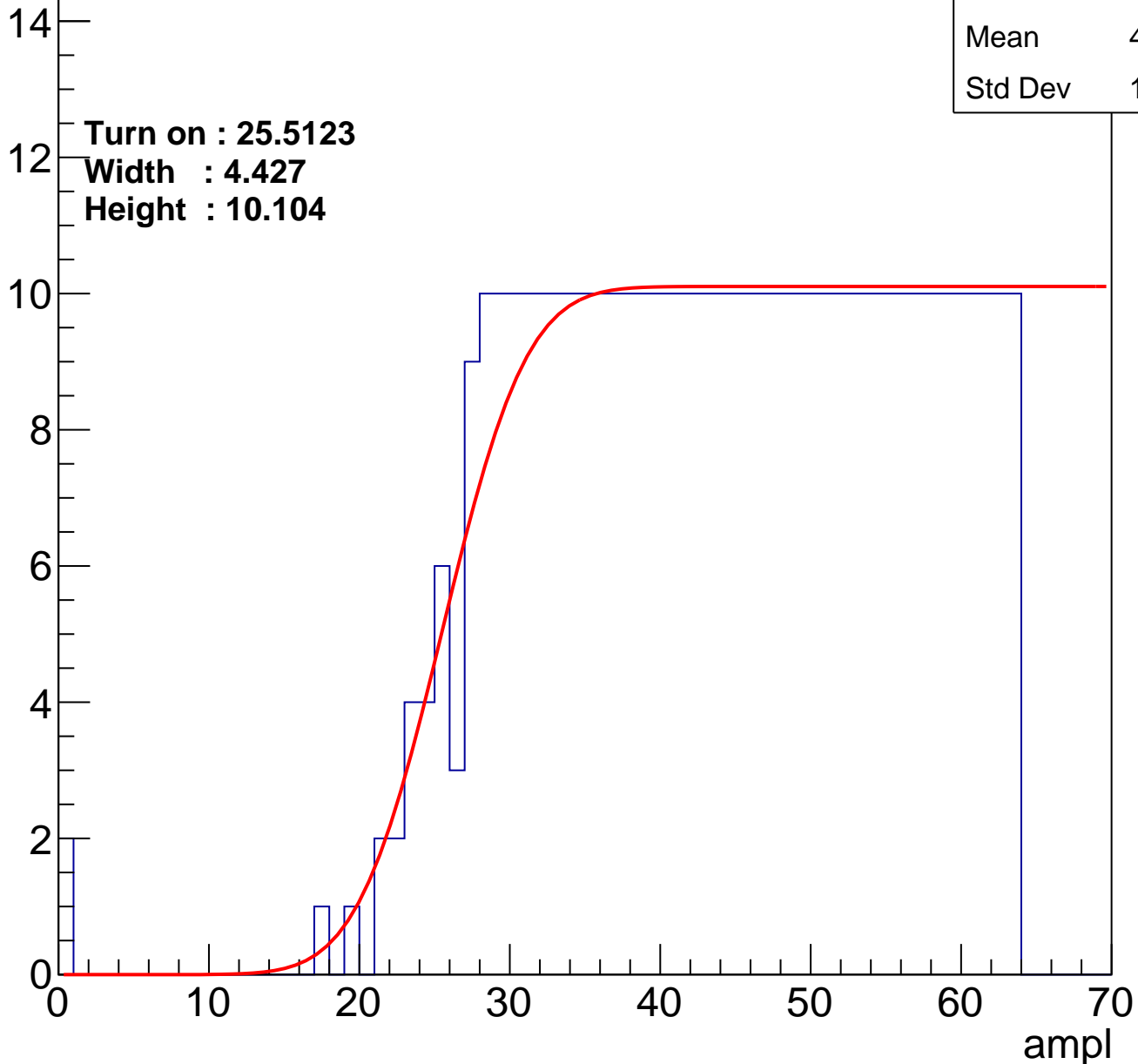
Entries	394
Mean	43.56
Std Dev	11.92

Turn on : 25.5123

Width : 4.427

Height : 10.104

Entry



B1L103S, U24-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 27.1227

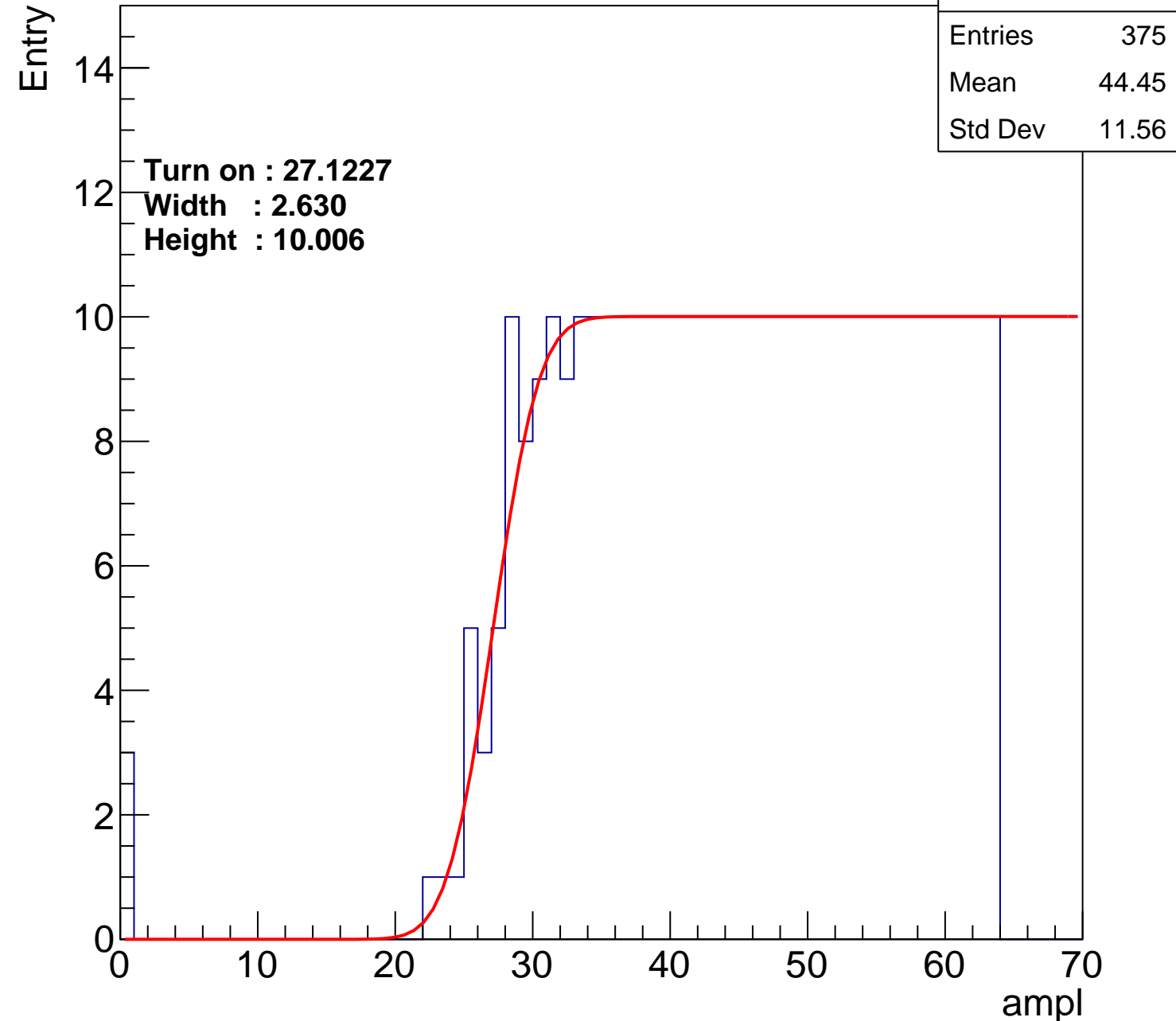
Width : 2.630

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch40

calib_packv5_042523_0143.root, FC#7, port C2

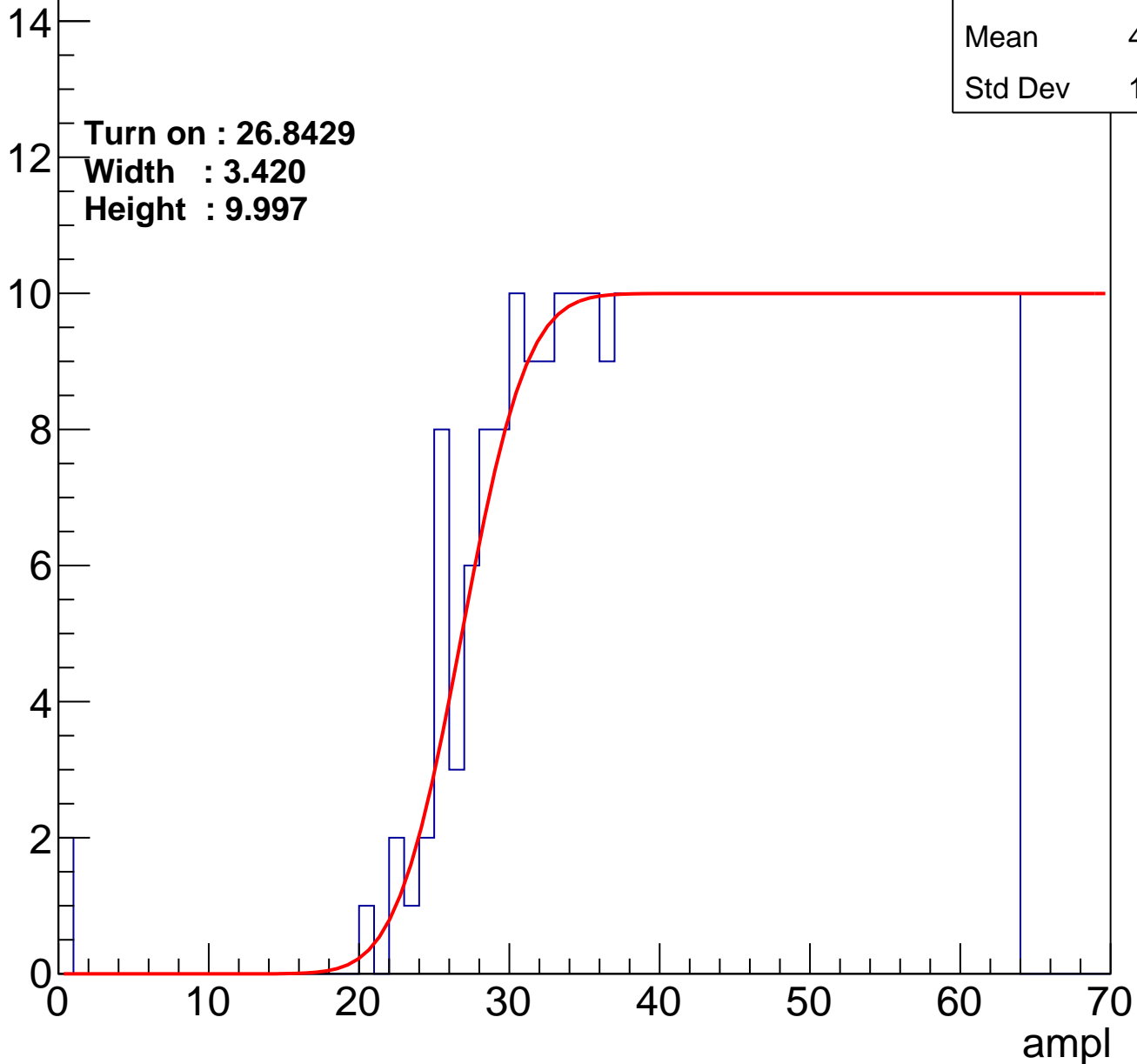
Entries	378
Mean	44.29
Std Dev	11.56

Turn on : 26.8429

Width : 3.420

Height : 9.997

Entry



B1L103S, U24-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.47
Std Dev	11.49

Turn on : 26.8183

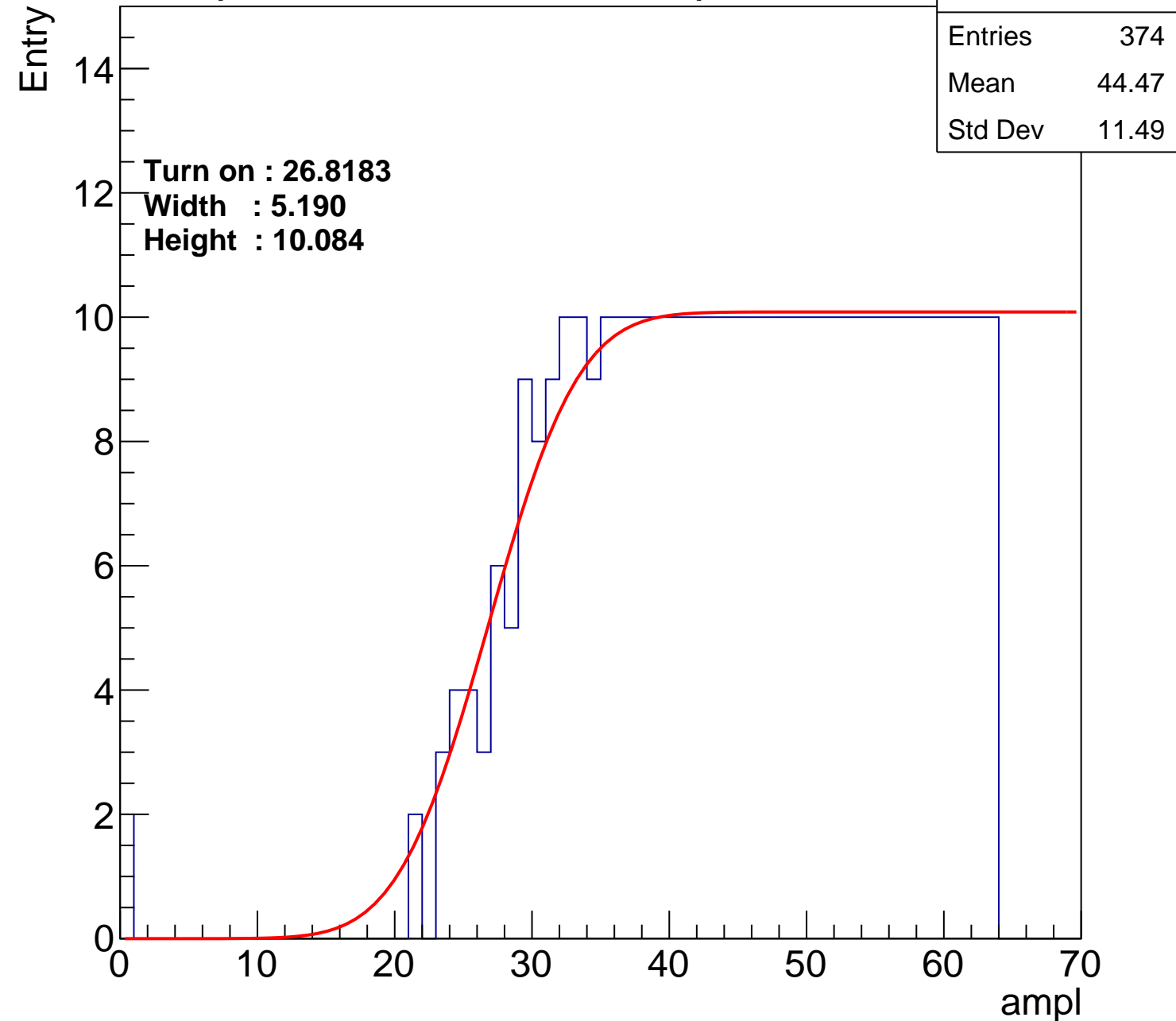
Width : 5.190

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch42

calib_packv5_042523_0143.root, FC#7, port C2

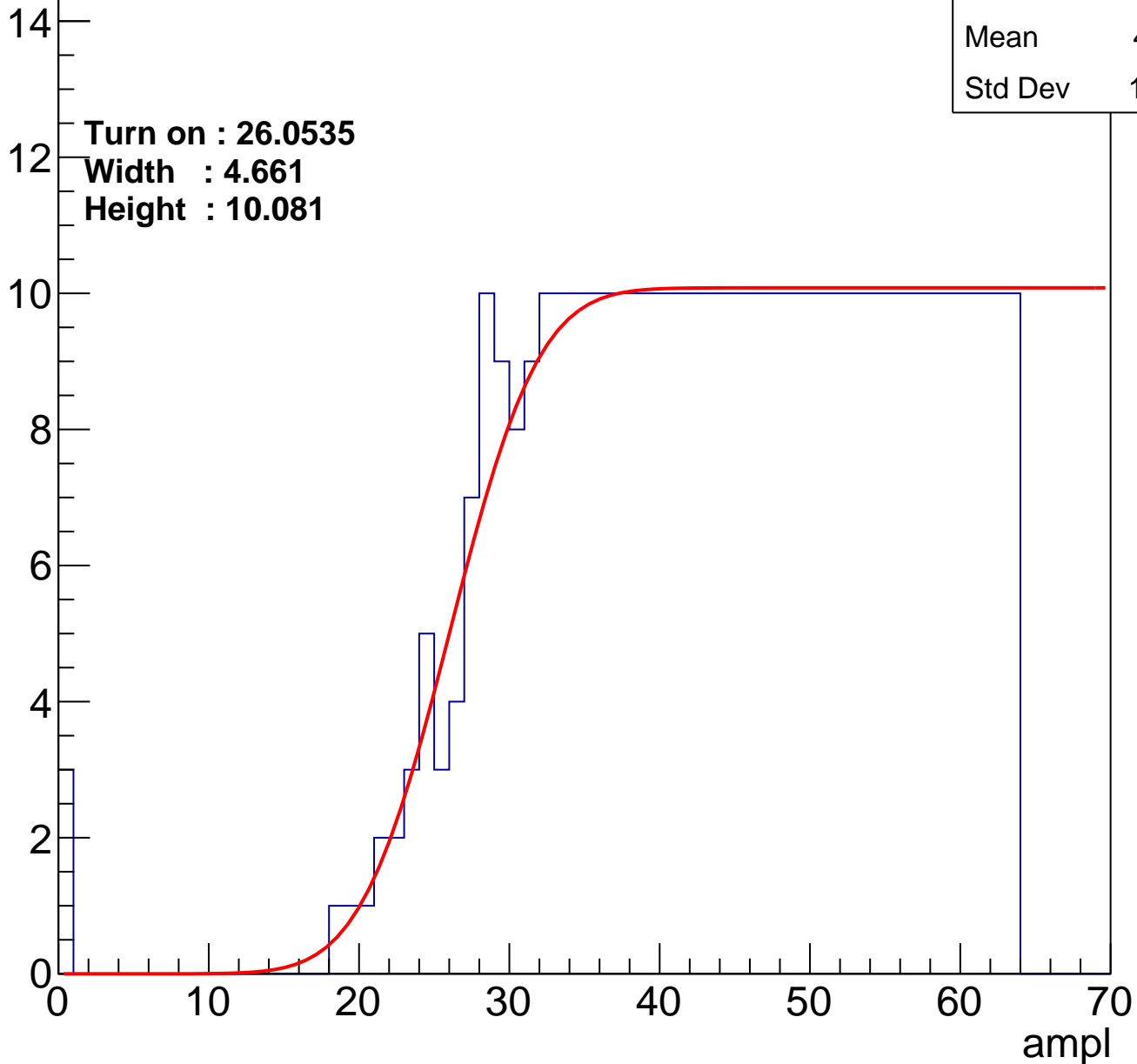
Entries	388
Mean	43.71
Std Dev	12.04

Turn on : 26.0535

Width : 4.661

Height : 10.081

Entry



B1L103S, U24-ch43

calib_packv5_042523_0143.root, FC#7, port C2

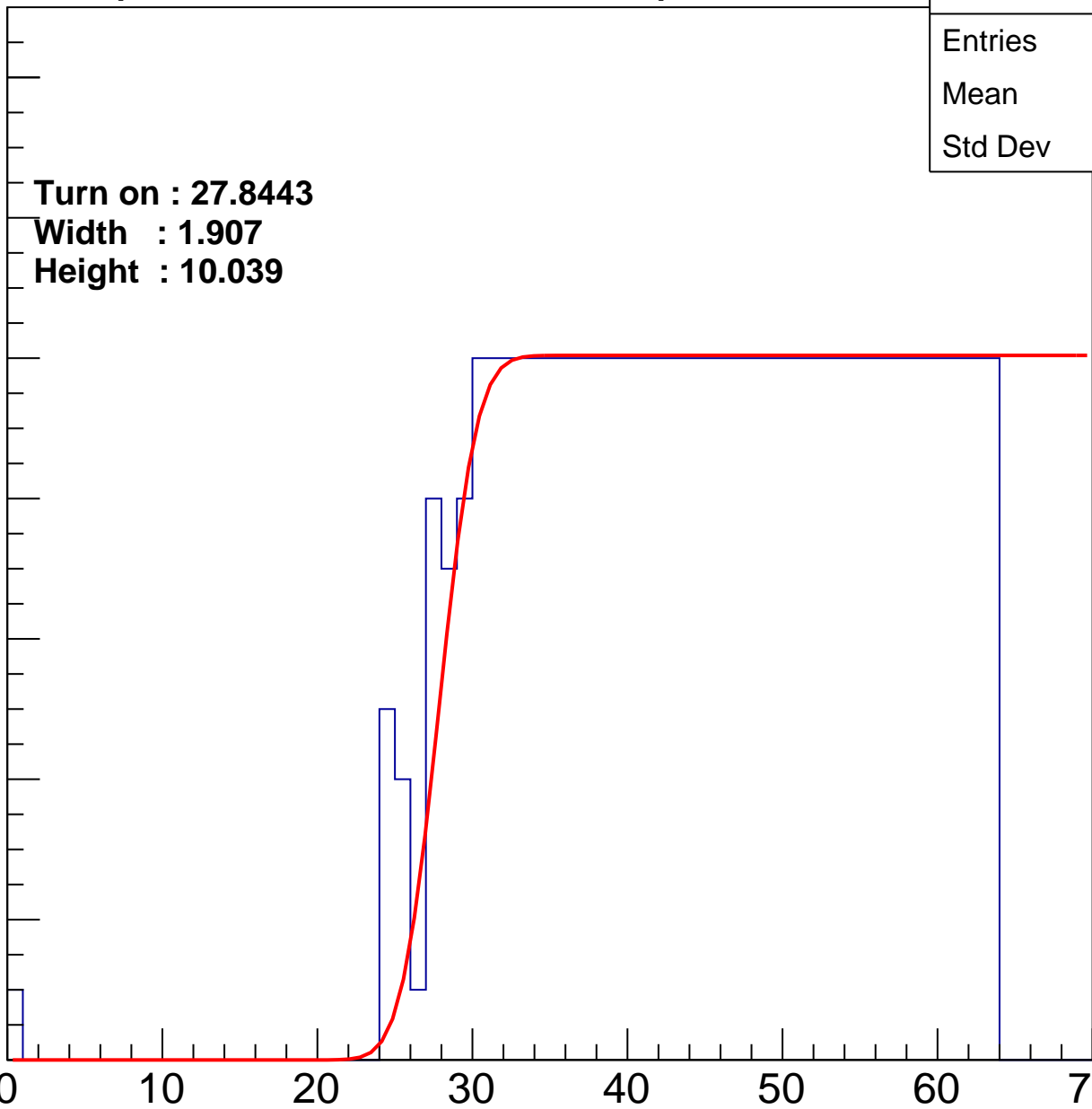
Entry

14
12
10
8
6
4
2
0

Turn on : 27.8443
Width : 1.907
Height : 10.039

Entries	374
Mean	44.65
Std Dev	11.13

ampl



B1L103S, U24-ch44

calib_packv5_042523_0143.root, FC#7, port C2

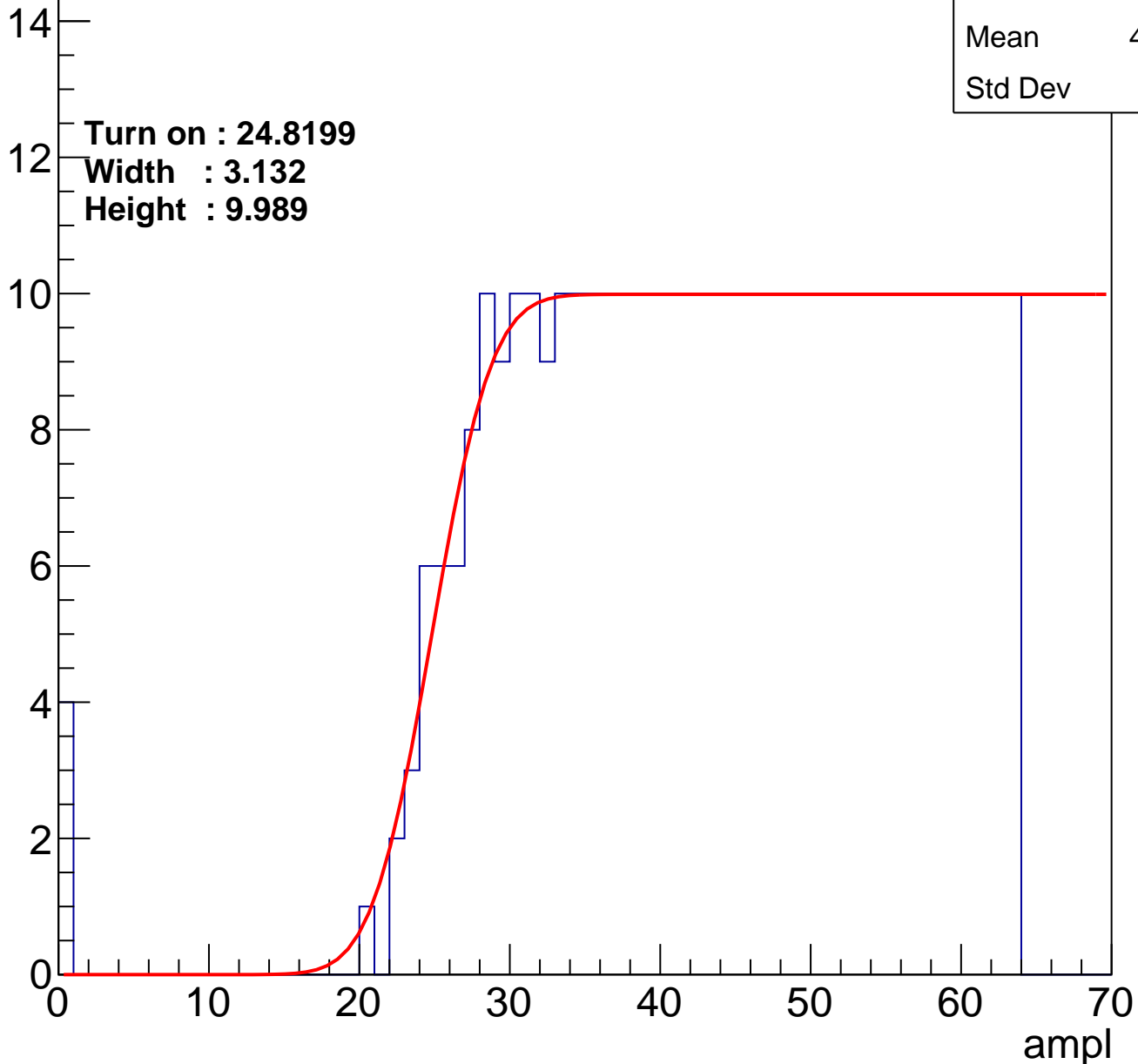
Entries	394
Mean	43.45
Std Dev	12.2

Turn on : 24.8199

Width : 3.132

Height : 9.989

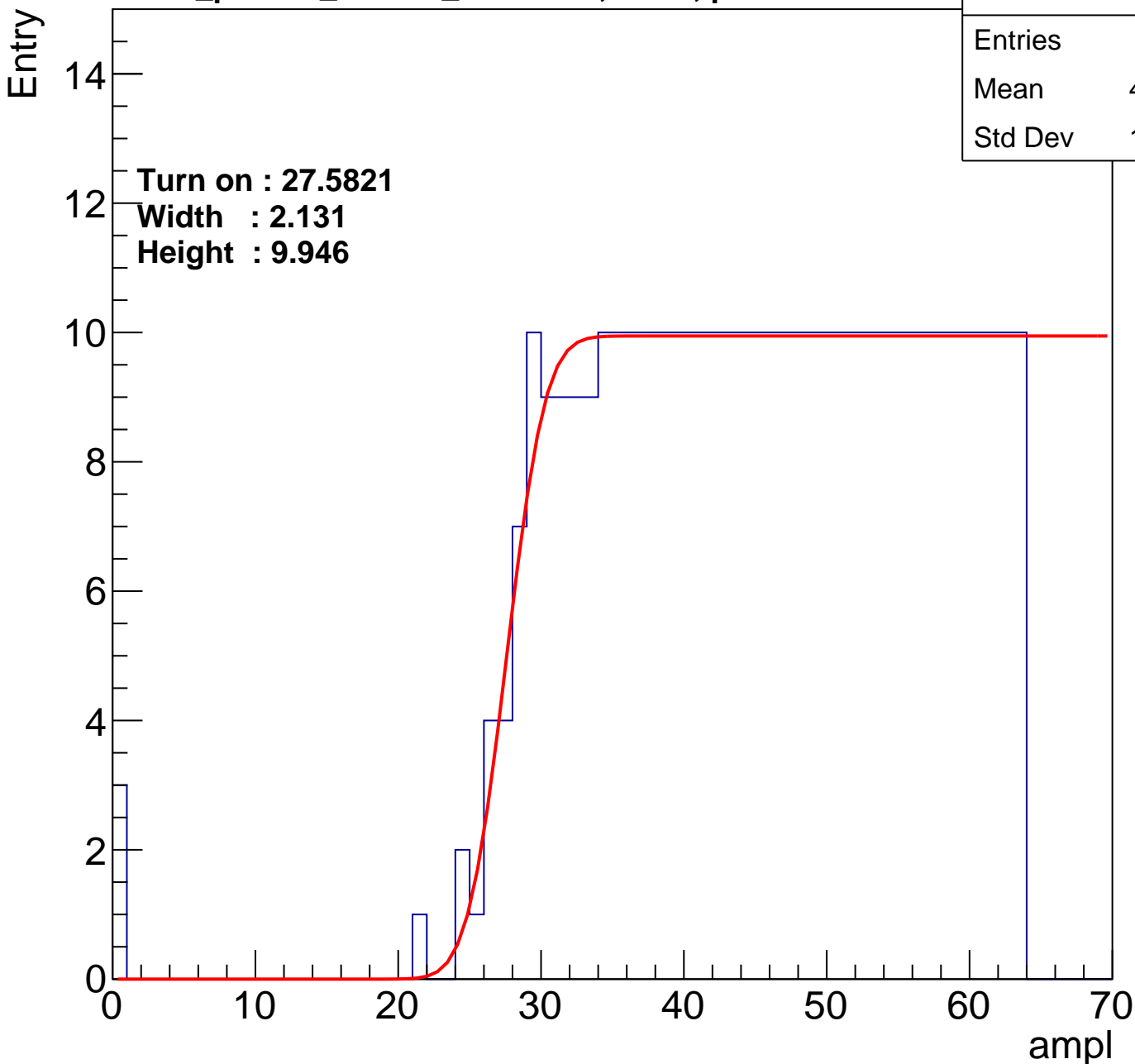
Entry



calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Turn on : 27.5821
Width : 2.131
Height : 9.946



B1L103S, U24-ch46

calib_packv5_042523_0143.root, FC#7, port C2

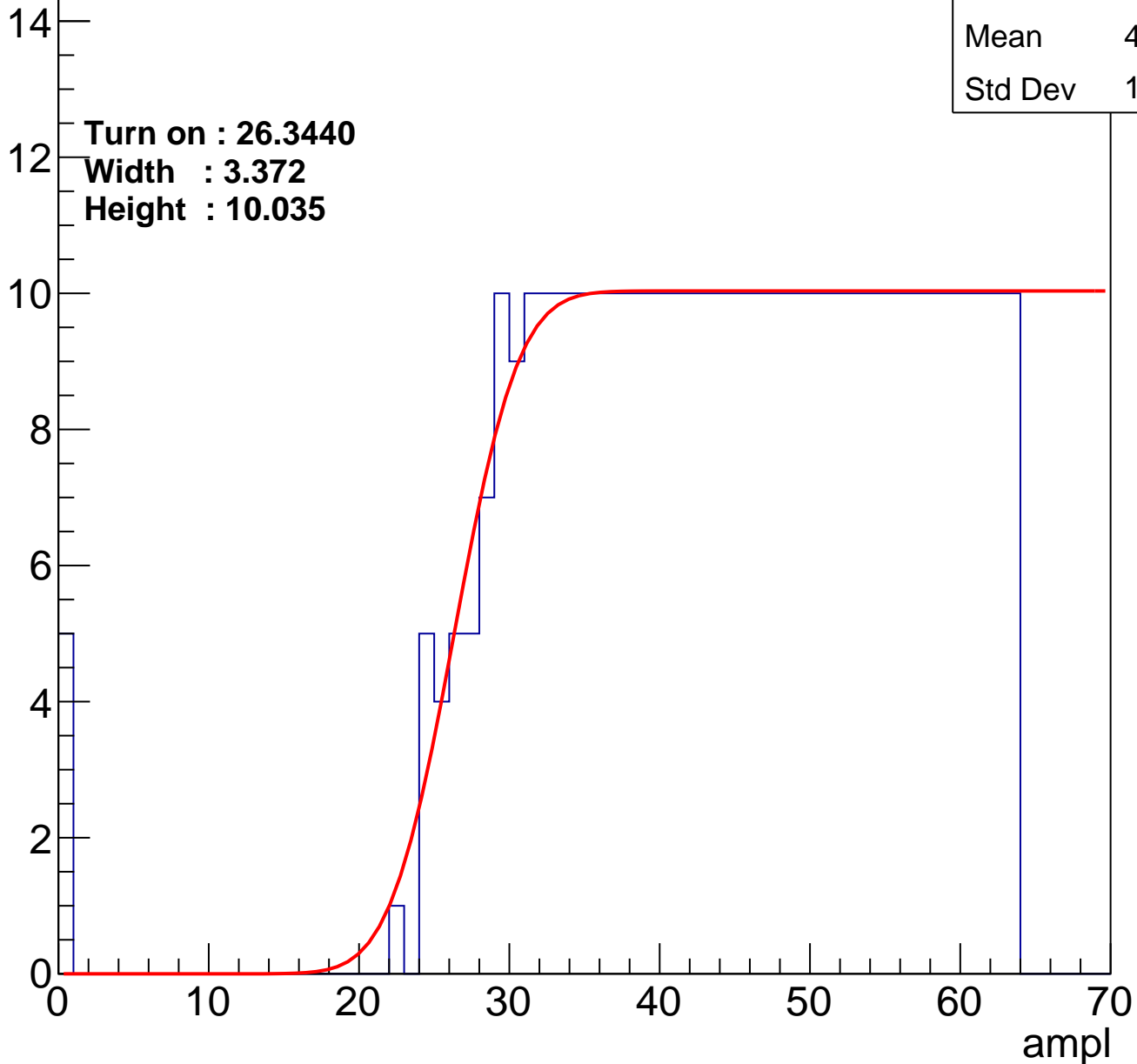
Entries	381
Mean	44.02
Std Dev	12.06

Turn on : 26.3440

Width : 3.372

Height : 10.035

Entry



B1L103S, U24-ch47

calib_packv5_042523_0143.root, FC#7, port C2

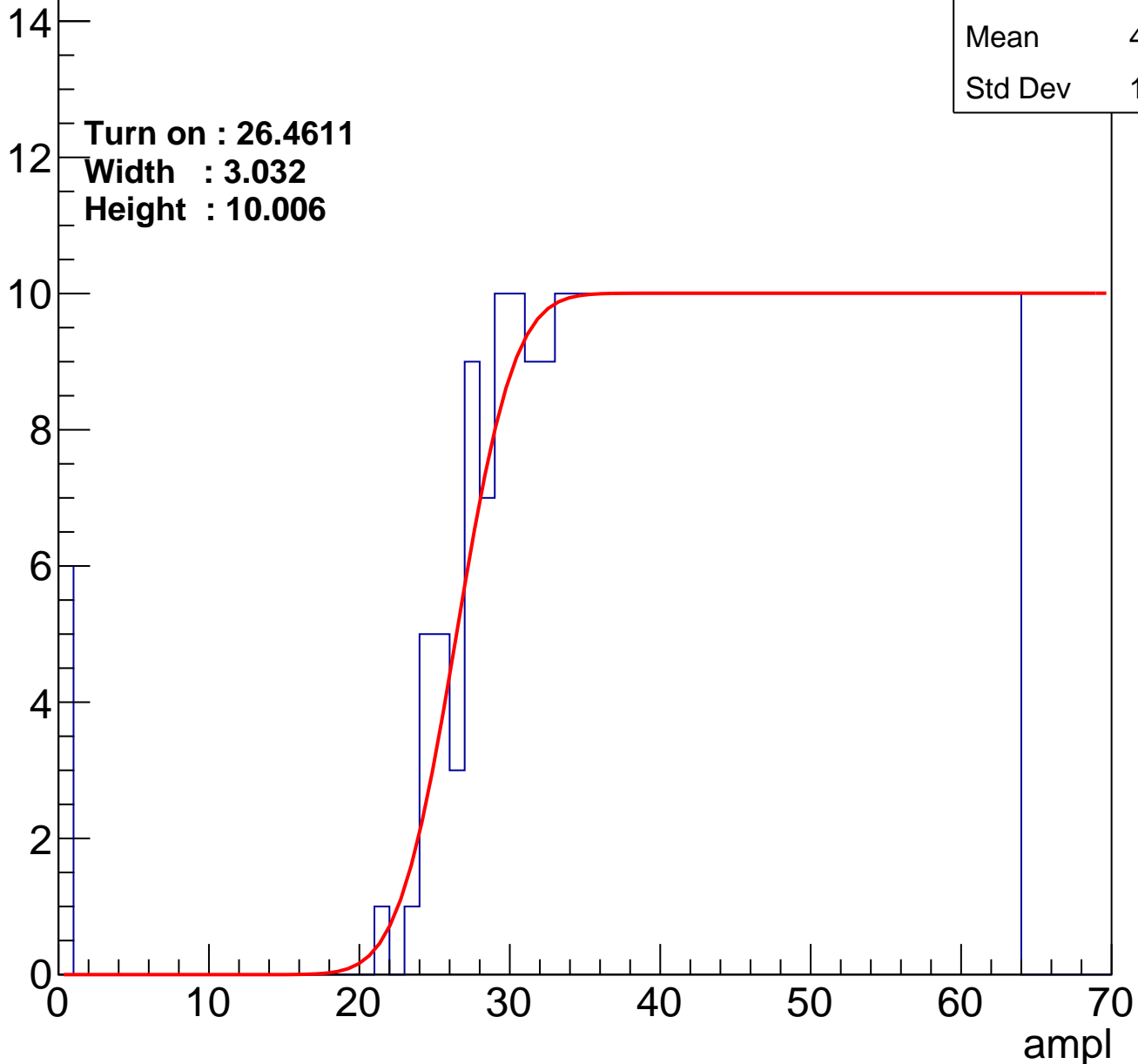
Entries	385
Mean	43.75
Std Dev	12.33

Turn on : 26.4611

Width : 3.032

Height : 10.006

Entry



B1L103S, U24-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 24.9003

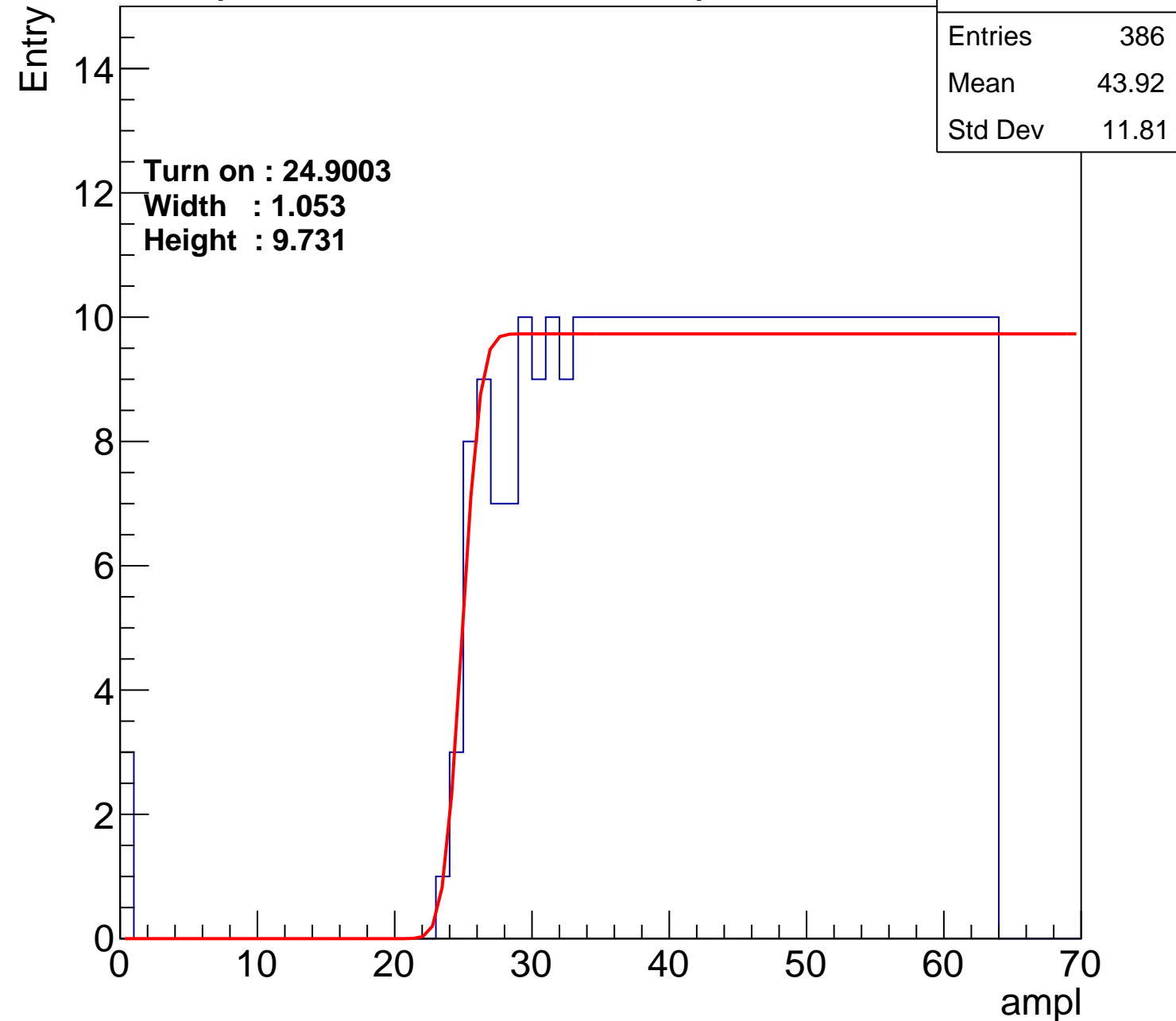
Width : 1.053

Height : 9.731

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch49

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.76
Std Dev	12.19

Turn on : 25.9833

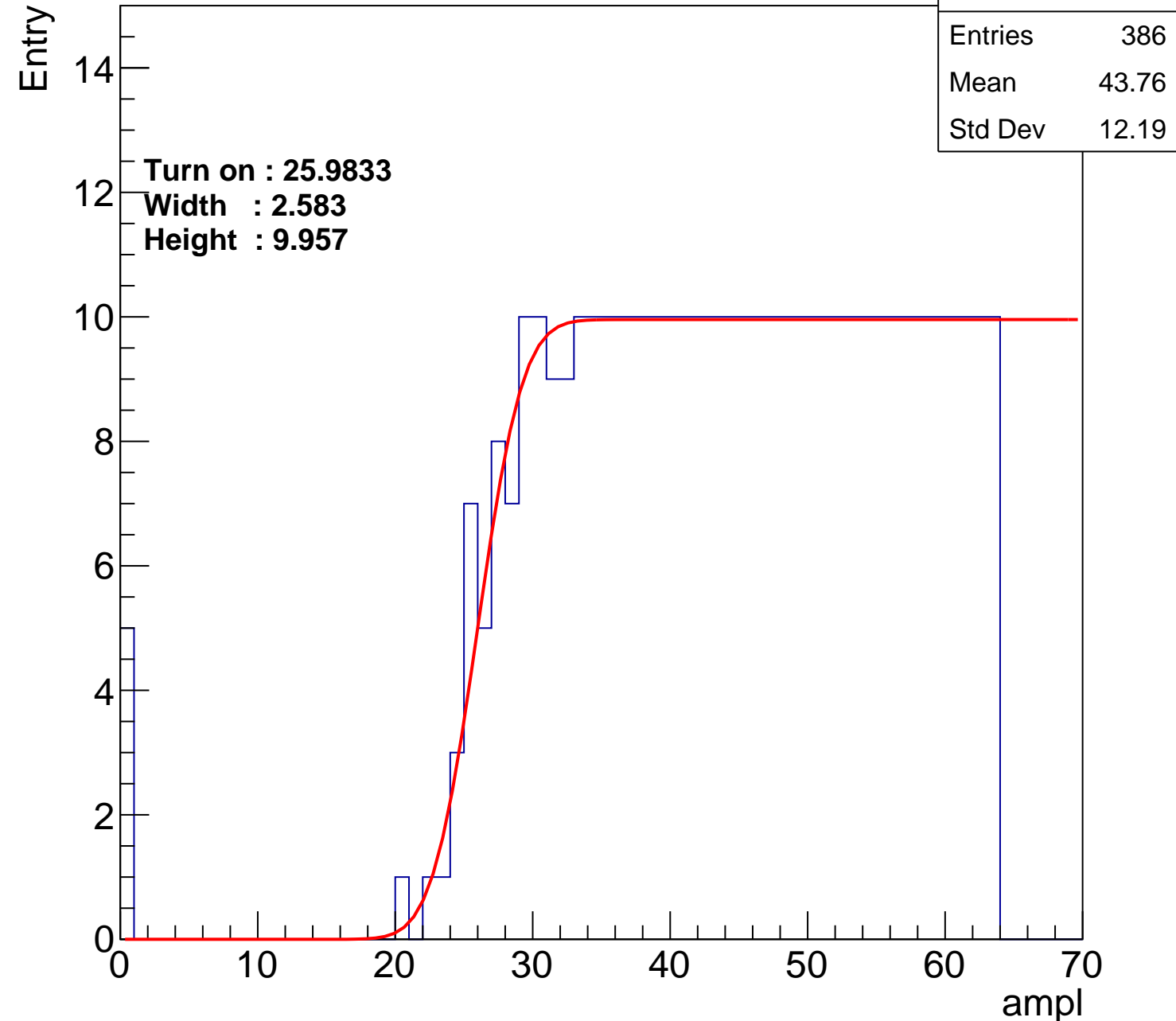
Width : 2.583

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch50

calib_packv5_042523_0143.root, FC#7, port C2

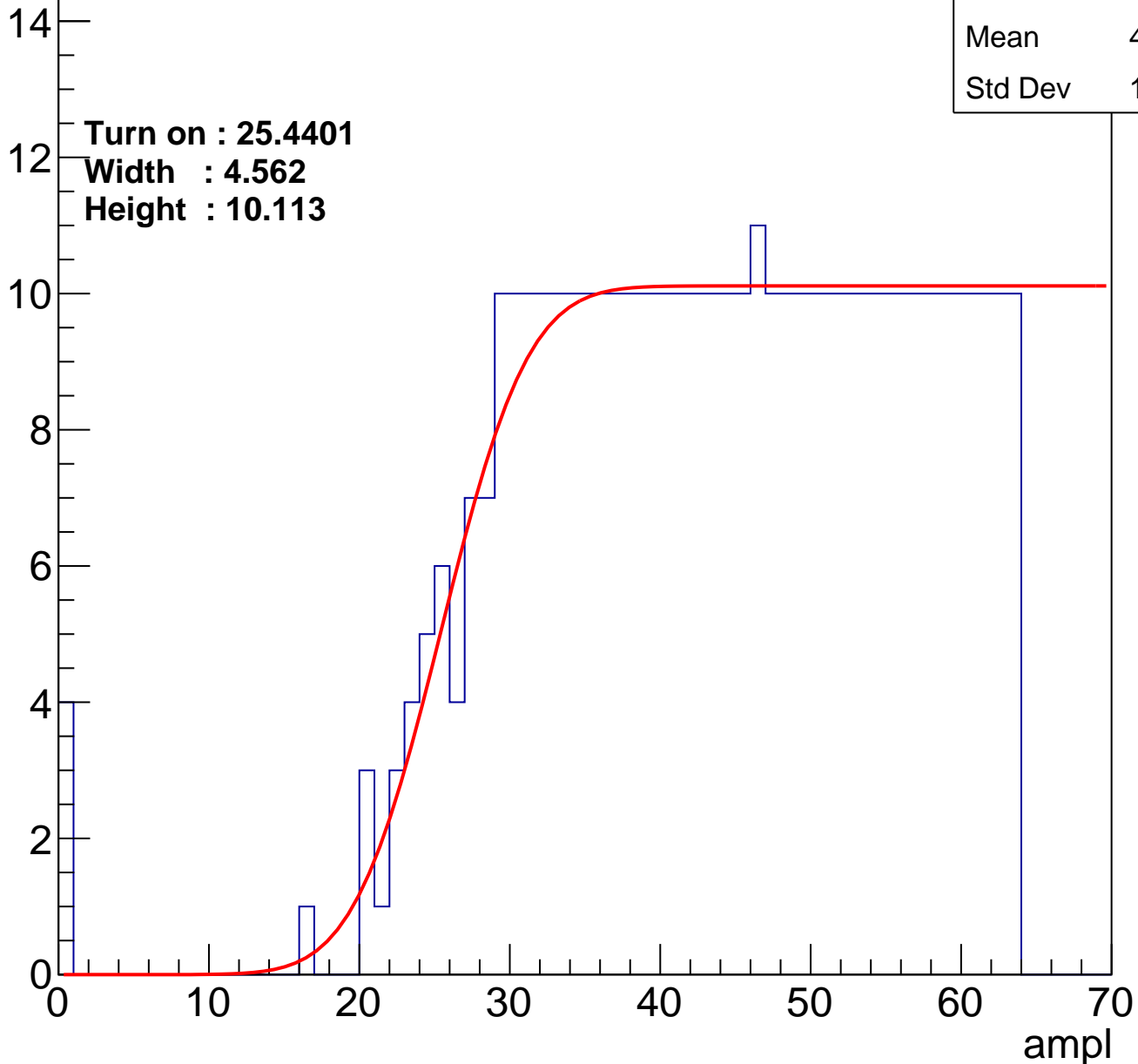
Entries	396
Mean	43.33
Std Dev	12.33

Turn on : 25.4401

Width : 4.562

Height : 10.113

Entry



B1L103S, U24-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.2
Std Dev	11.73

Turn on : 26.4905

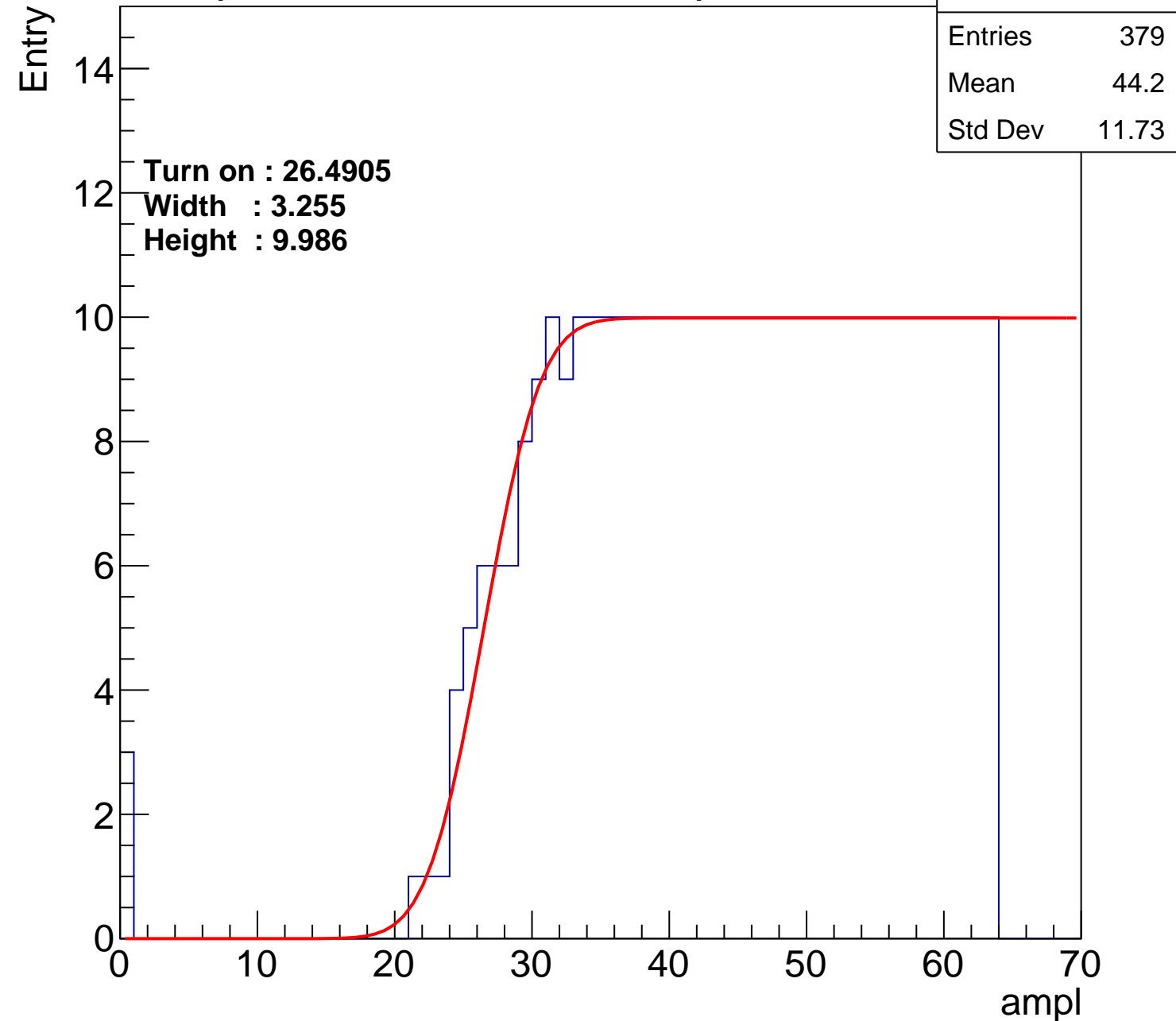
Width : 3.255

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.98
Std Dev	11.95

Turn on : 27.0926

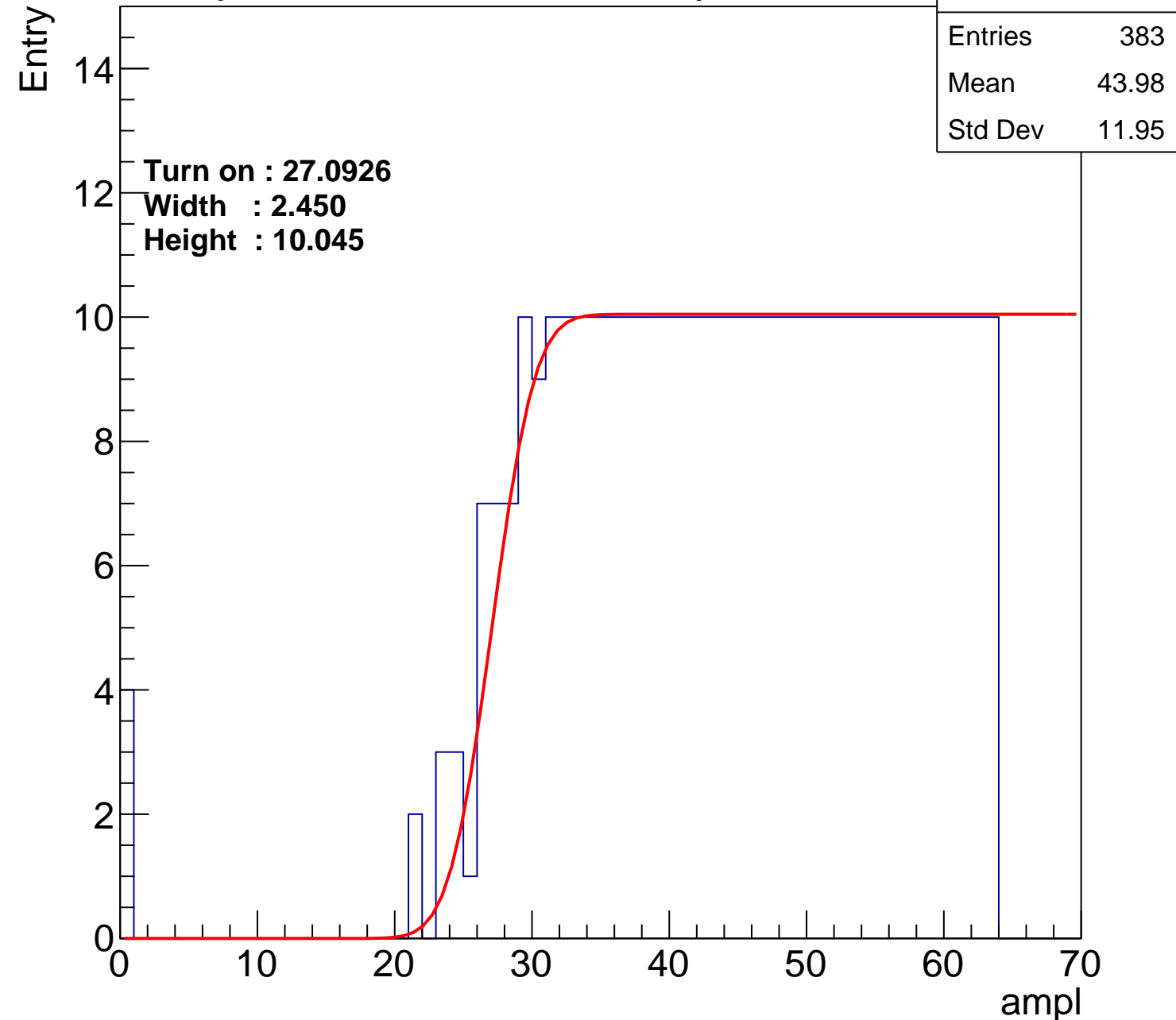
Width : 2.450

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.55
Std Dev	11.43

Turn on : 27.3198

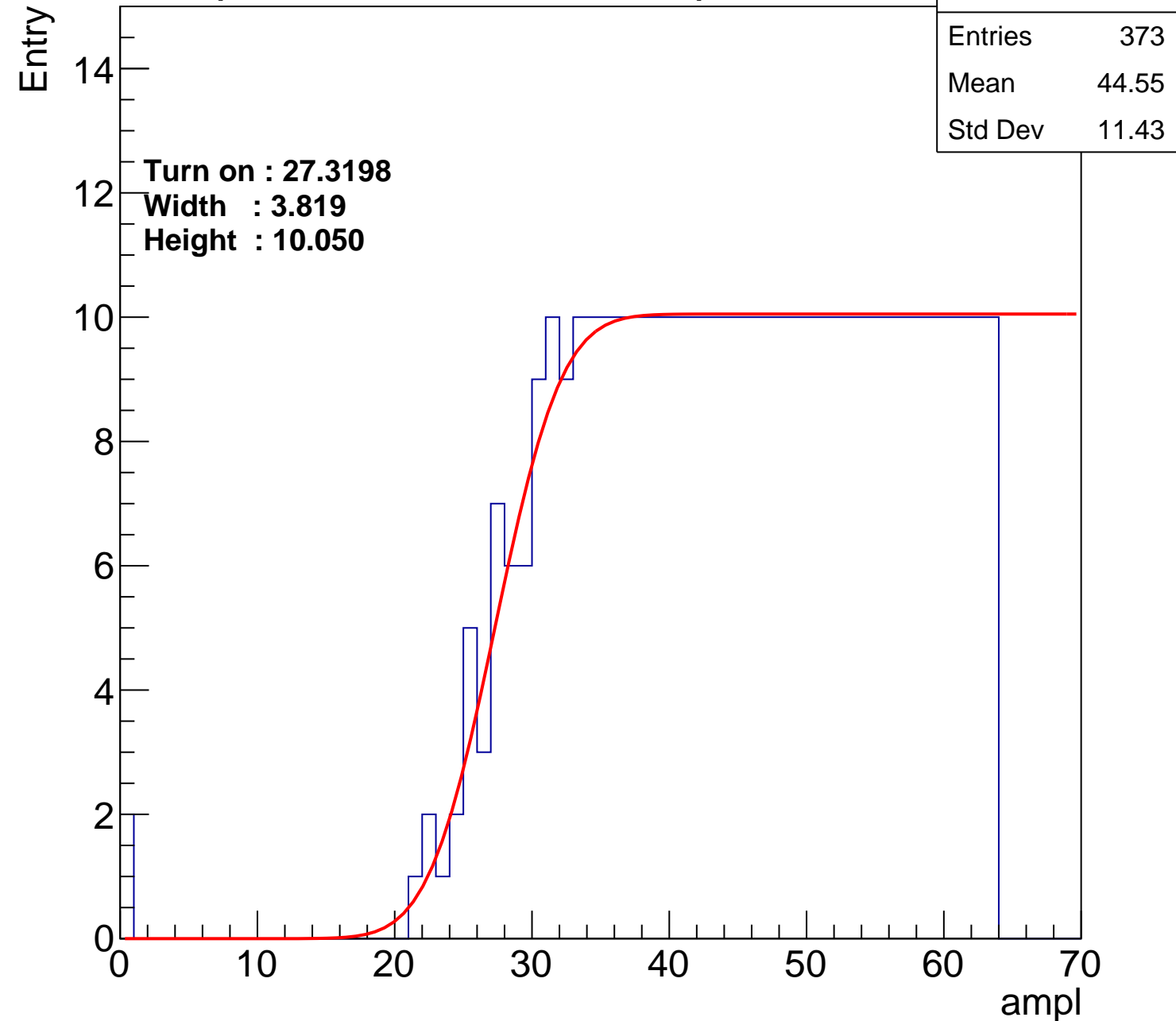
Width : 3.819

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.68
Std Dev	11.39

Turn on : 27.6022

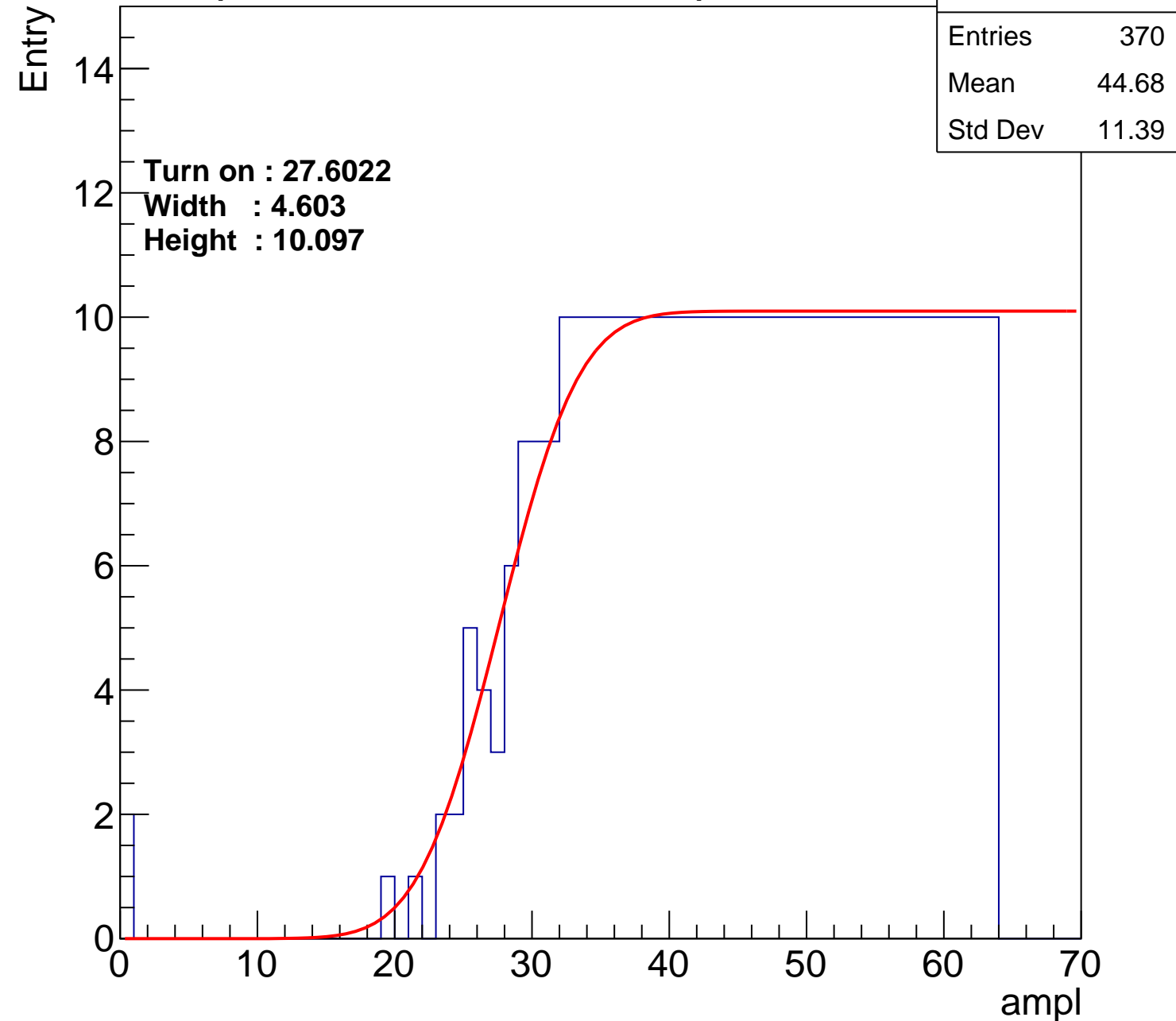
Width : 4.603

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.31
Std Dev	12.11

Turn on : 28.2307

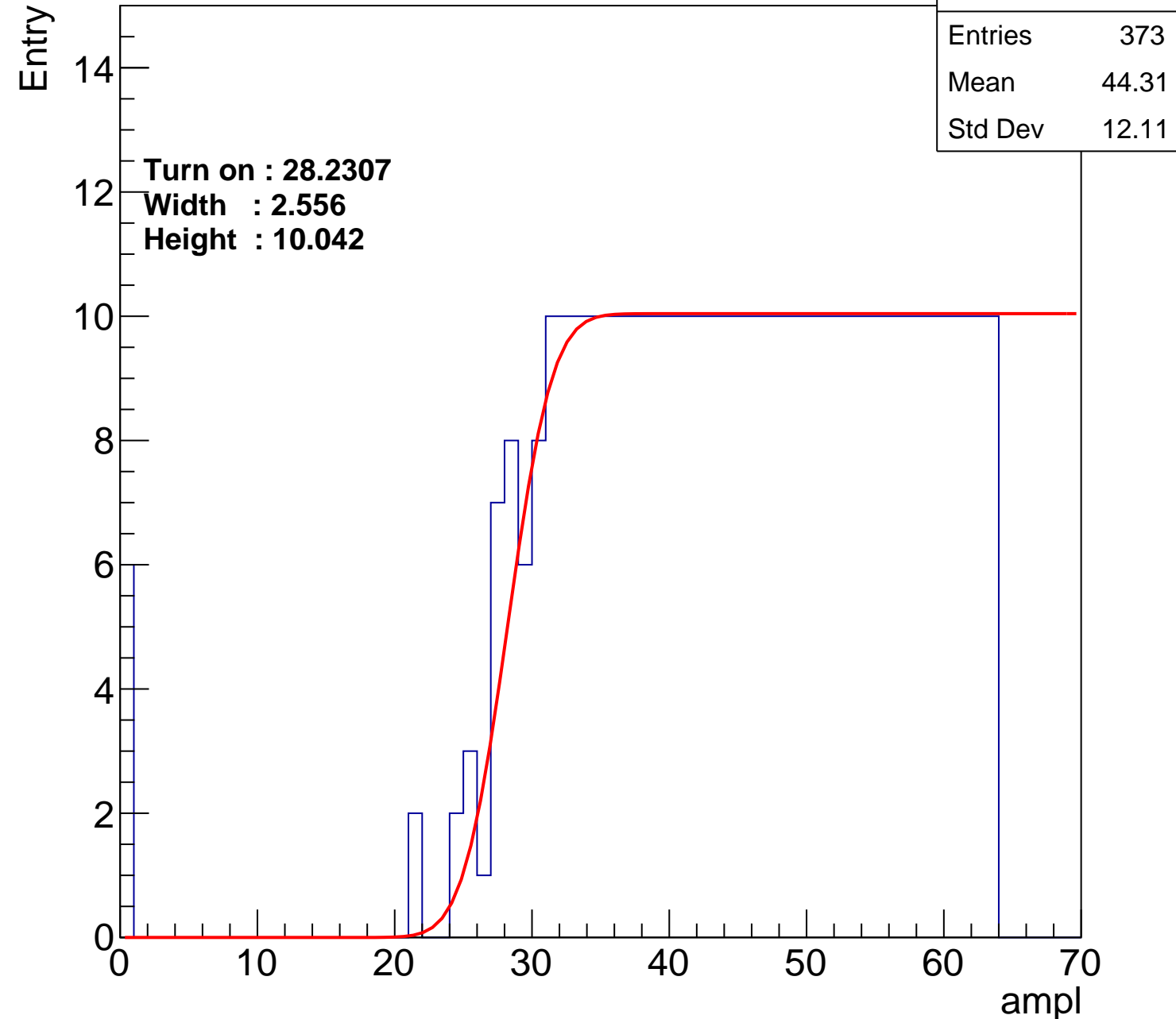
Width : 2.556

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch56

calib_packv5_042523_0143.root, FC#7, port C2

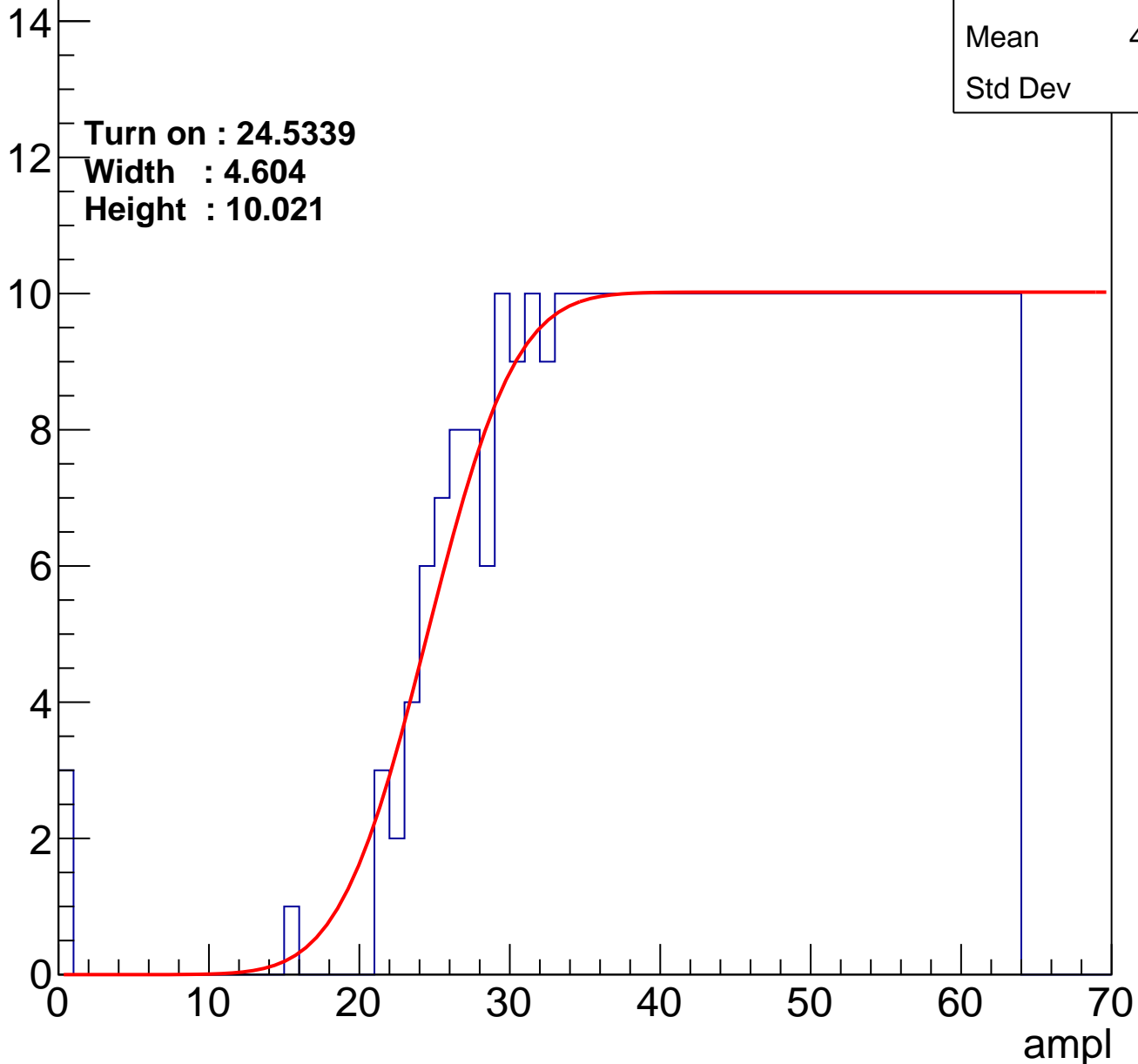
Entries	396
Mean	43.34
Std Dev	12.2

Turn on : 24.5339

Width : 4.604

Height : 10.021

Entry



B1L103S, U24-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 27.2884

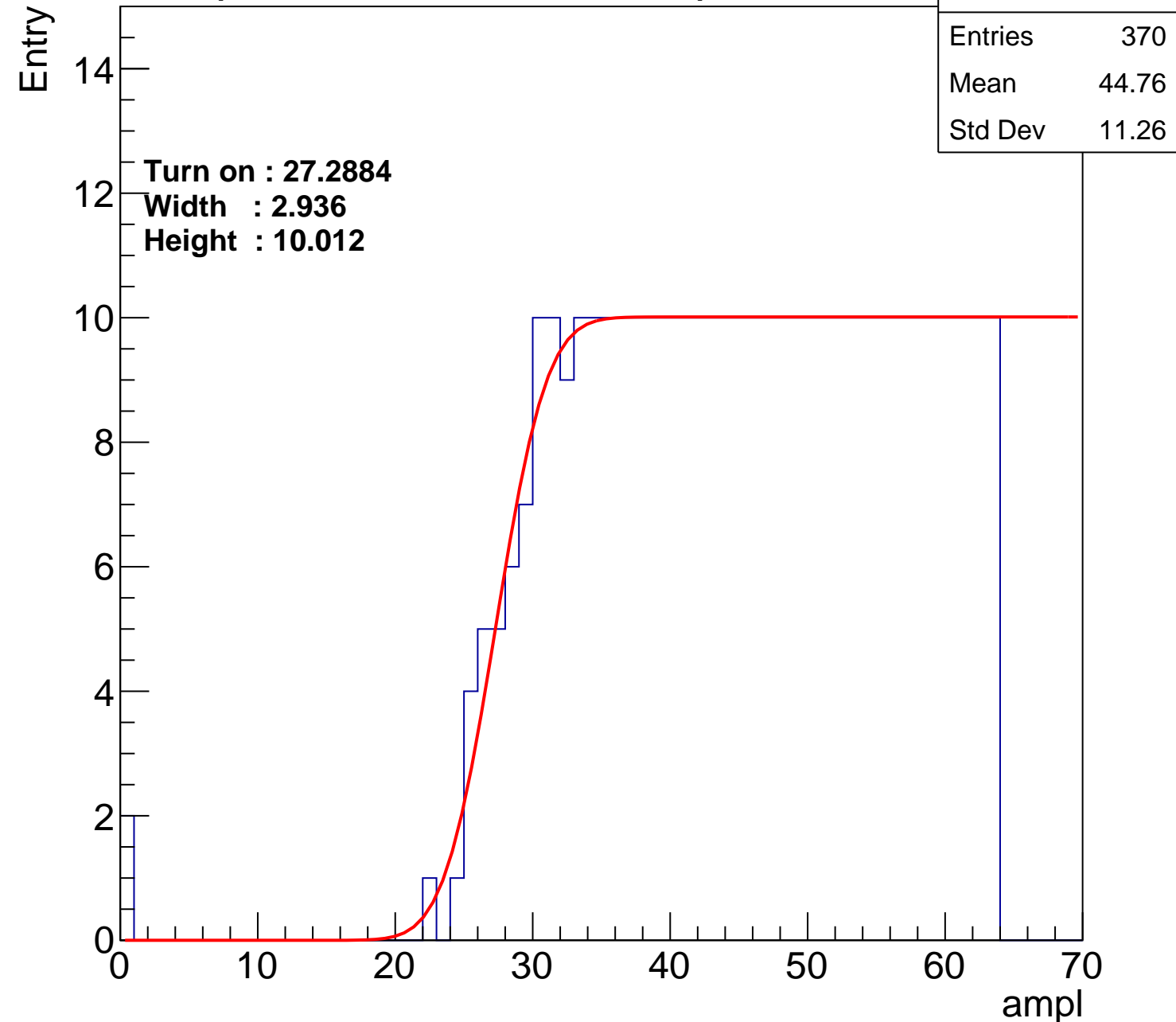
Width : 2.936

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.56
Std Dev	11.9

Turn on : 24.9159

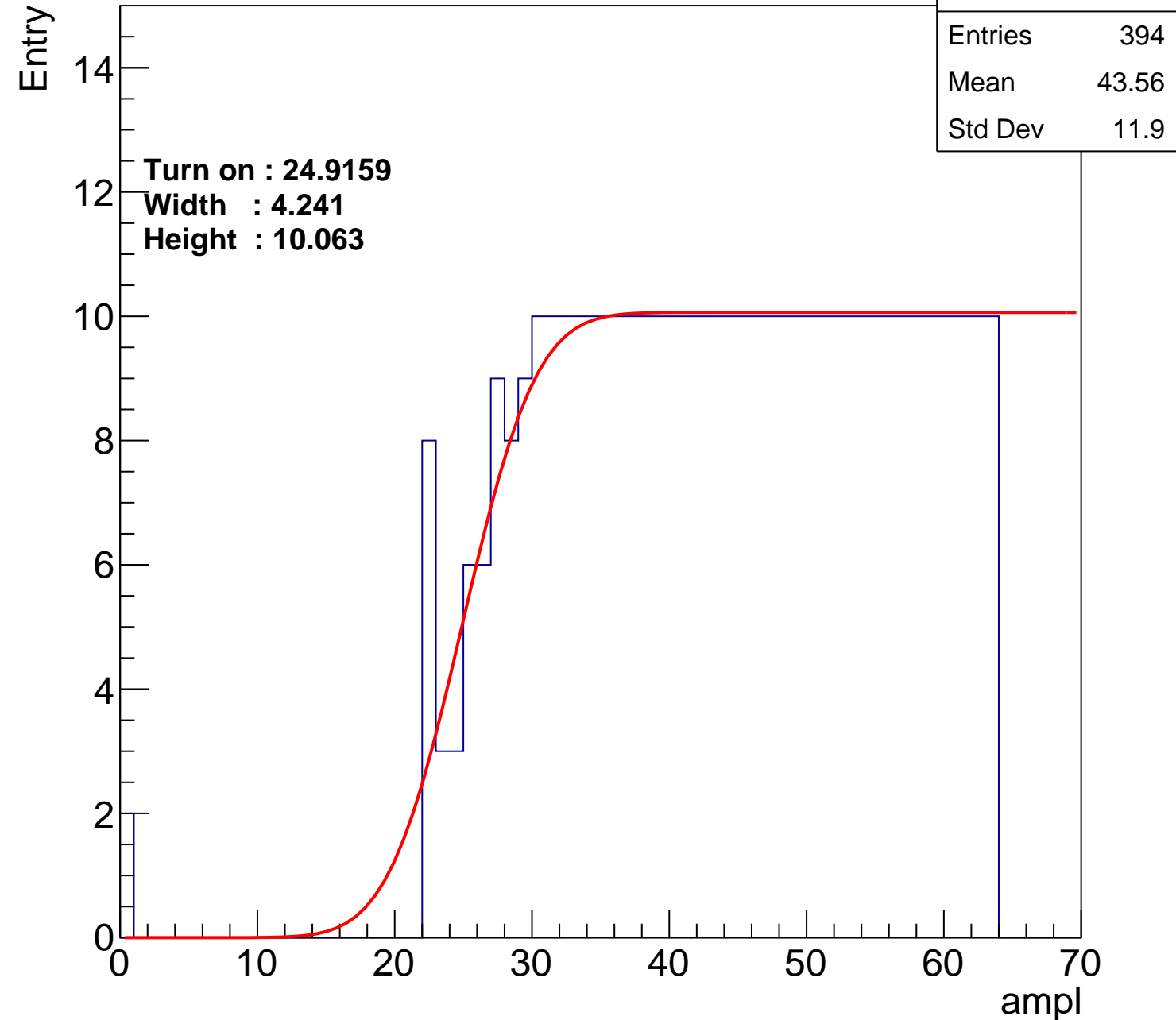
Width : 4.241

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.07
Std Dev	11.86

Turn on : 26.7710

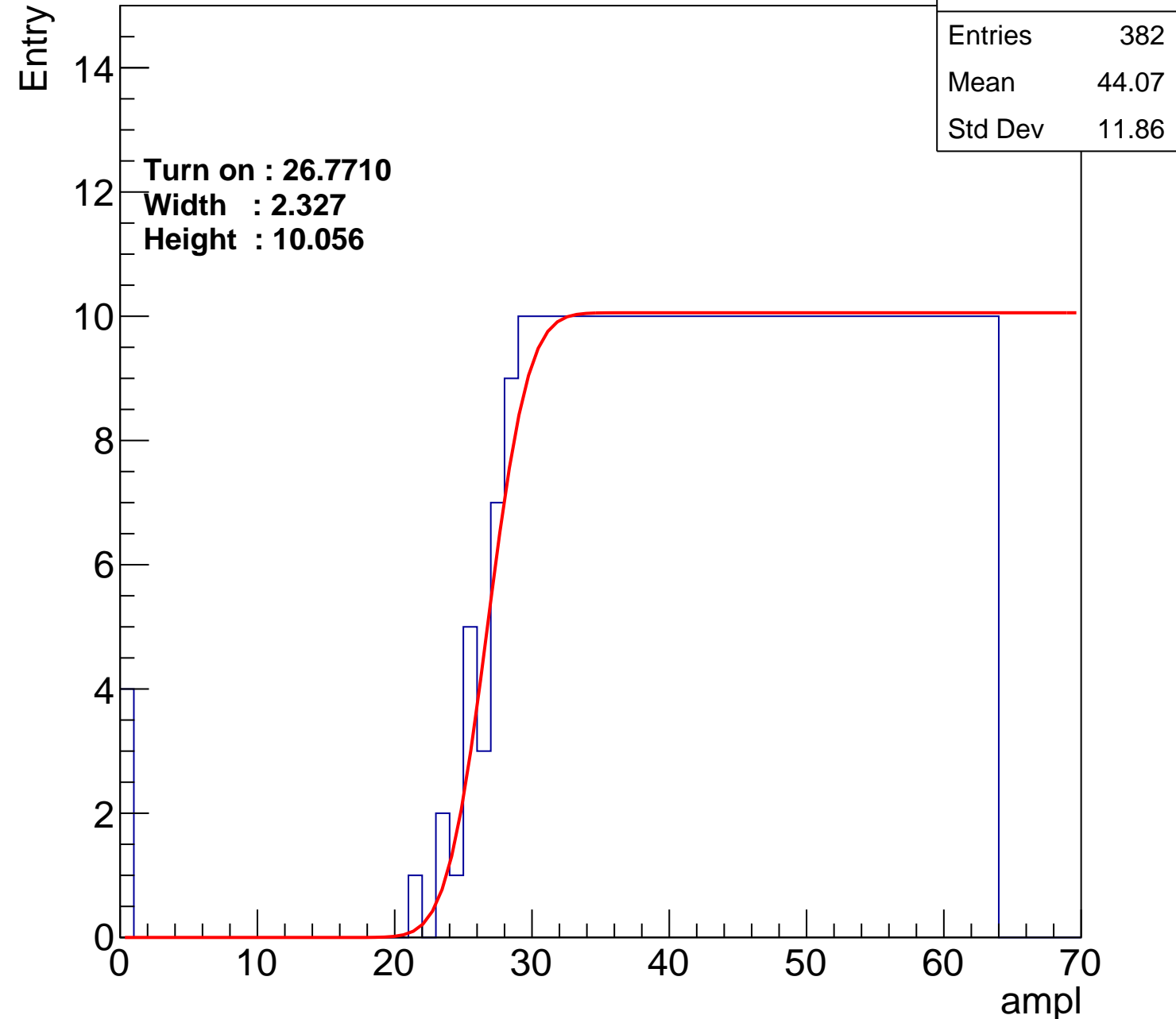
Width : 2.327

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch60

calib_packv5_042523_0143.root, FC#7, port C2

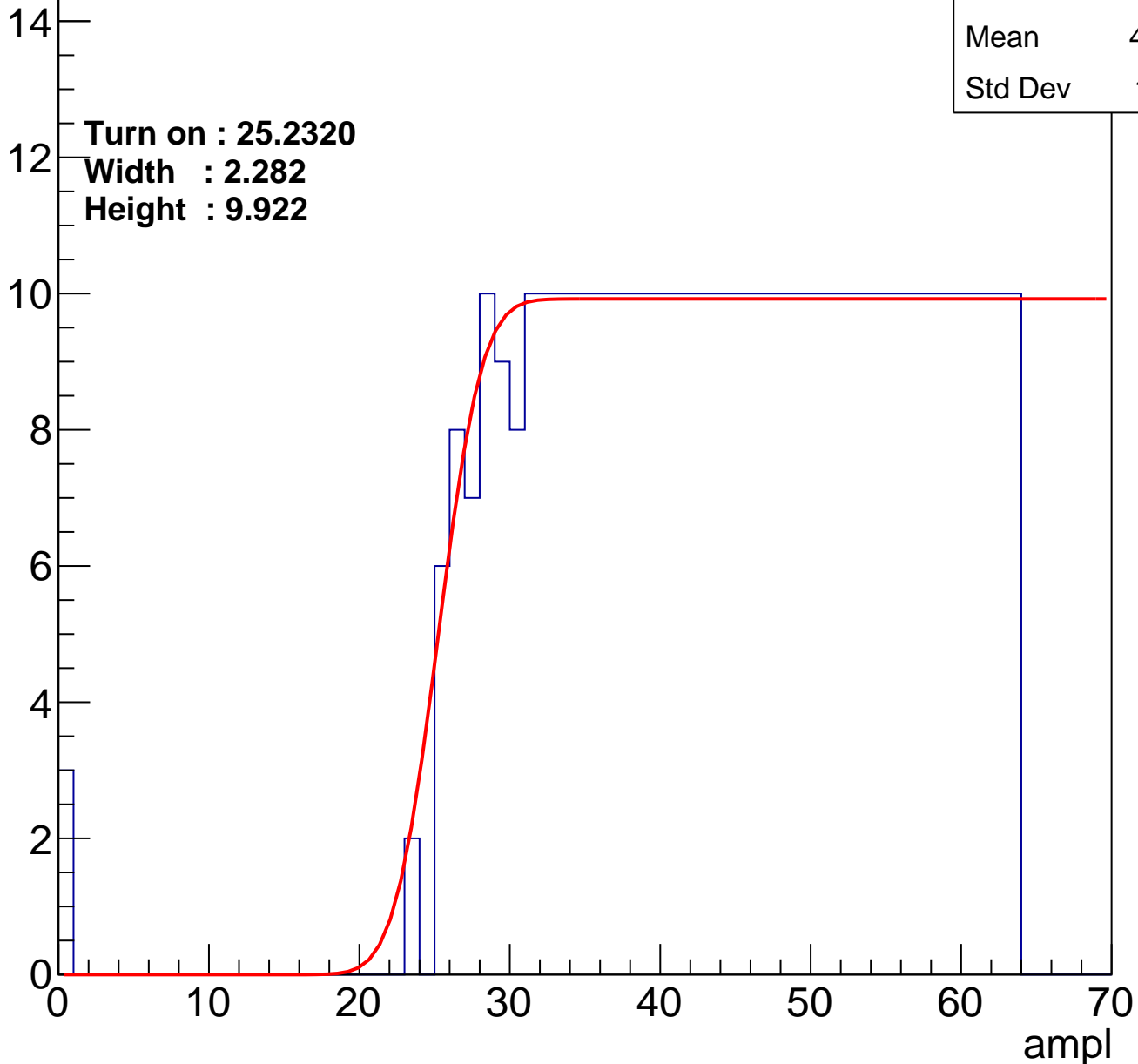
Entries	383
Mean	44.08
Std Dev	11.71

Turn on : 25.2320

Width : 2.282

Height : 9.922

Entry



B1L103S, U24-ch61

calib_packv5_042523_0143.root, FC#7, port C2

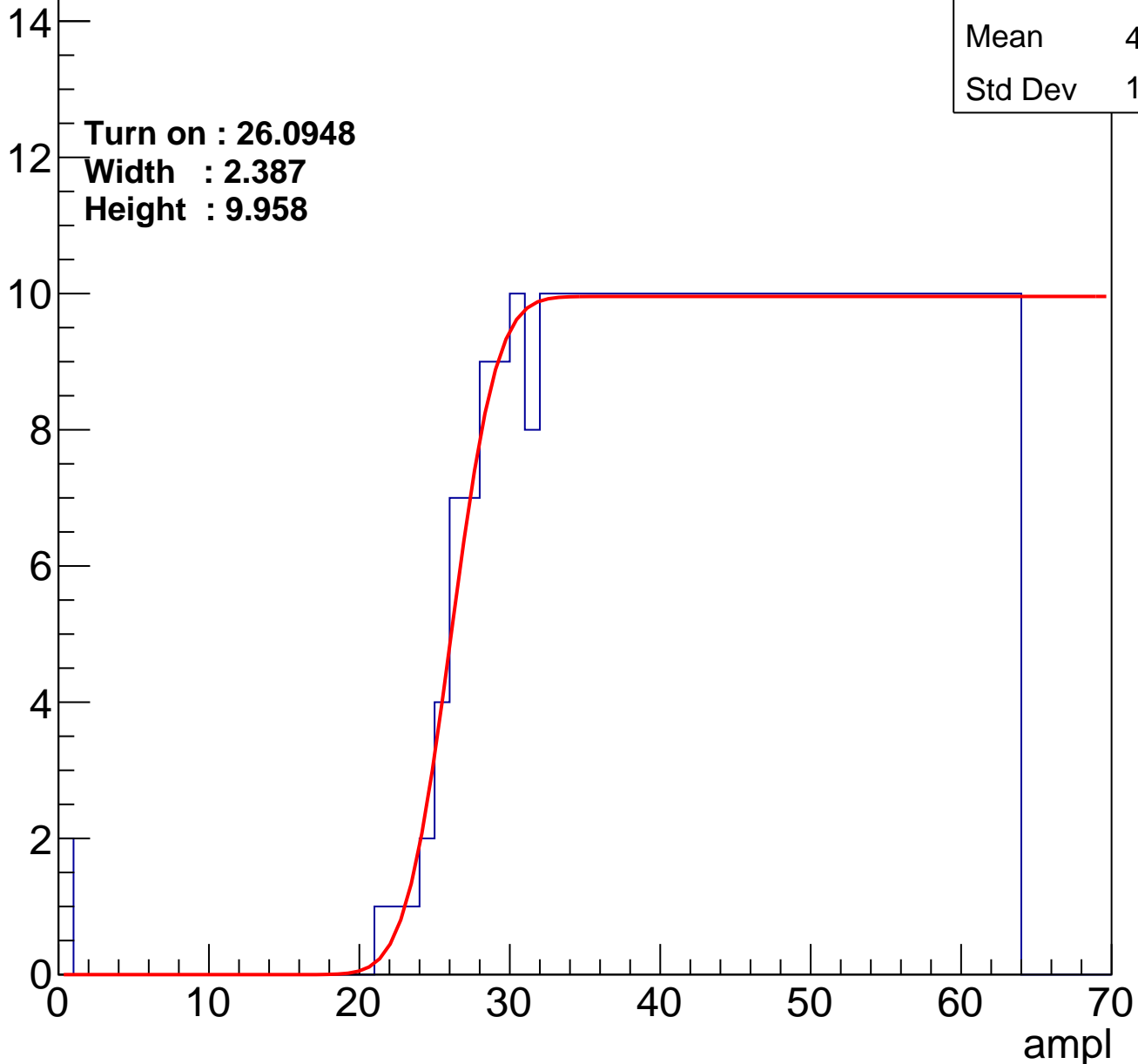
Entry

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.0948

Width : 2.387

Height : 9.958



B1L103S, U24-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.7
Std Dev	11.55

Turn on : 28.3467

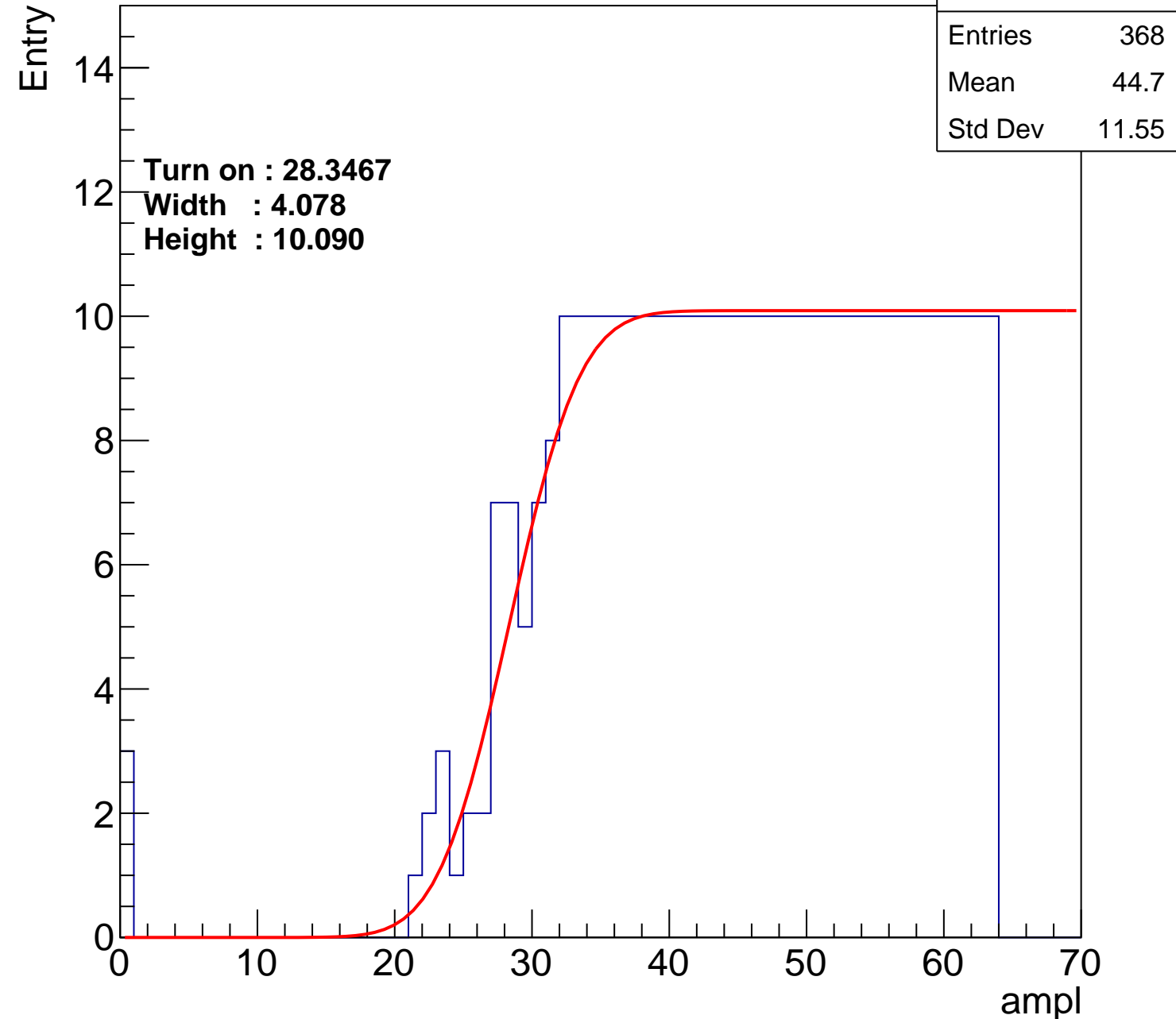
Width : 4.078

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.68
Std Dev	11.47

Turn on : 27.3380

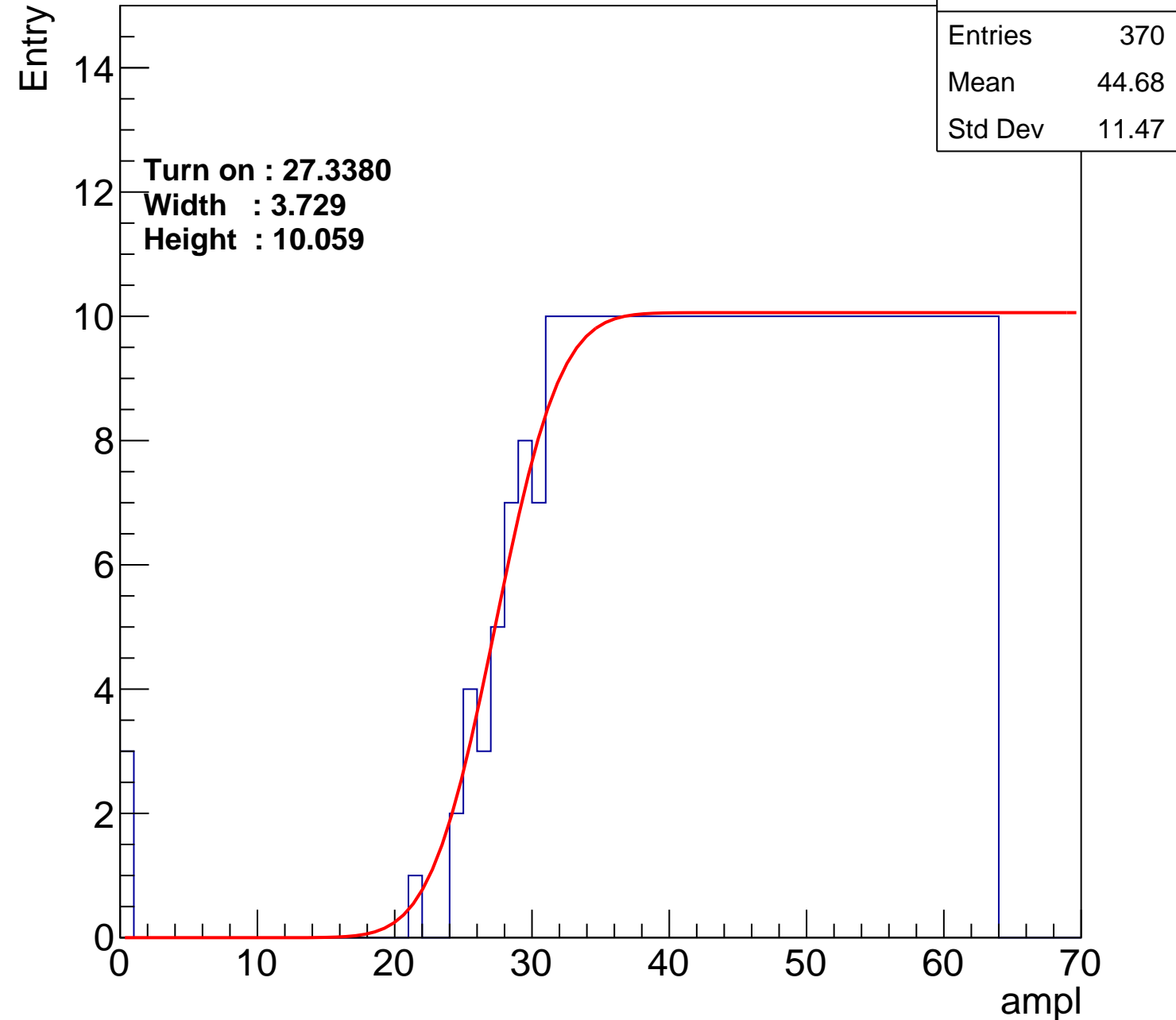
Width : 3.729

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.2
Std Dev	12

Turn on : 27.4250

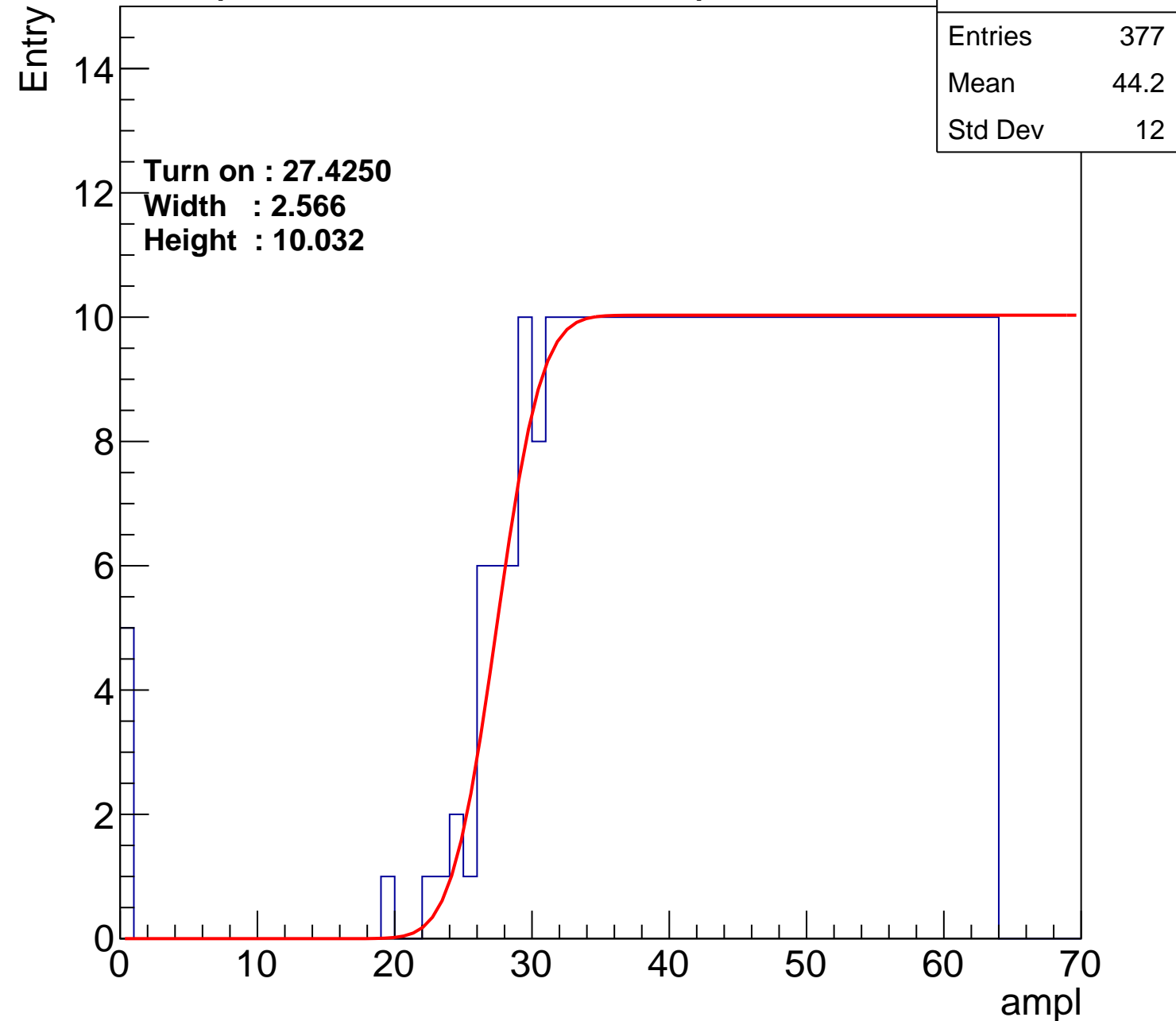
Width : 2.566

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.34
Std Dev	11.36

Turn on : 26.2566

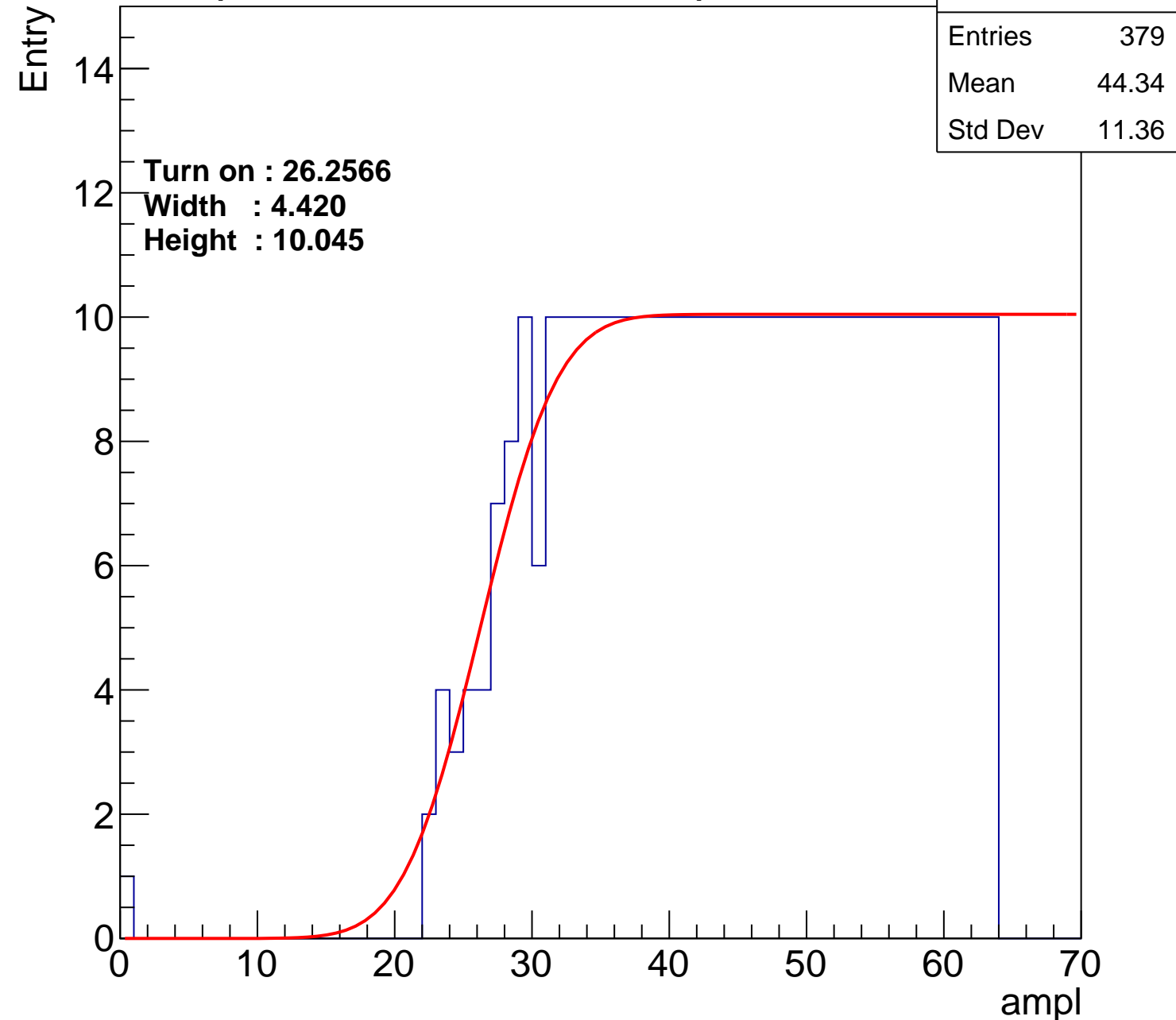
Width : 4.420

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch66

calib_packv5_042523_0143.root, FC#7, port C2

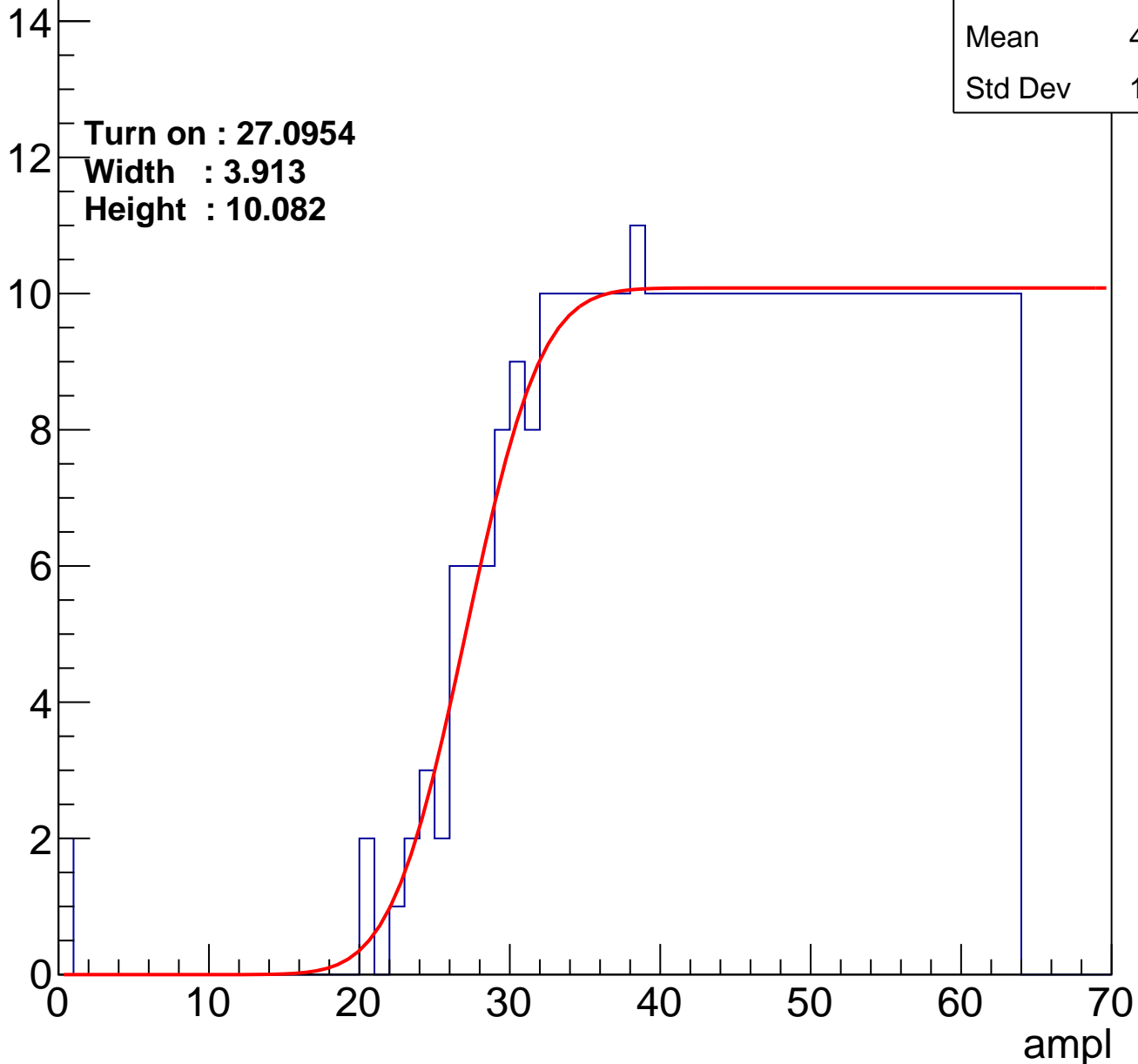
Entries	376
Mean	44.43
Std Dev	11.49

Turn on : 27.0954

Width : 3.913

Height : 10.082

Entry



B1L103S, U24-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.35
Std Dev	12.14

Turn on : 24.8965

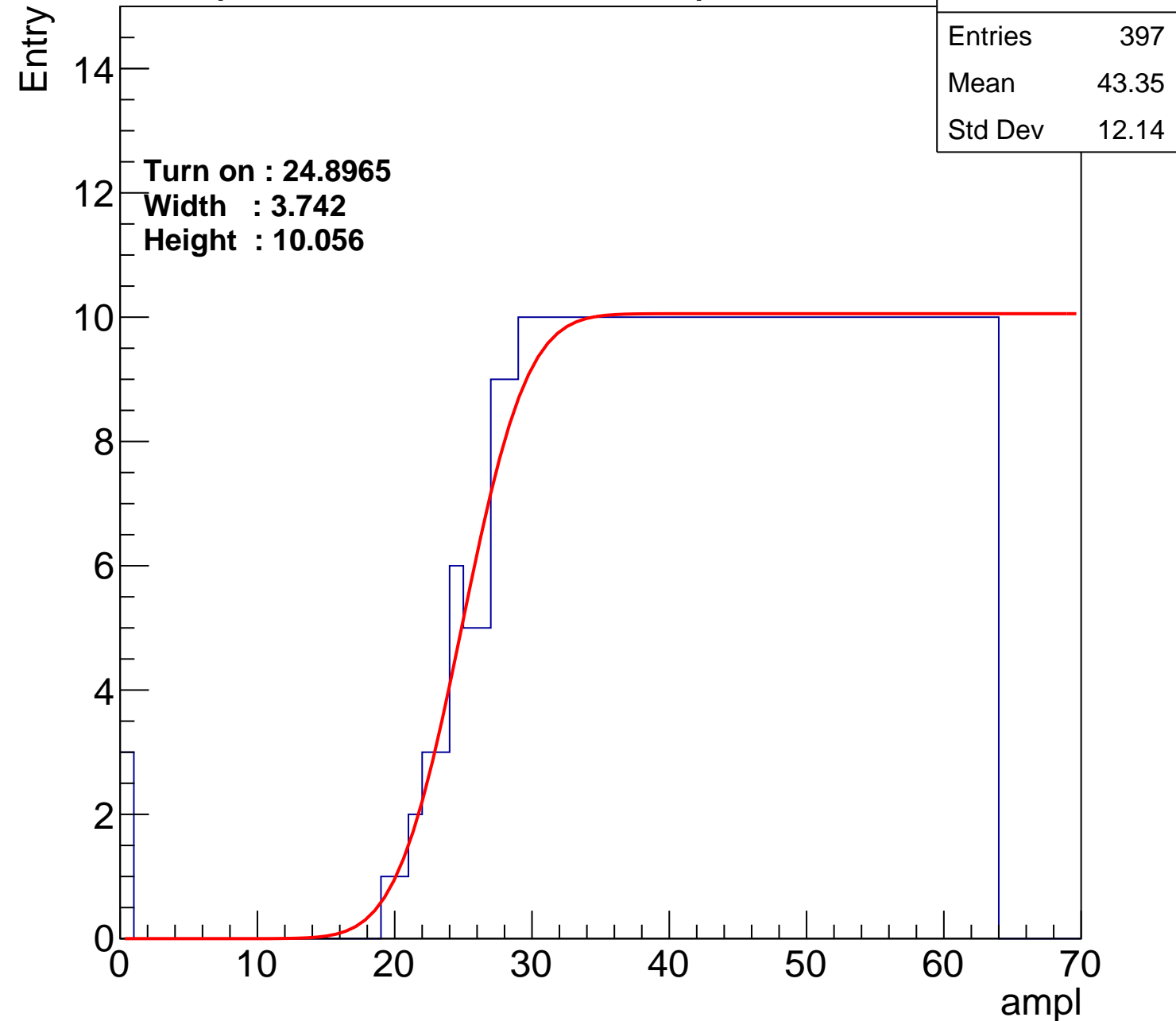
Width : 3.742

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.38
Std Dev	11.98

Turn on : 24.6444

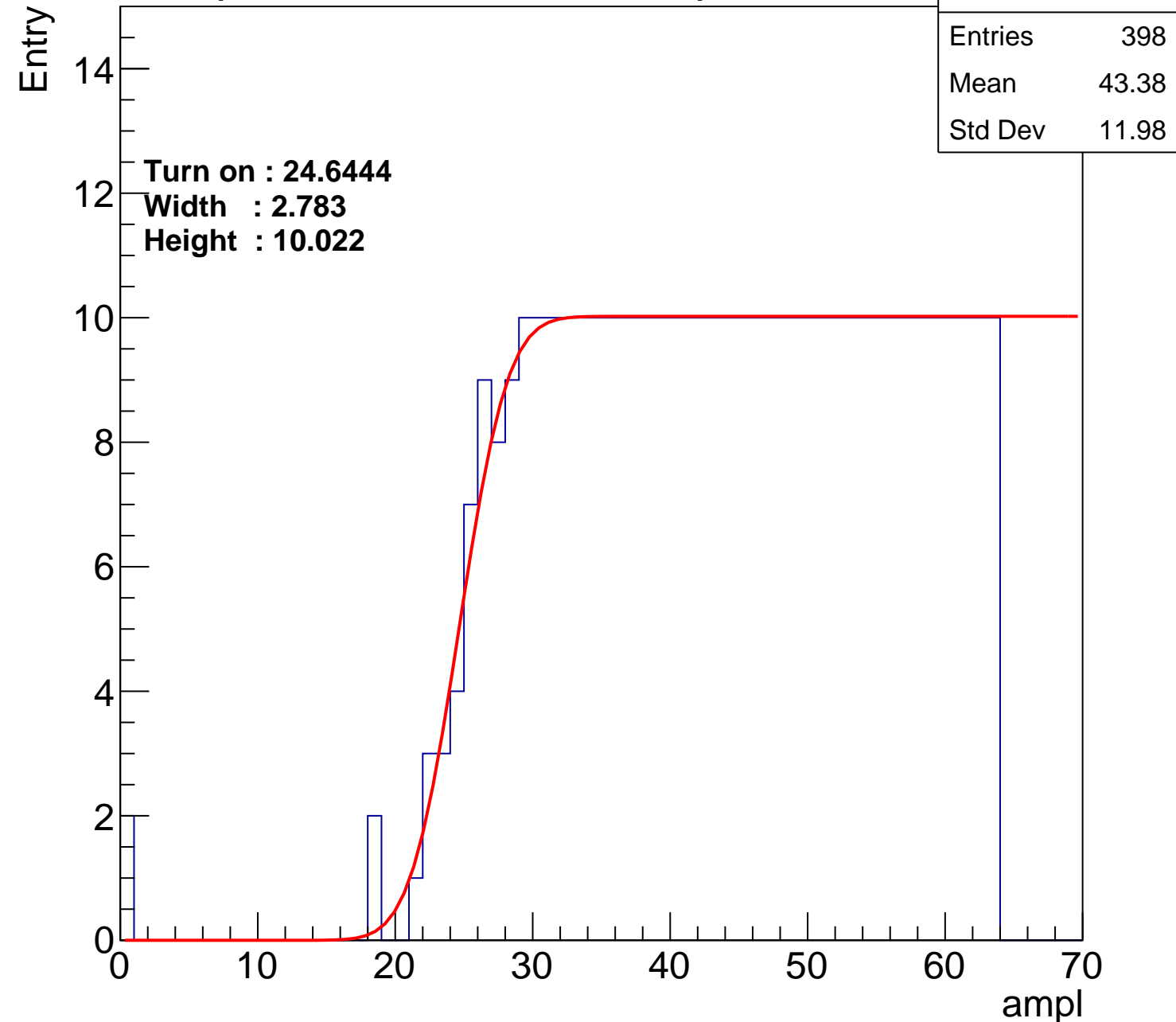
Width : 2.783

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.59
Std Dev	11.25

Turn on : 30.0046

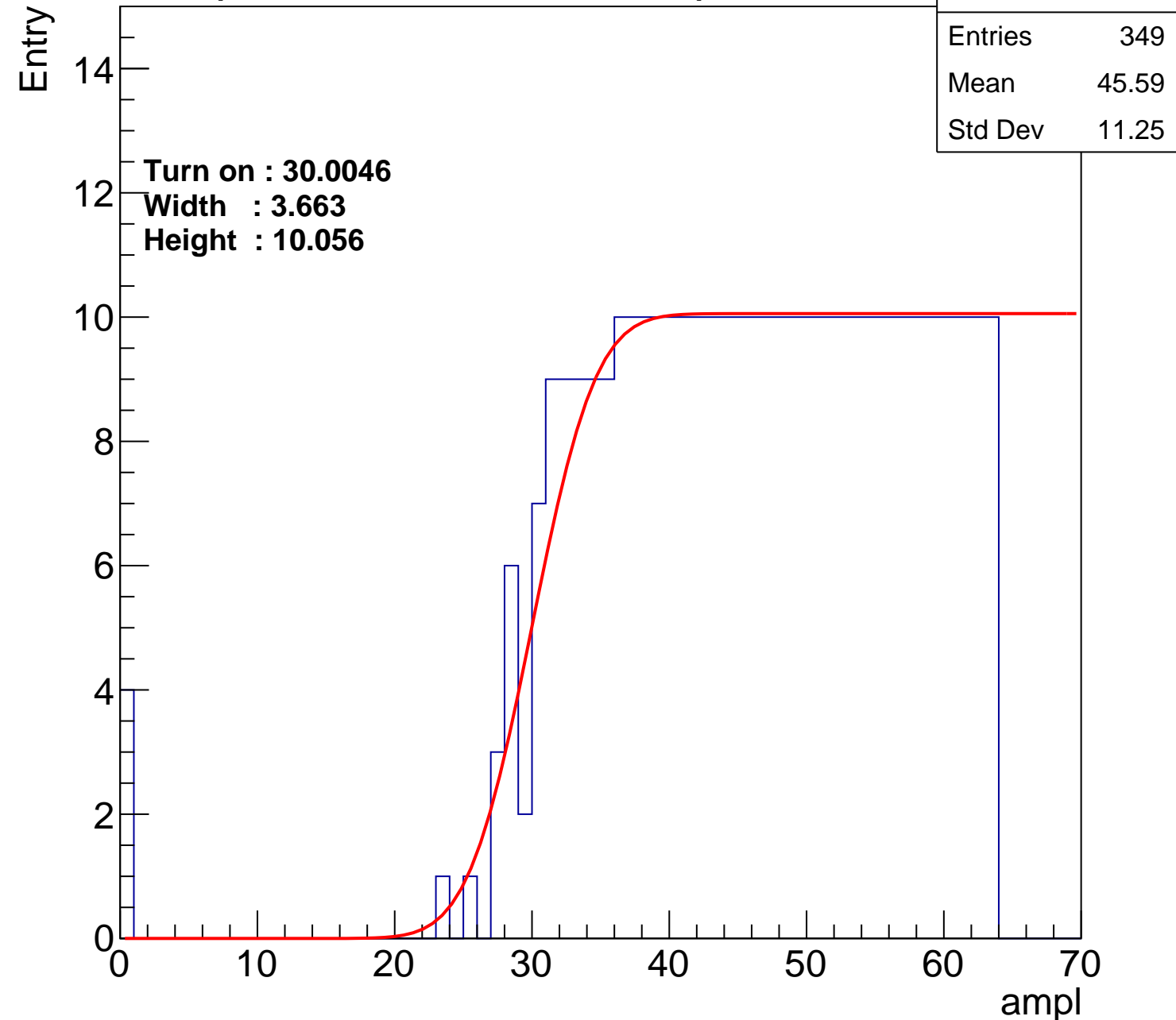
Width : 3.663

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch70

calib_packv5_042523_0143.root, FC#7, port C2

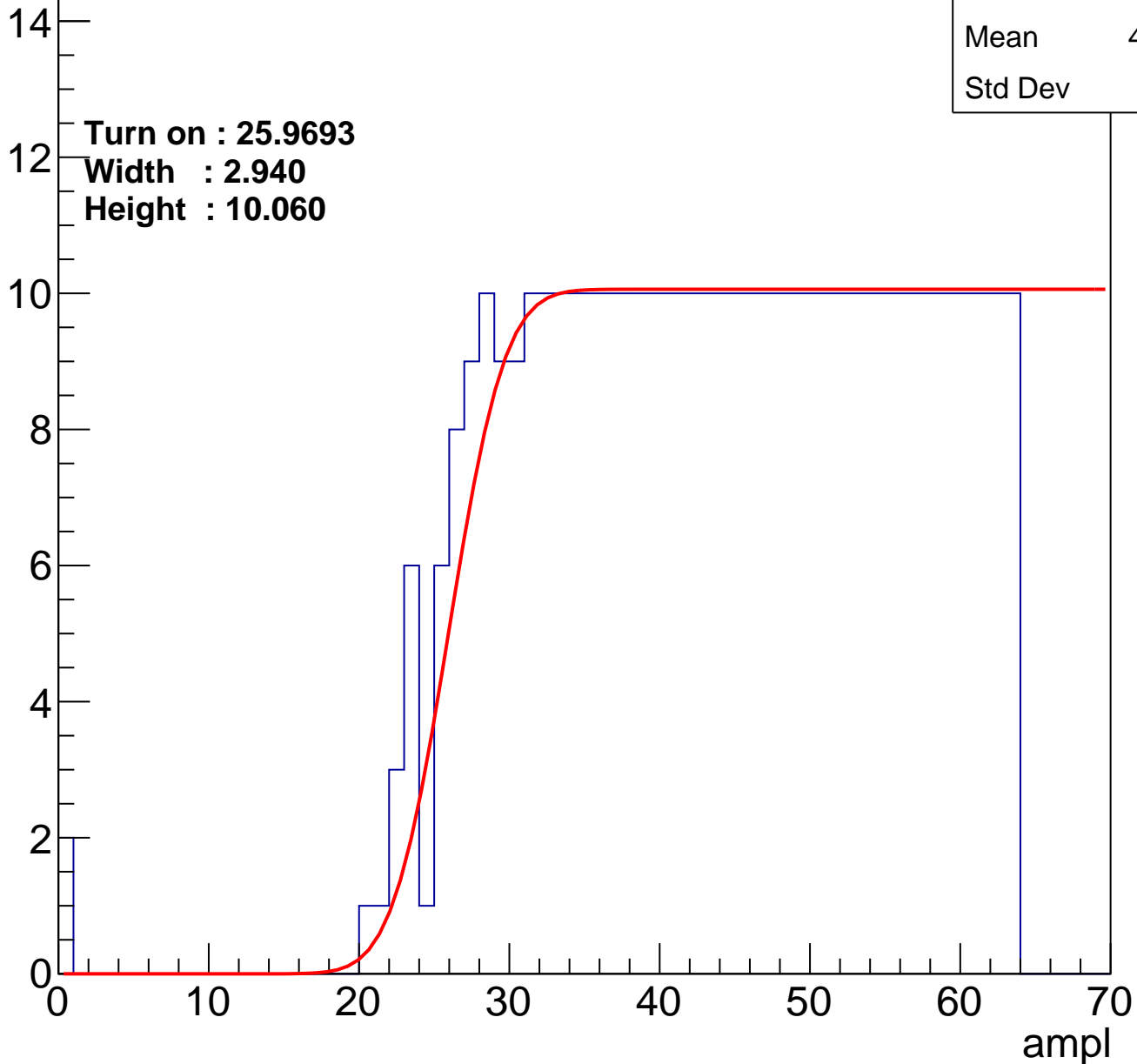
Entries	395
Mean	43.52
Std Dev	11.9

Turn on : 25.9693

Width : 2.940

Height : 10.060

Entry



B1L103S, U24-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.58
Std Dev	12.04

Turn on : 25.4575

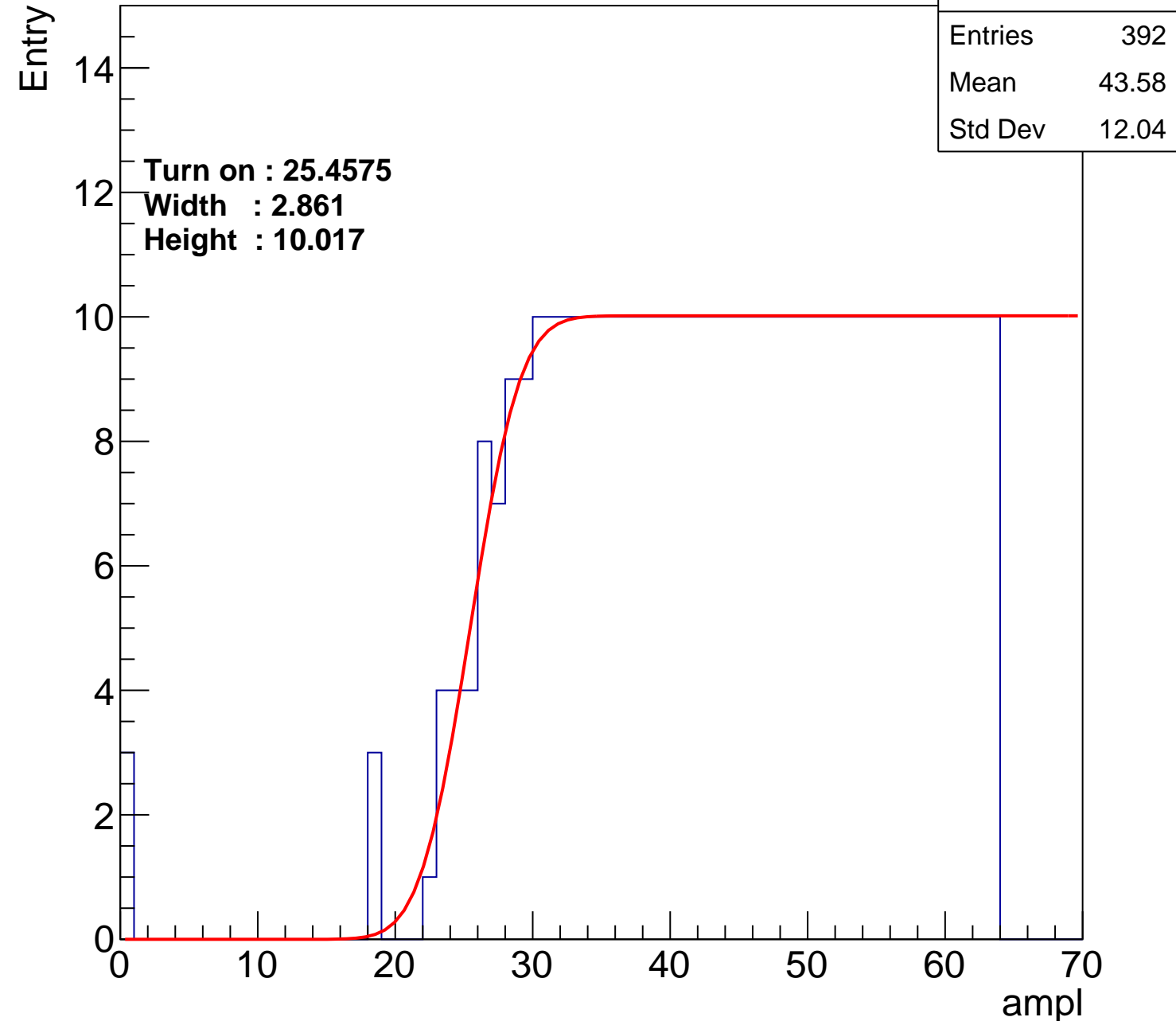
Width : 2.861

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch72

calib_packv5_042523_0143.root, FC#7, port C2

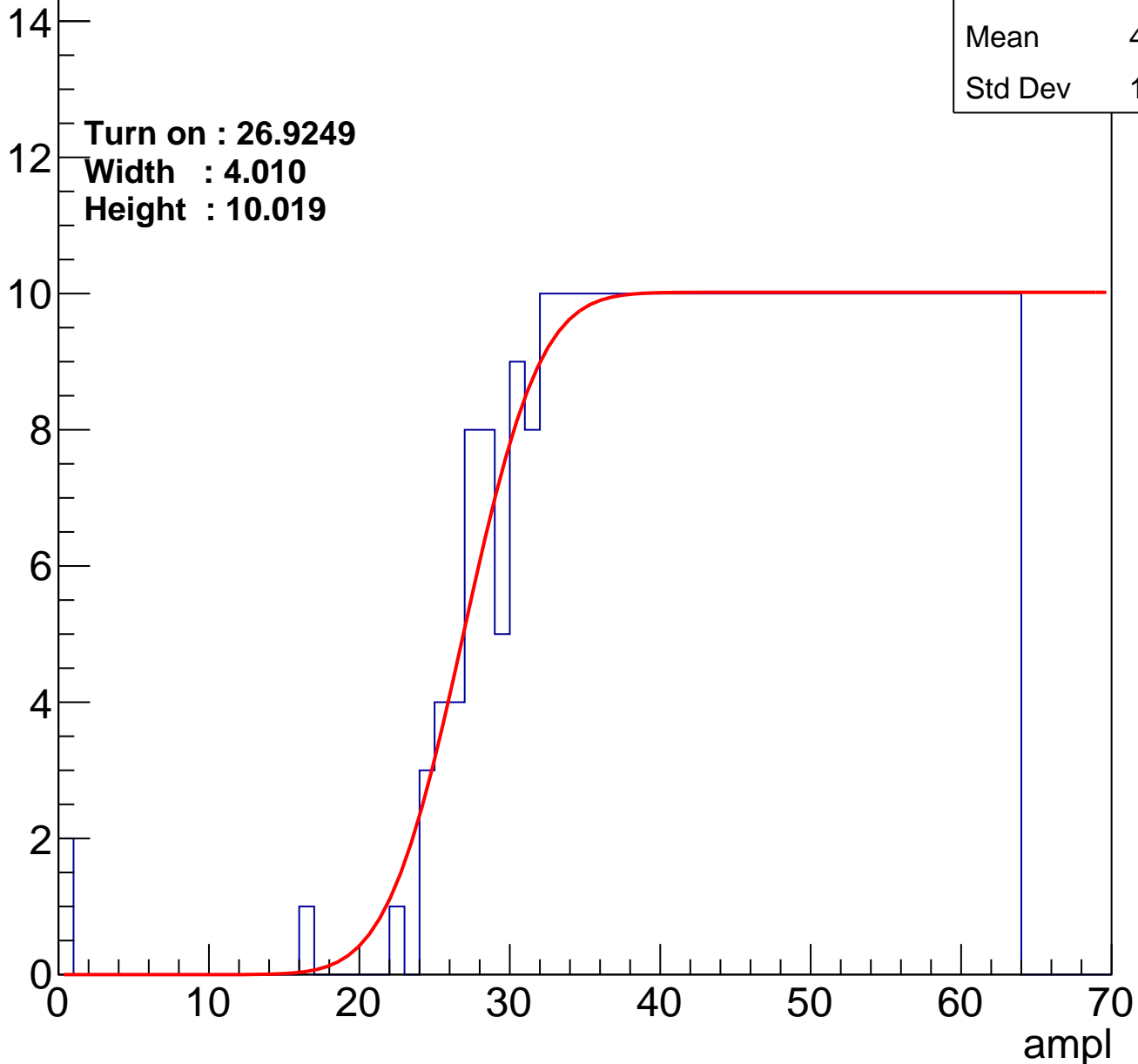
Entries	373
Mean	44.55
Std Dev	11.43

Turn on : 26.9249

Width : 4.010

Height : 10.019

Entry



B1L103S, U24-ch73

calib_packv5_042523_0143.root, FC#7, port C2

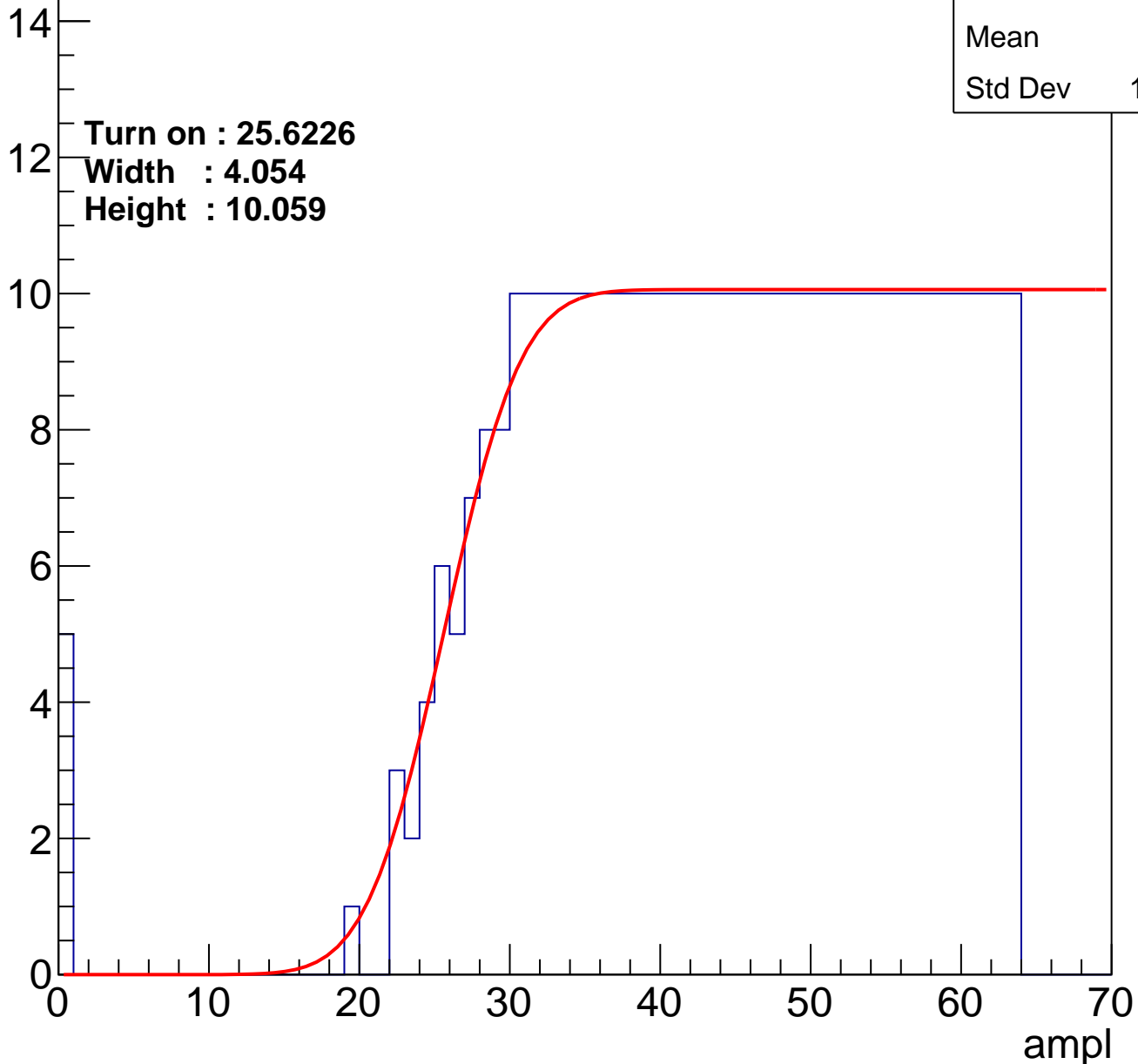
Entries	389
Mean	43.6
Std Dev	12.28

Turn on : 25.6226

Width : 4.054

Height : 10.059

Entry



B1L103S, U24-ch74

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.44
Std Dev	12.14

Turn on : 25.1525

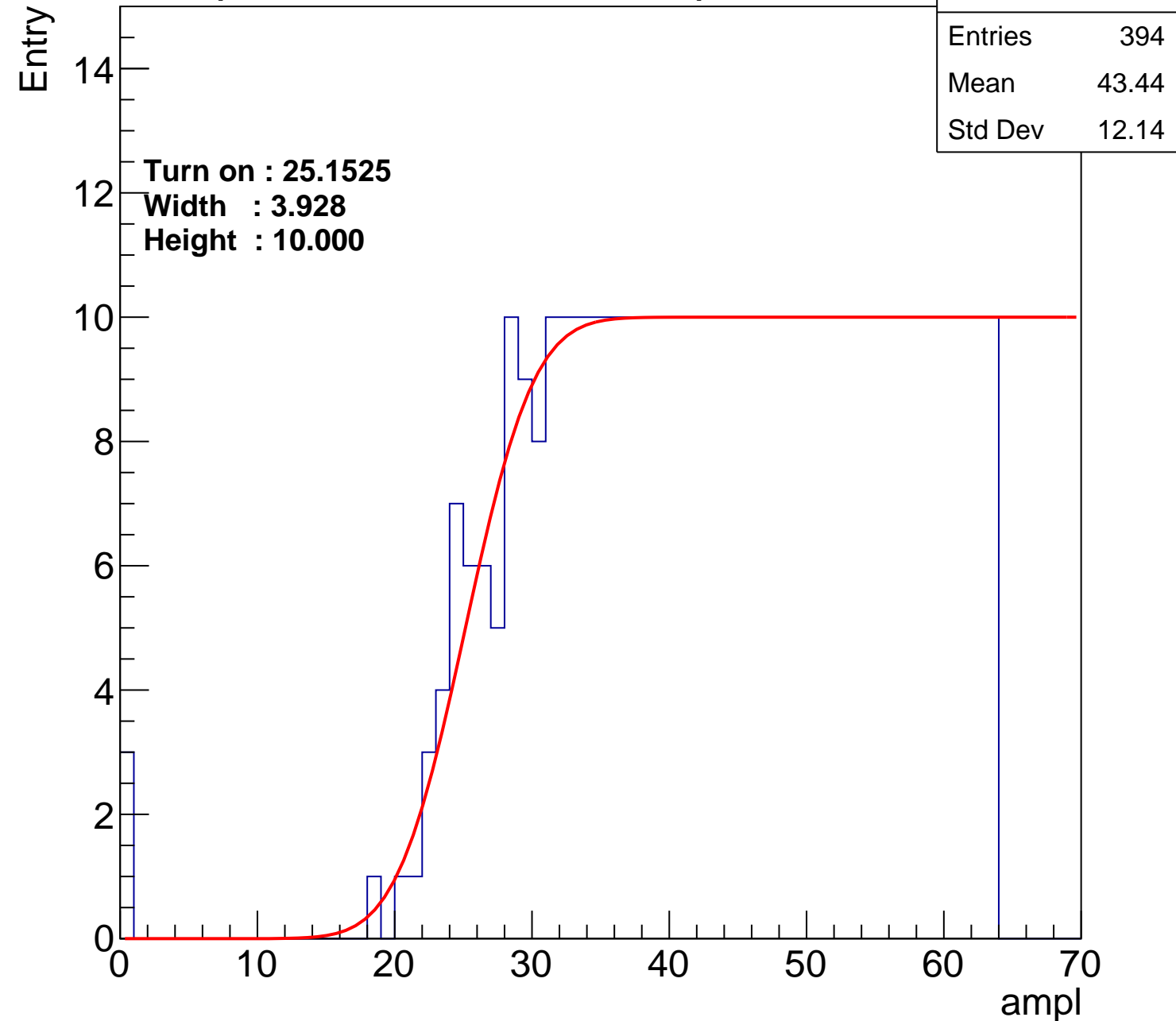
Width : 3.928

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch75

calib_packv5_042523_0143.root, FC#7, port C2

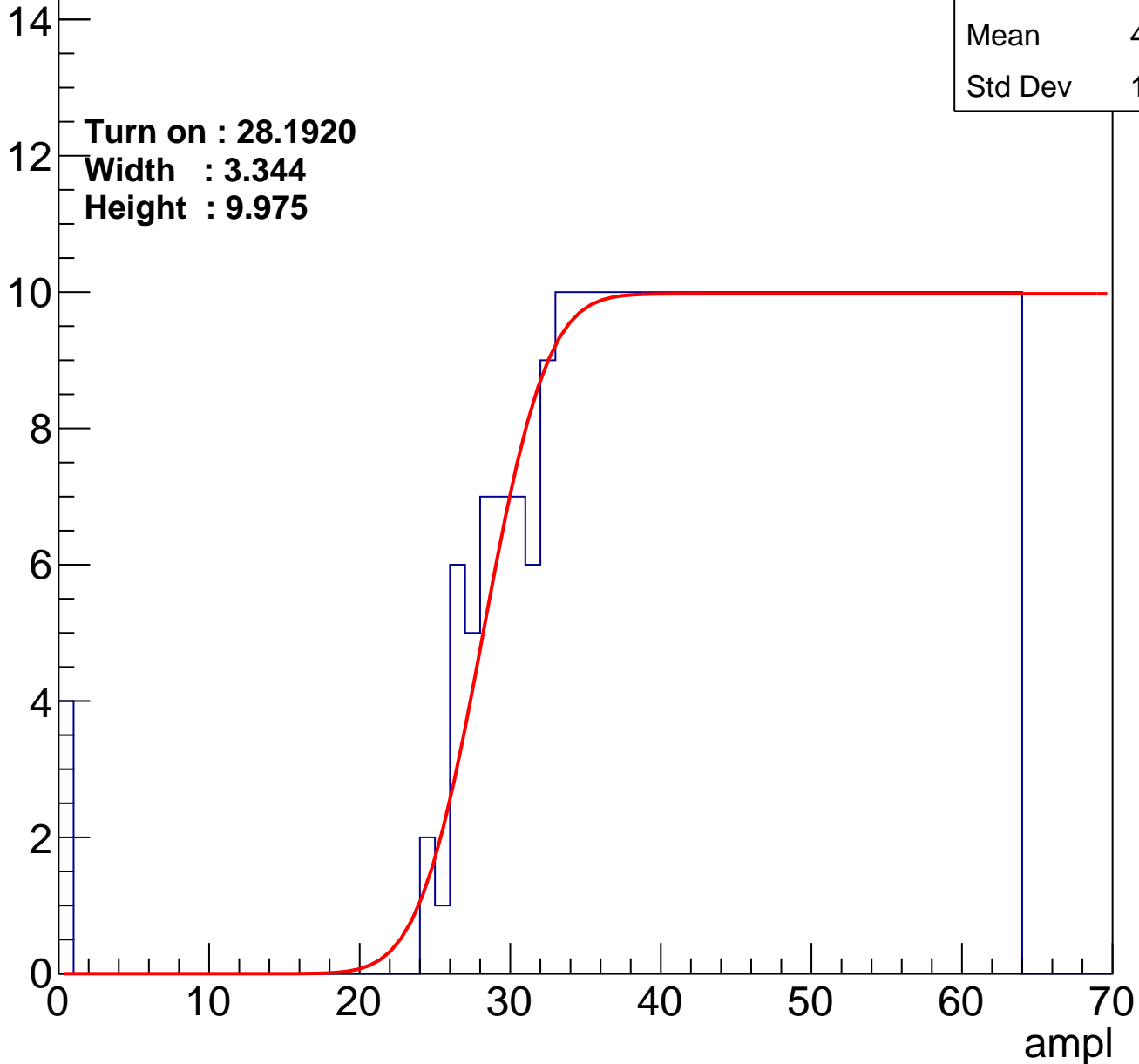
Entries	364
Mean	44.85
Std Dev	11.58

Turn on : 28.1920

Width : 3.344

Height : 9.975

Entry



B1L103S, U24-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.05
Std Dev	11.94

Turn on : 26.6239

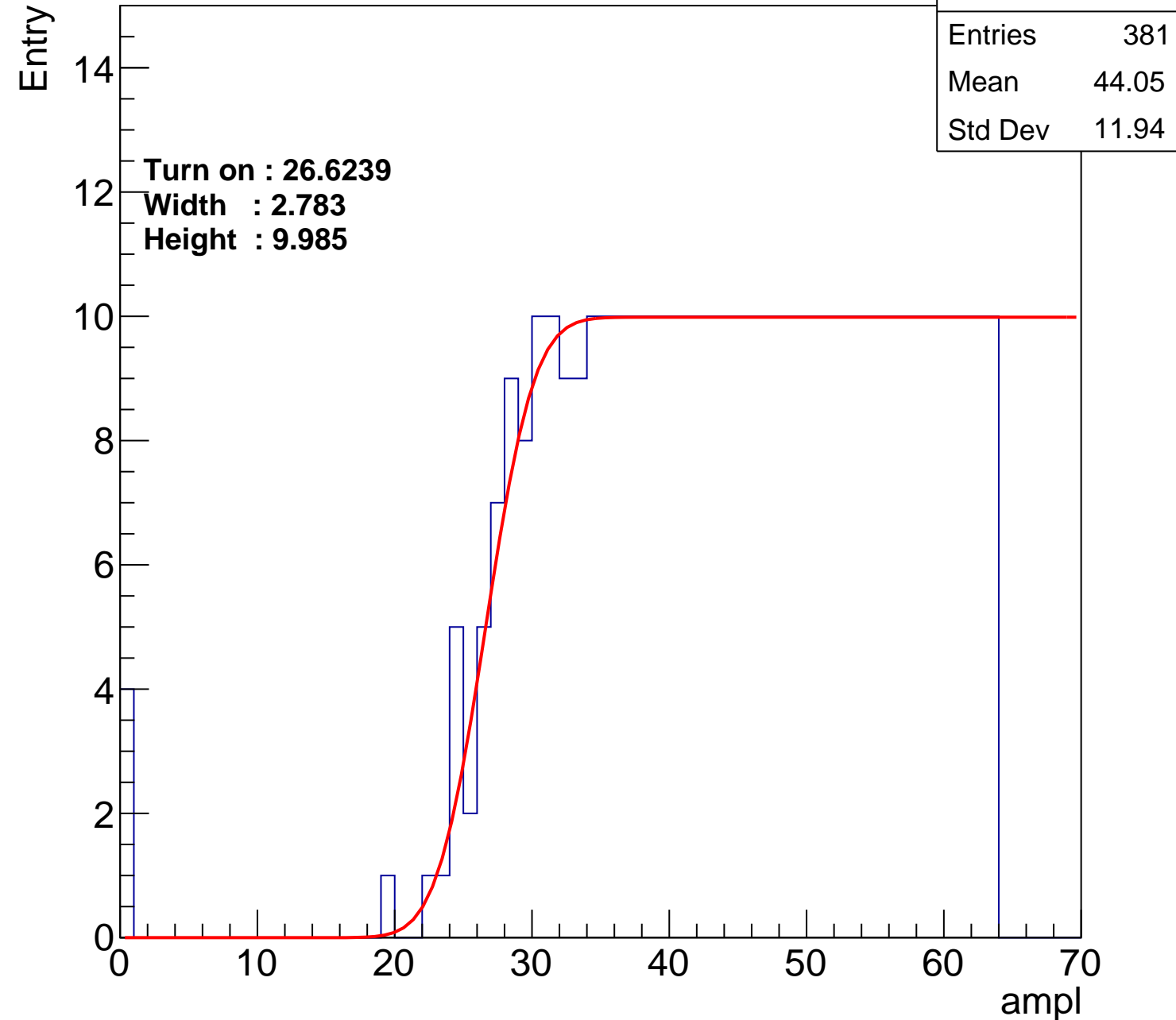
Width : 2.783

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.59
Std Dev	11.37

Turn on : 27.3175

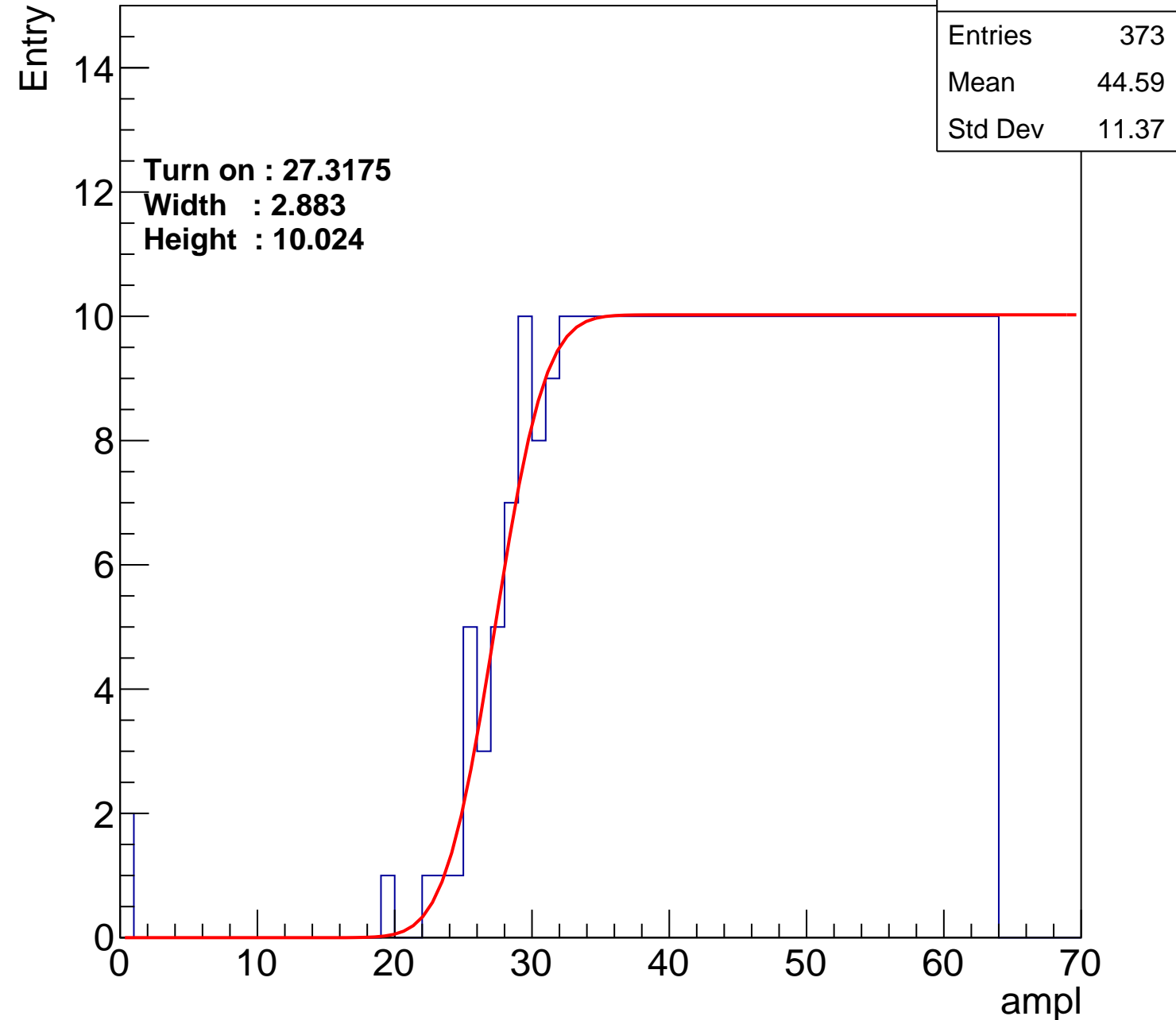
Width : 2.883

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch78

calib_packv5_042523_0143.root, FC#7, port C2

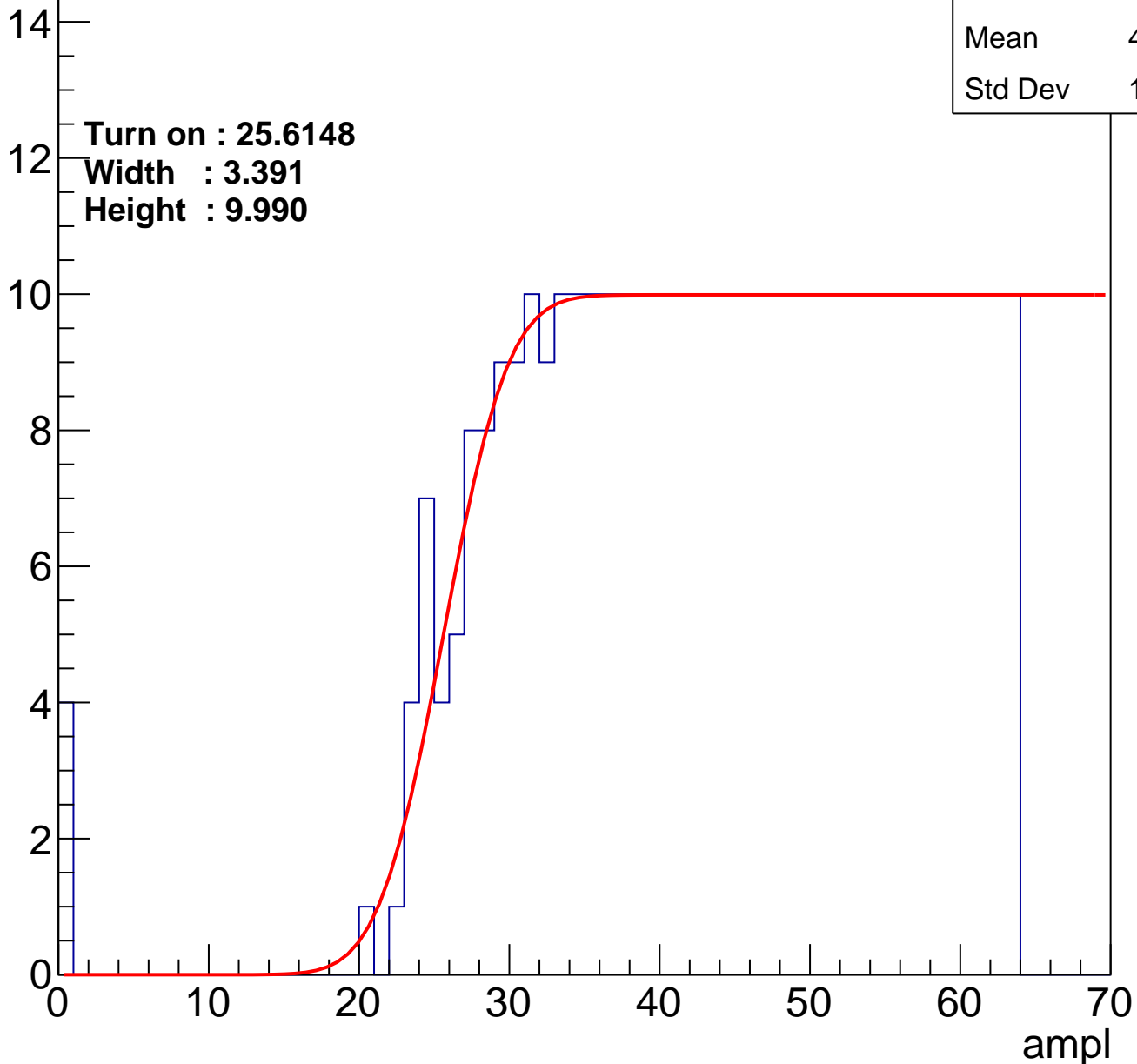
Entries	389
Mean	43.65
Std Dev	12.13

Turn on : 25.6148

Width : 3.391

Height : 9.990

Entry



B1L103S, U24-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.9198

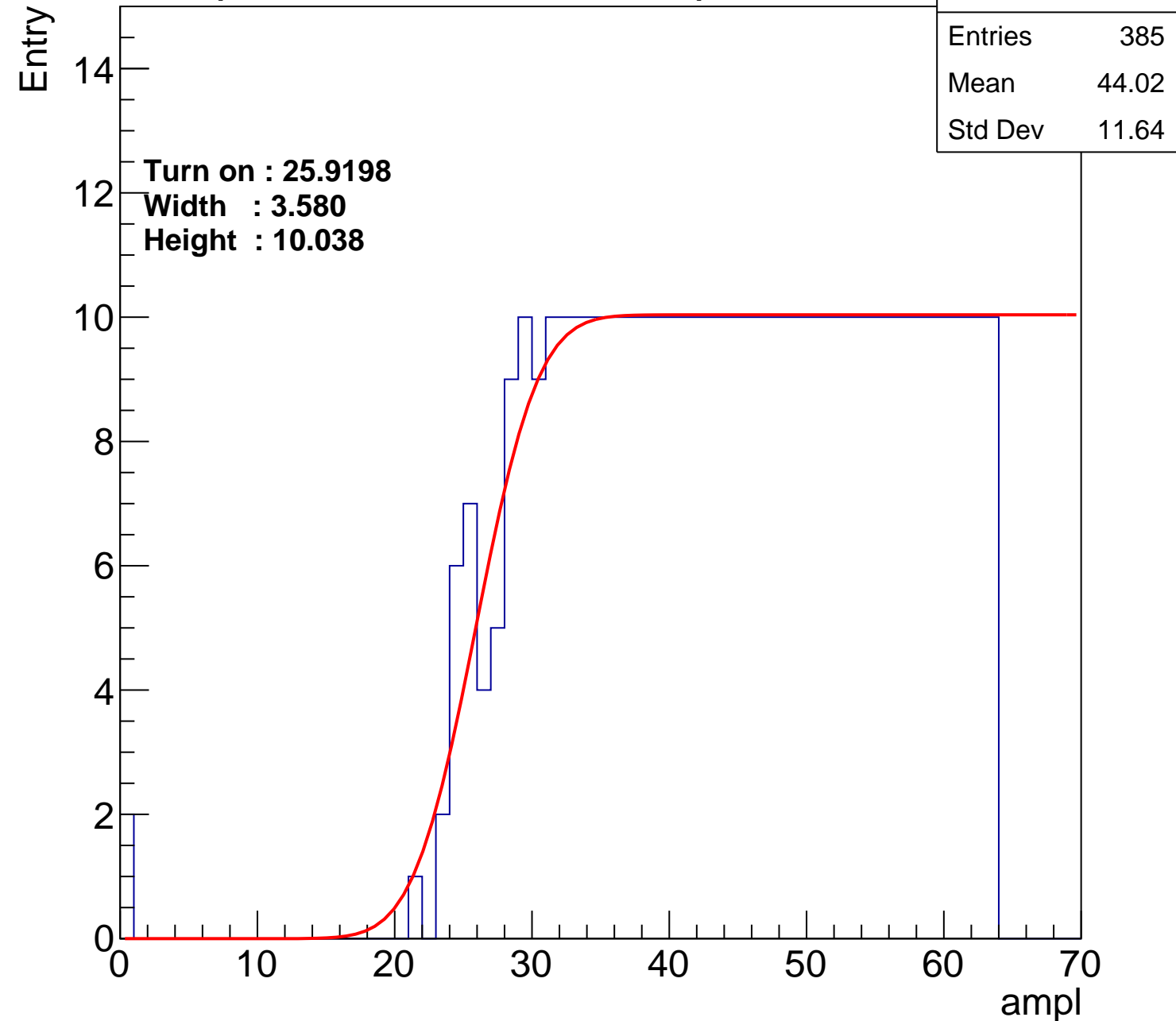
Width : 3.580

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.92
Std Dev	12.05

Turn on : 26.5585

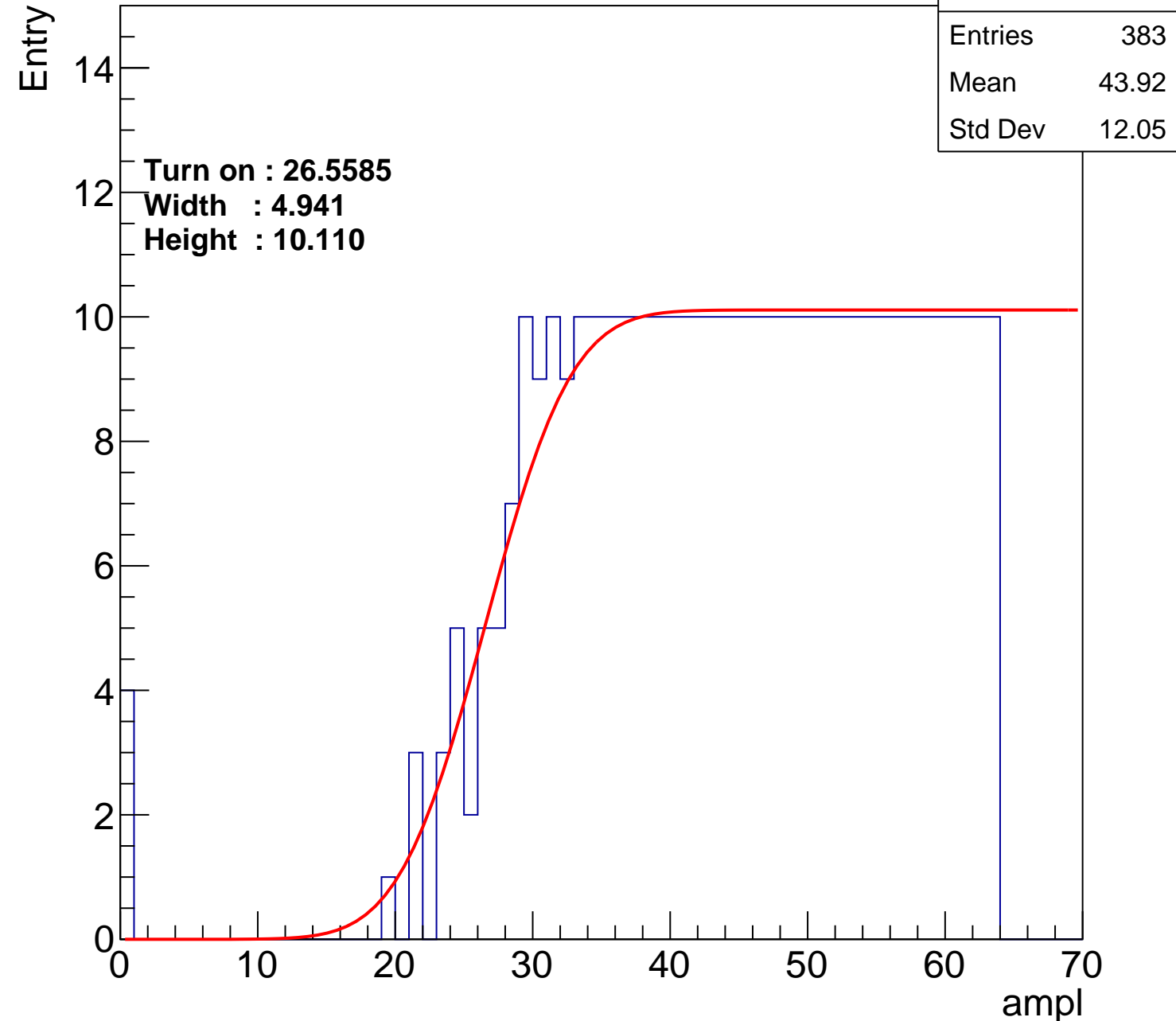
Width : 4.941

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch81

calib_packv5_042523_0143.root, FC#7, port C2

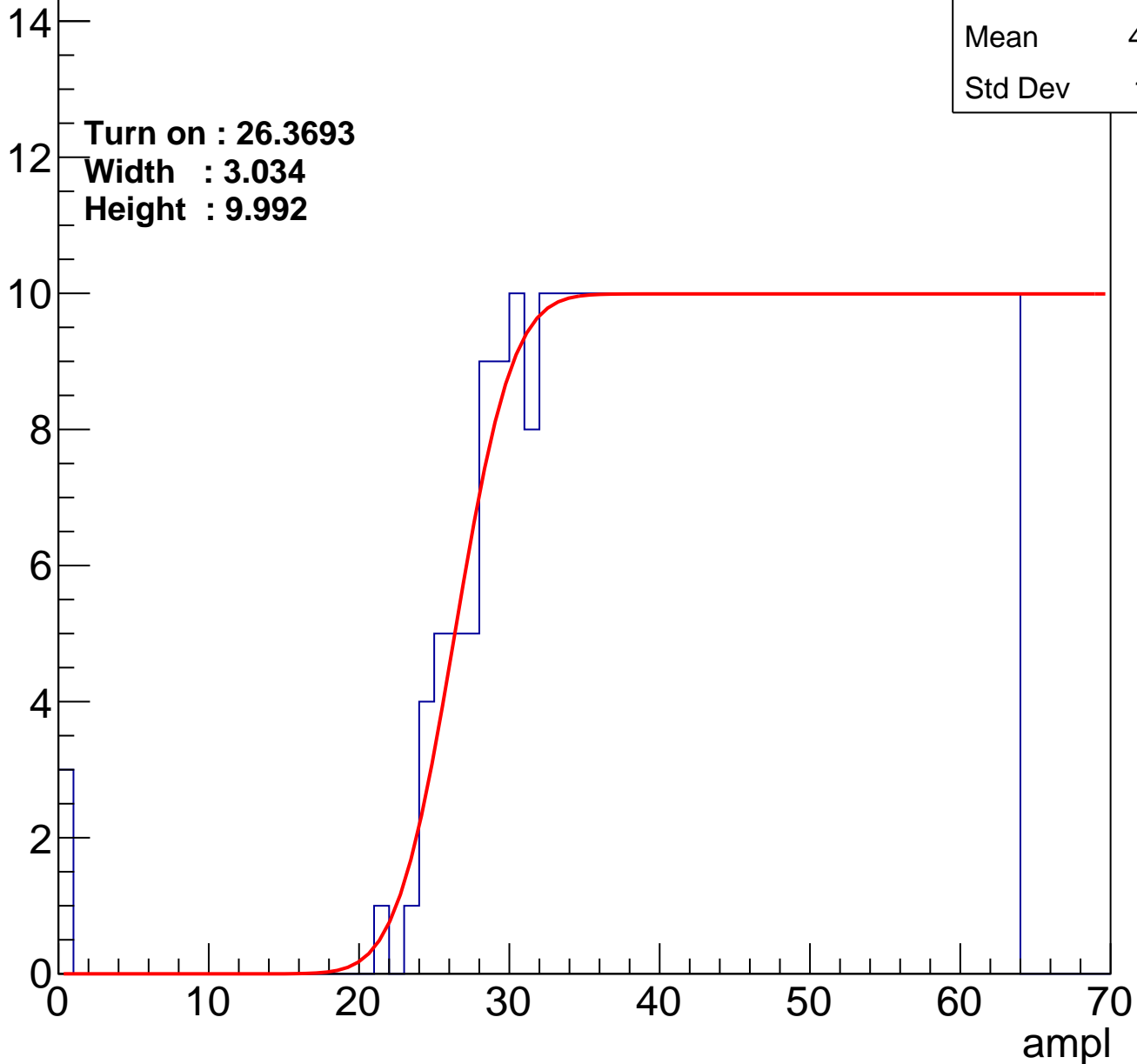
Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.3693

Width : 3.034

Height : 9.992

Entry



B1L103S, U24-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.92
Std Dev	11.86

Turn on : 26.4138

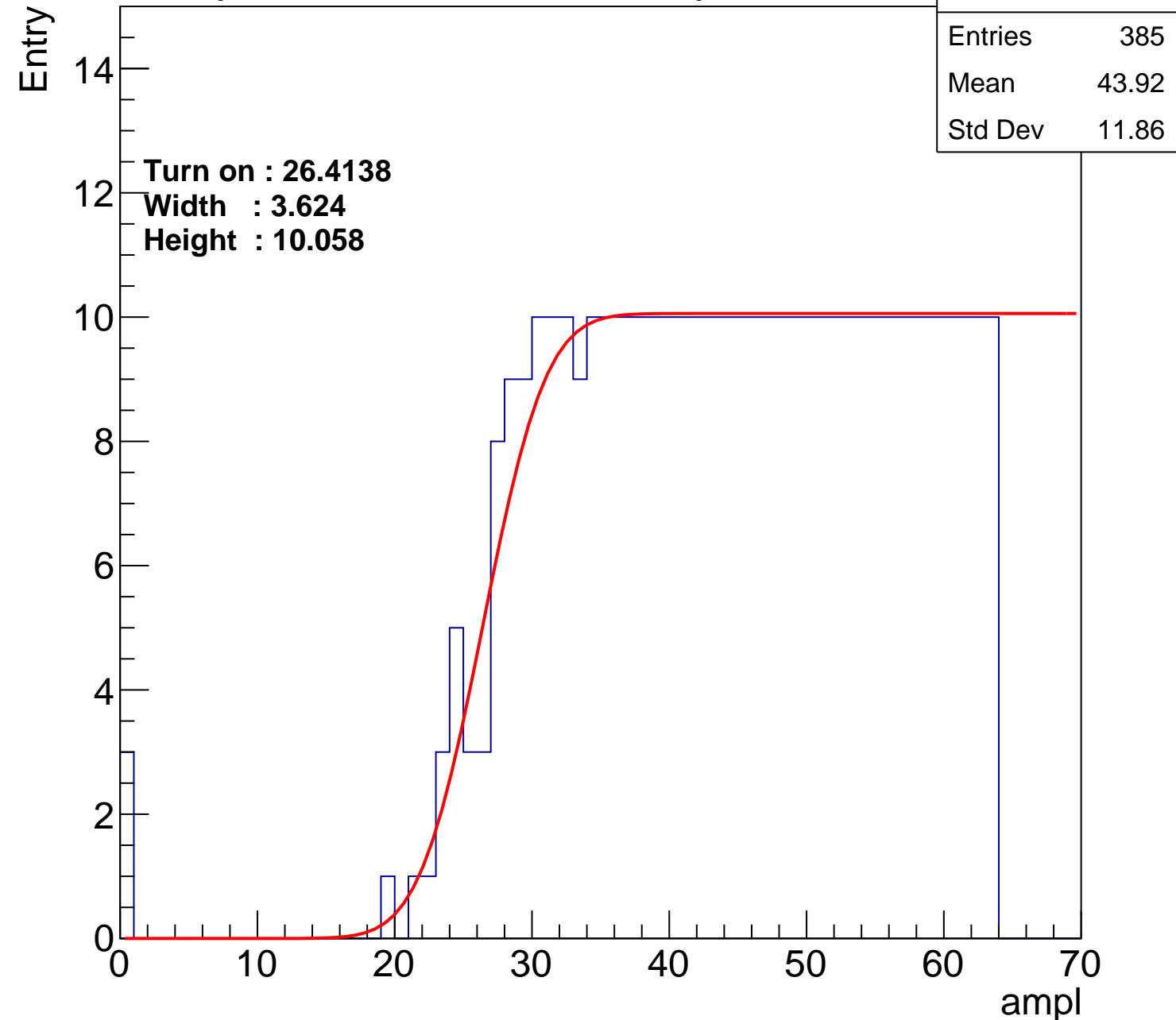
Width : 3.624

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43.21
Std Dev	11.93

Turn on : 23.9157

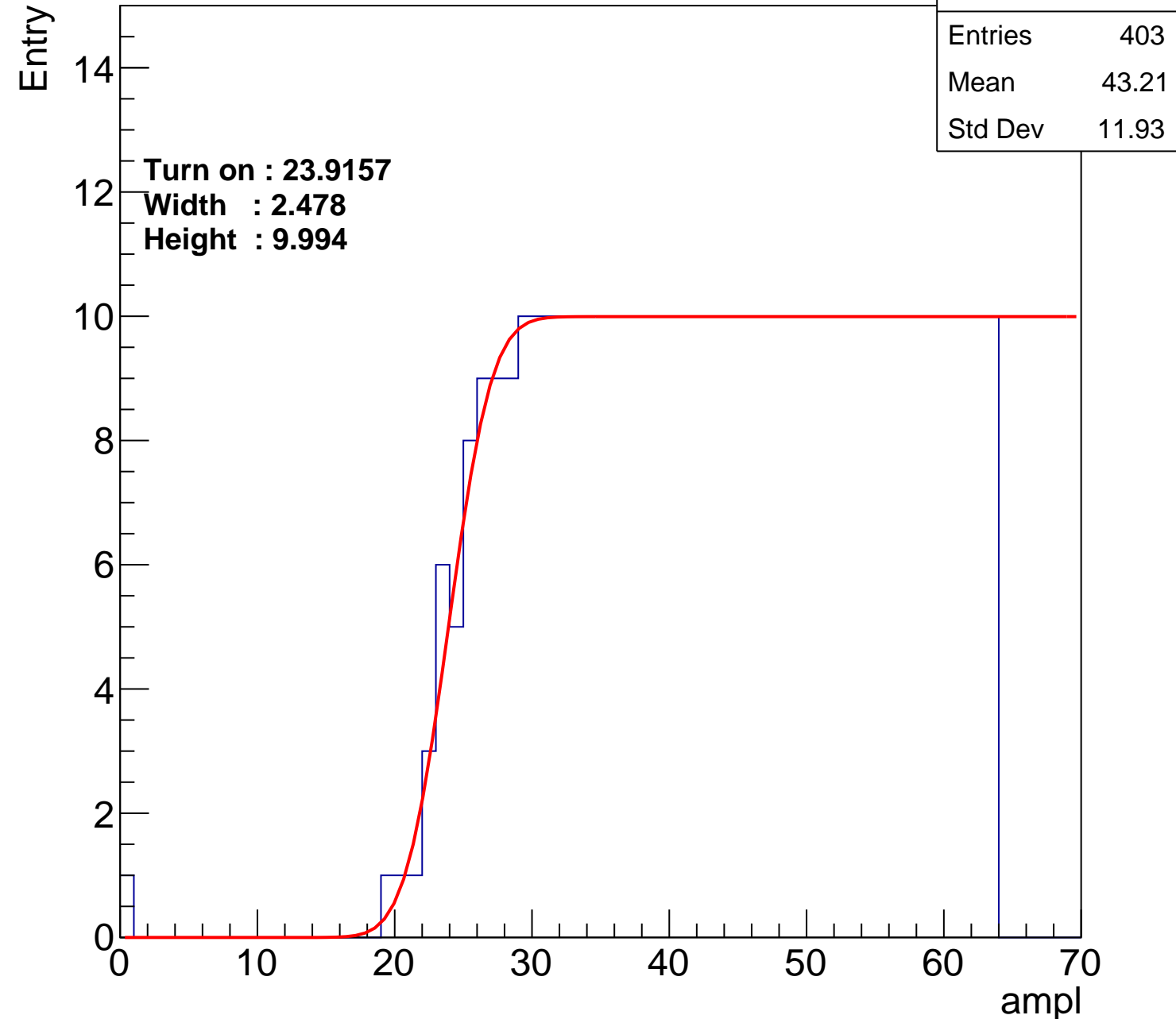
Width : 2.478

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.08
Std Dev	12.01

Turn on : 26.7942

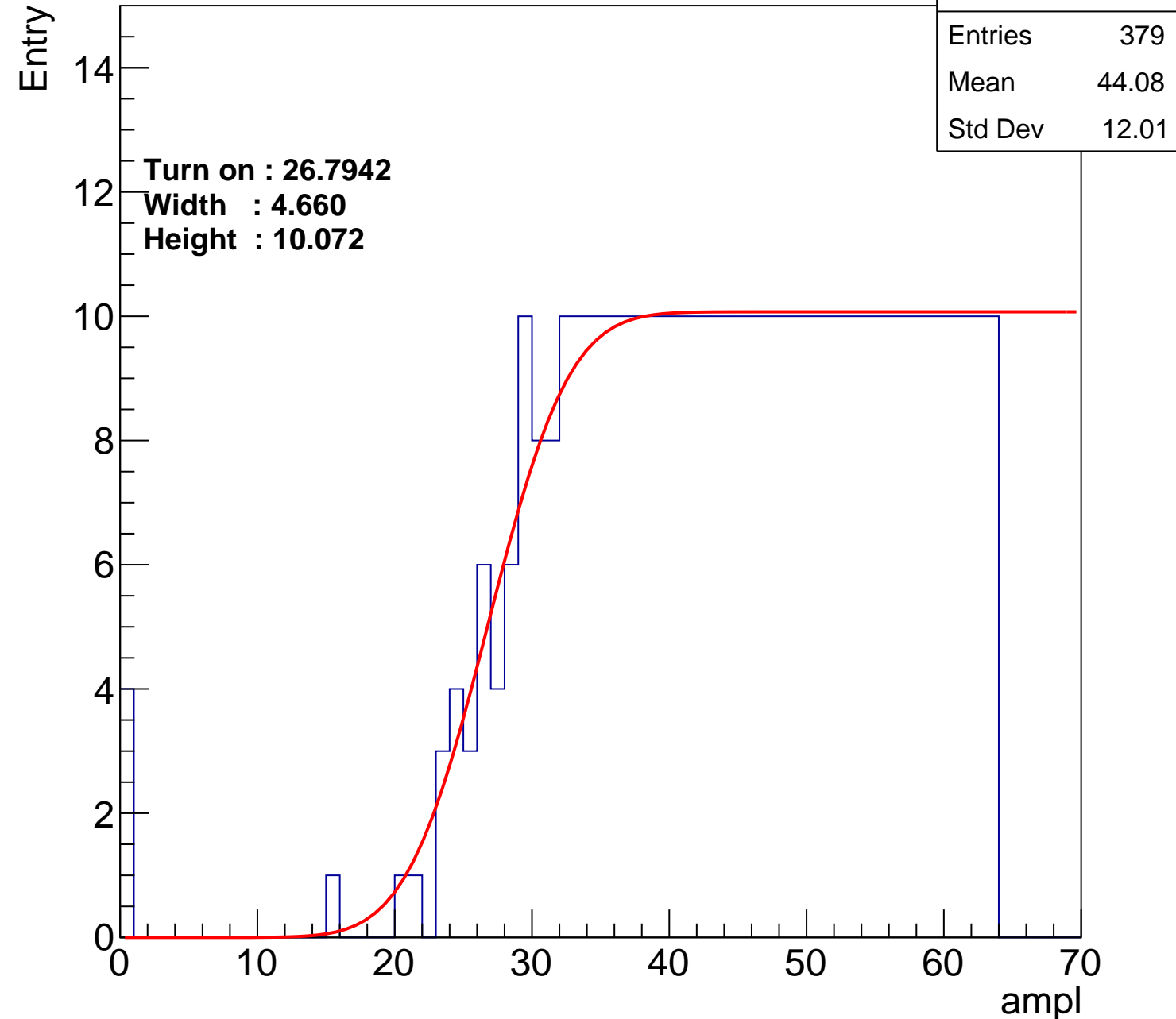
Width : 4.660

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch85

calib_packv5_042523_0143.root, FC#7, port C2

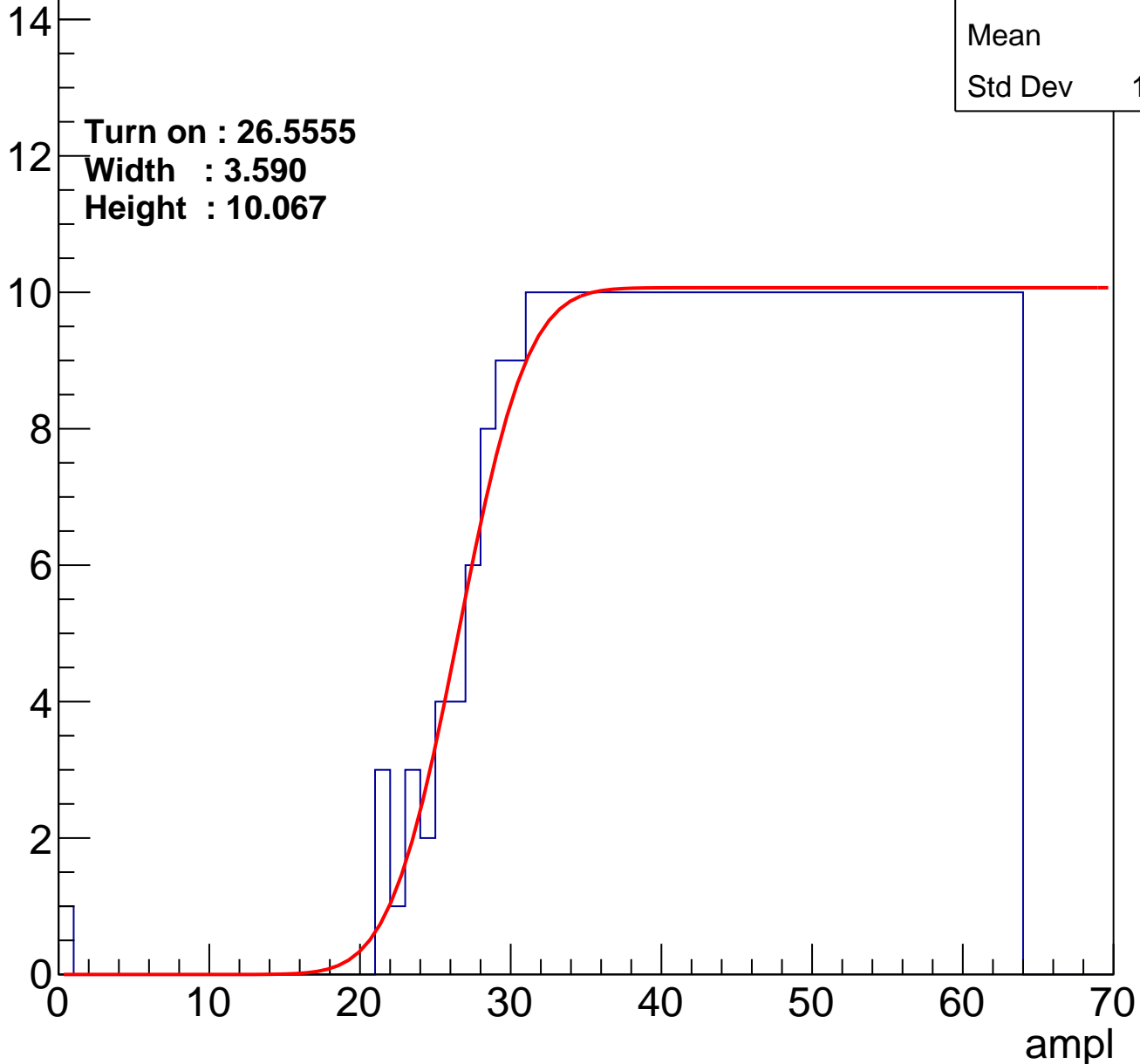
Entries	380
Mean	44.3
Std Dev	11.39

Turn on : 26.5555

Width : 3.590

Height : 10.067

Entry



B1L103S, U24-ch86

calib_packv5_042523_0143.root, FC#7, port C2

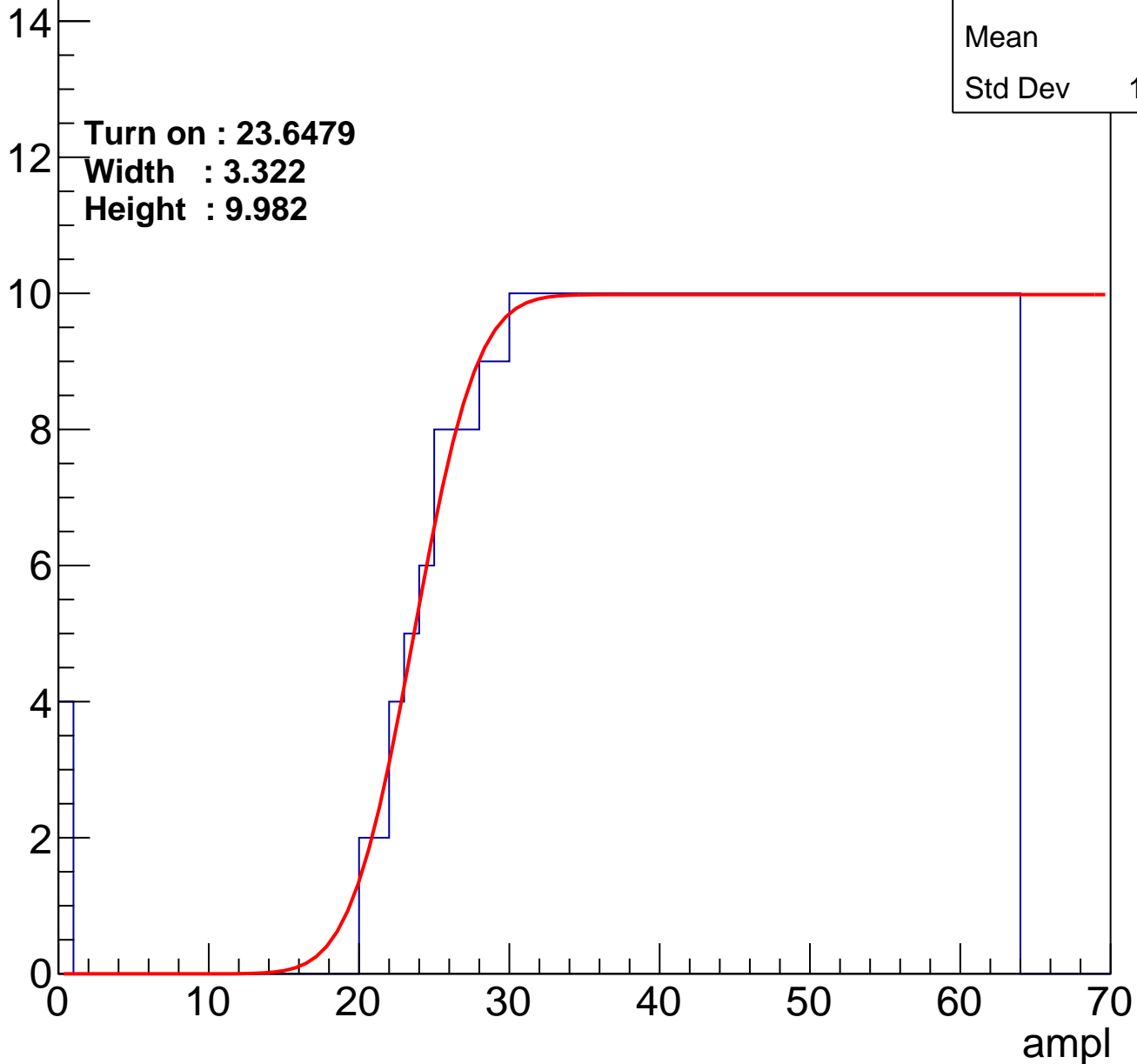
Entries	405
Mean	42.9
Std Dev	12.47

Turn on : 23.6479

Width : 3.322

Height : 9.982

Entry



B1L103S, U24-ch87

calib_packv5_042523_0143.root, FC#7, port C2

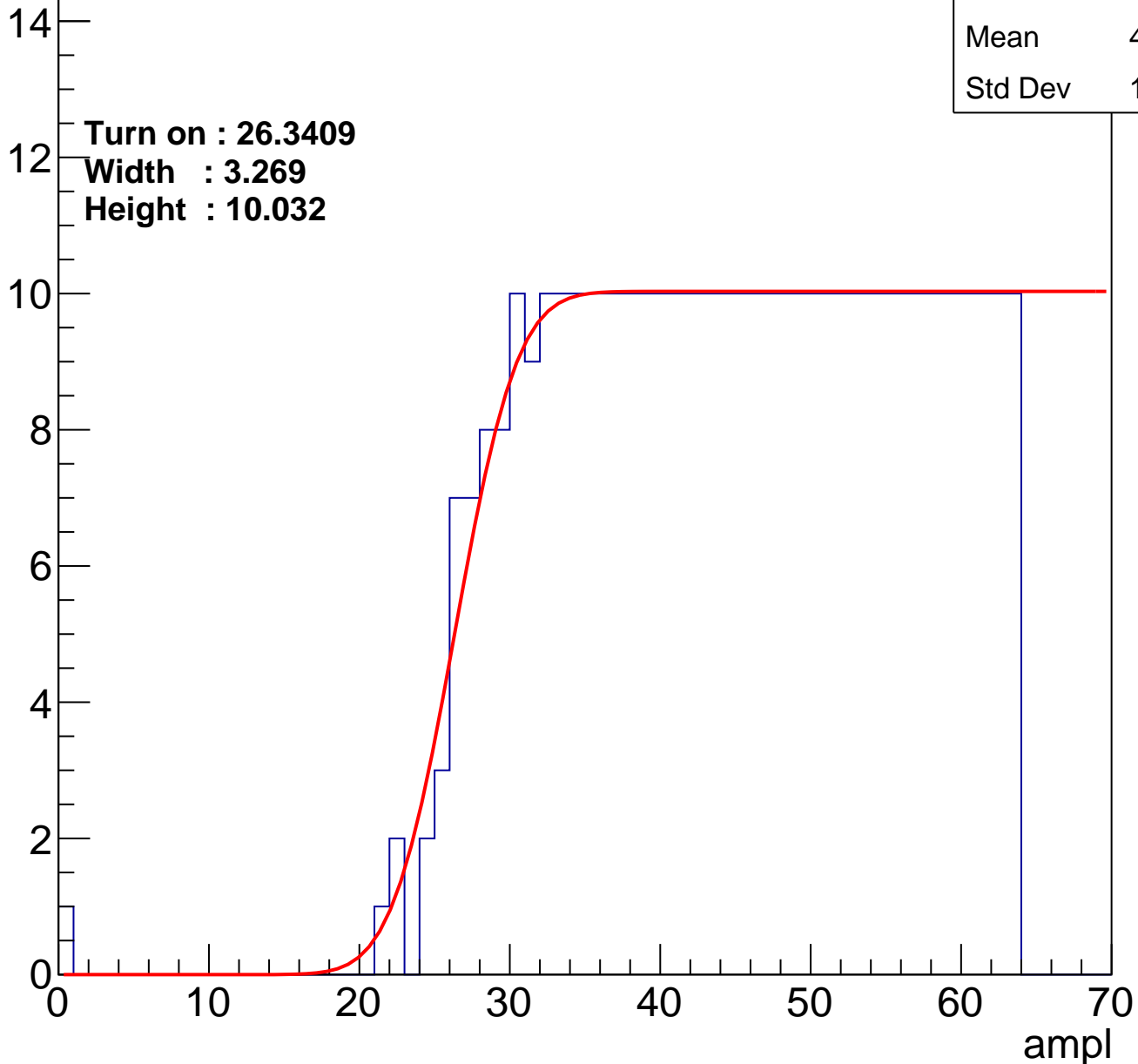
Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.3409

Width : 3.269

Height : 10.032

Entry



B1L103S, U24-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.68
Std Dev	11.25

Turn on : 26.7510

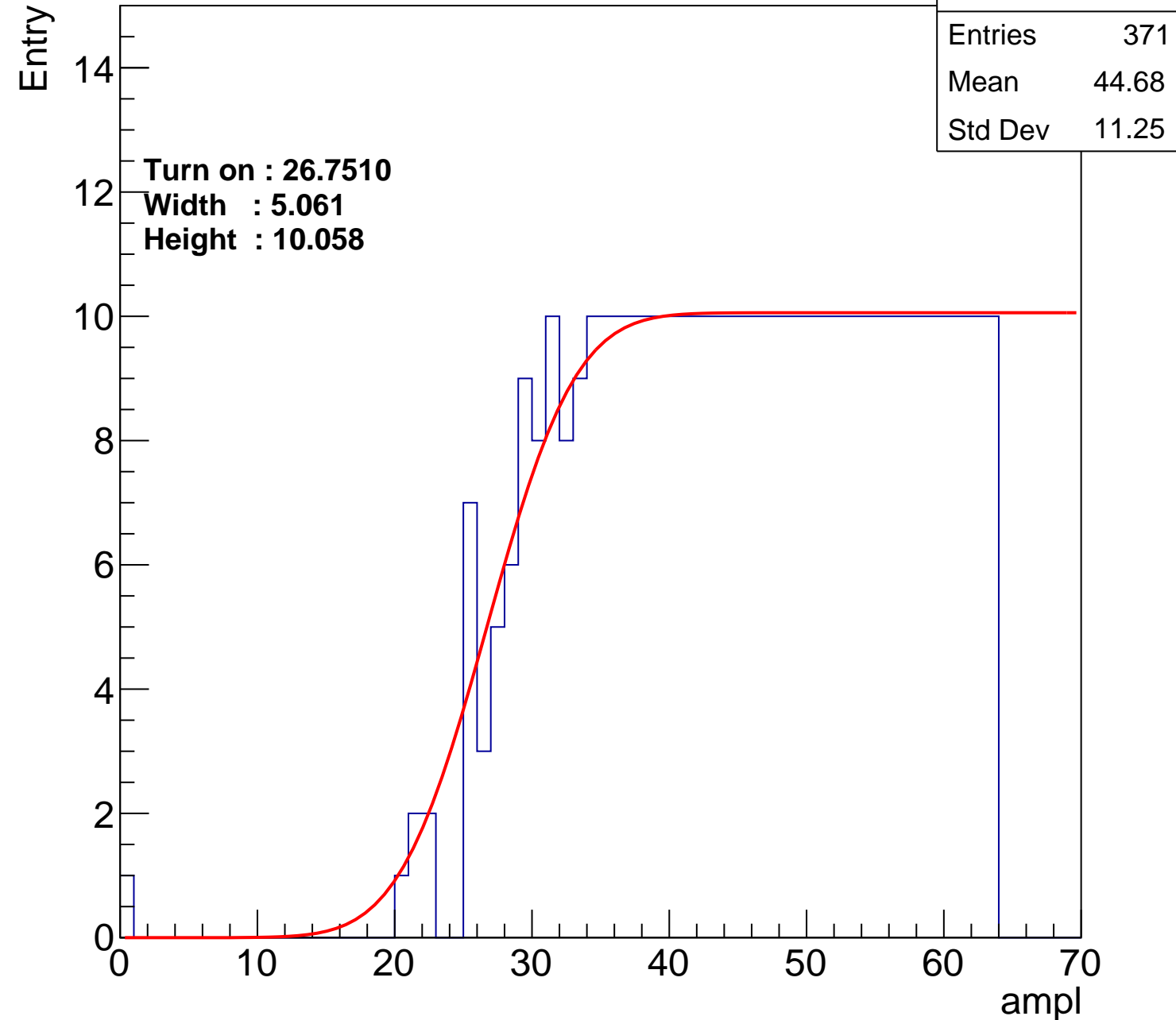
Width : 5.061

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.35
Std Dev	11.84

Turn on : 27.4486

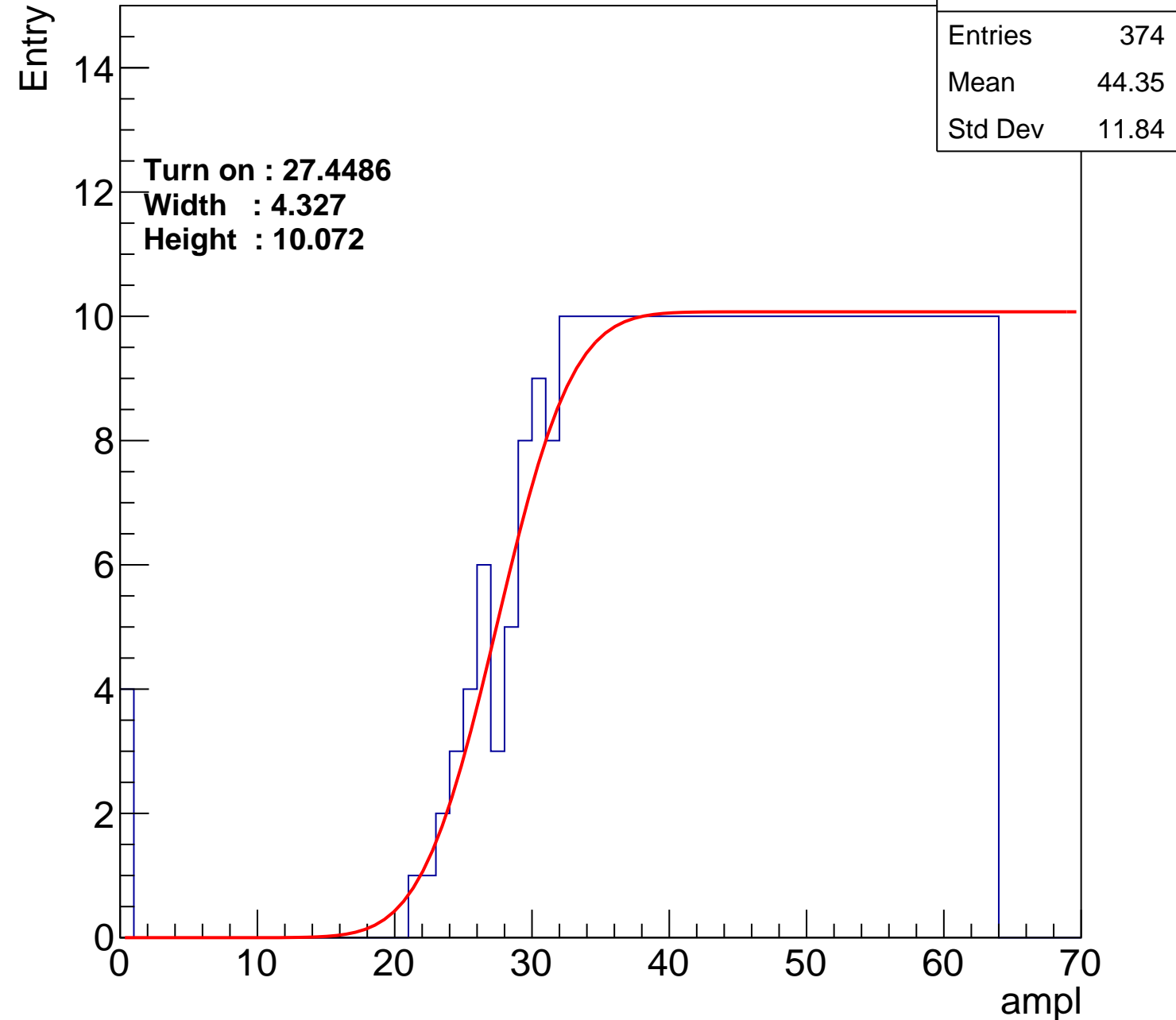
Width : 4.327

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch90

calib_packv5_042523_0143.root, FC#7, port C2

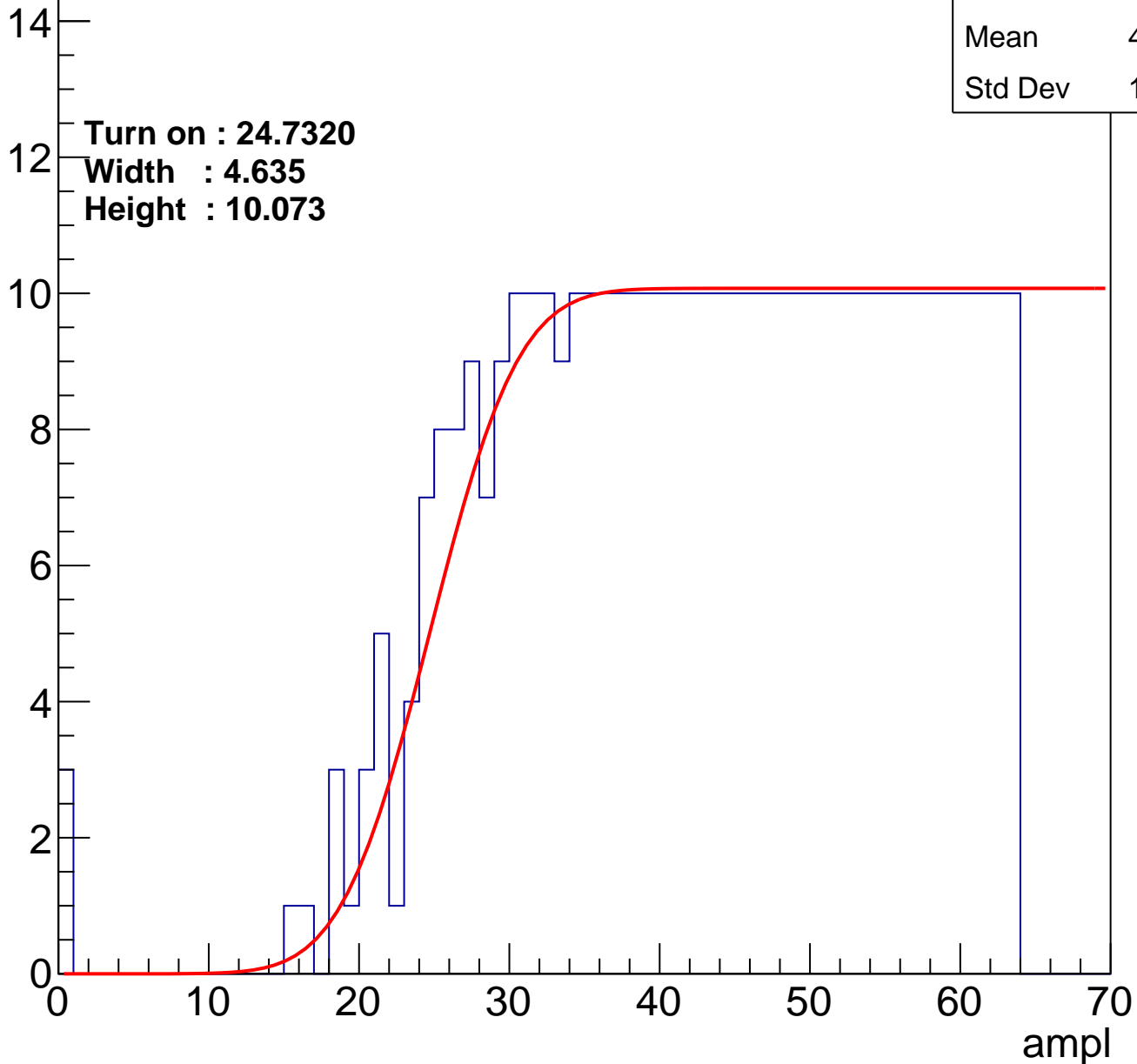
Entries	409
Mean	42.63
Std Dev	12.64

Turn on : 24.7320

Width : 4.635

Height : 10.073

Entry



B1L103S, U24-ch91

calib_packv5_042523_0143.root, FC#7, port C2

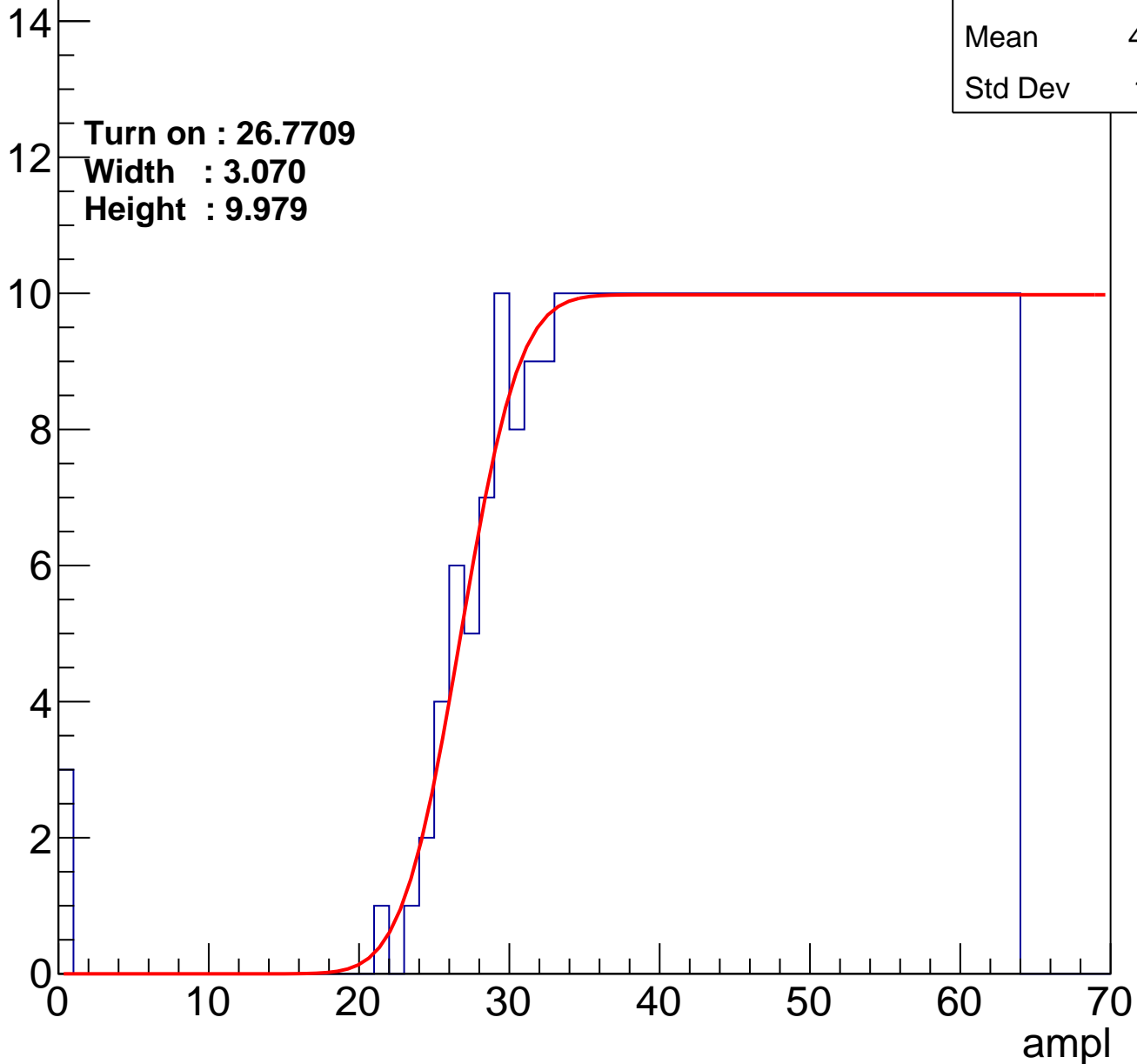
Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 26.7709

Width : 3.070

Height : 9.979

Entry



B1L103S, U24-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.66
Std Dev	11.87

Turn on : 28.5449

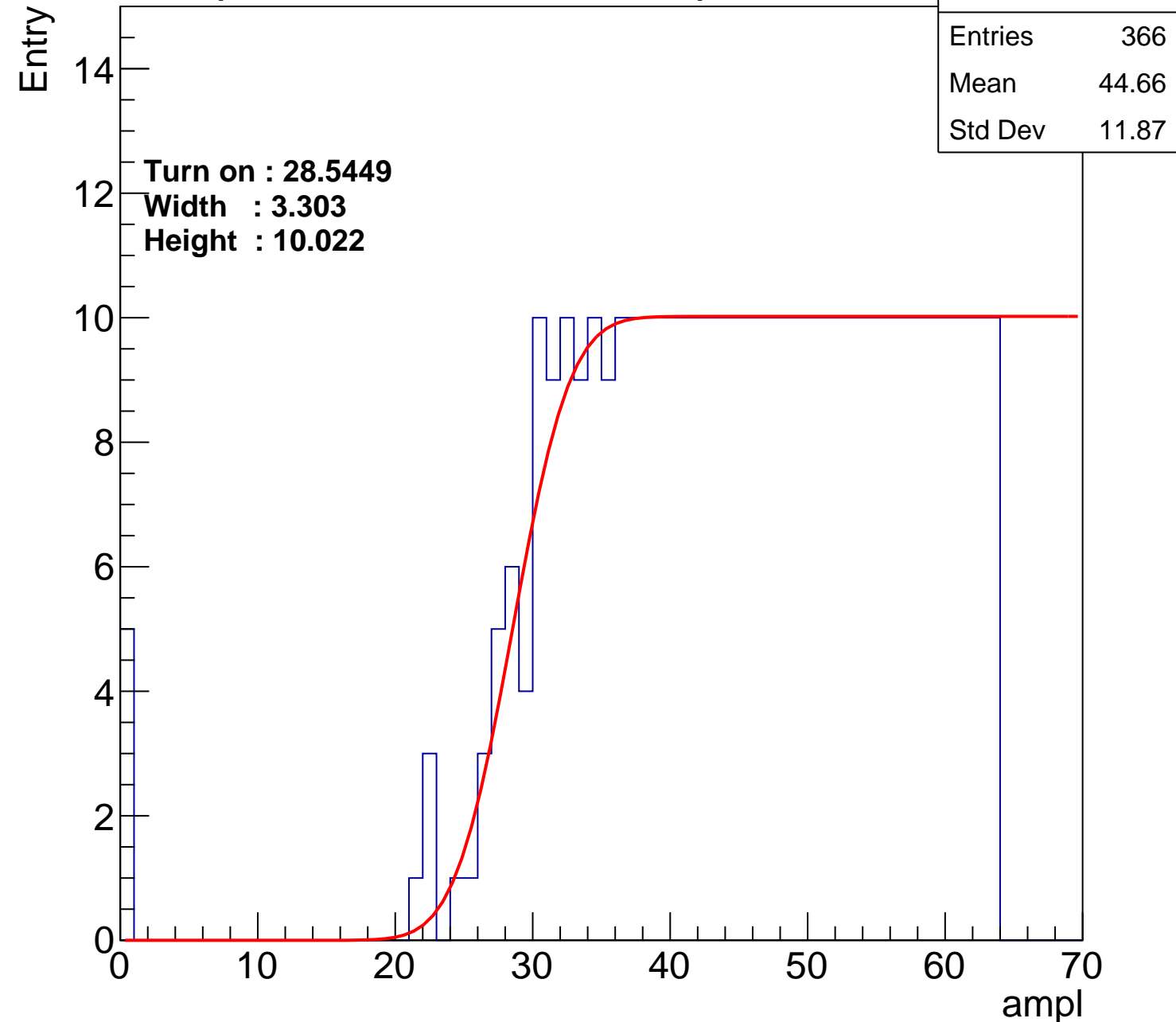
Width : 3.303

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch93

calib_packv5_042523_0143.root, FC#7, port C2

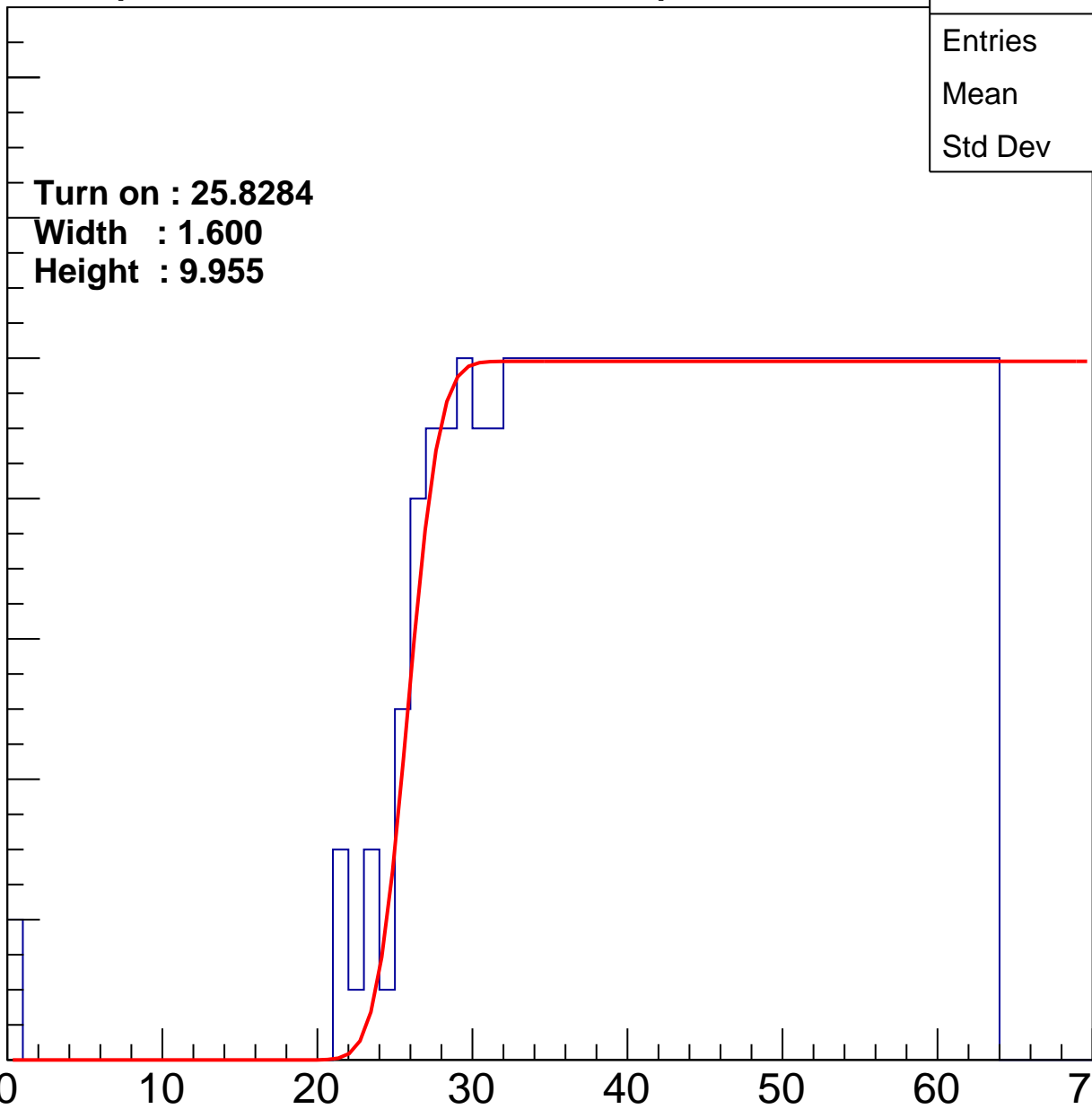
Entry

14
12
10
8
6
4
2
0

Turn on : 25.8284
Width : 1.600
Height : 9.955

Entries	389
Mean	43.82
Std Dev	11.75

ampl



B1L103S, U24-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.06
Std Dev	11.81

Turn on : 26.6171

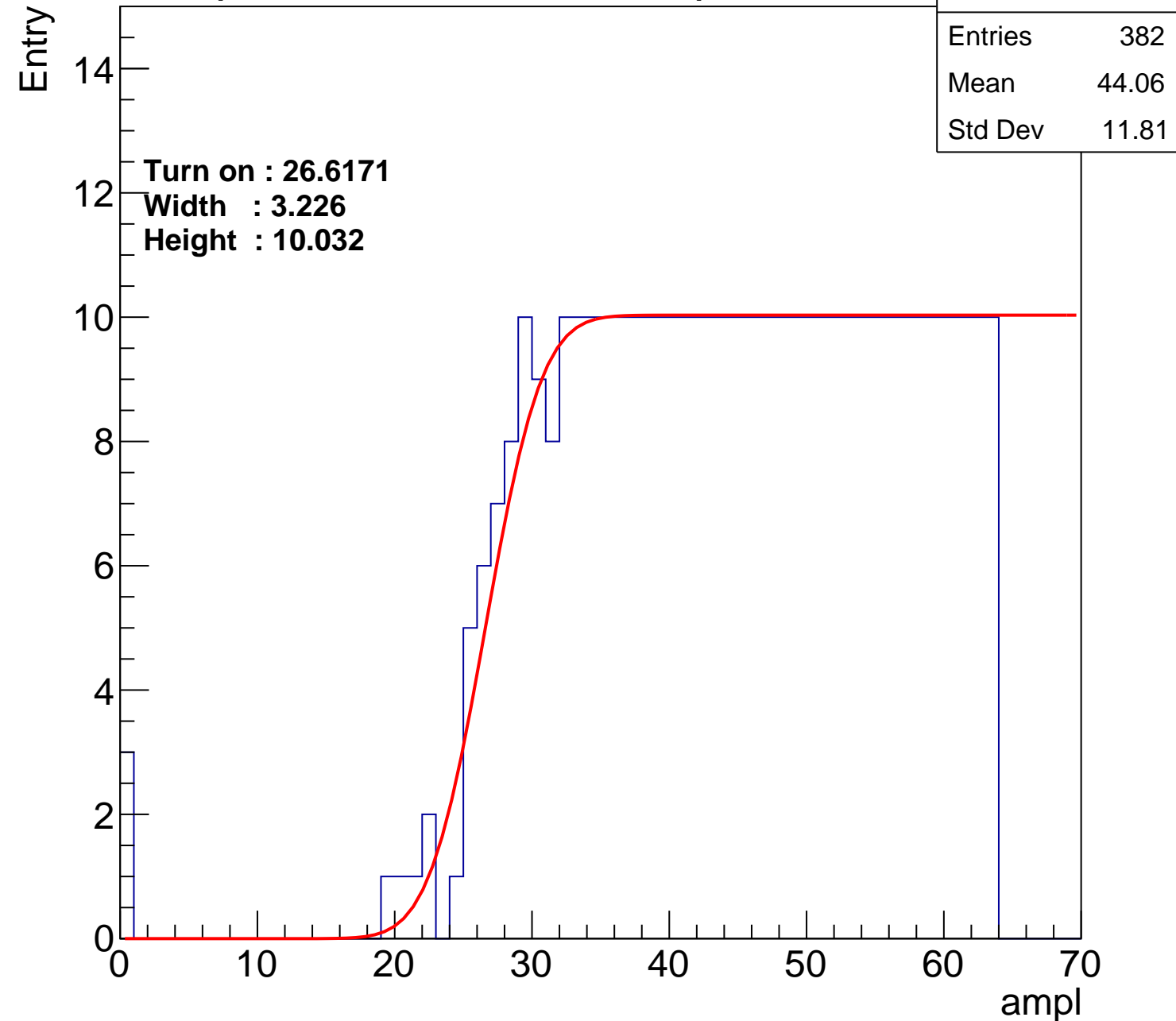
Width : 3.226

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch95

calib_packv5_042523_0143.root, FC#7, port C2

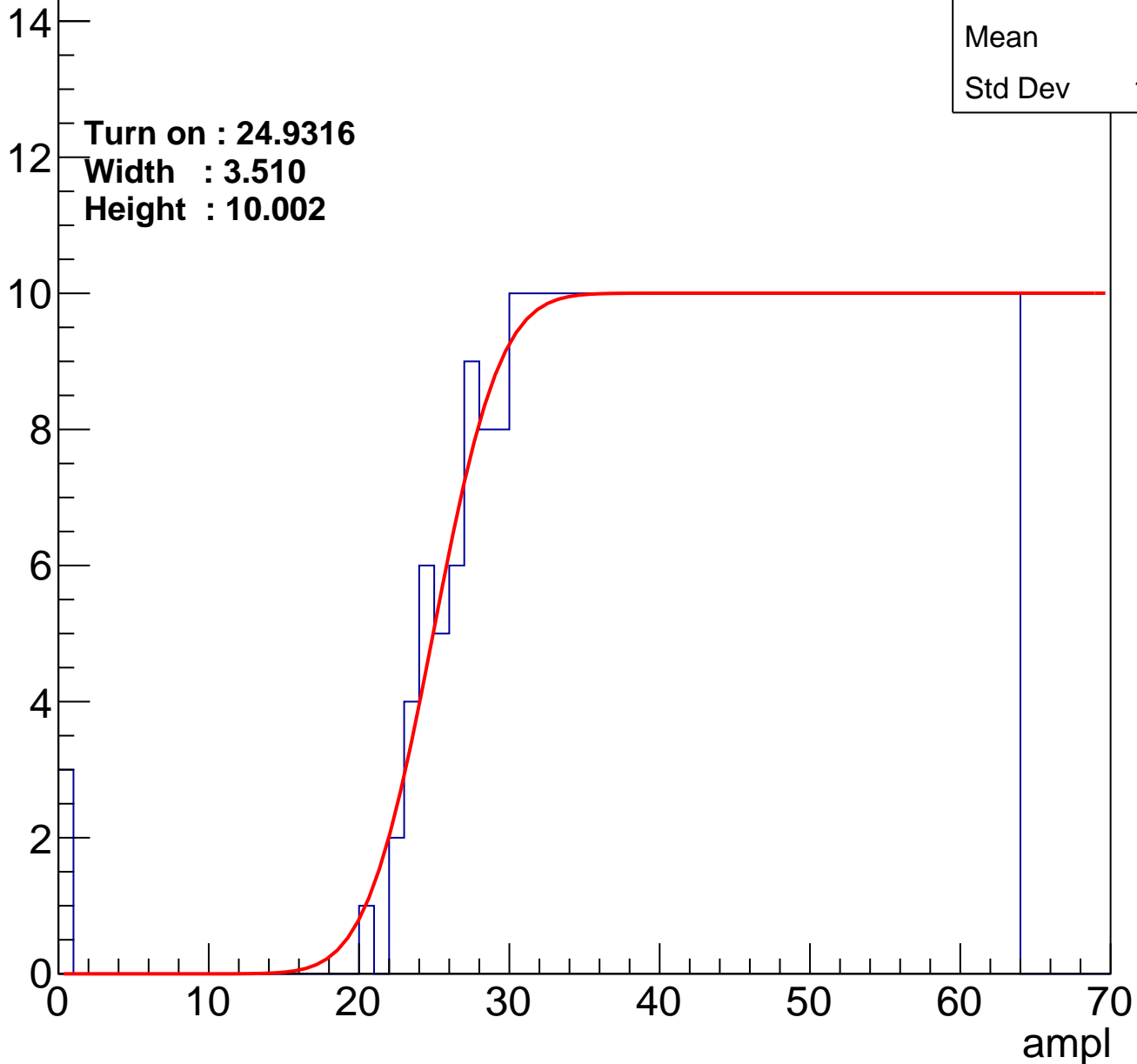
Entries	392
Mean	43.6
Std Dev	12.01

Turn on : 24.9316

Width : 3.510

Height : 10.002

Entry



B1L103S, U24-ch96

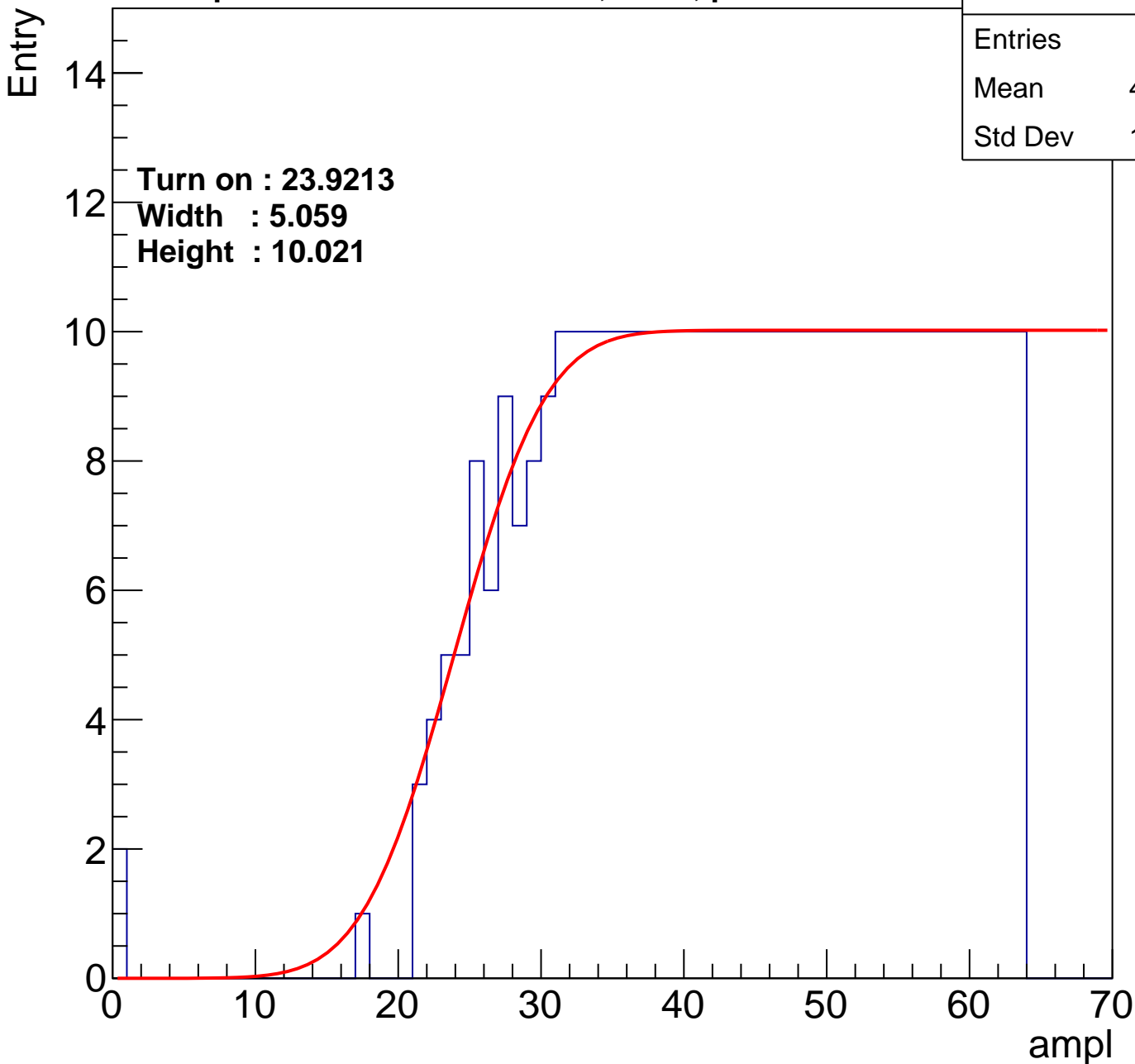
calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.35
Std Dev	12.07

Turn on : 23.9213

Width : 5.059

Height : 10.021



B1L103S, U24-ch97

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.79
Std Dev	11.98

Turn on : 25.9128

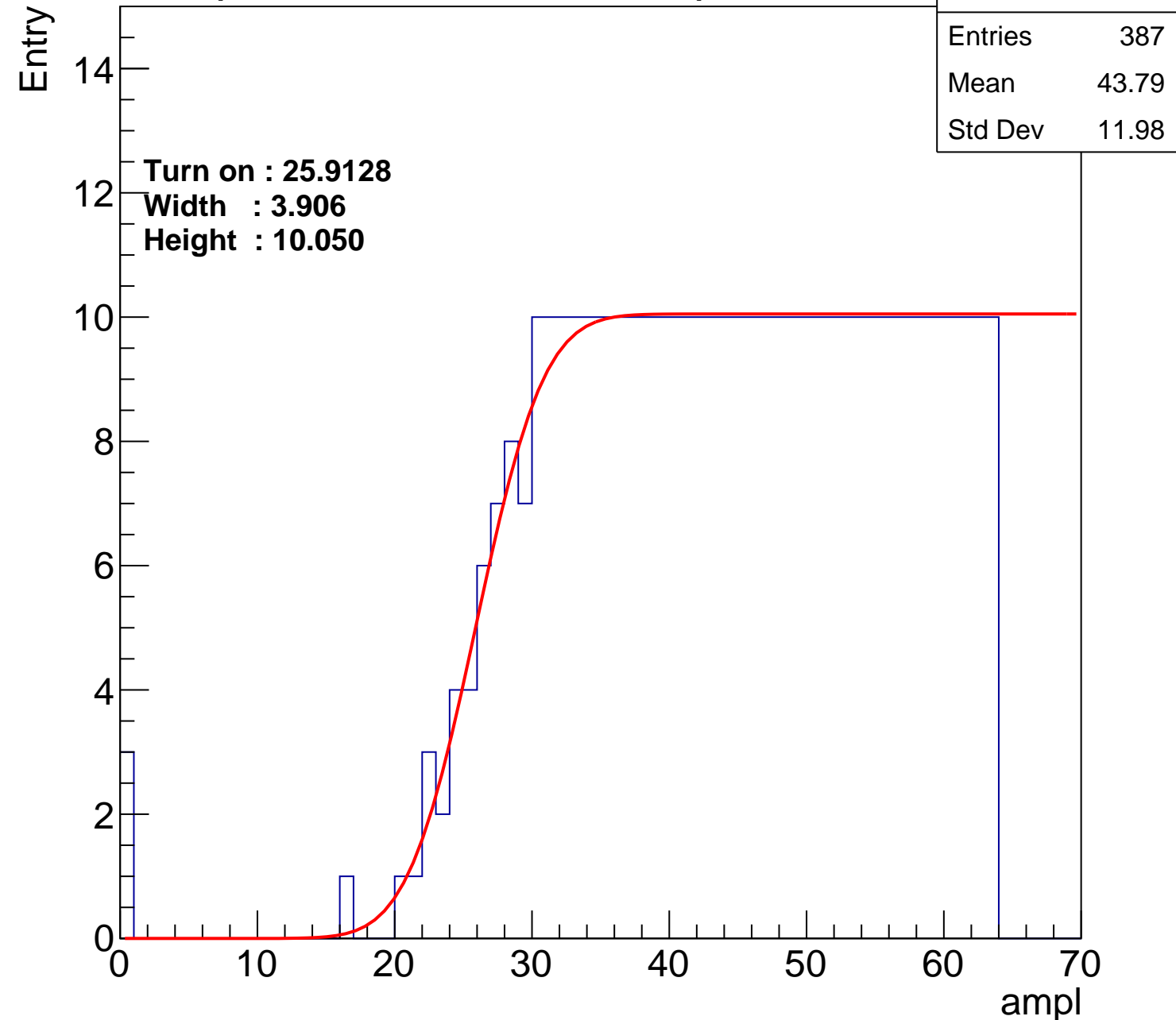
Width : 3.906

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.8
Std Dev	11.88

Turn on : 25.7137

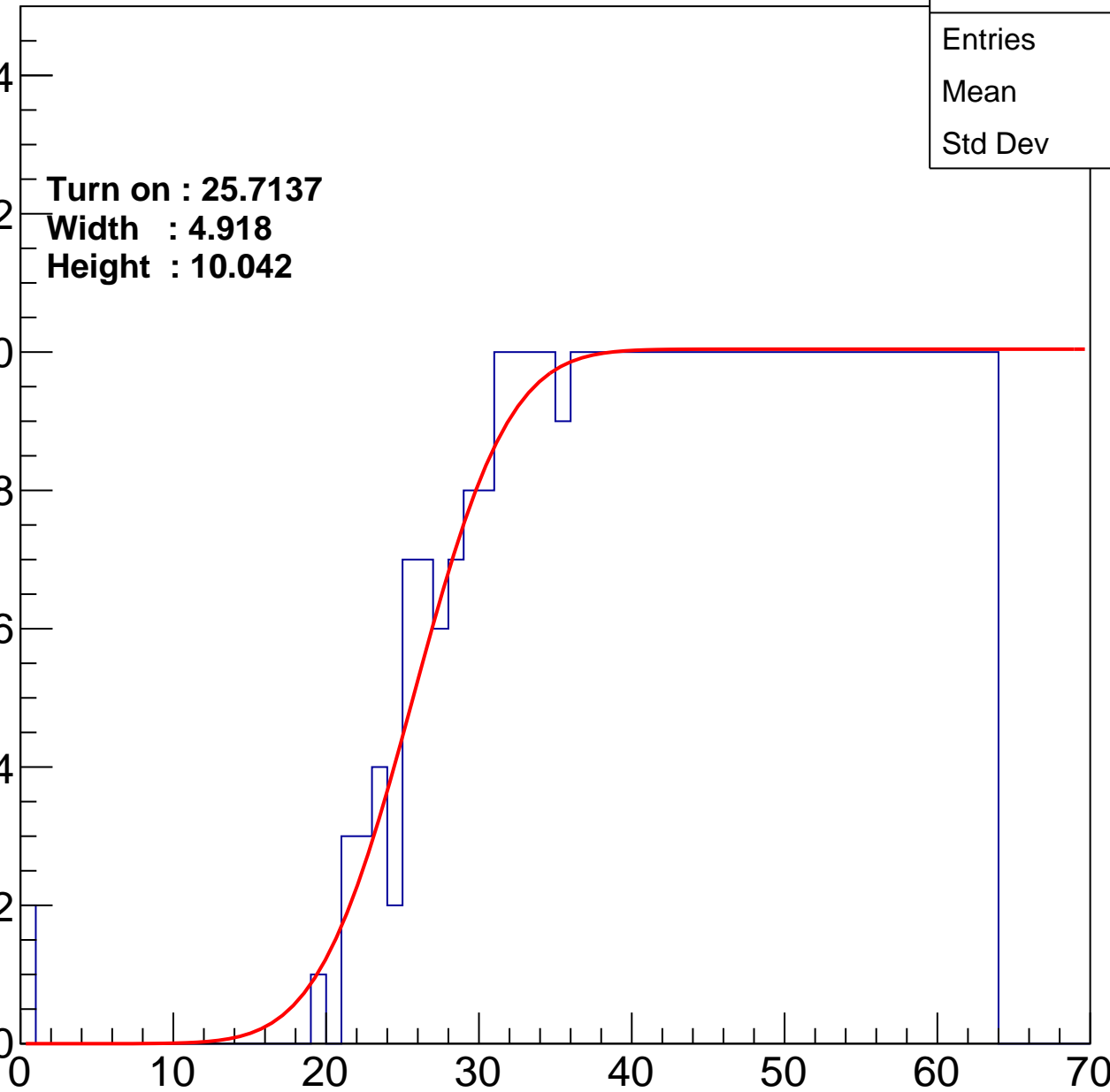
Width : 4.918

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.68
Std Dev	12.38

Turn on : 26.3686

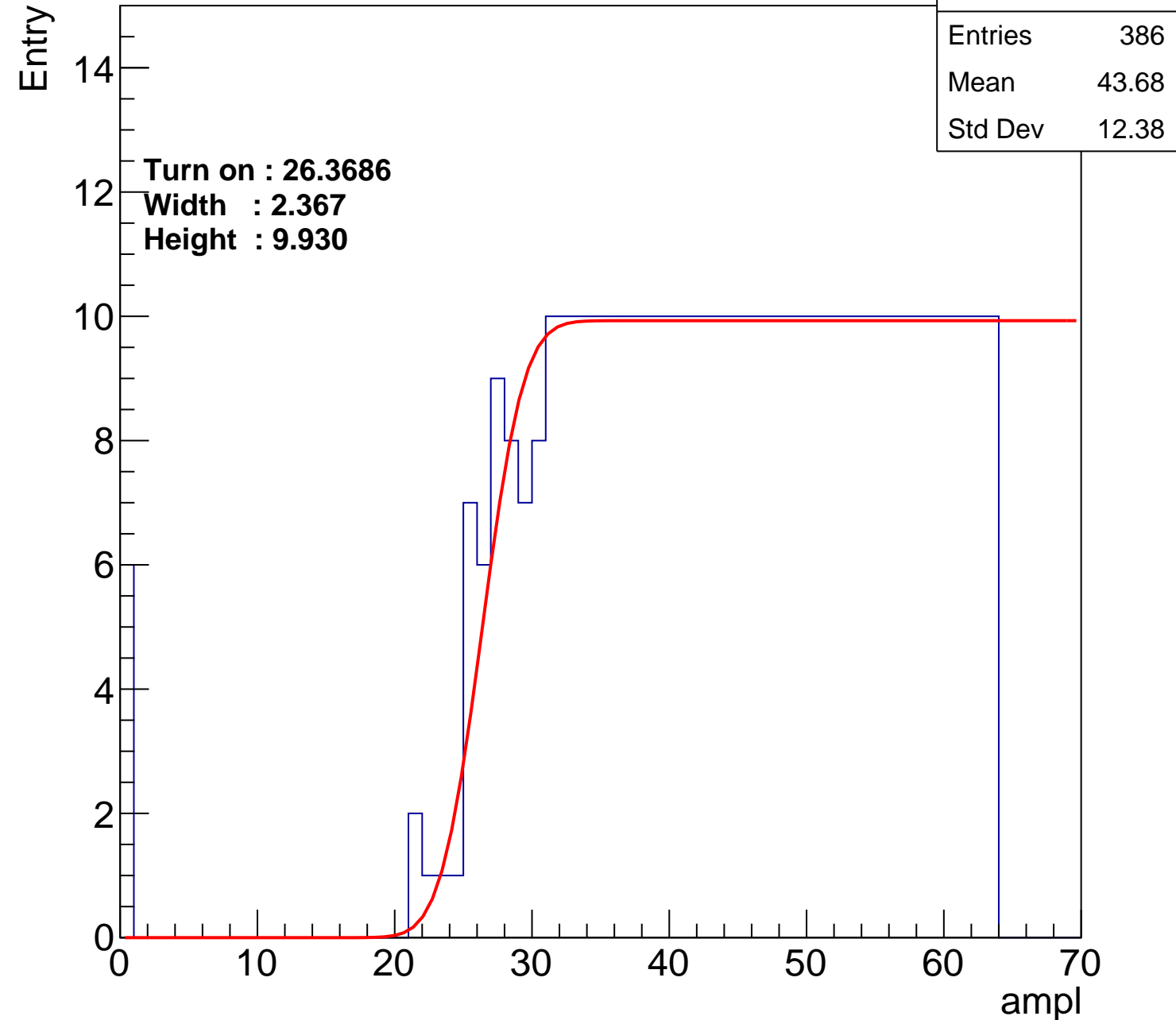
Width : 2.367

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch100

calib_packv5_042523_0143.root, FC#7, port C2

Entries	411
Mean	42.59
Std Dev	12.66

Turn on : 24.7640

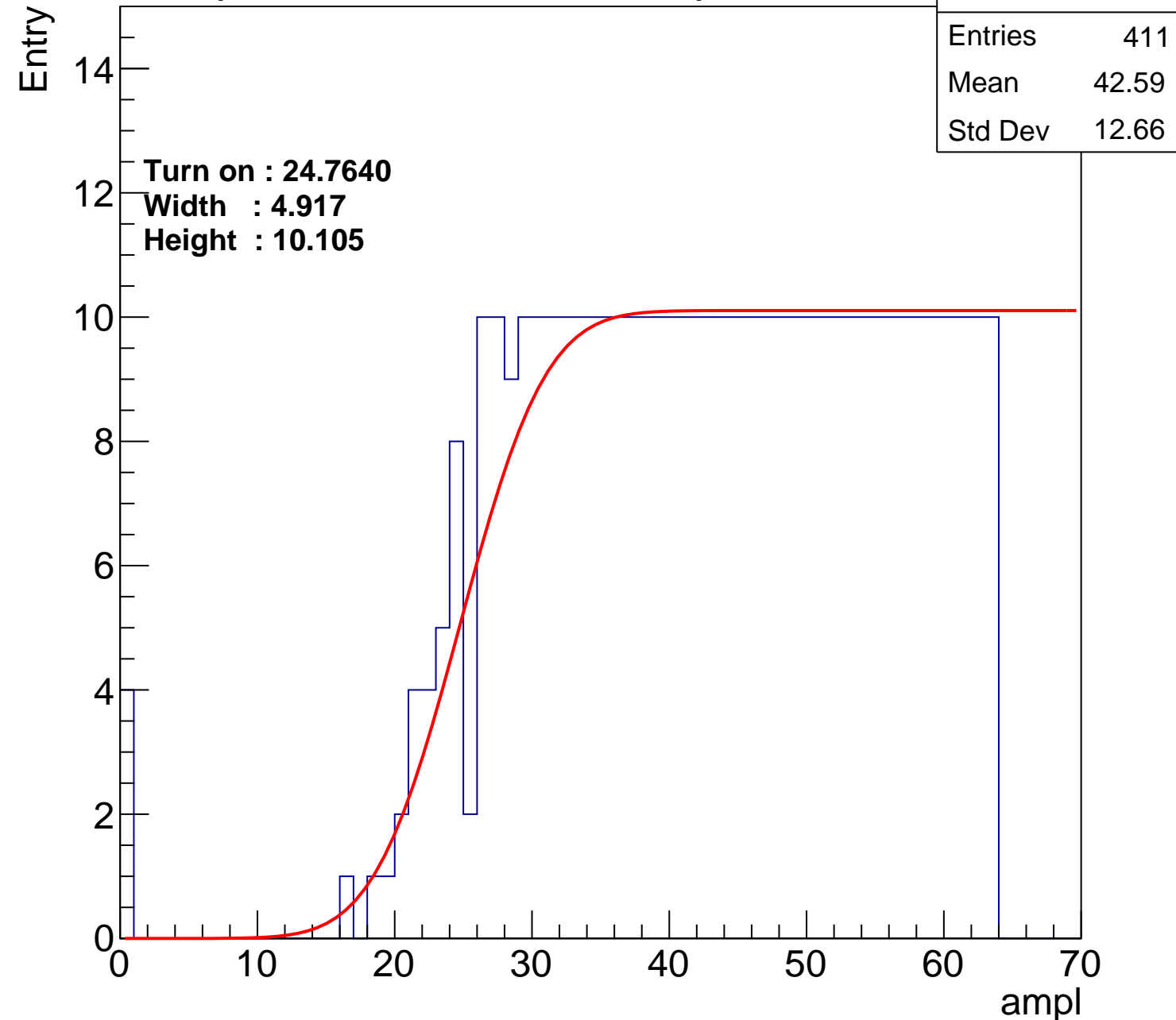
Width : 4.917

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch101

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.45
Std Dev	11.35

Turn on : 28.4207

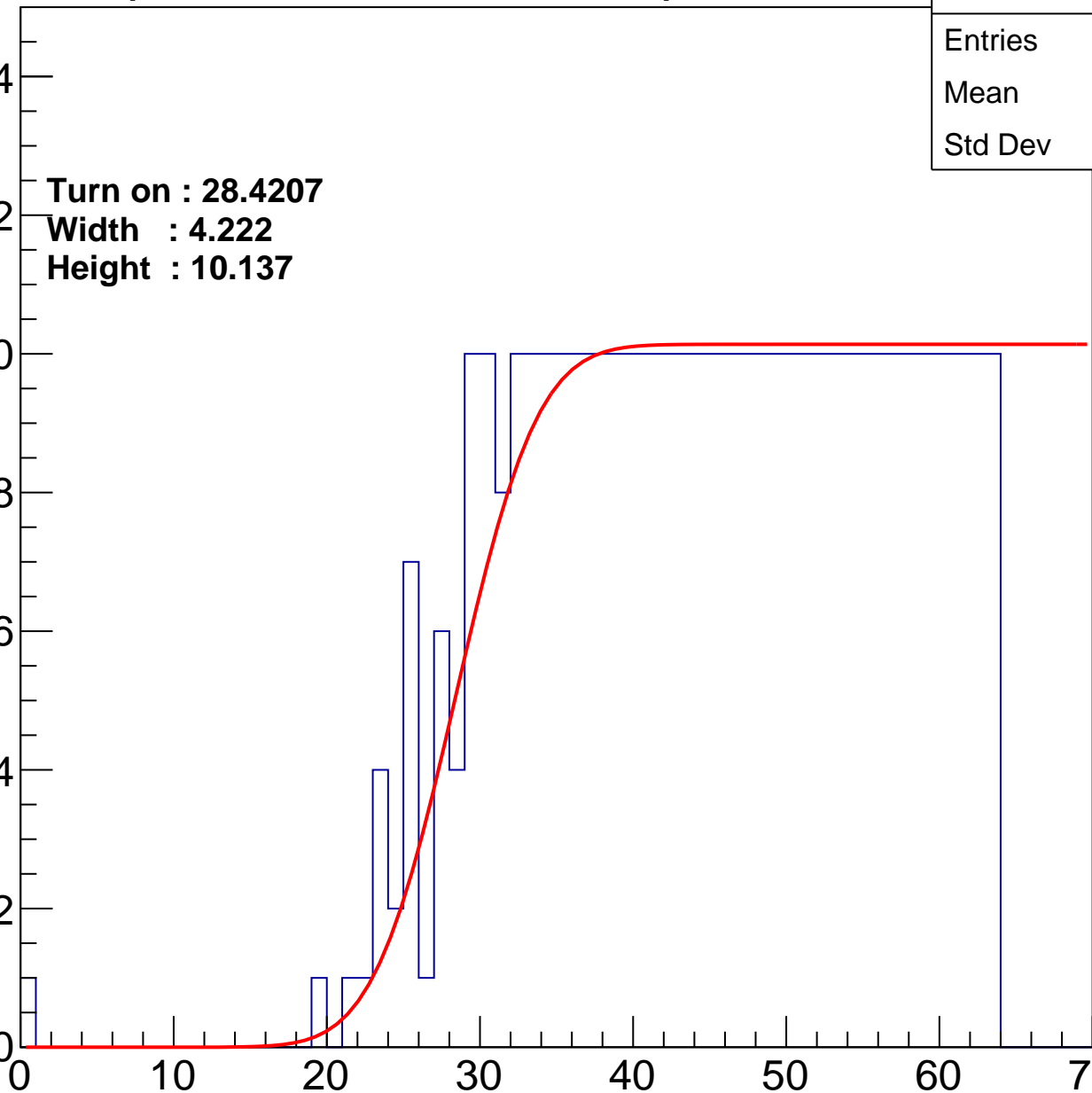
Width : 4.222

Height : 10.137

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch102

calib_packv5_042523_0143.root, FC#7, port C2

Entries	401
Mean	43.09
Std Dev	12.39

Turn on : 25.1843

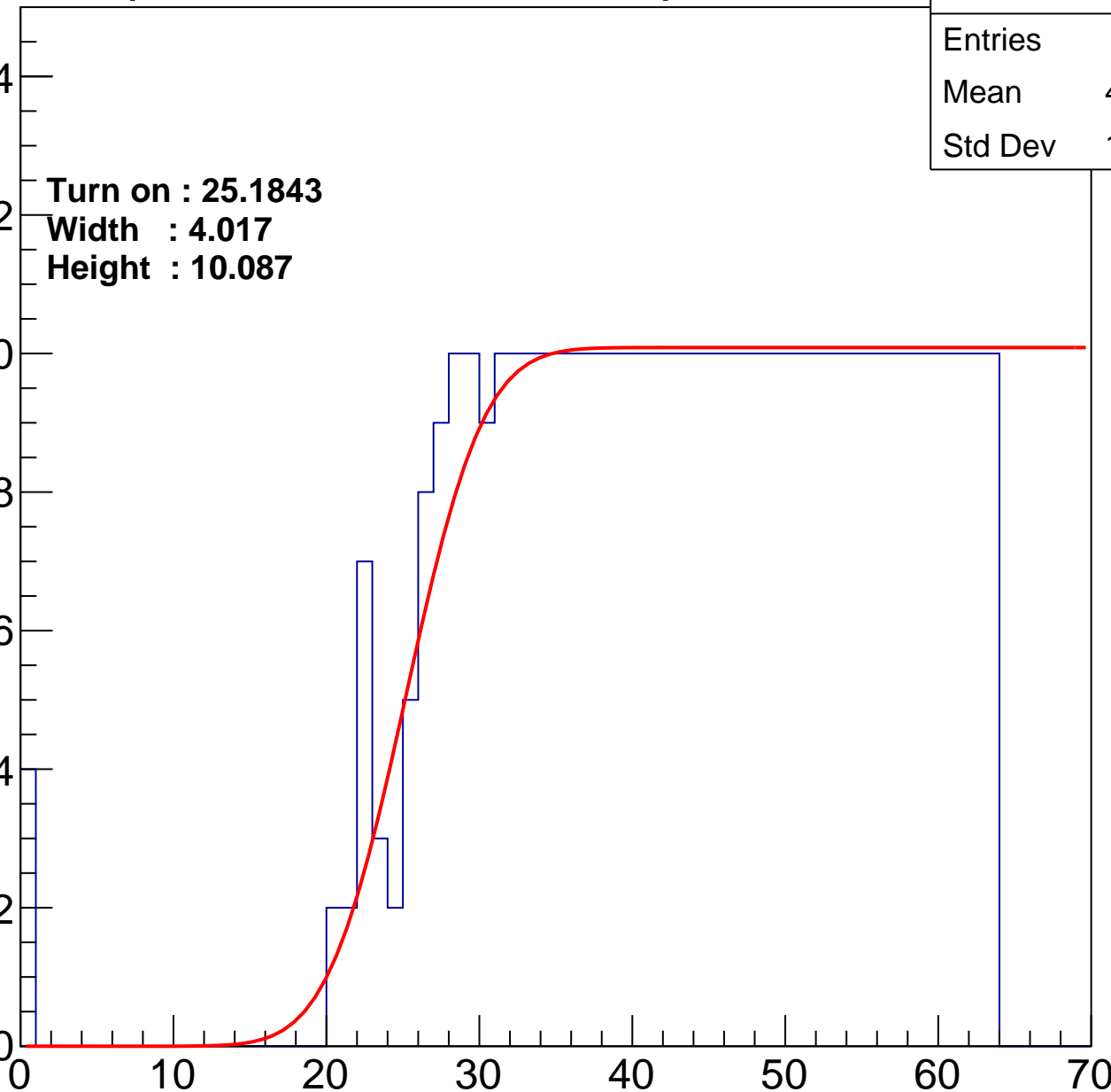
Width : 4.017

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.11
Std Dev	11.65

Turn on : 26.3934

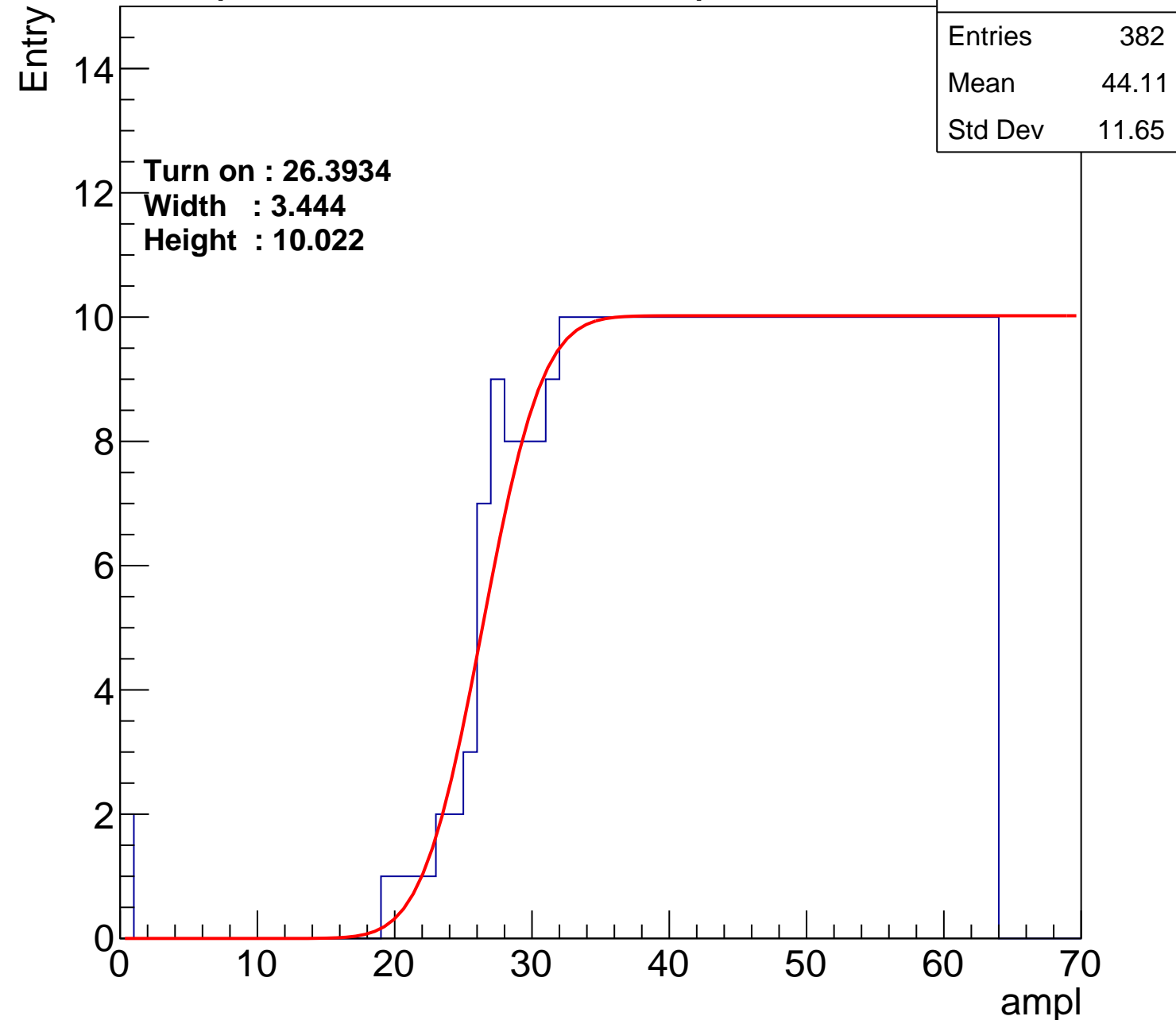
Width : 3.444

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 25.4856

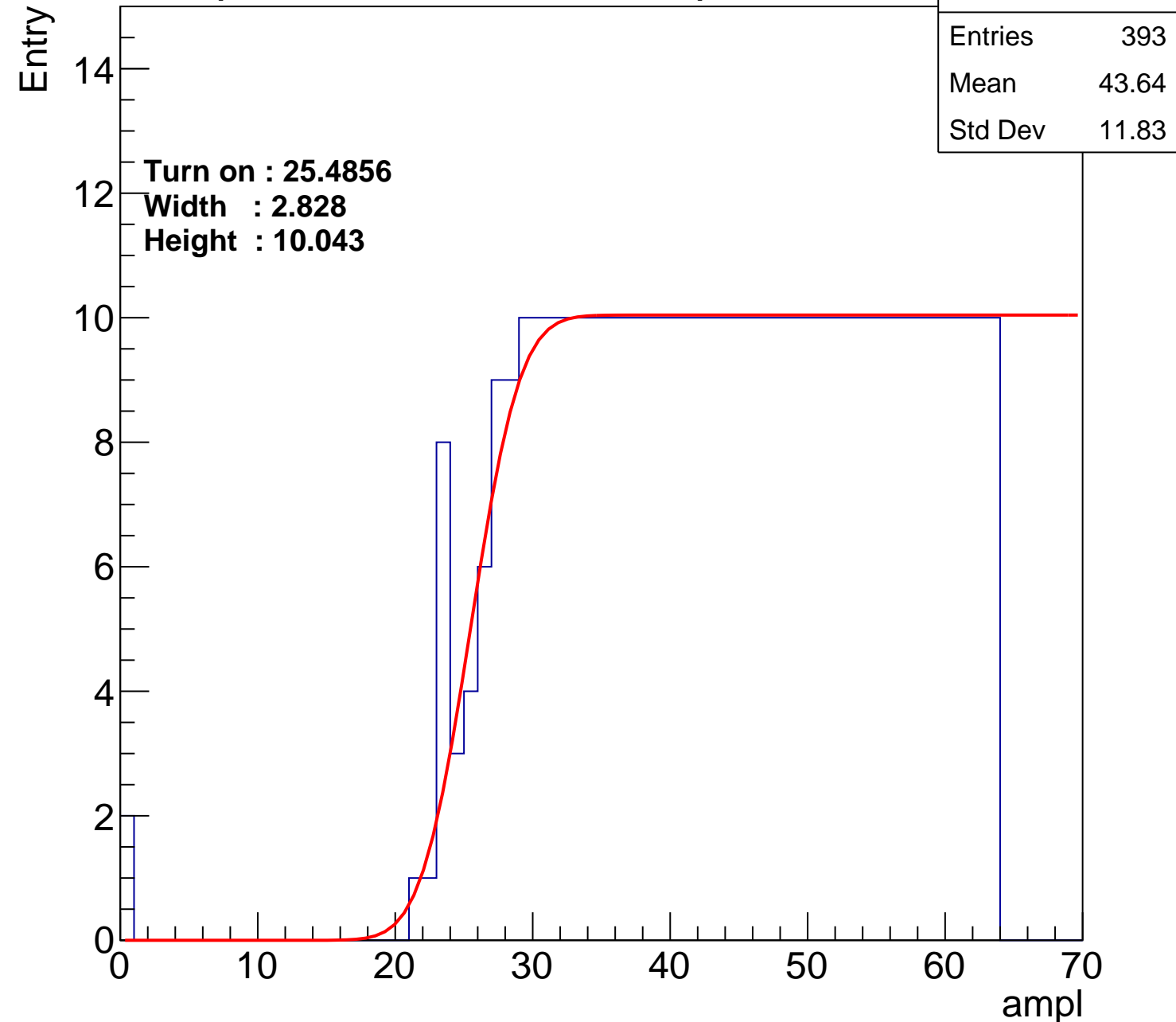
Width : 2.828

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch105

calib_packv5_042523_0143.root, FC#7, port C2

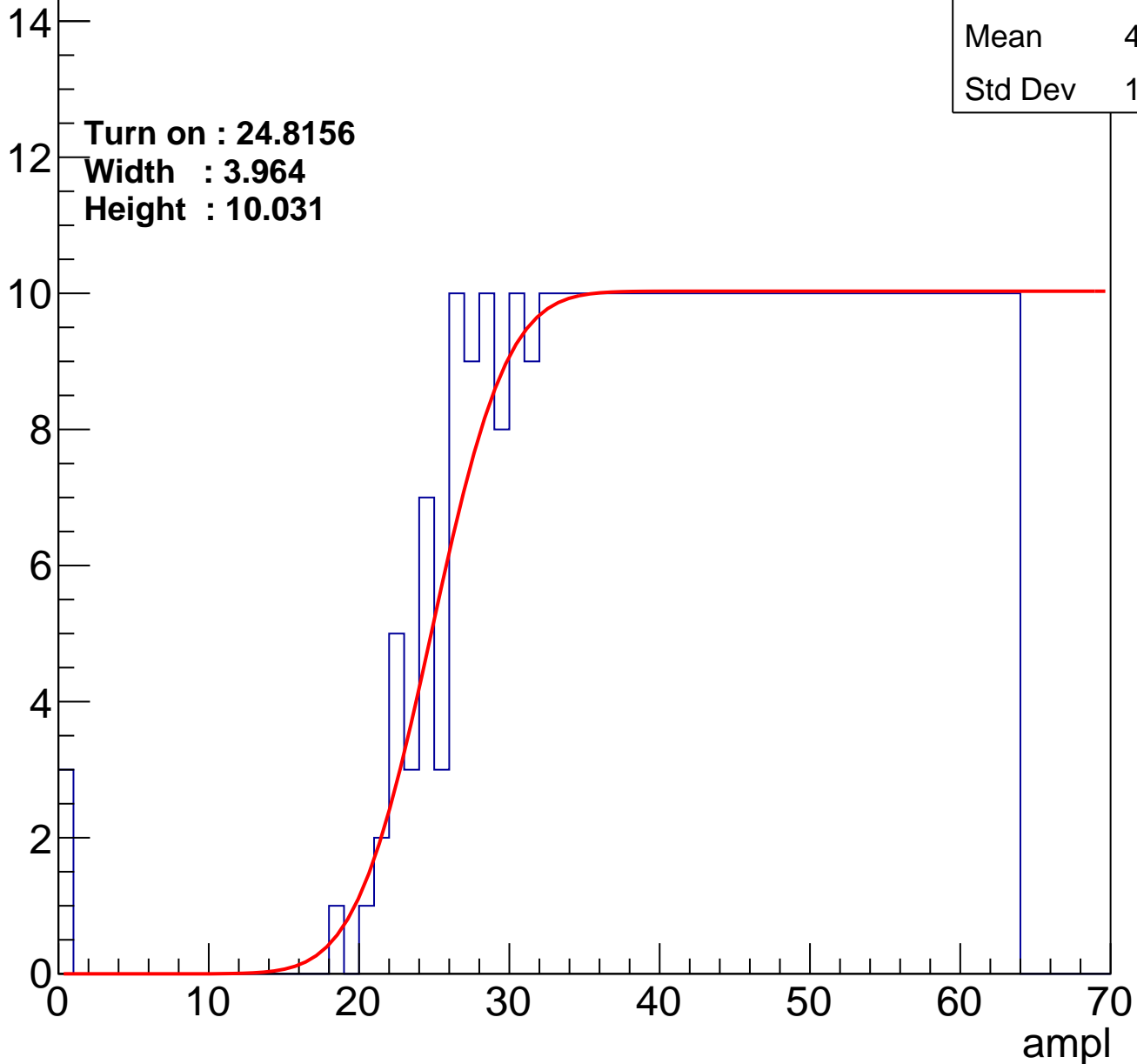
Entries	401
Mean	43.13
Std Dev	12.27

Turn on : 24.8156

Width : 3.964

Height : 10.031

Entry



B1L103S, U24-ch106

calib_packv5_042523_0143.root, FC#7, port C2

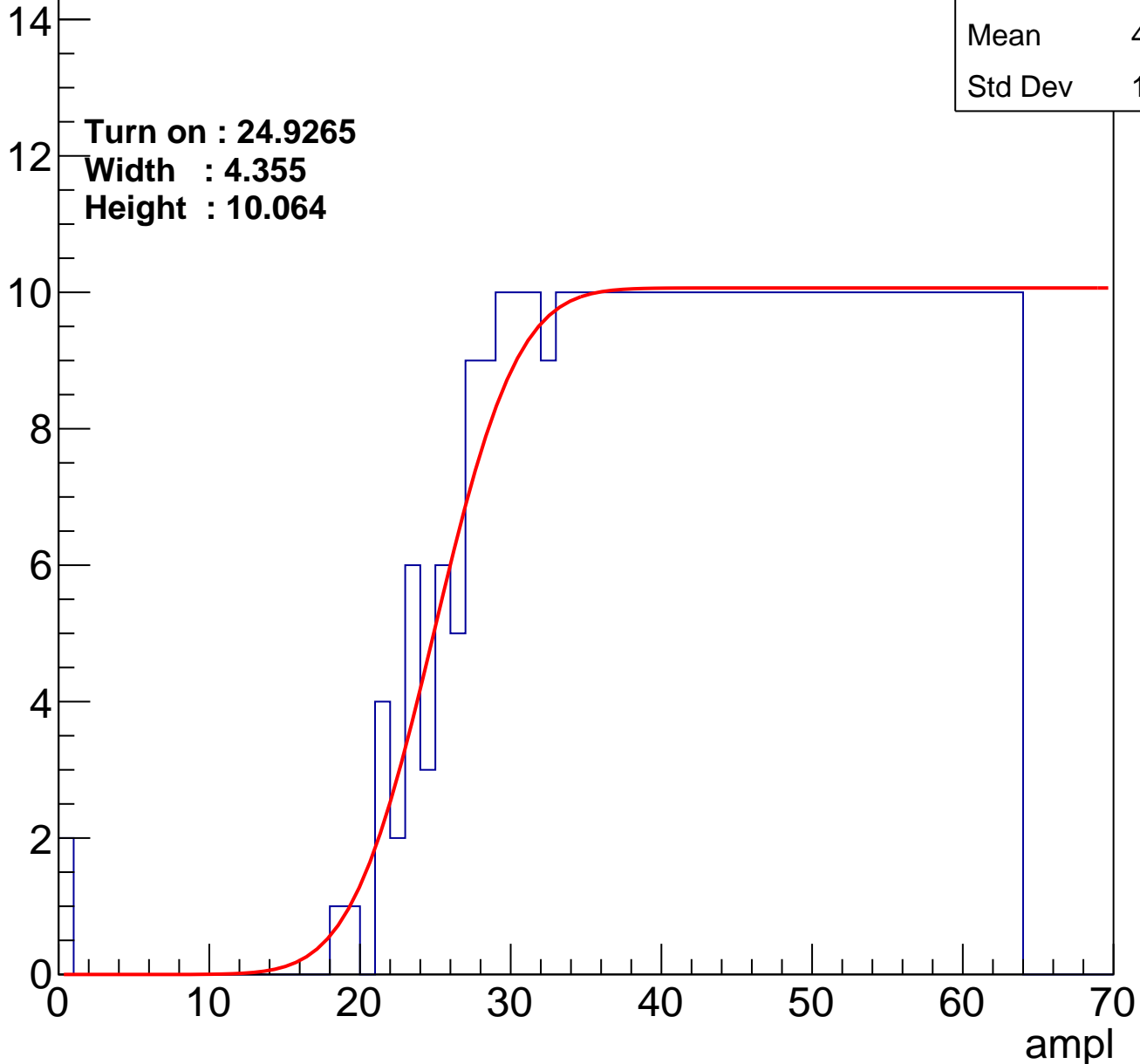
Entries	397
Mean	43.37
Std Dev	12.04

Turn on : 24.9265

Width : 4.355

Height : 10.064

Entry



B1L103S, U24-ch107

calib_packv5_042523_0143.root, FC#7, port C2

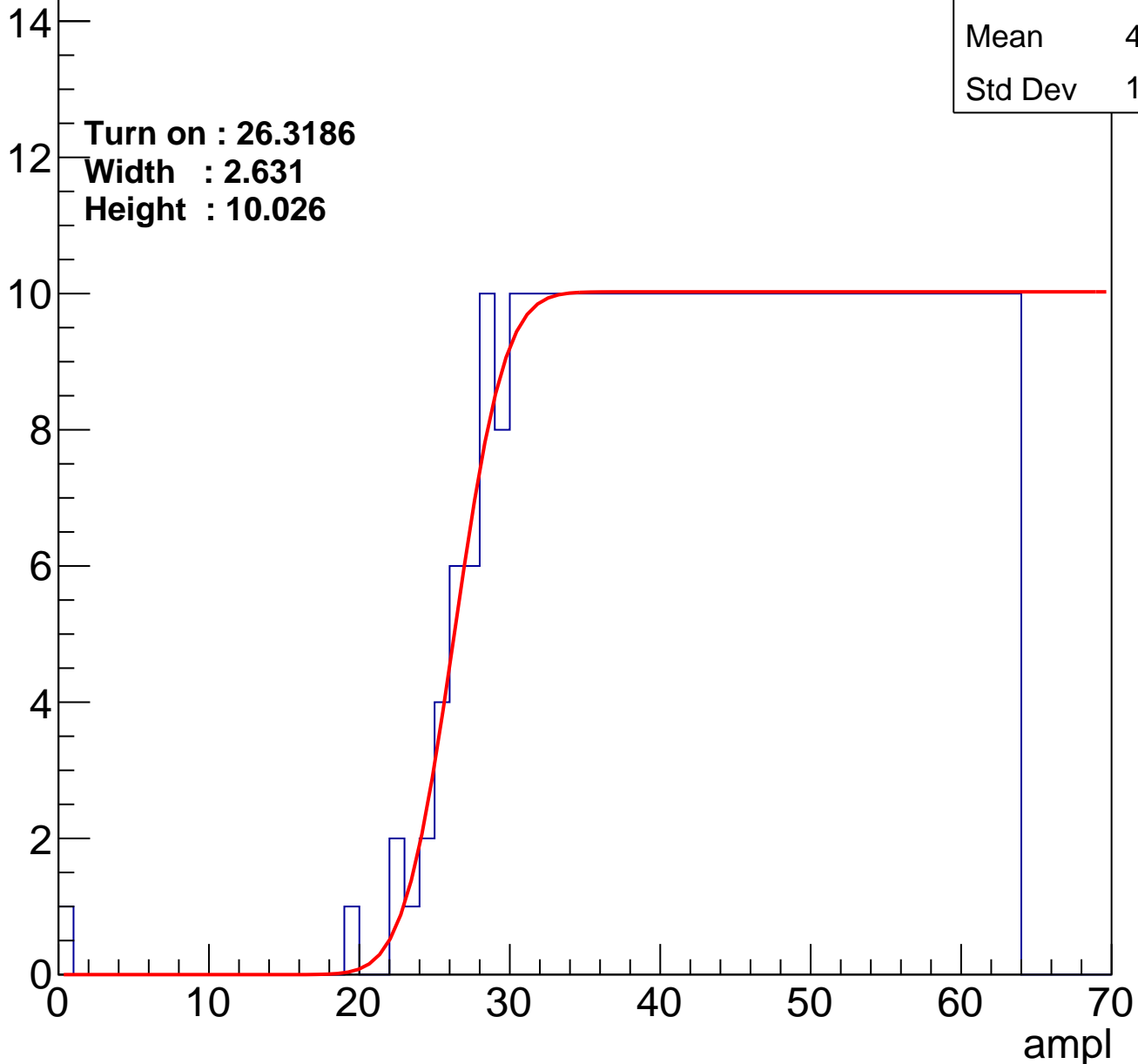
Entries	381
Mean	44.29
Std Dev	11.35

Turn on : 26.3186

Width : 2.631

Height : 10.026

Entry



B1L103S, U24-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.62
Std Dev	11.97

Turn on : 25.5630

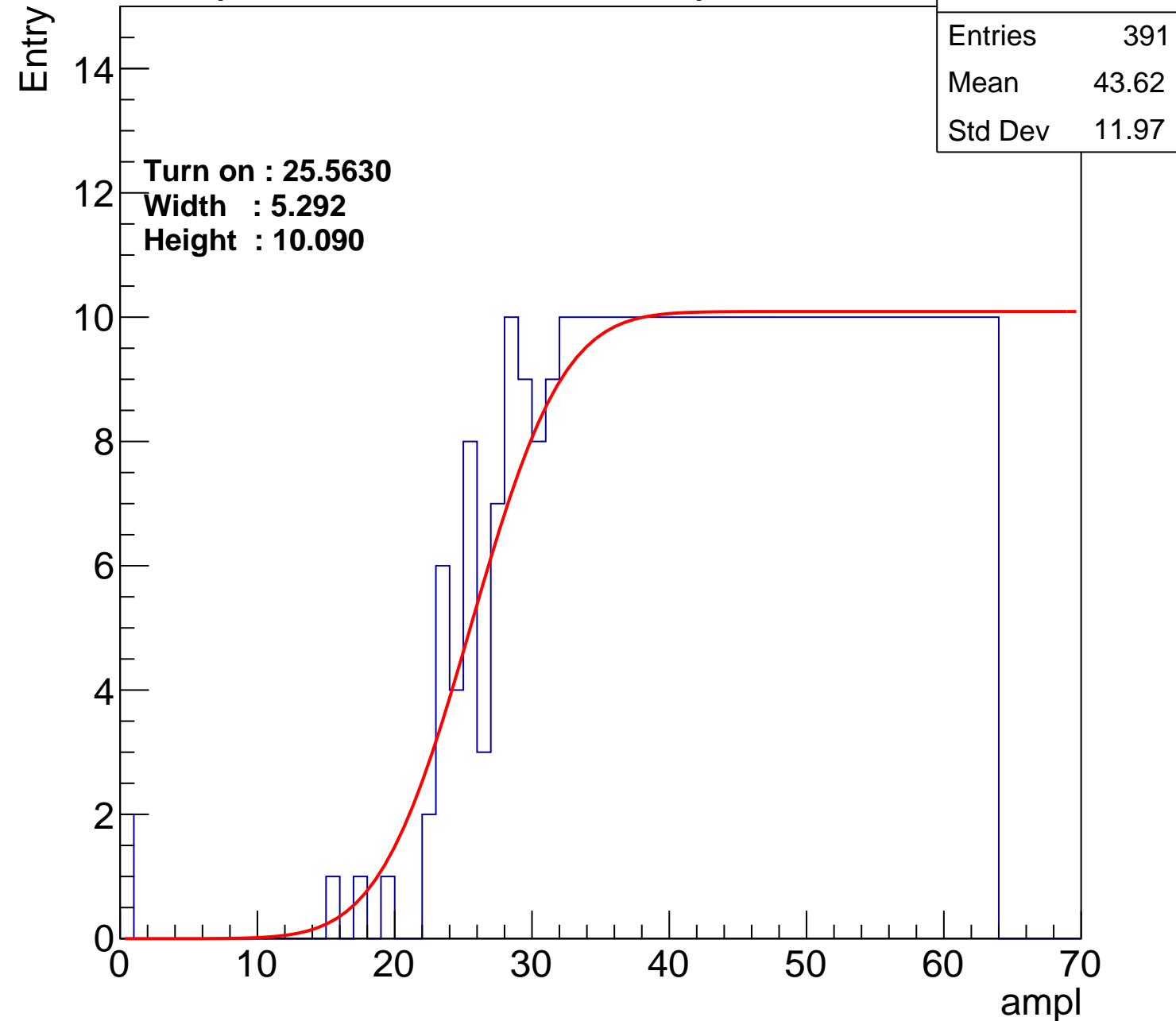
Width : 5.292

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch109

calib_packv5_042523_0143.root, FC#7, port C2

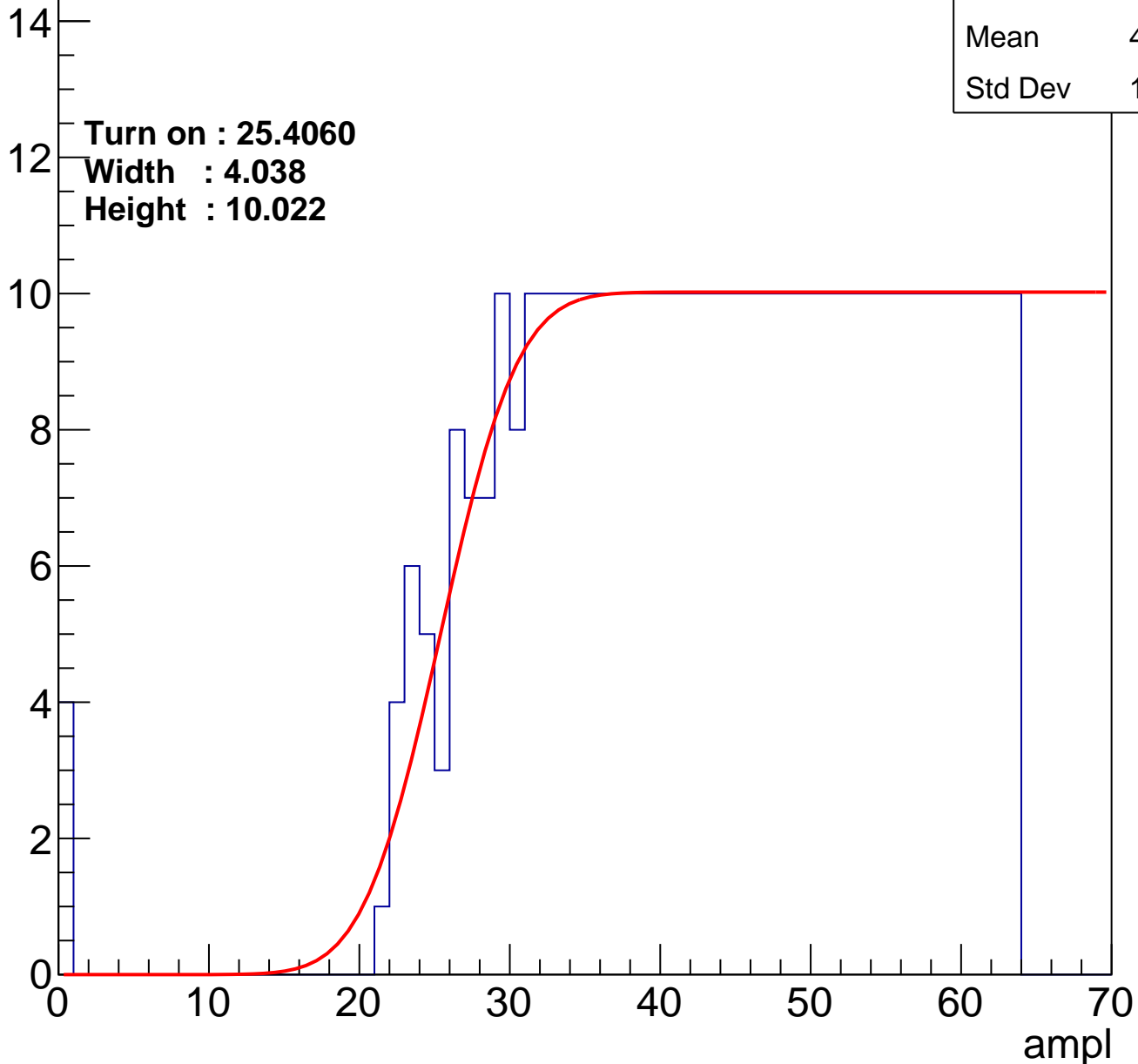
Entries	393
Mean	43.45
Std Dev	12.25

Turn on : 25.4060

Width : 4.038

Height : 10.022

Entry



B1L103S, U24-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.85
Std Dev	12.05

Turn on : 26.6324

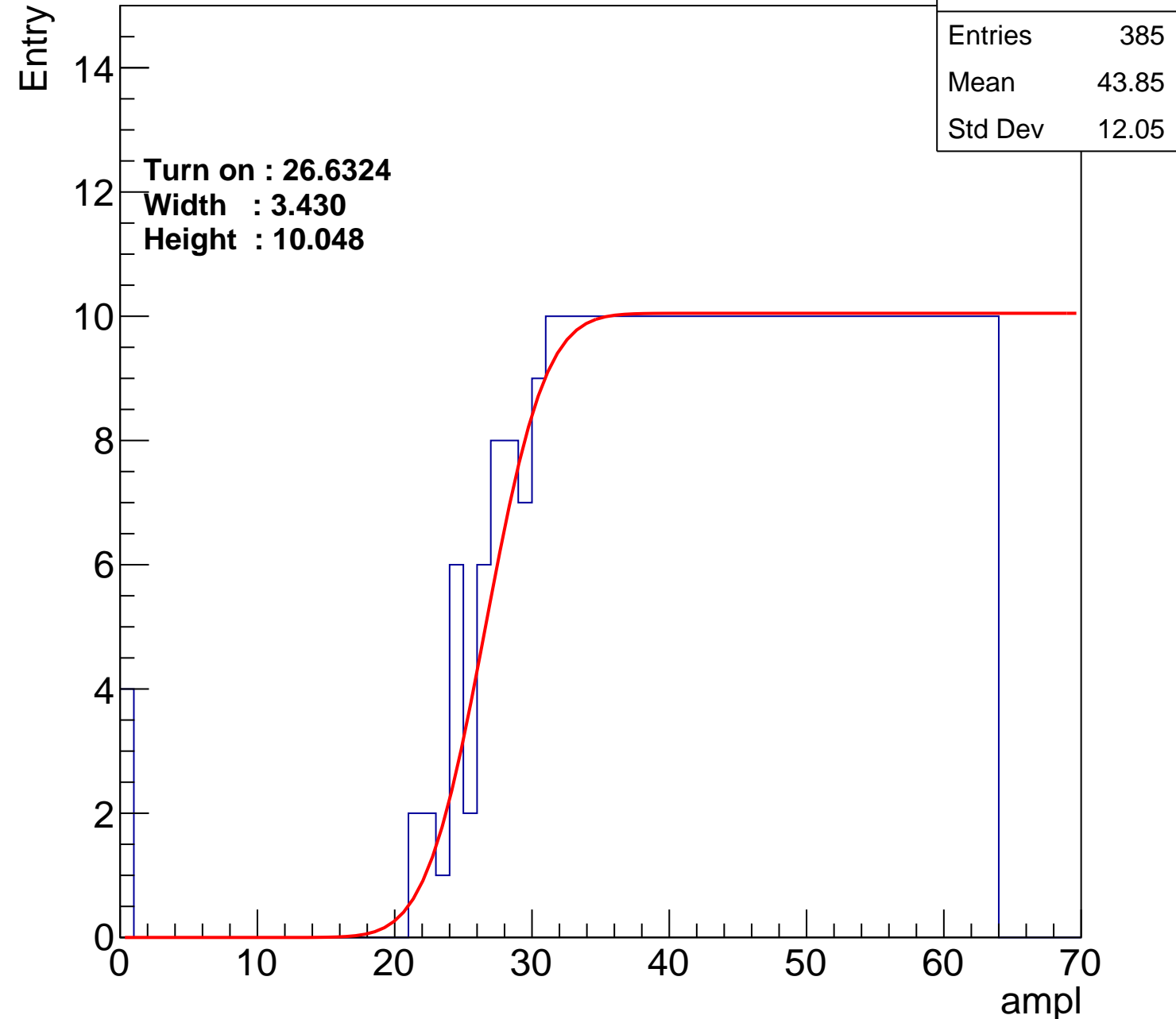
Width : 3.430

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch111

calib_packv5_042523_0143.root, FC#7, port C2

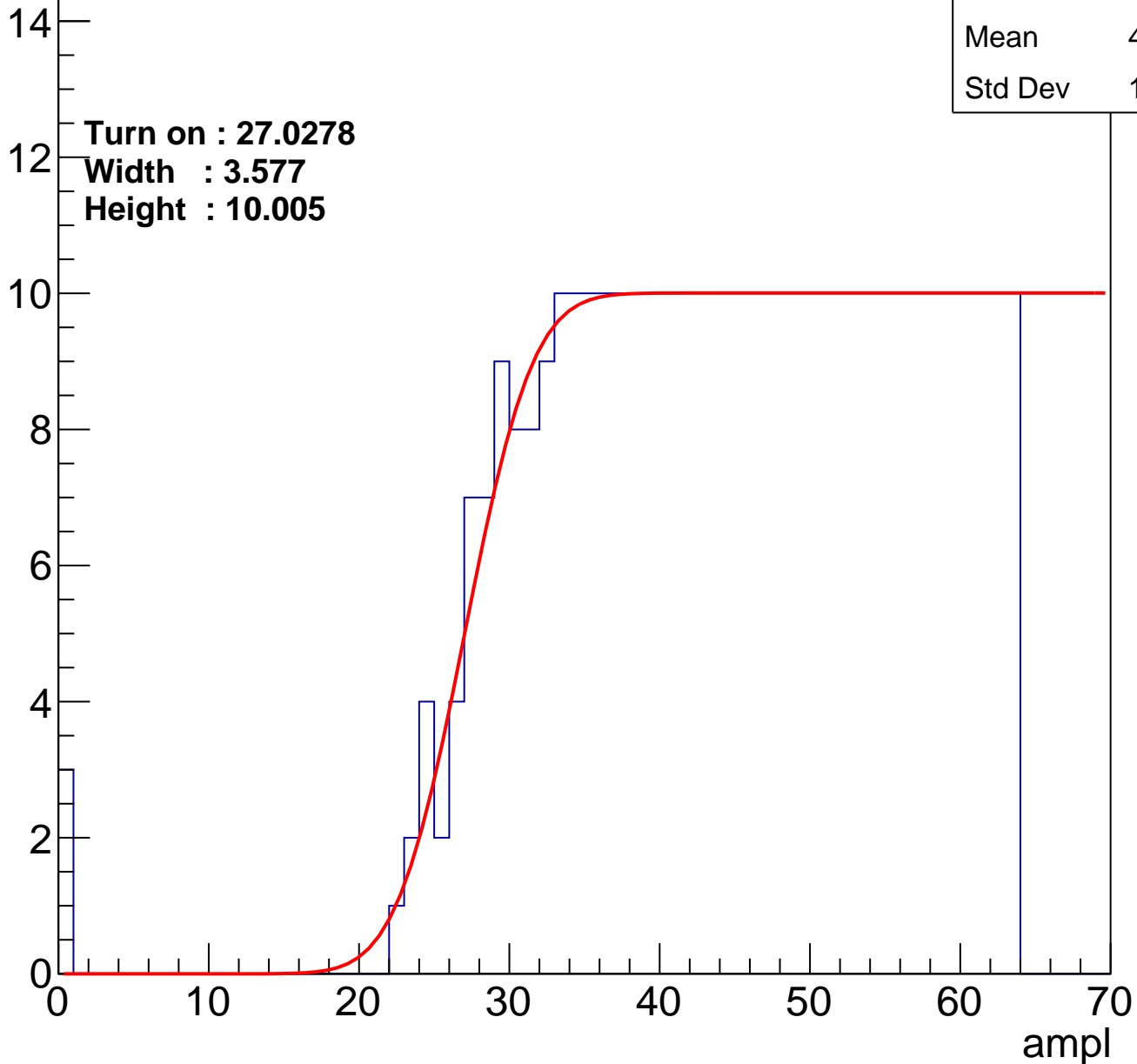
Entries	374
Mean	44.44
Std Dev	11.62

Turn on : 27.0278

Width : 3.577

Height : 10.005

Entry



B1L103S, U24-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.43
Std Dev	12.12

Turn on : 25.0973

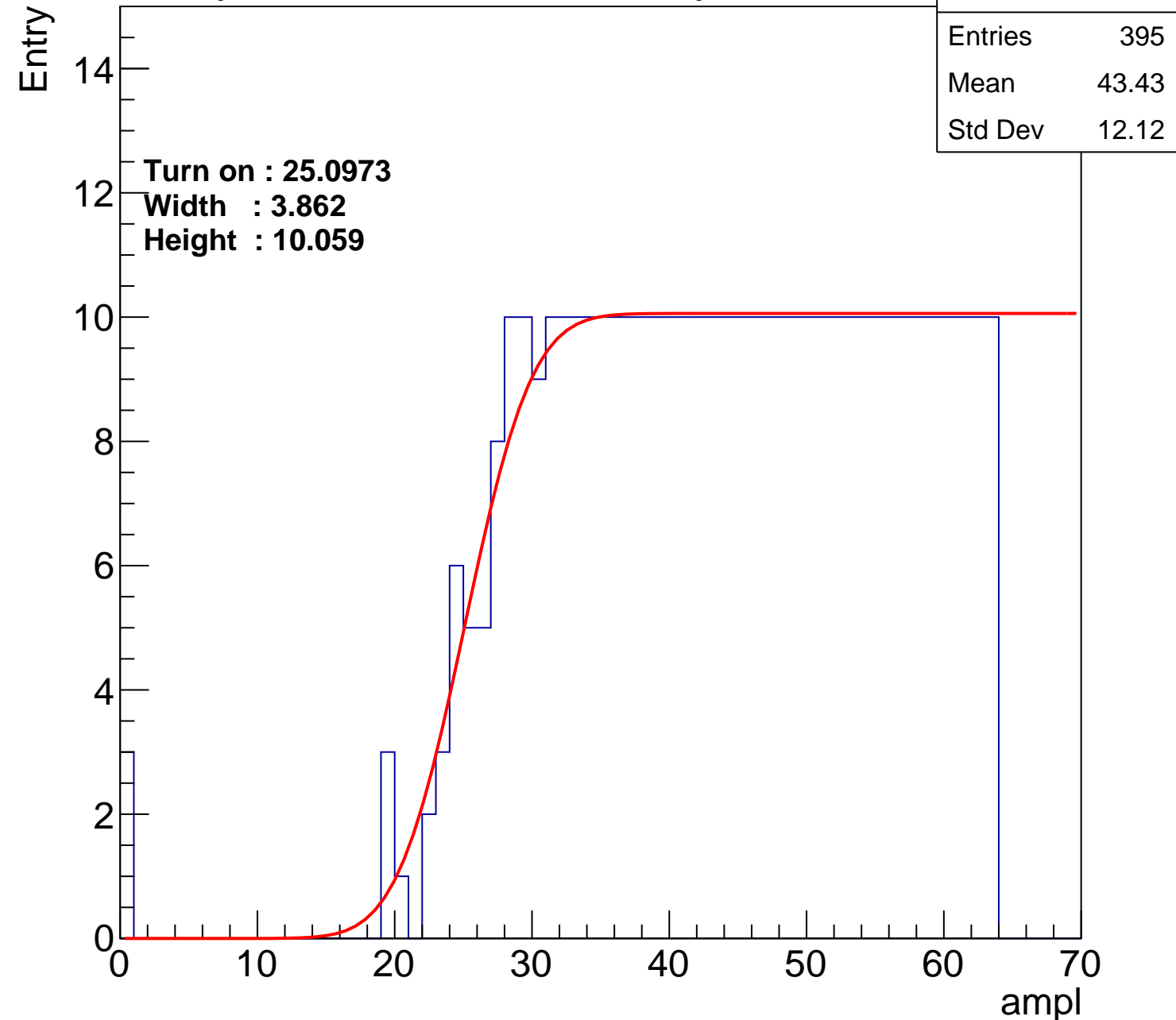
Width : 3.862

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.57
Std Dev	11.39

Turn on : 27.9876

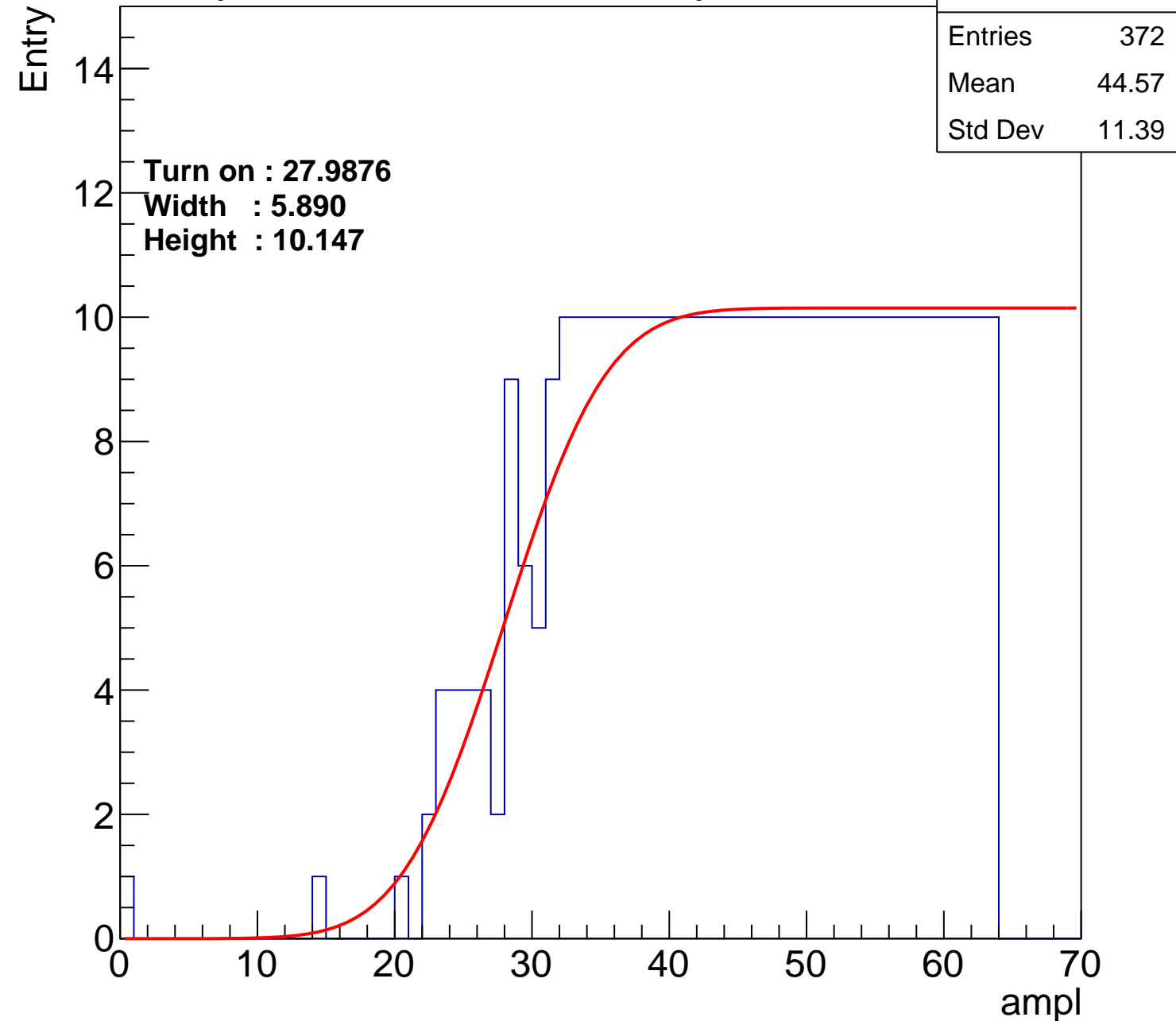
Width : 5.890

Height : 10.147

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch114

calib_packv5_042523_0143.root, FC#7, port C2

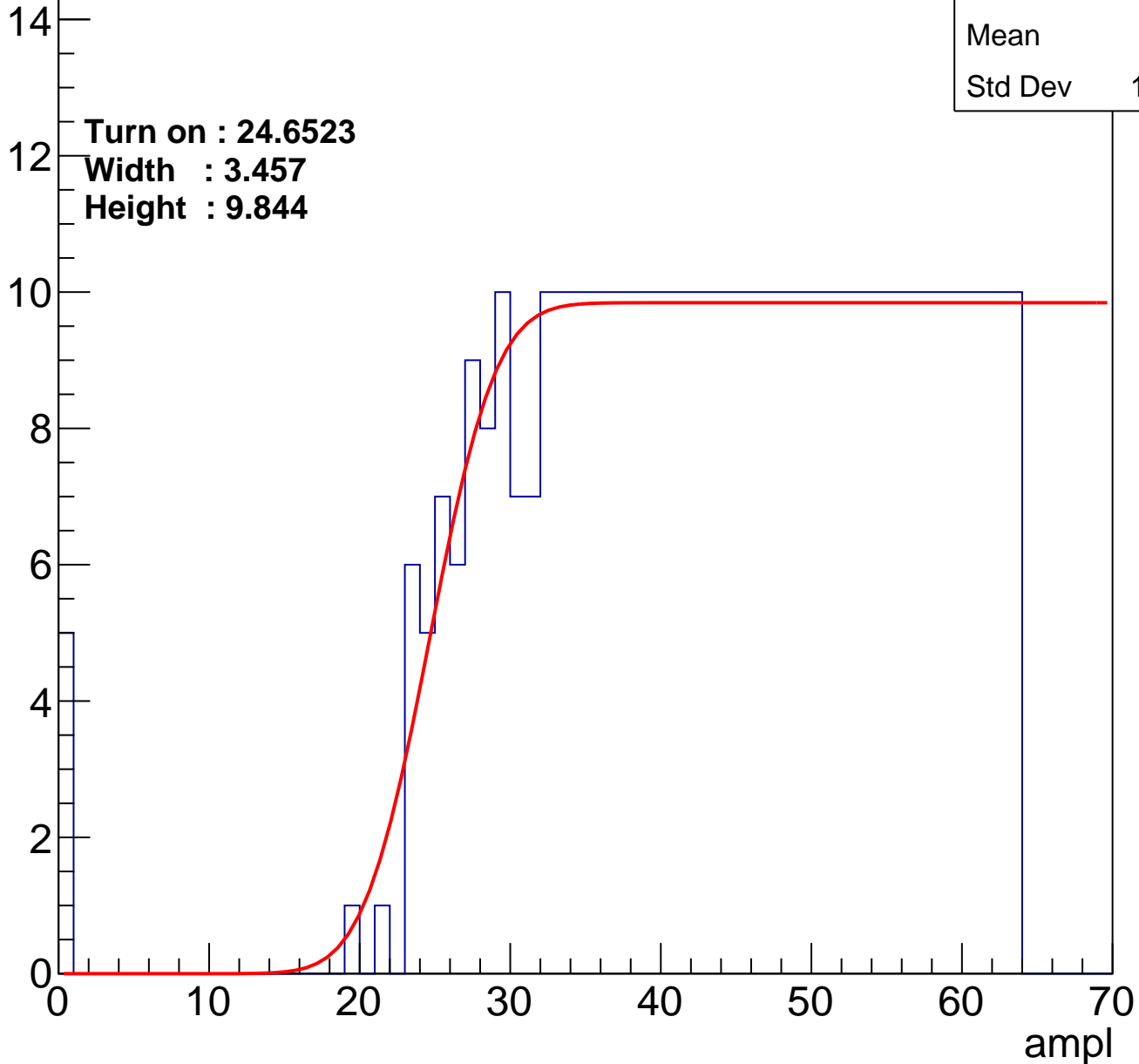
Entries	392
Mean	43.4
Std Dev	12.42

Turn on : 24.6523

Width : 3.457

Height : 9.844

Entry



B1L103S, U24-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.84
Std Dev	11.89

Turn on : 26.1994

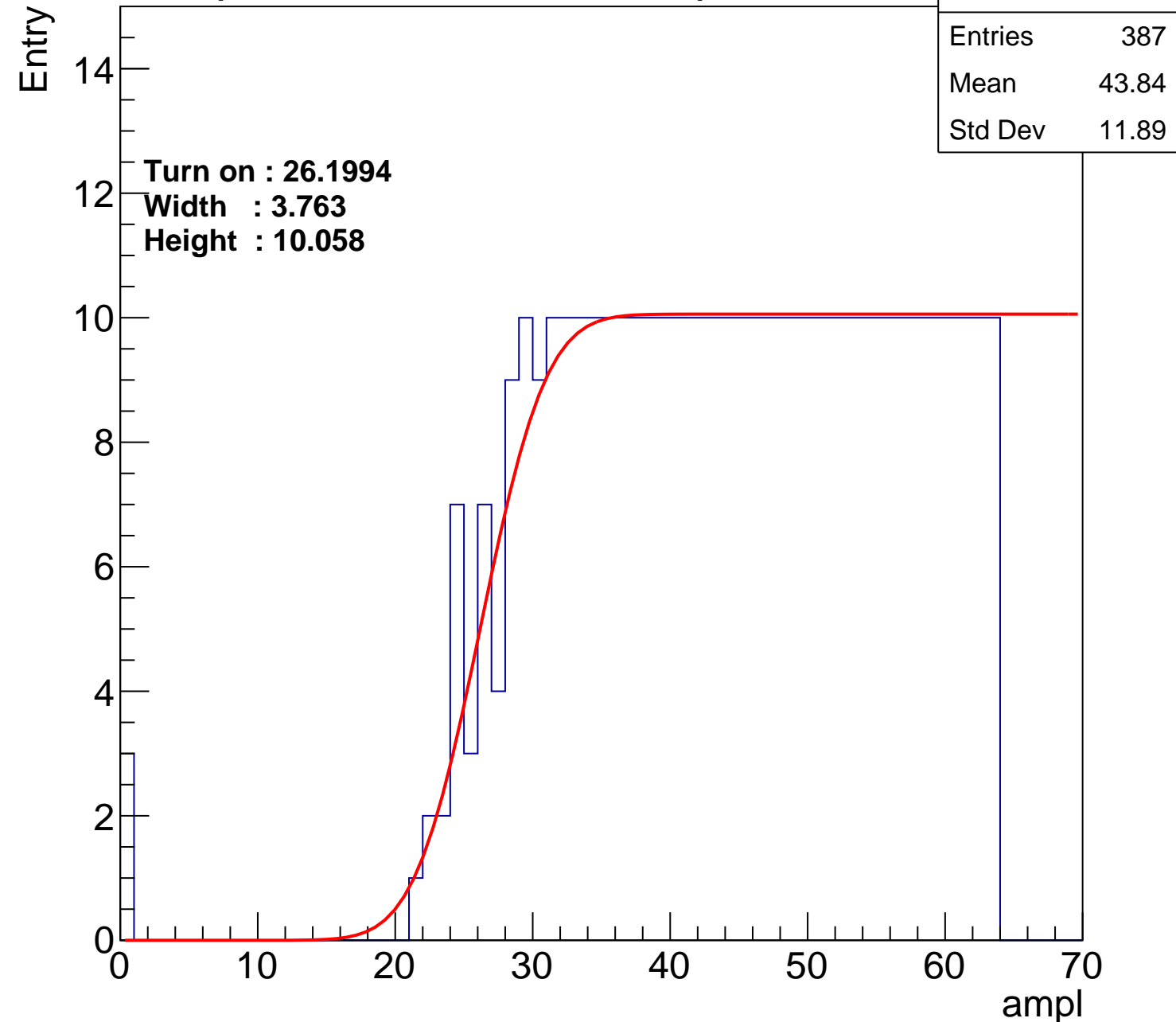
Width : 3.763

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43.06
Std Dev	12.24

Turn on : 24.4103

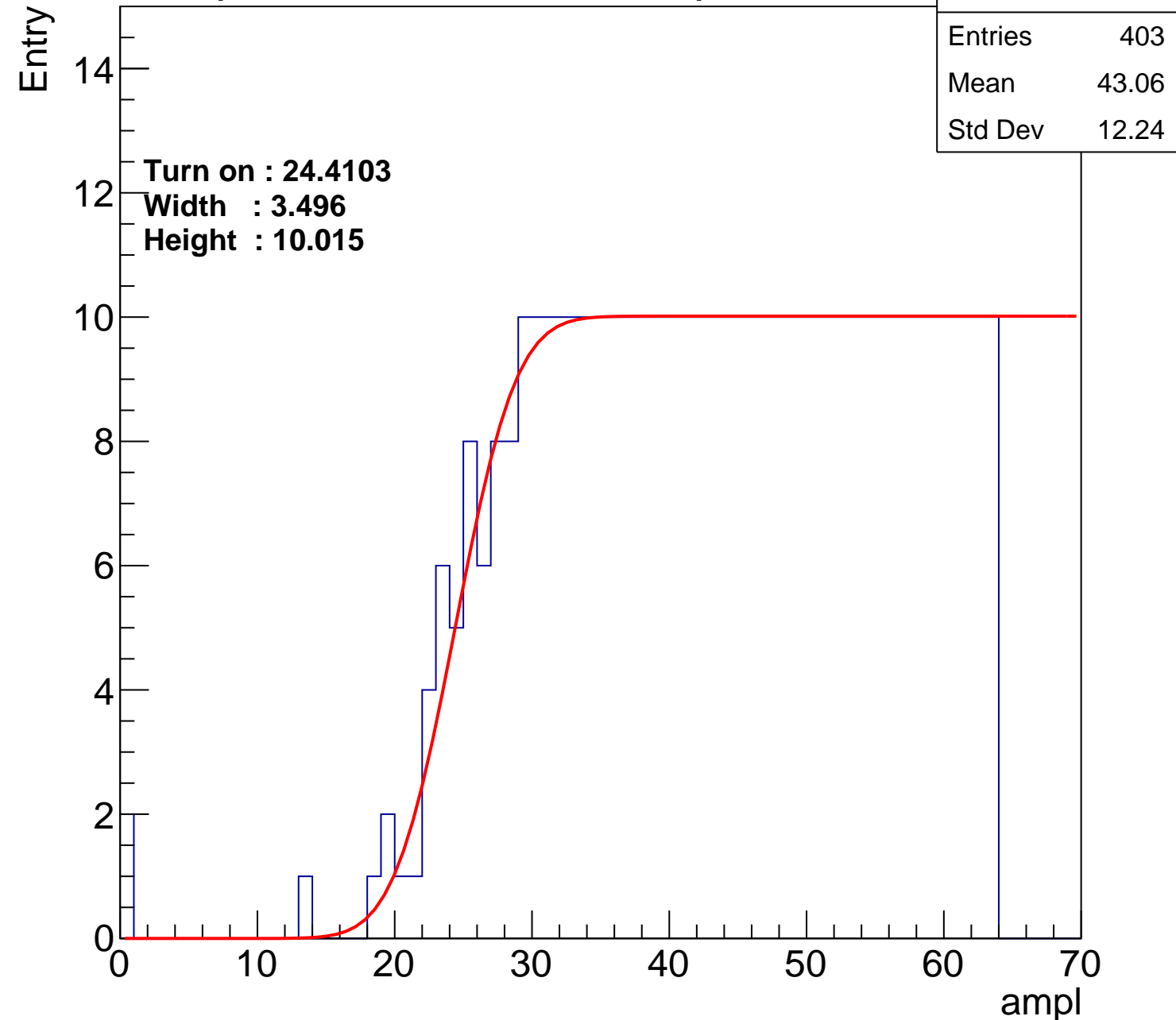
Width : 3.496

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.83
Std Dev	11.44

Turn on : 28.1363

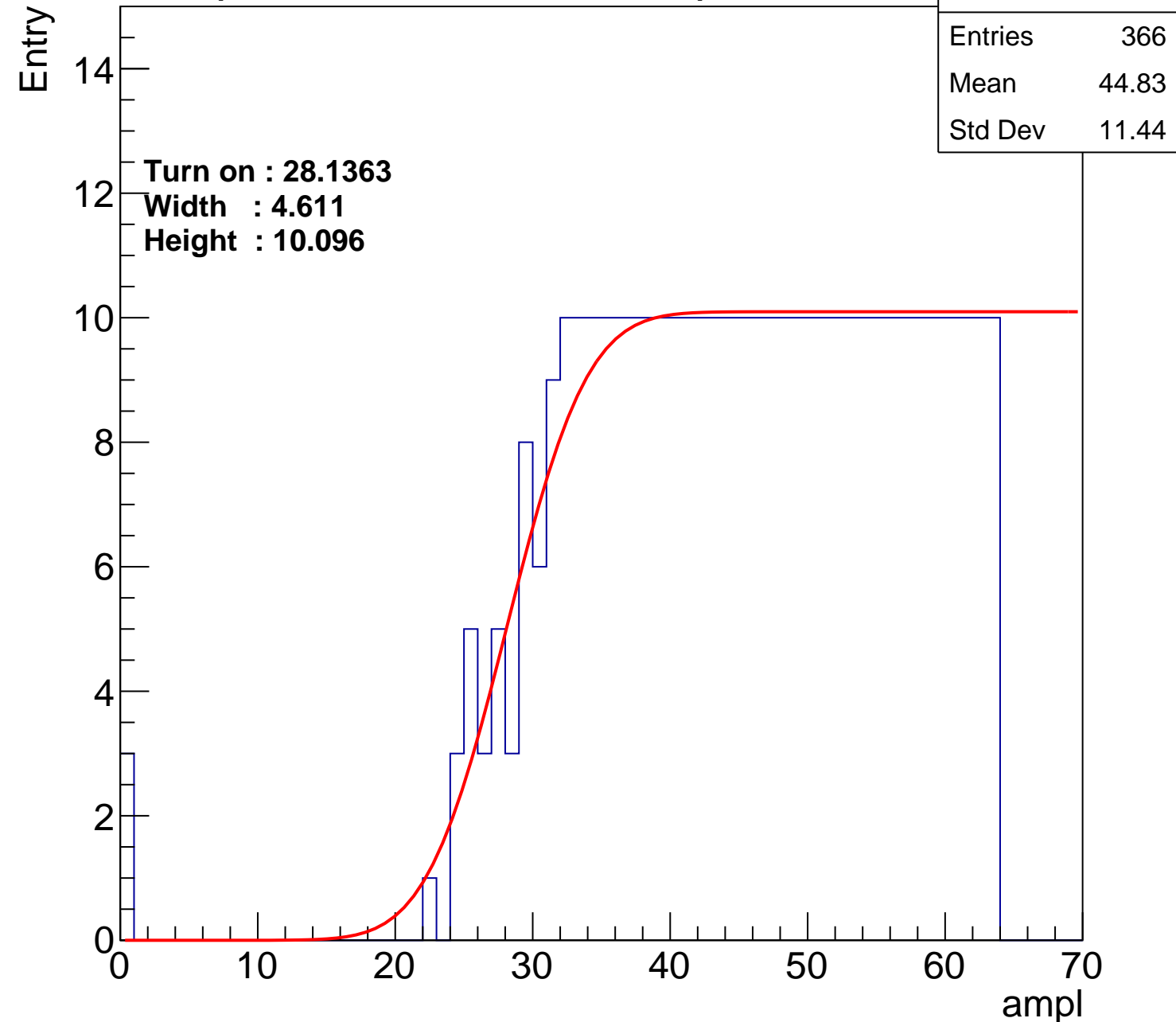
Width : 4.611

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.42
Std Dev	12.09

Turn on : 25.1335

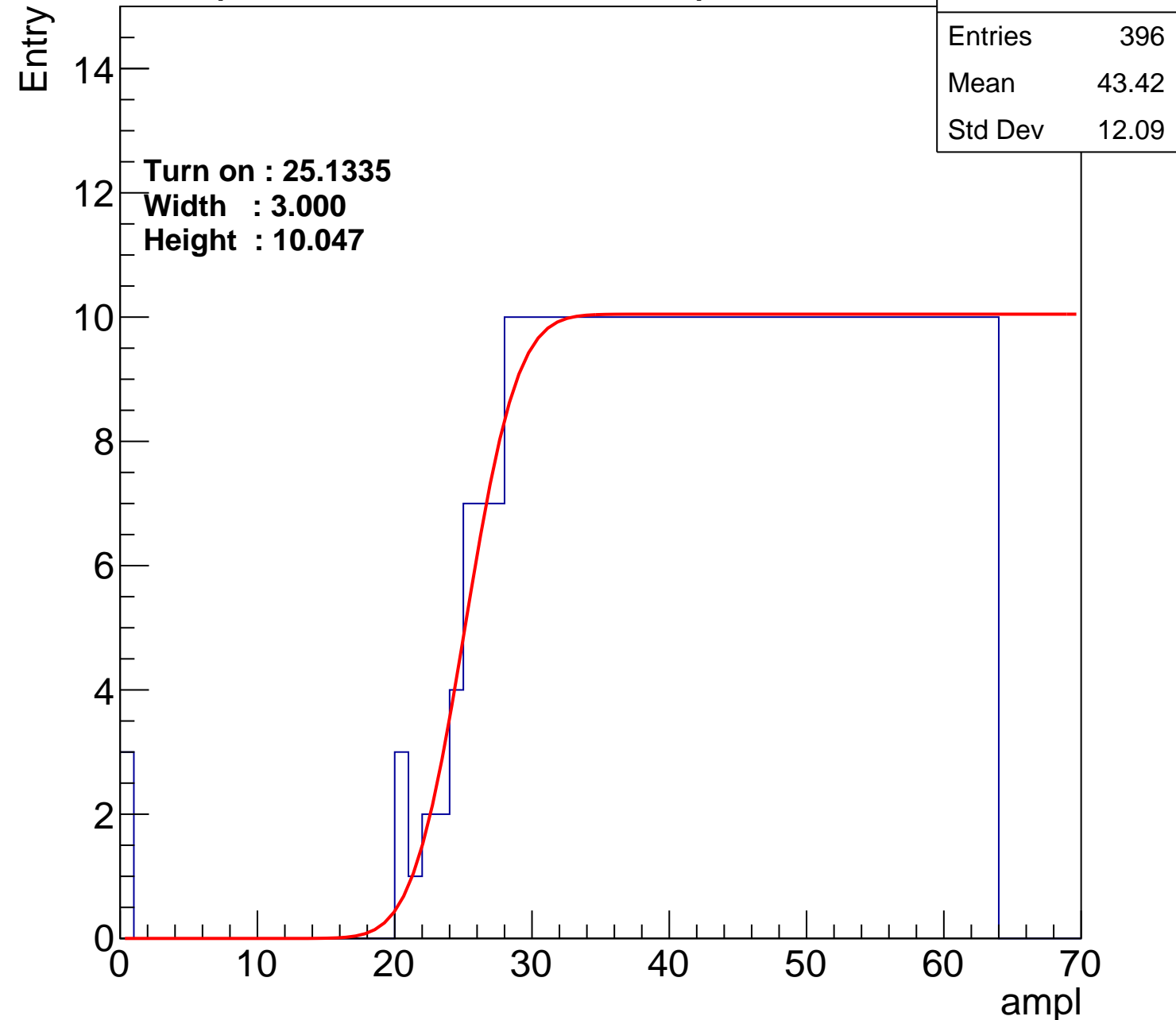
Width : 3.000

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.38
Std Dev	12.41

Turn on : 26.9761

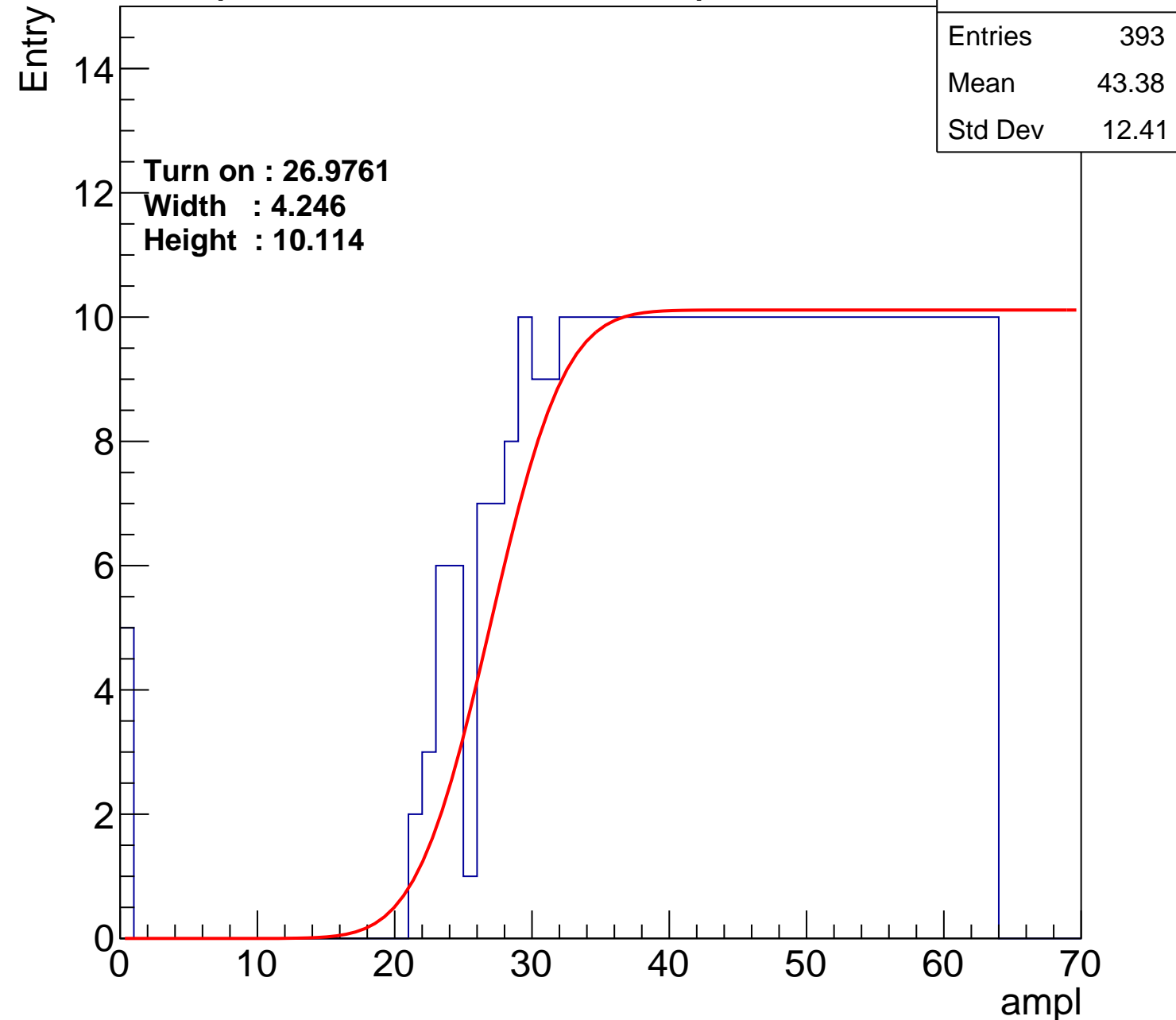
Width : 4.246

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	402
Mean	42.92
Std Dev	12.46

Turn on : 24.4785

Width : 3.102

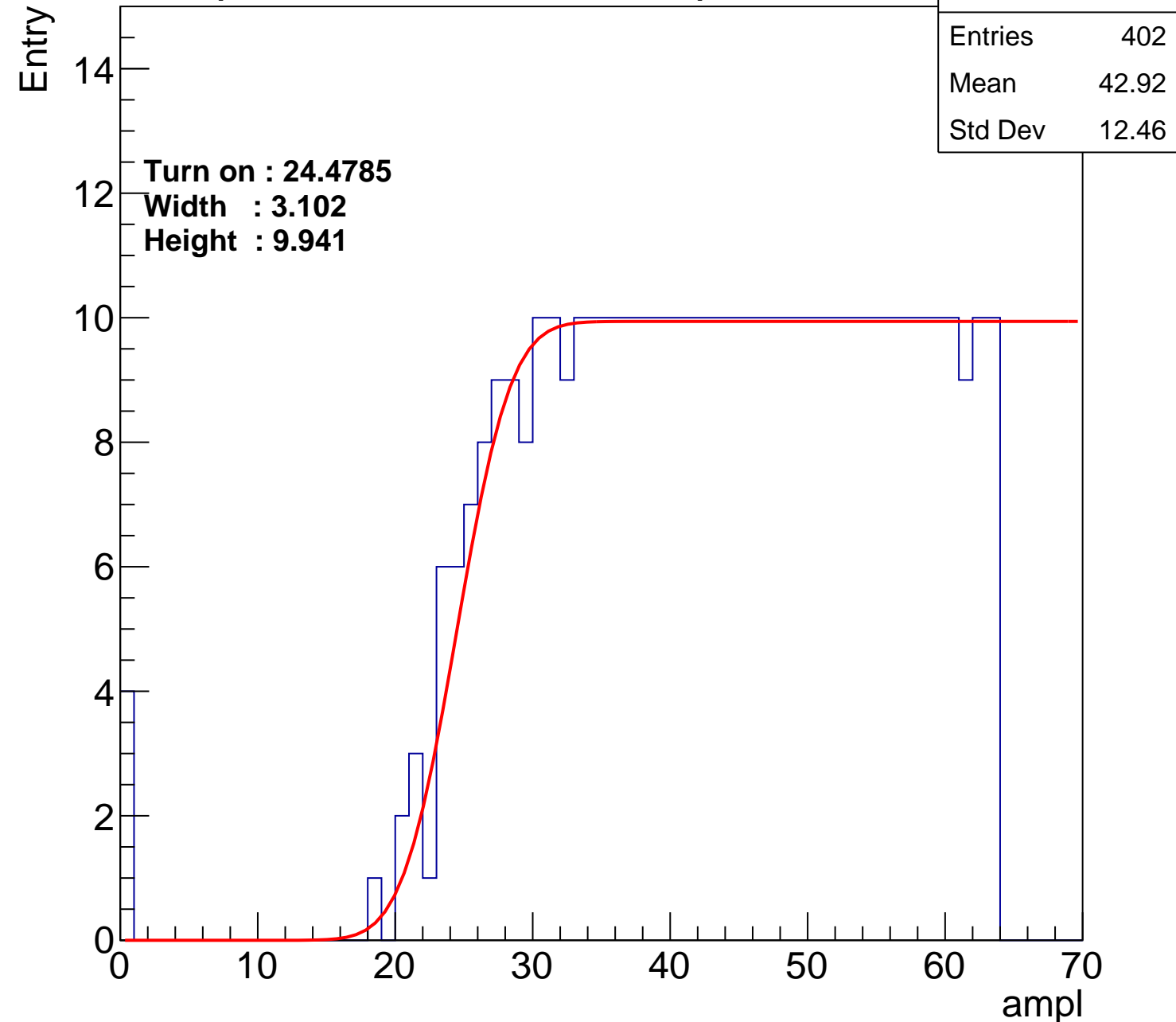
Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.58
Std Dev	12.03

Turn on : 25.4460

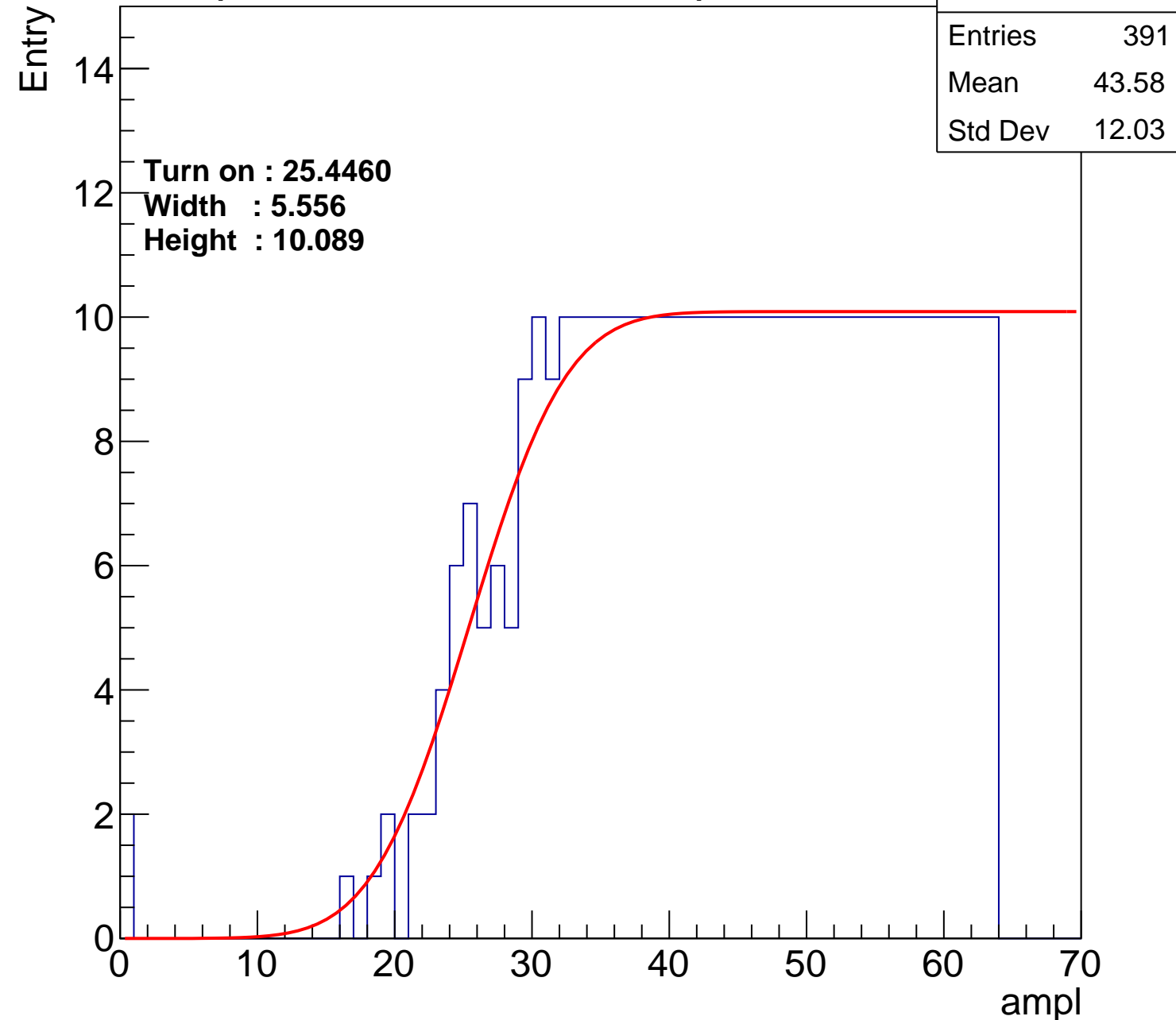
Width : 5.556

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch122

calib_packv5_042523_0143.root, FC#7, port C2

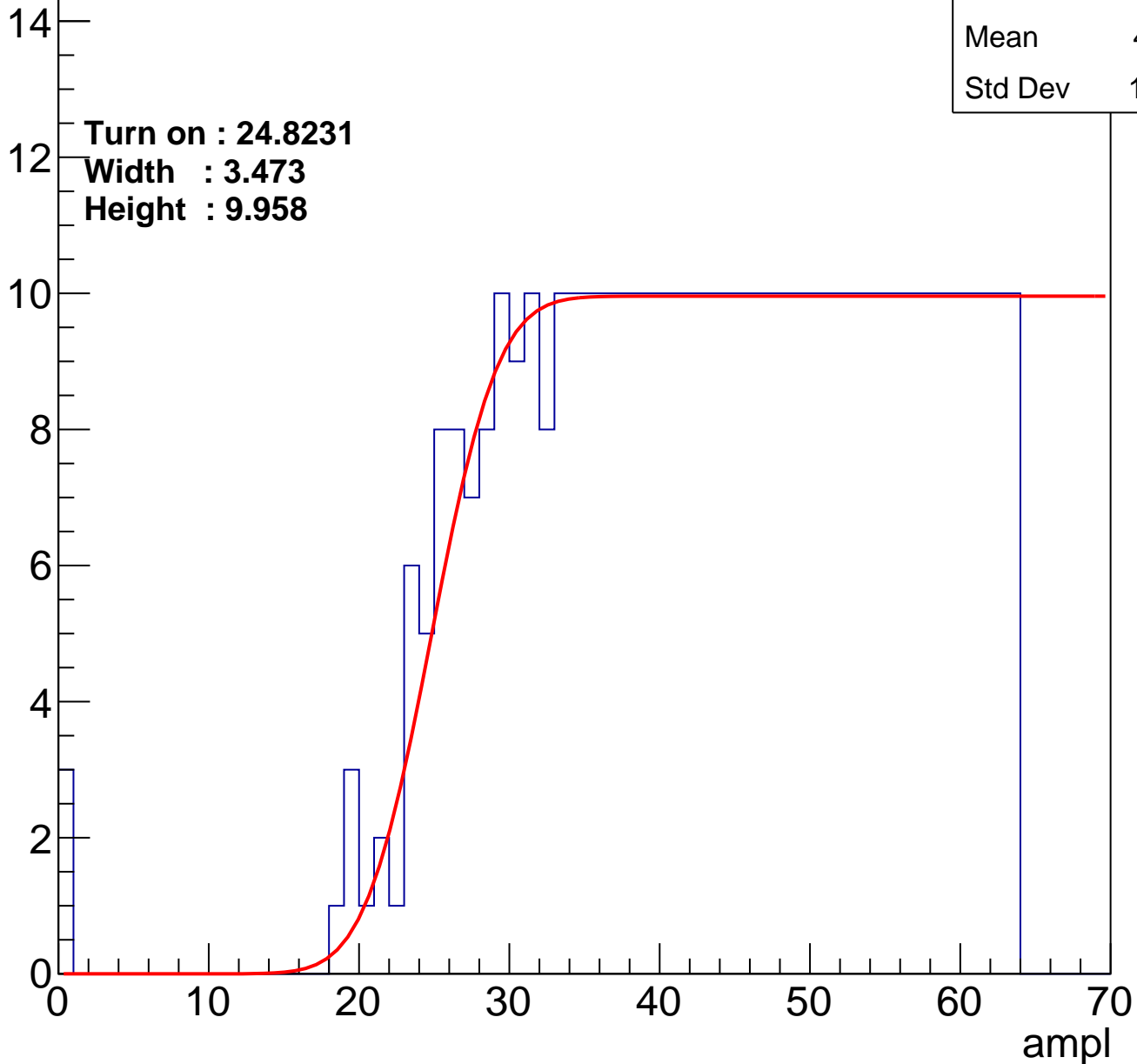
Entries	400
Mean	43.11
Std Dev	12.35

Turn on : 24.8231

Width : 3.473

Height : 9.958

Entry



B1L103S, U24-ch123

calib_packv5_042523_0143.root, FC#7, port C2

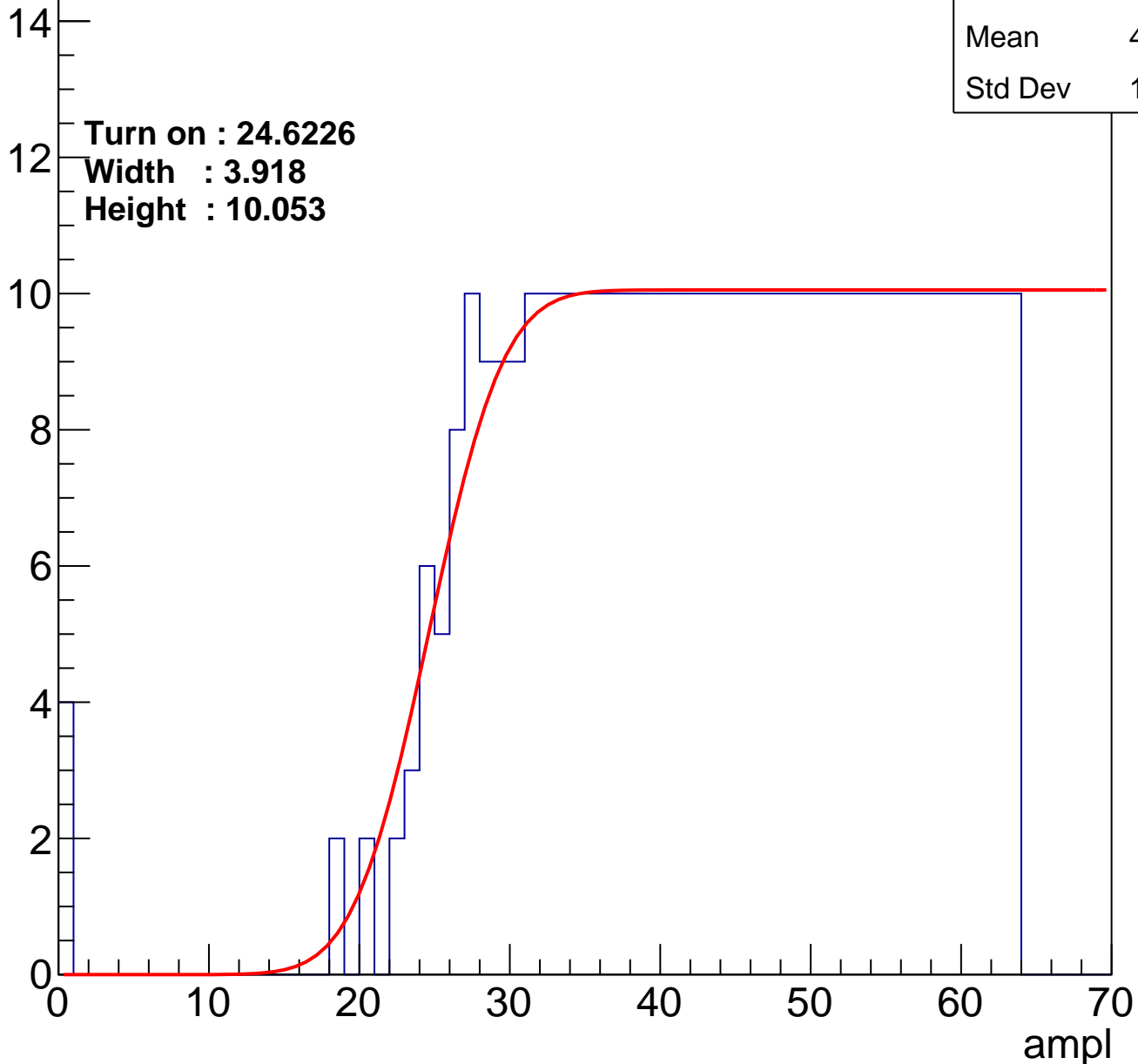
Entries	399
Mean	43.18
Std Dev	12.36

Turn on : 24.6226

Width : 3.918

Height : 10.053

Entry



B1L103S, U24-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.12
Std Dev	11.51

Turn on : 26.2530

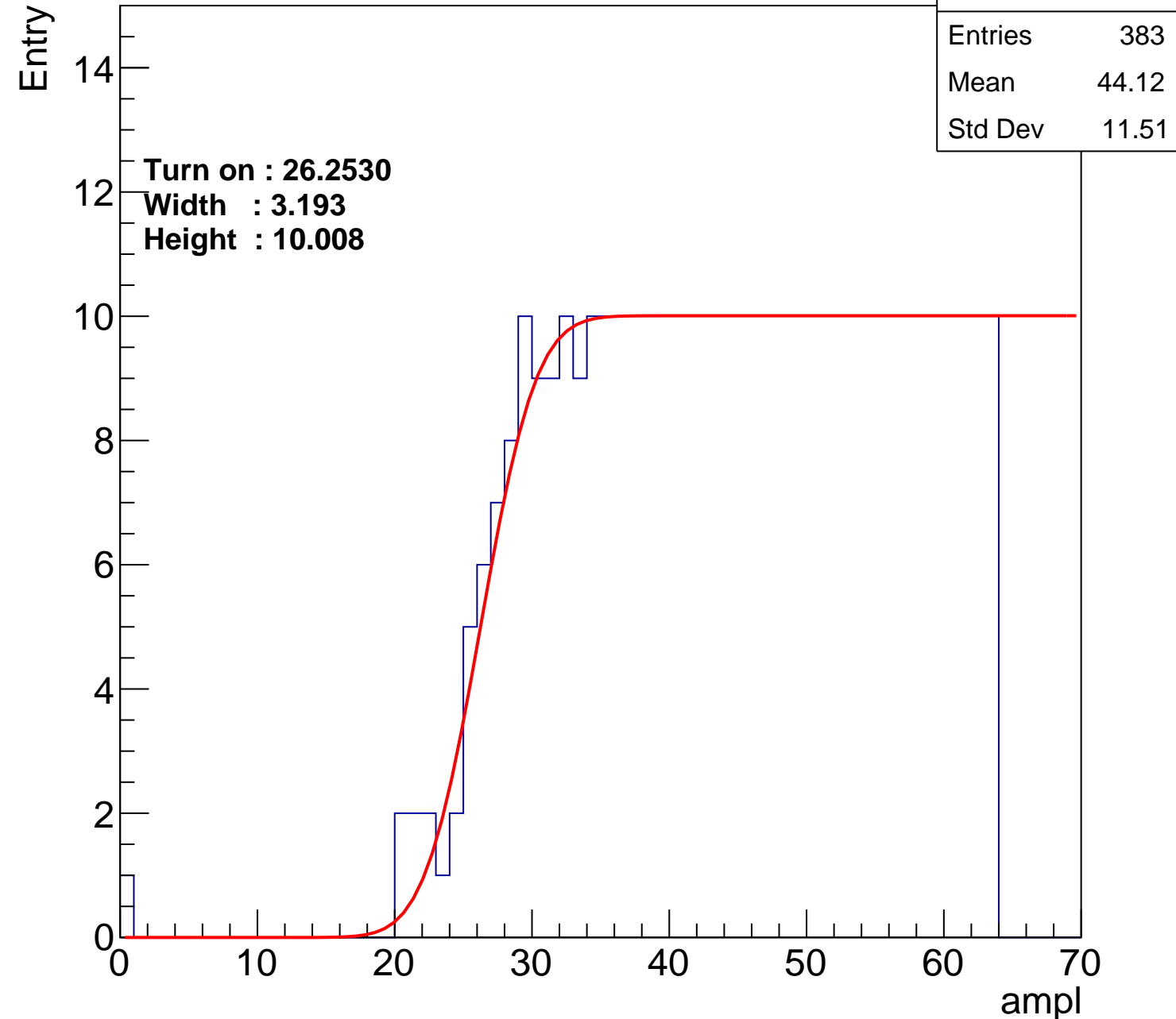
Width : 3.193

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.9
Std Dev	11.9

Turn on : 26.0691

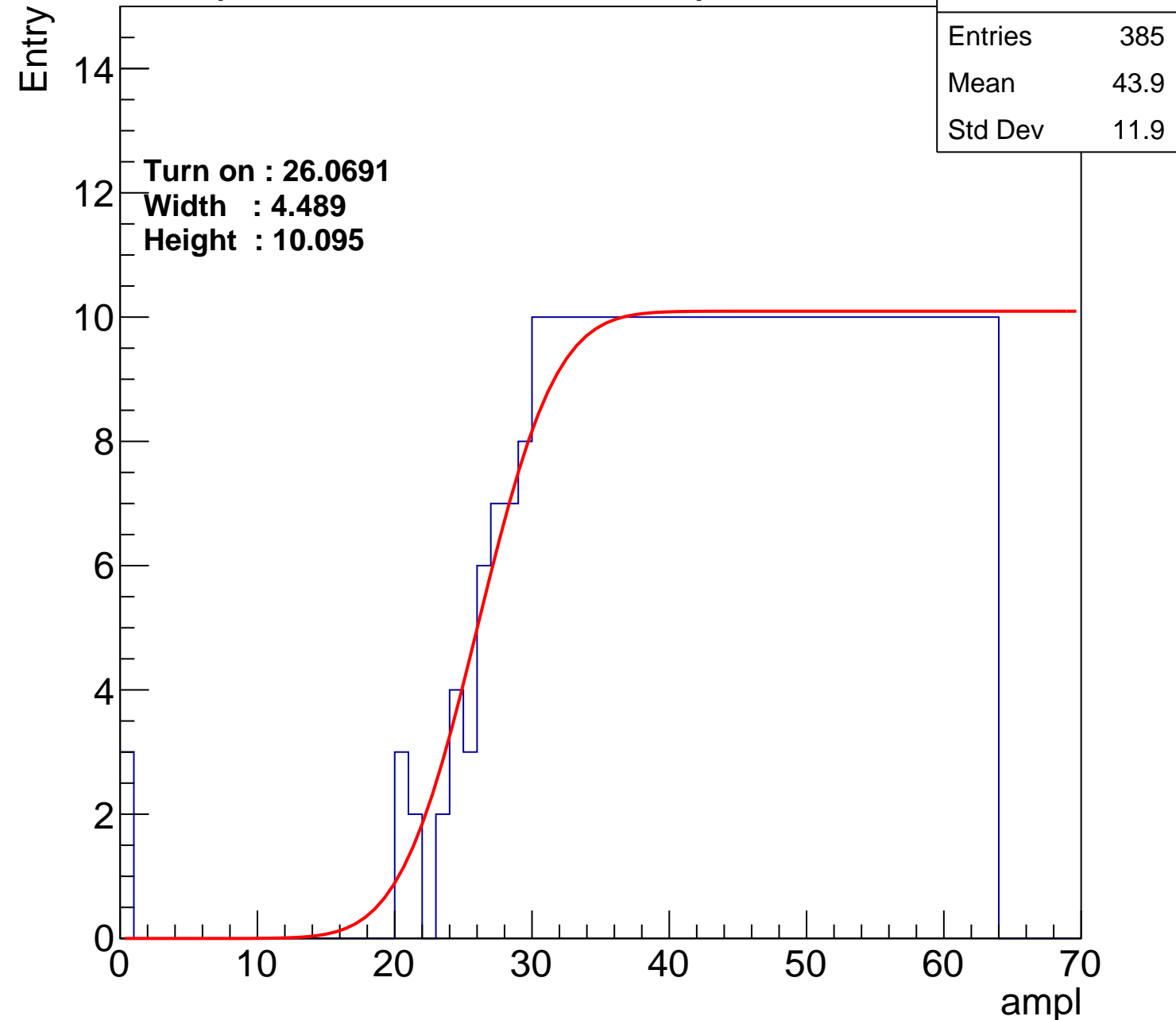
Width : 4.489

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch126

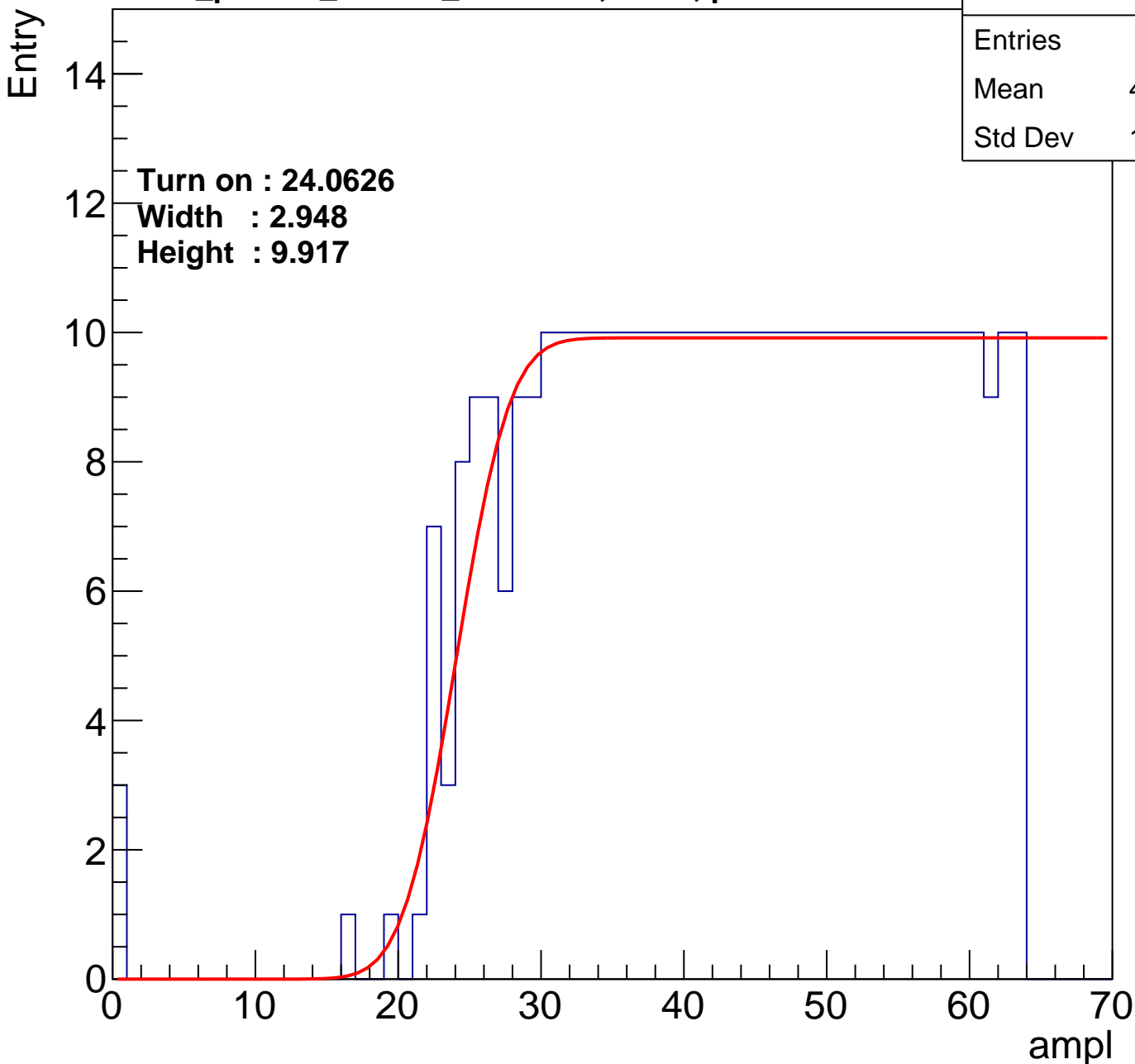
calib_packv5_042523_0143.root, FC#7, port C2

Entries	405
Mean	42.85
Std Dev	12.36

Turn on : 24.0626

Width : 2.948

Height : 9.917



B1L103S, U24-ch127

calib_packv5_042523_0143.root, FC#7, port C2

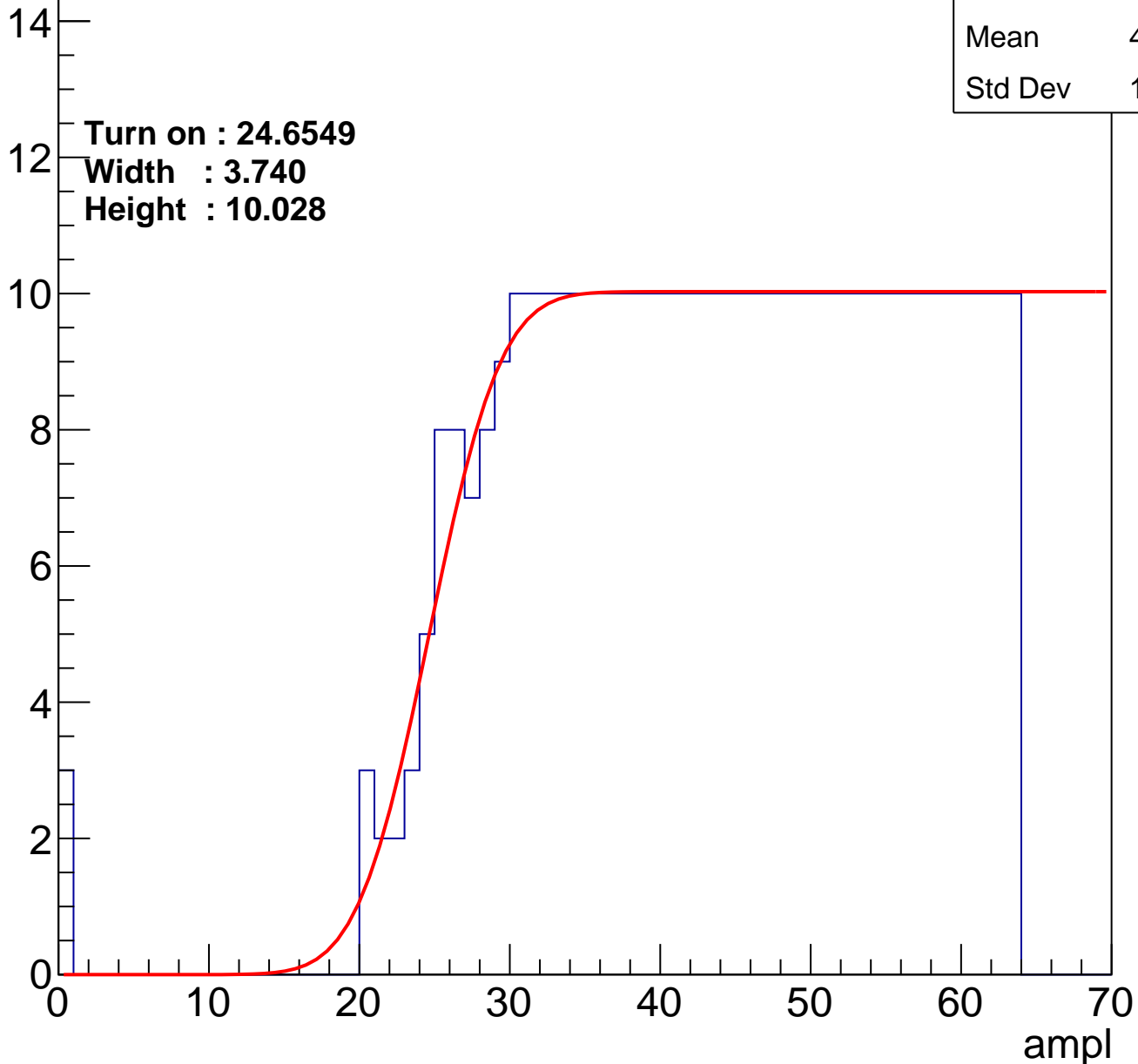
Entries	398
Mean	43.28
Std Dev	12.19

Turn on : 24.6549

Width : 3.740

Height : 10.028

Entry



B1L103S, U24-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.28
Std Dev	12.19

Turn on : 24.6549

Width : 3.740

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

