

B0L102S, U4-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.18
Std Dev	11.66

Turn on : 26.6813

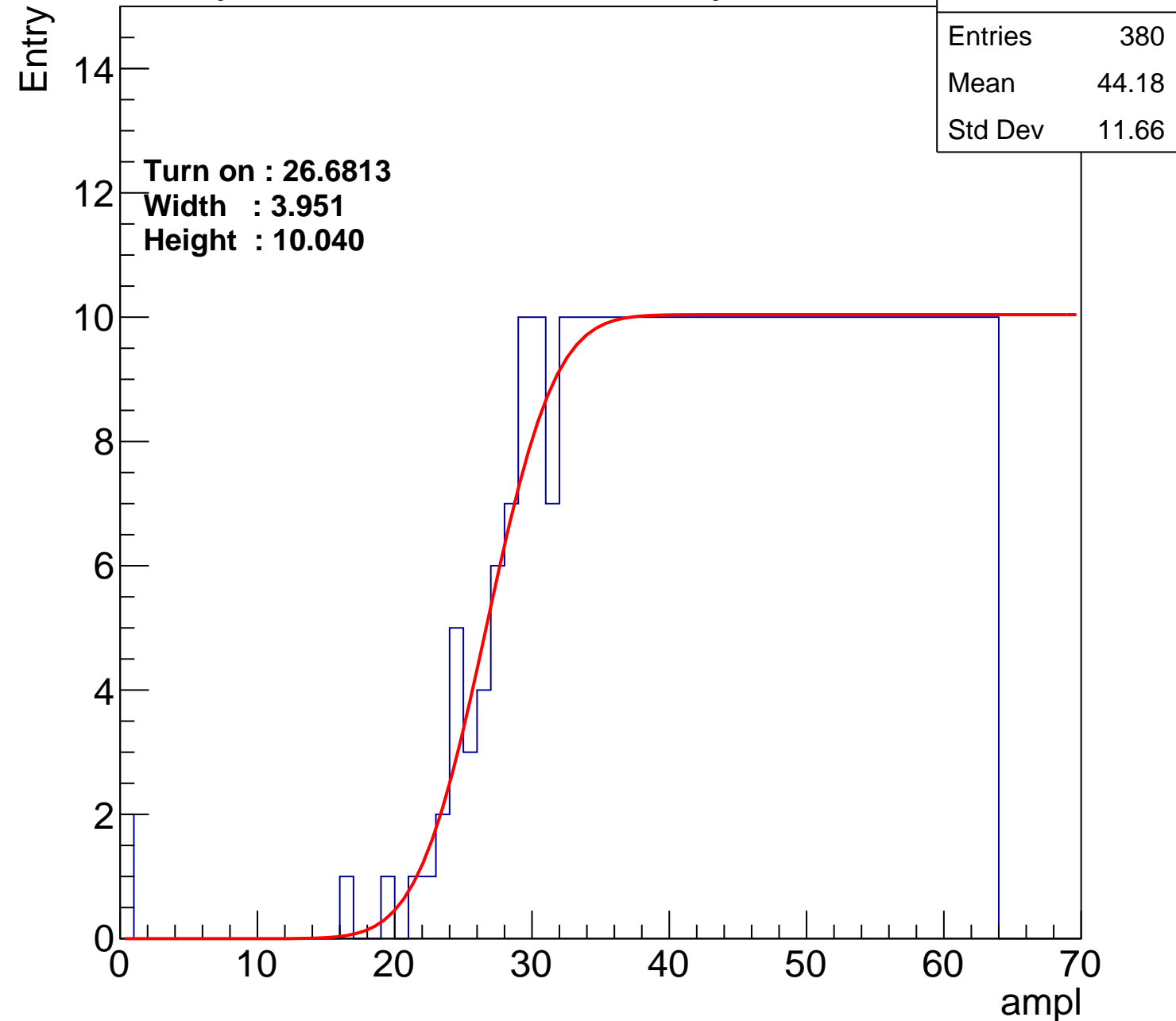
Width : 3.951

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 24.8690

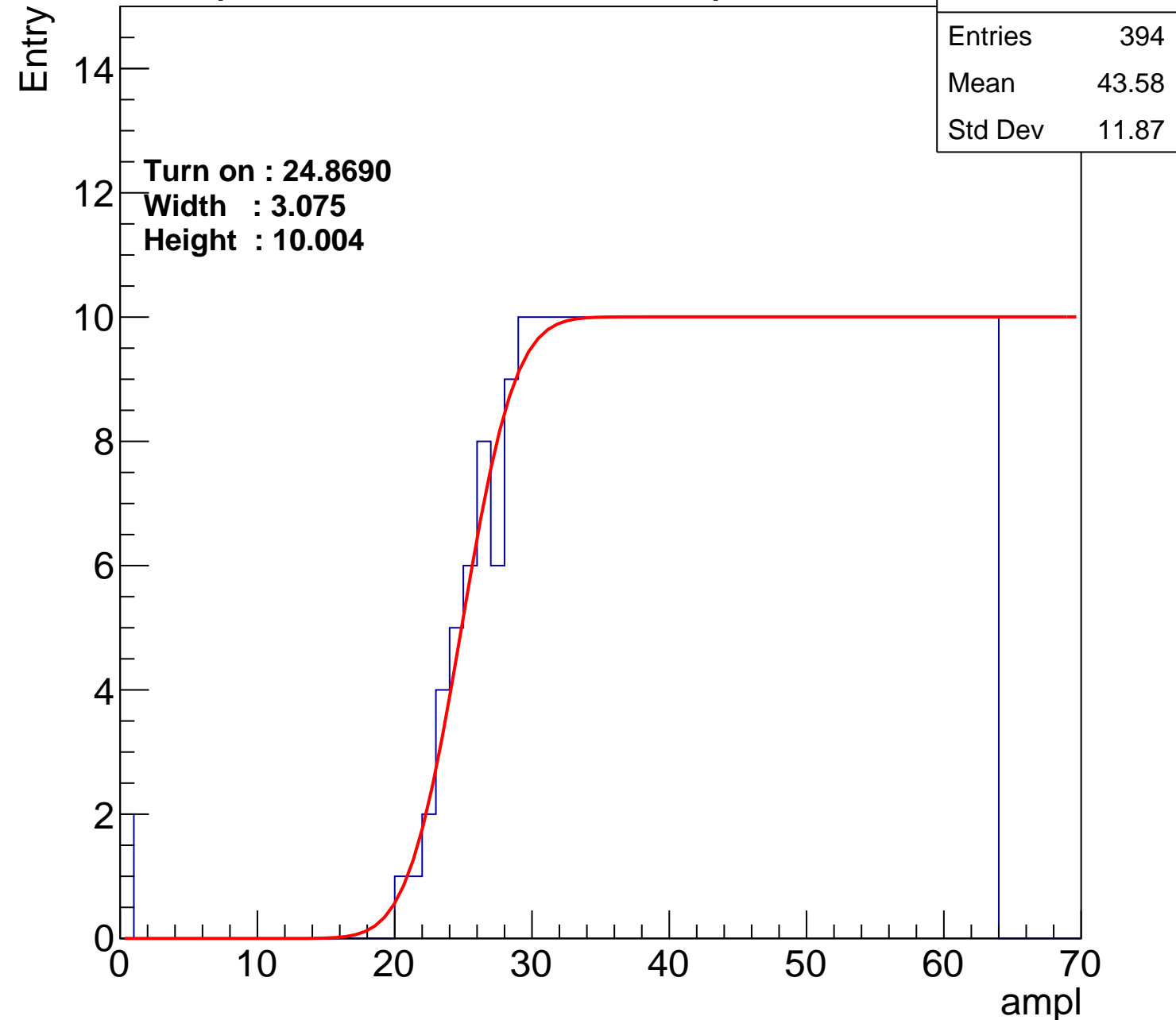
Width : 3.075

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch2

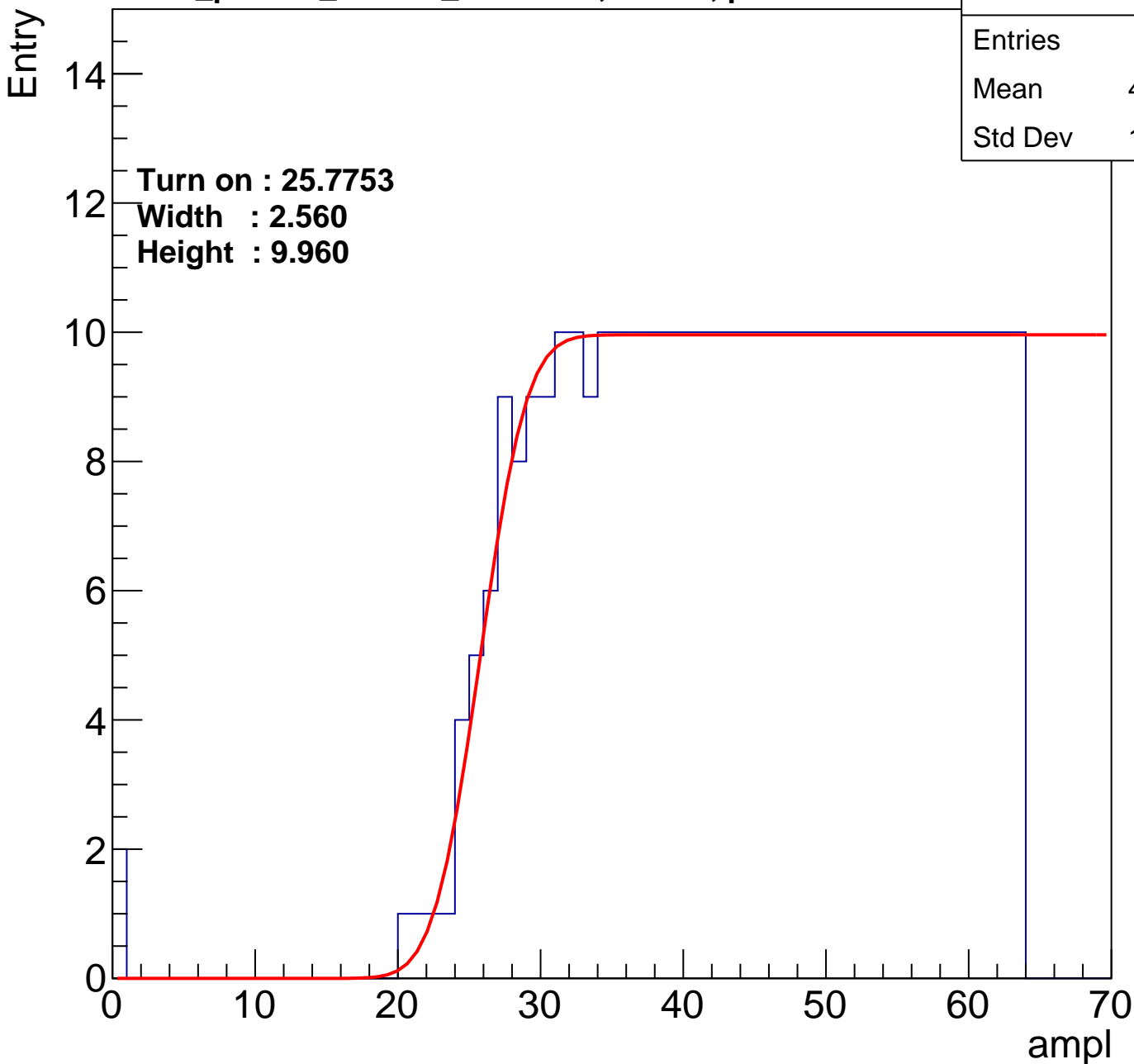
calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.99
Std Dev	11.67

Turn on : 25.7753

Width : 2.560

Height : 9.960



B0L102S, U4-ch3

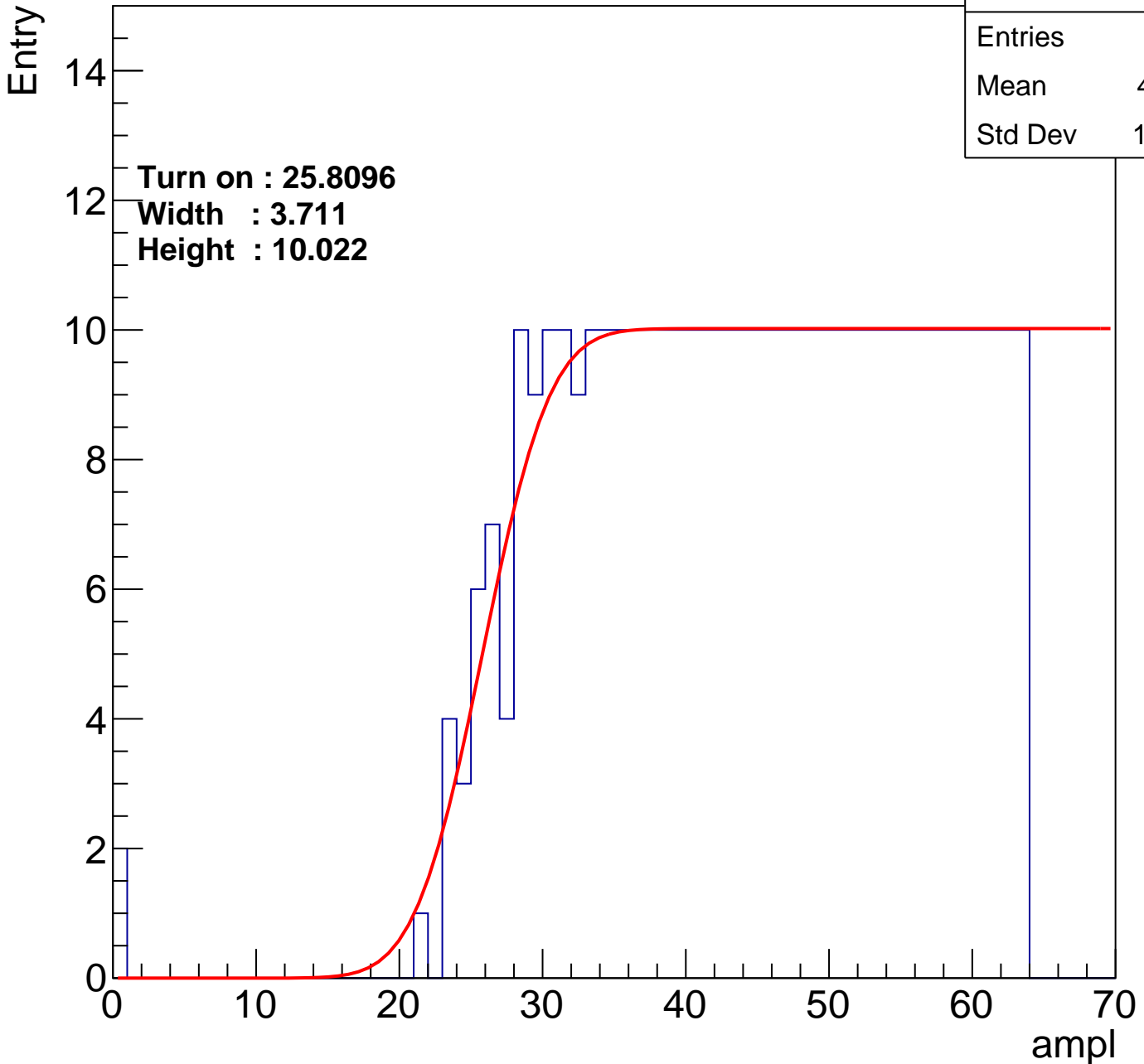
calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 25.8096

Width : 3.711

Height : 10.022



B0L102S, U4-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.37
Std Dev	11.77

Turn on : 27.2239

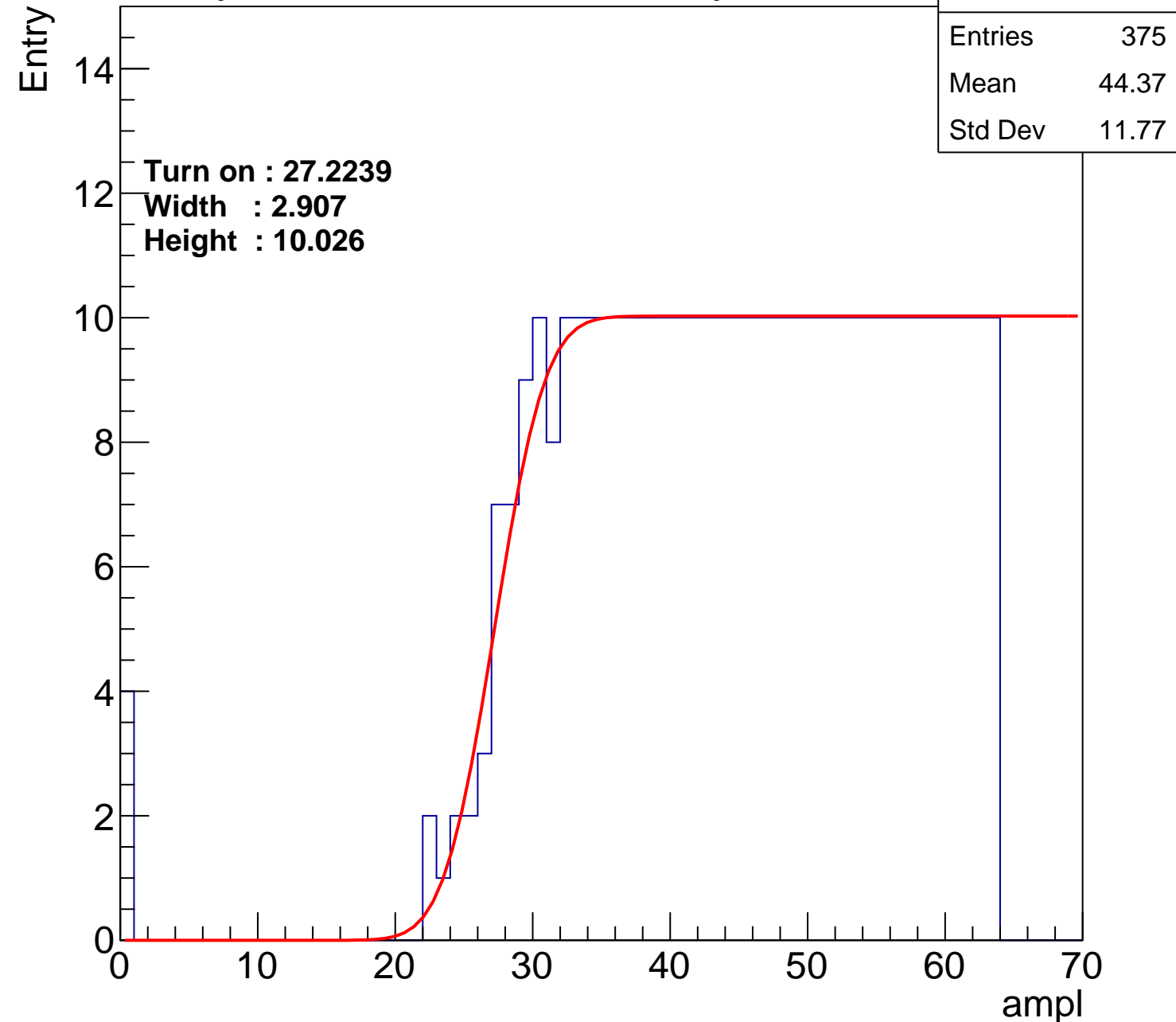
Width : 2.907

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 26.2968

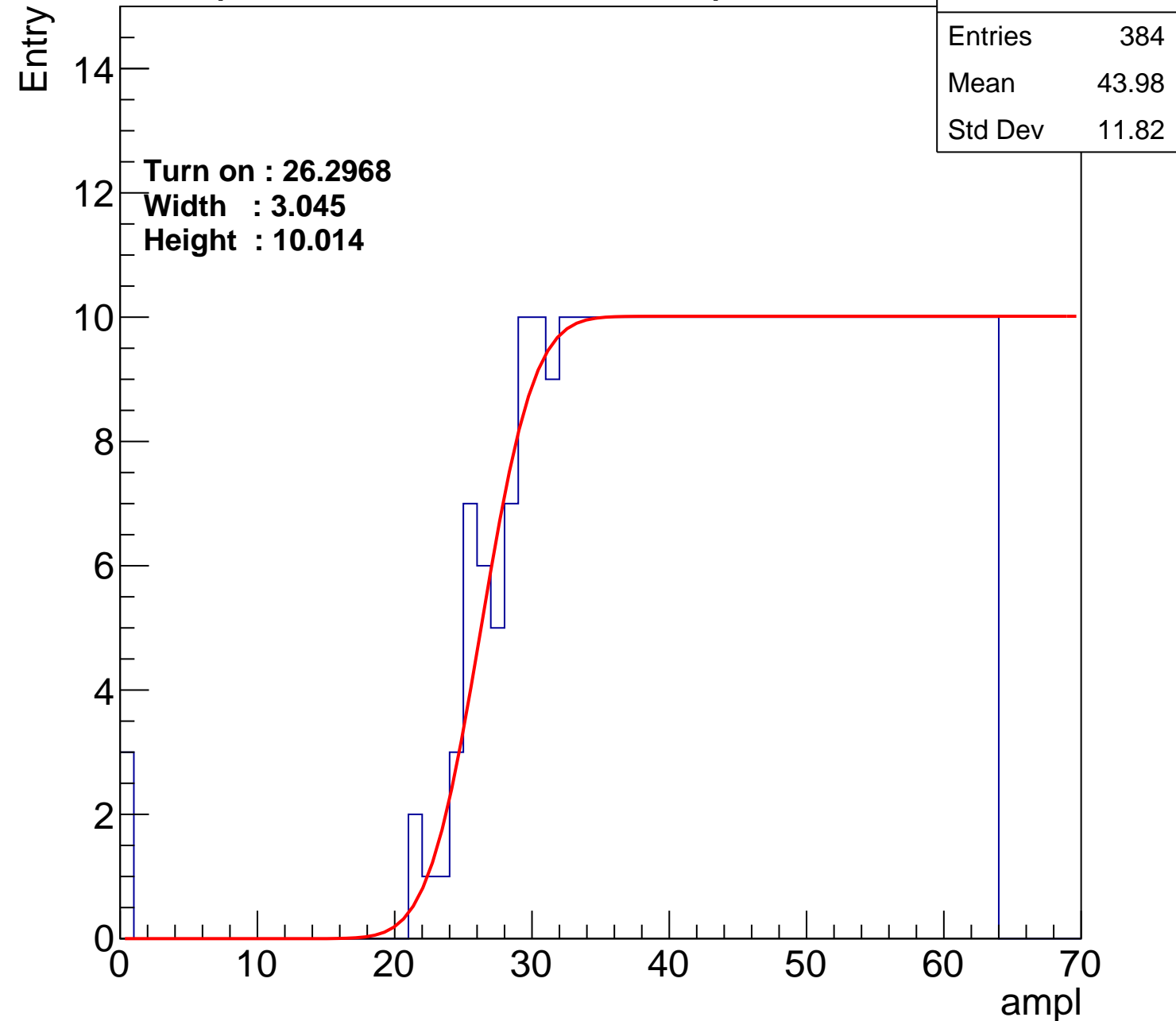
Width : 3.045

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.13
Std Dev	11.62

Turn on : 26.1069

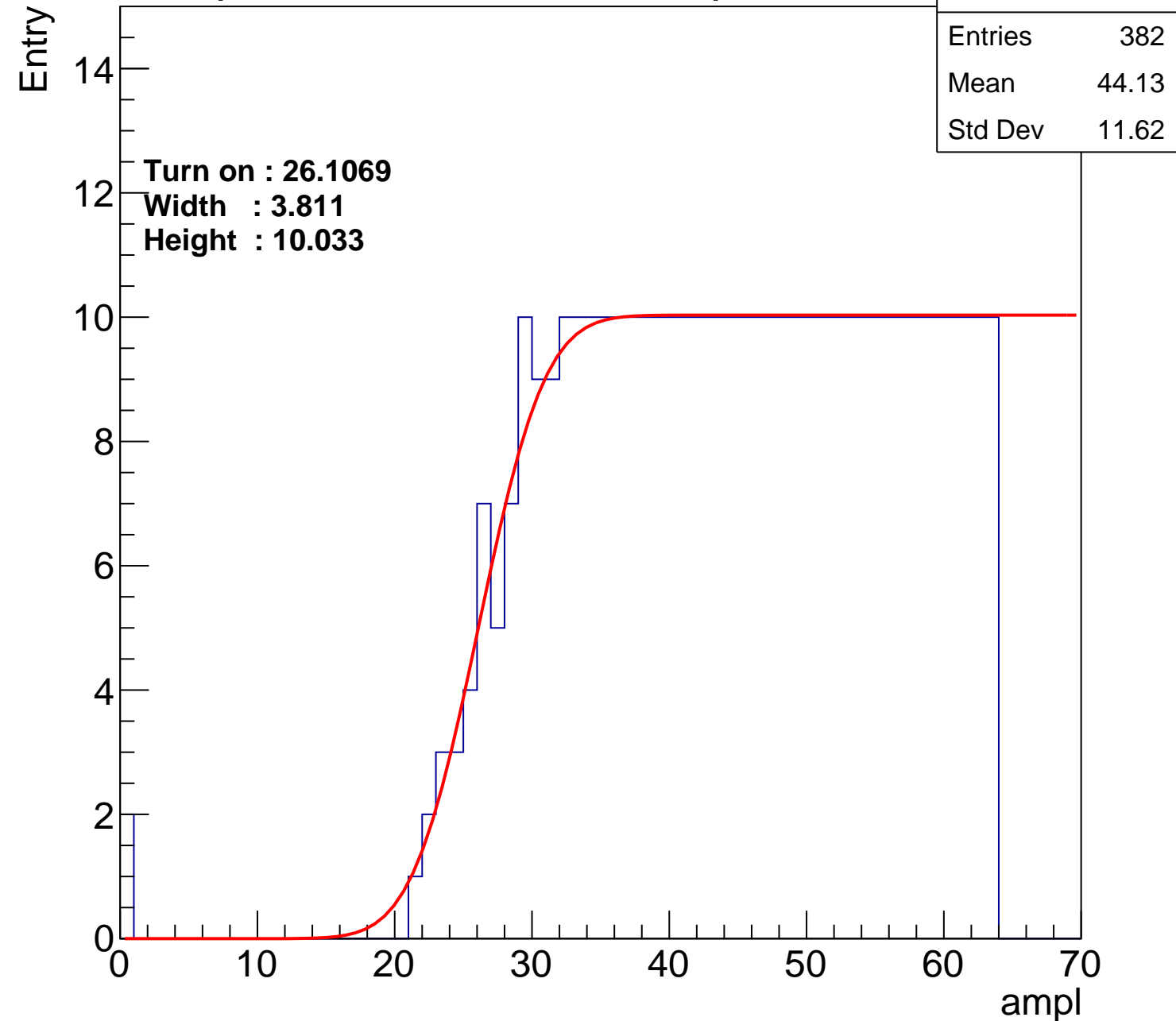
Width : 3.811

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 26.4075

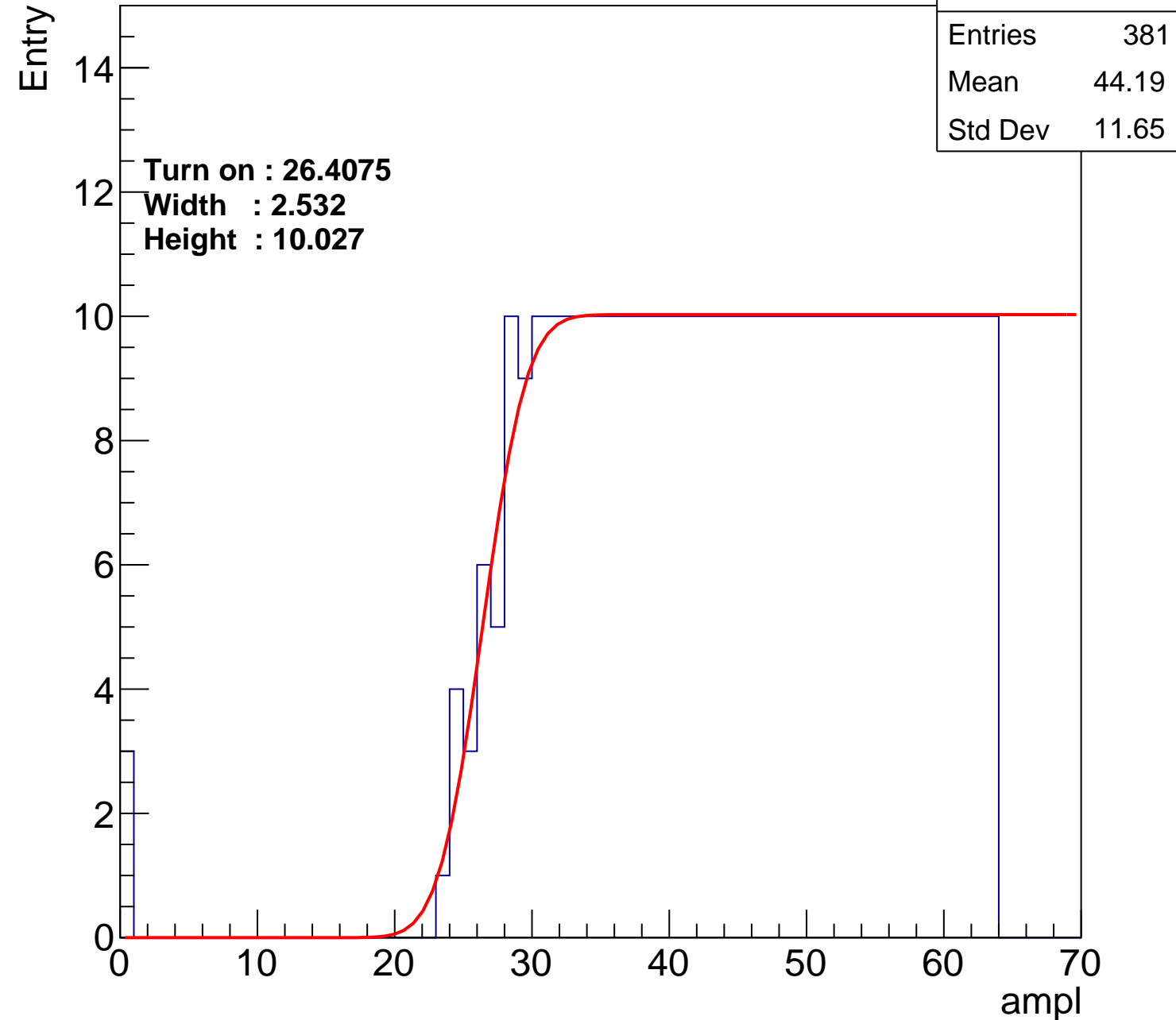
Width : 2.532

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 26.9840

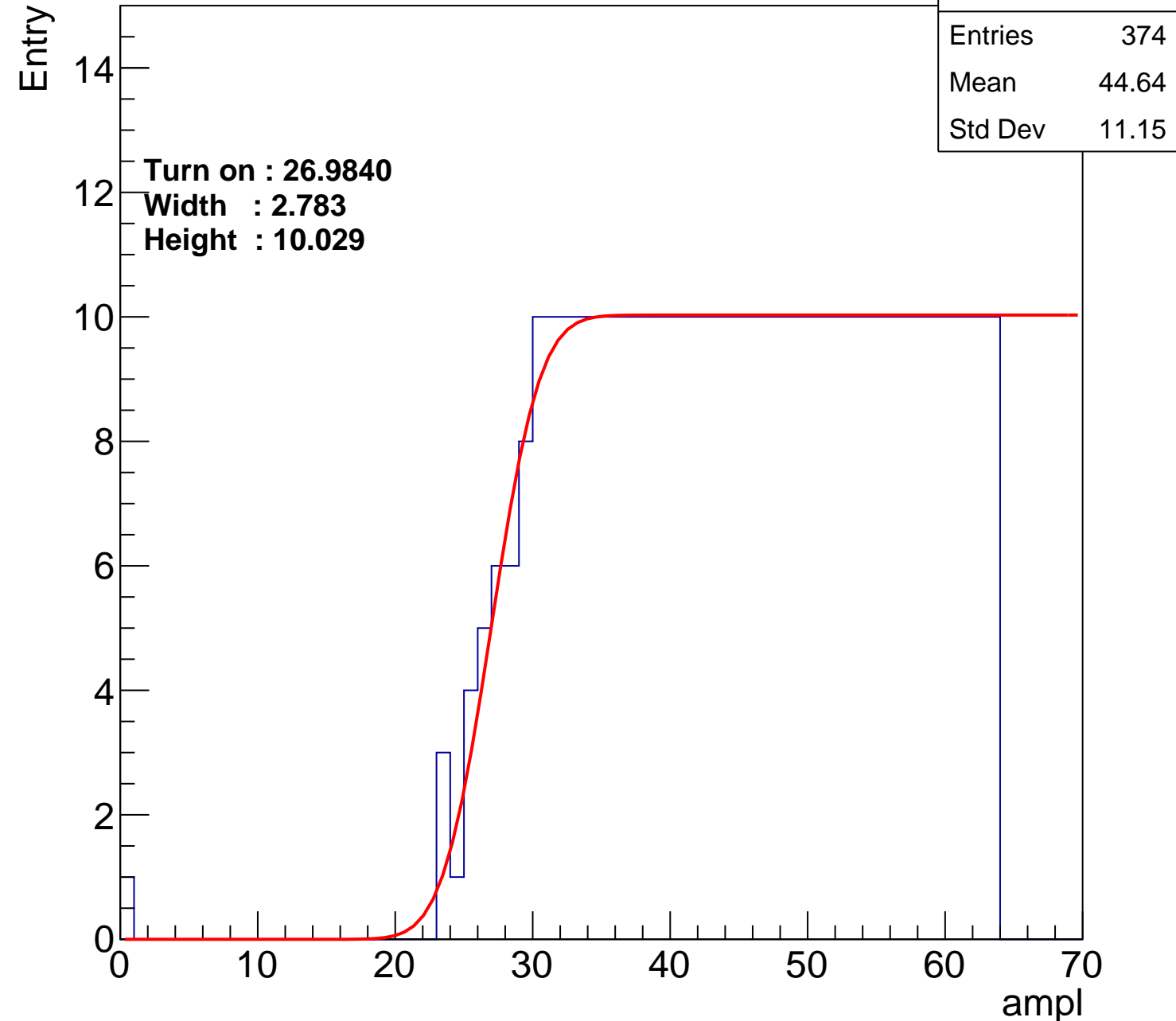
Width : 2.783

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch9

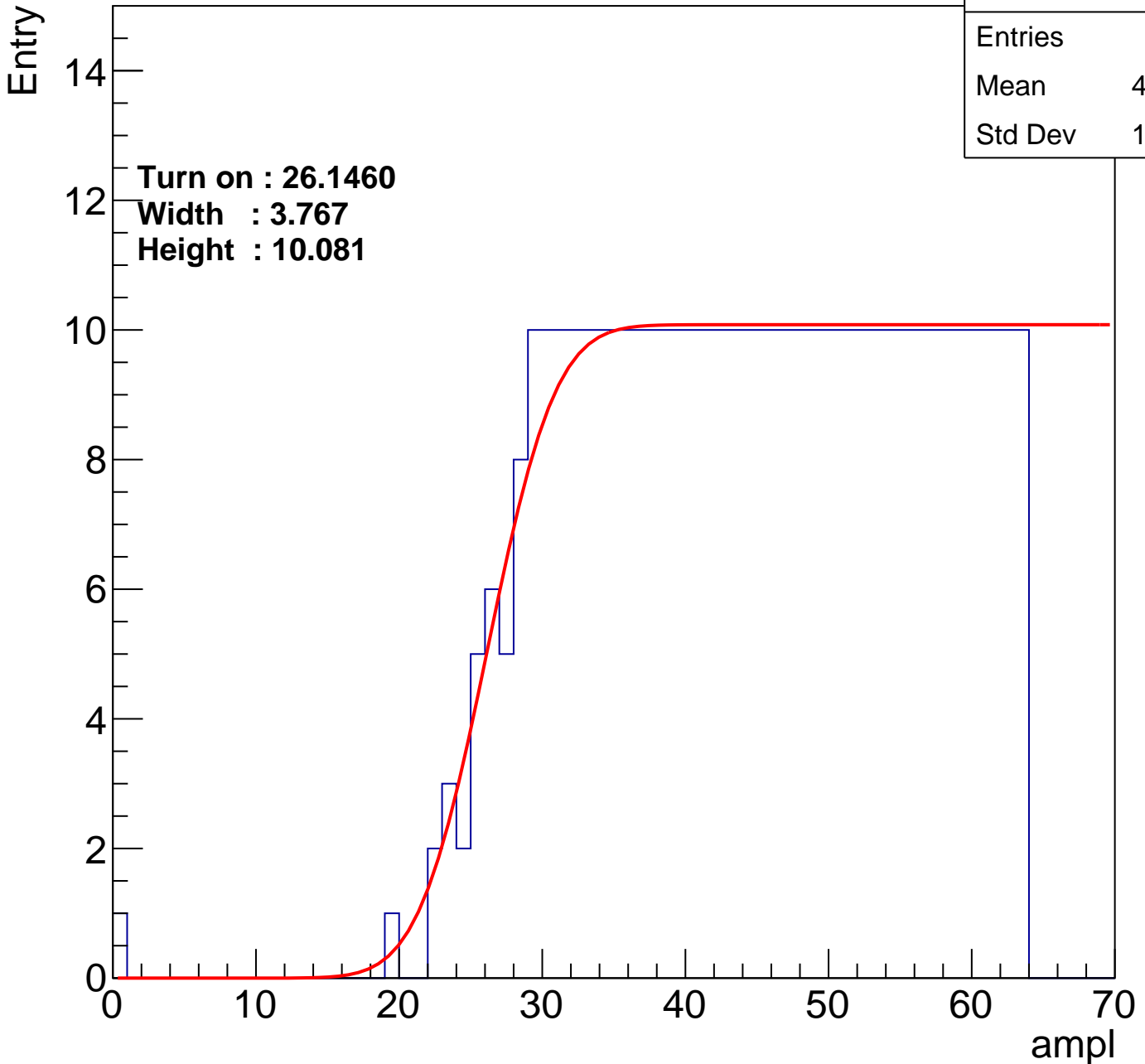
calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.18
Std Dev	11.42

Turn on : 26.1460

Width : 3.767

Height : 10.081



B0L102S, U4-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.04
Std Dev	12.08

Turn on : 27.1025

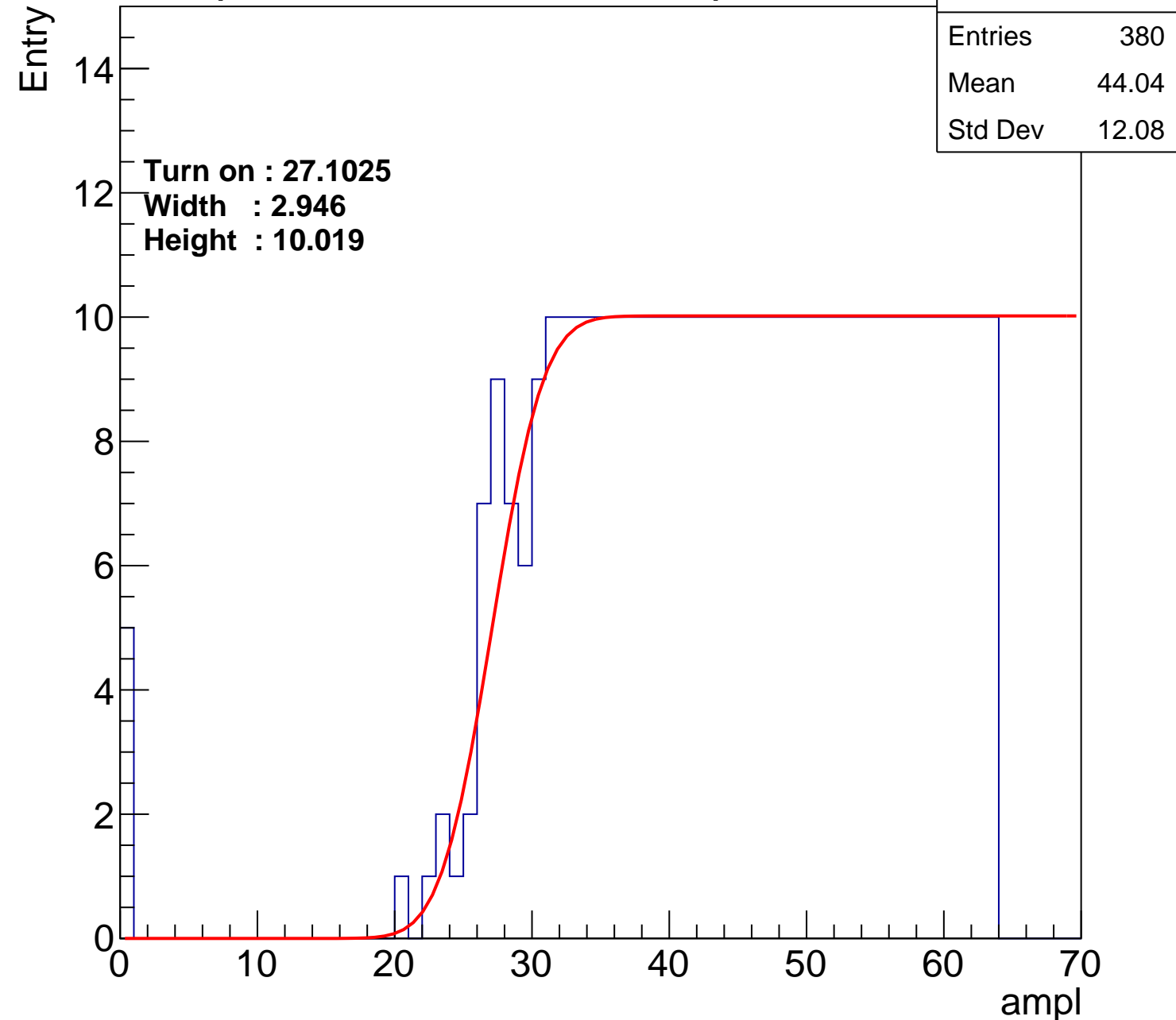
Width : 2.946

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.14
Std Dev	12.14

Turn on : 24.1836

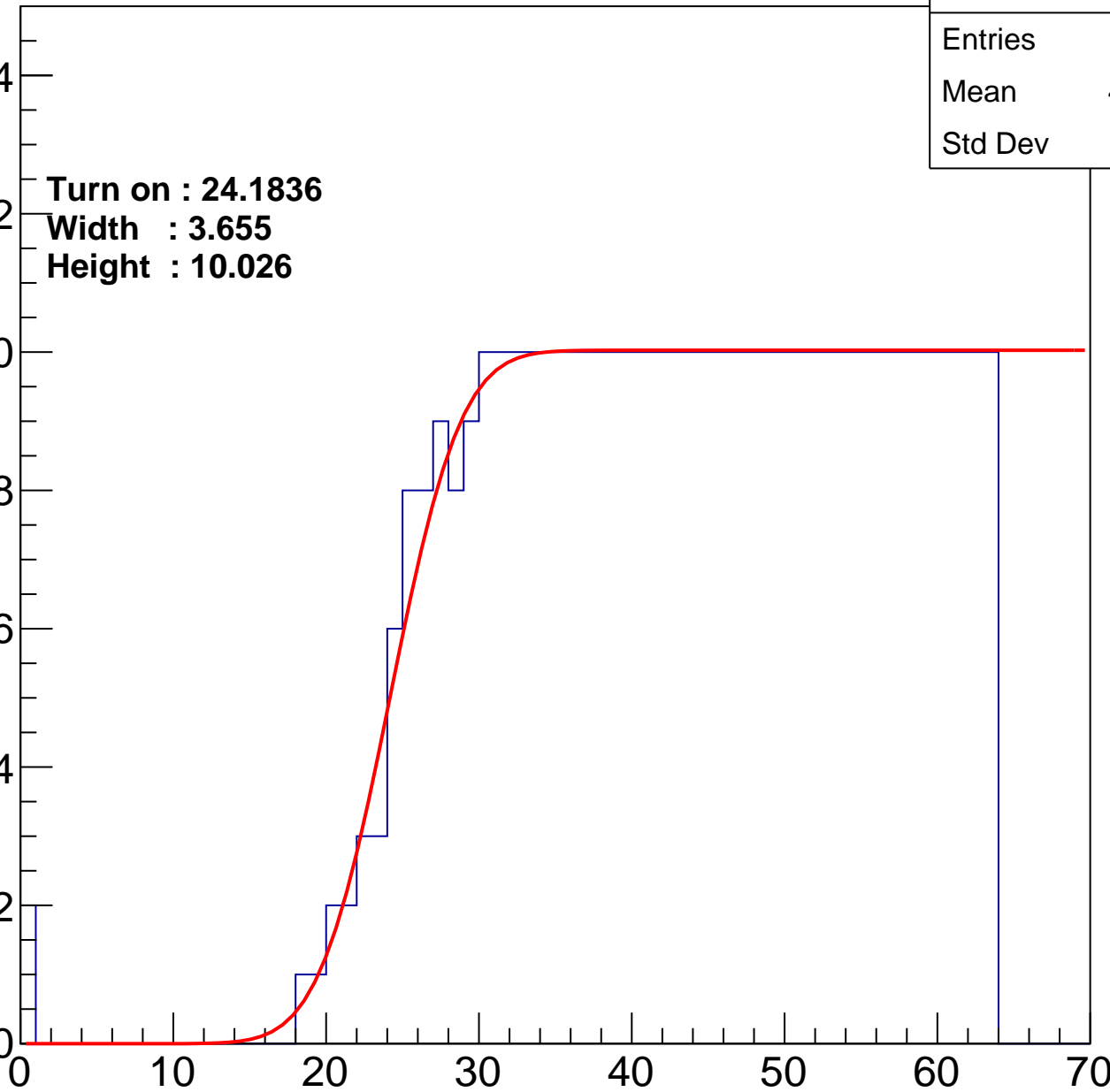
Width : 3.655

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.95
Std Dev	11.75

Turn on : 26.1738

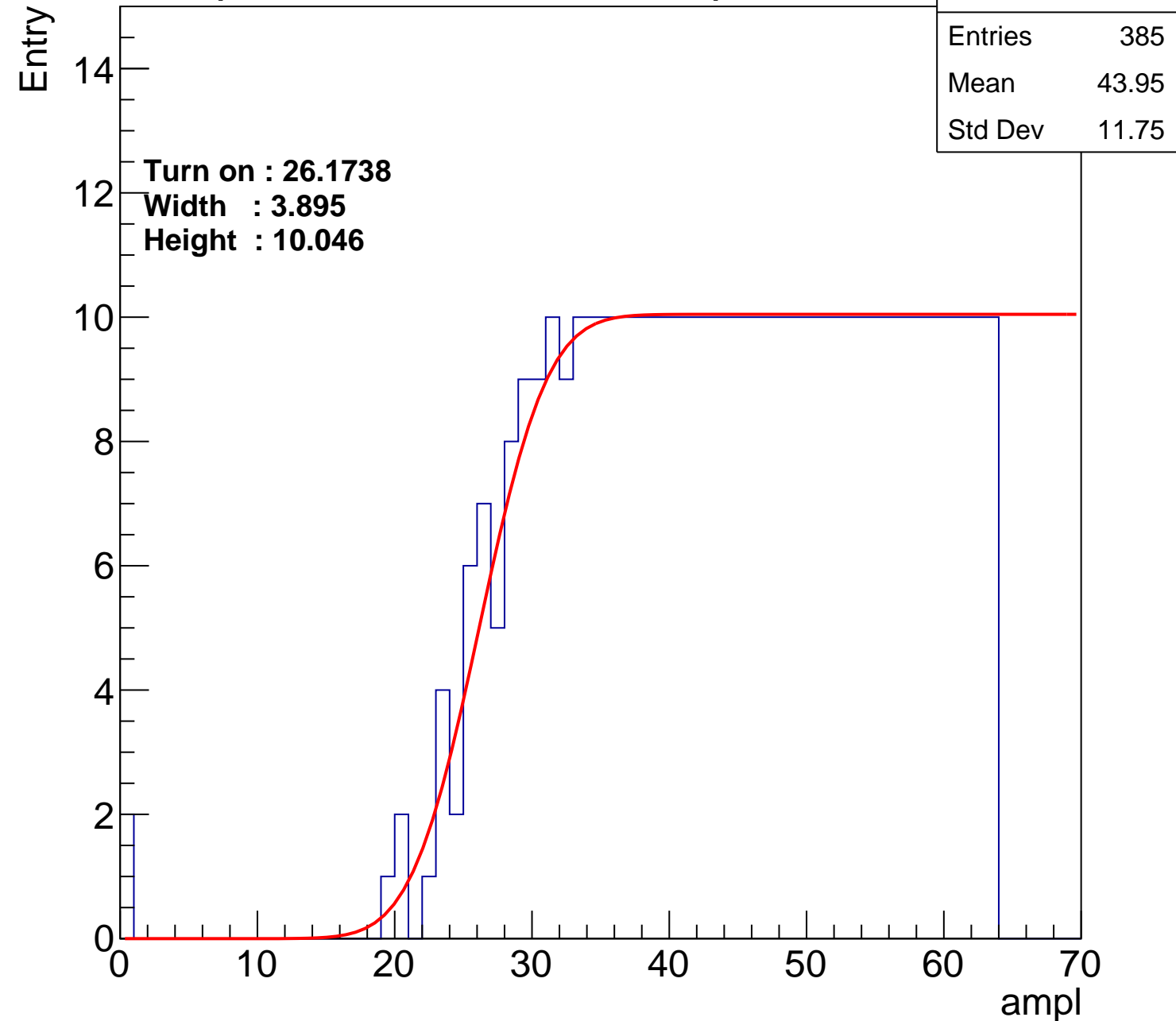
Width : 3.895

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.83
Std Dev	12.14

Turn on : 26.4011

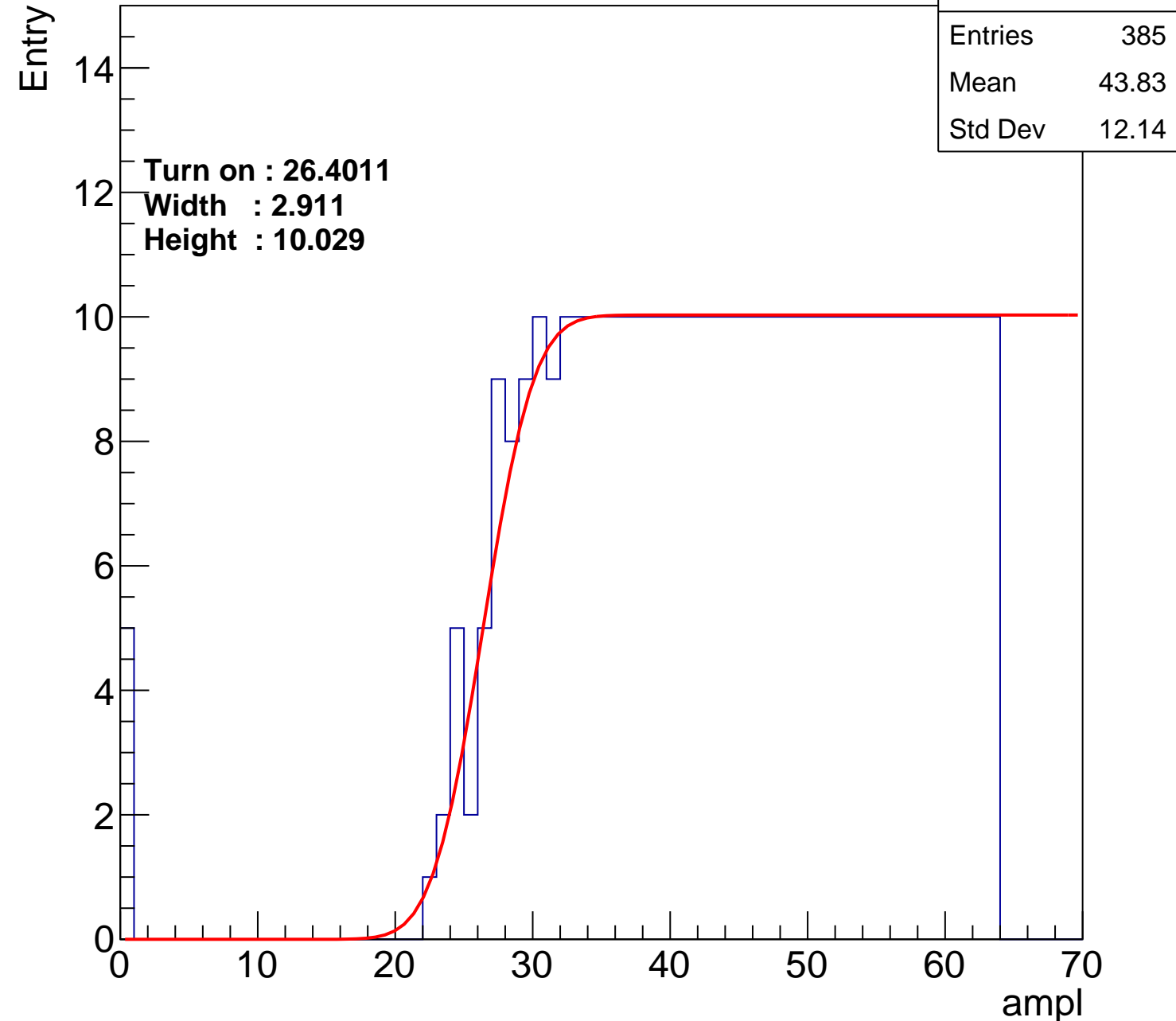
Width : 2.911

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch14

calib_packv5_042523_0143.root, FC#12, port B1

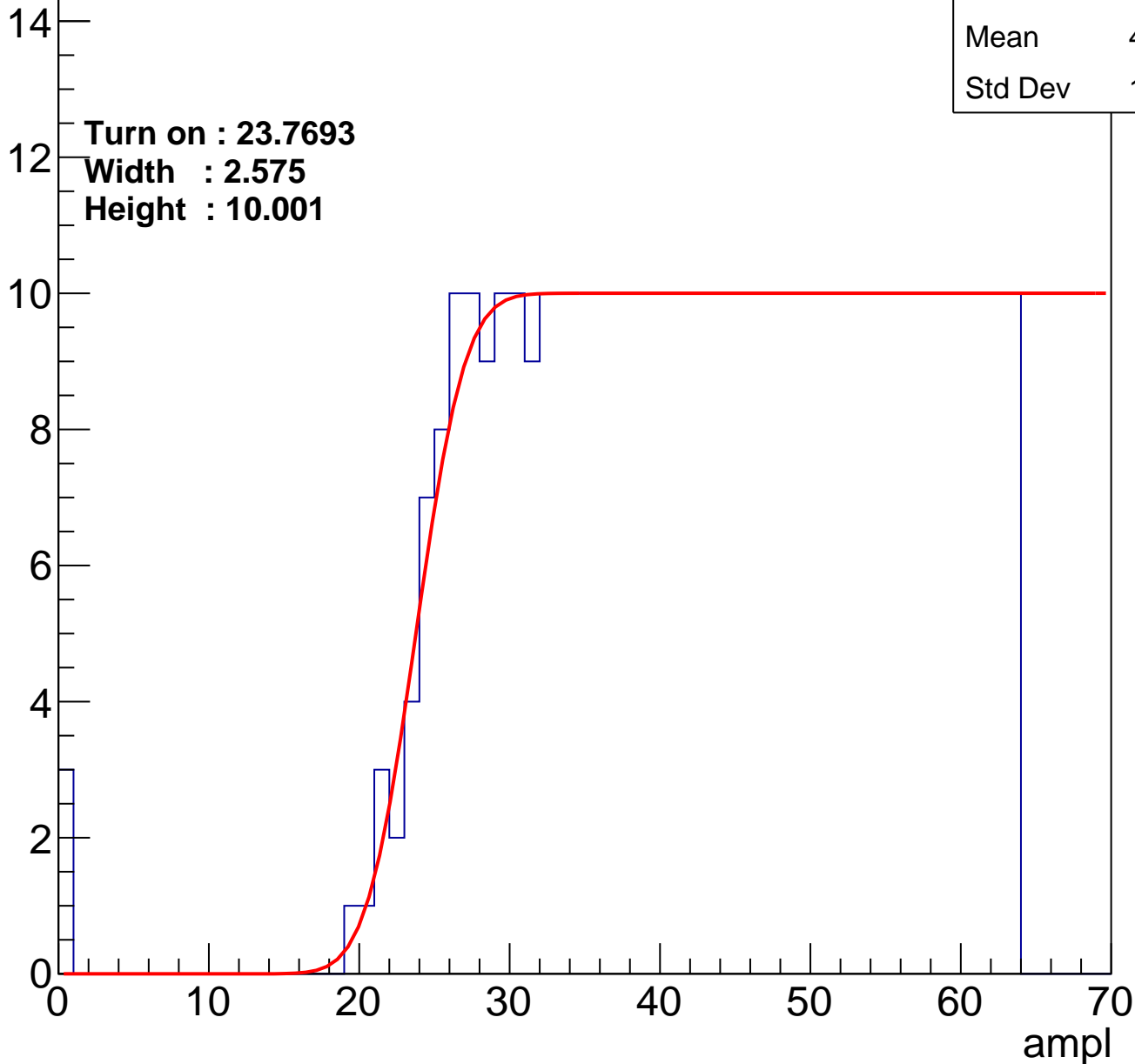
Entry

Entries	407
Mean	42.89
Std Dev	12.33

Turn on : 23.7693

Width : 2.575

Height : 10.001



B0L102S, U4-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.02
Std Dev	11.1

Turn on : 27.8675

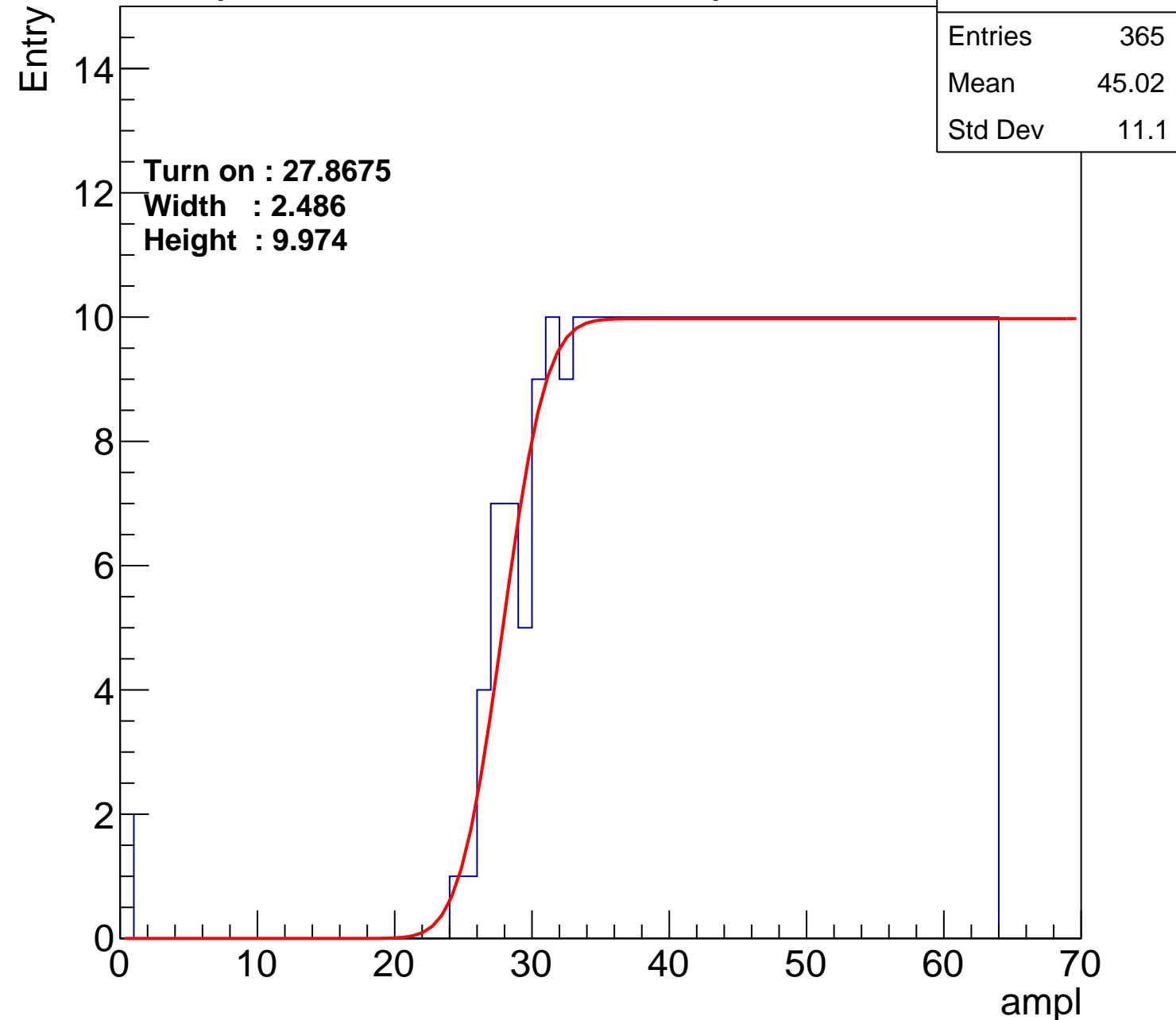
Width : 2.486

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 27.0277

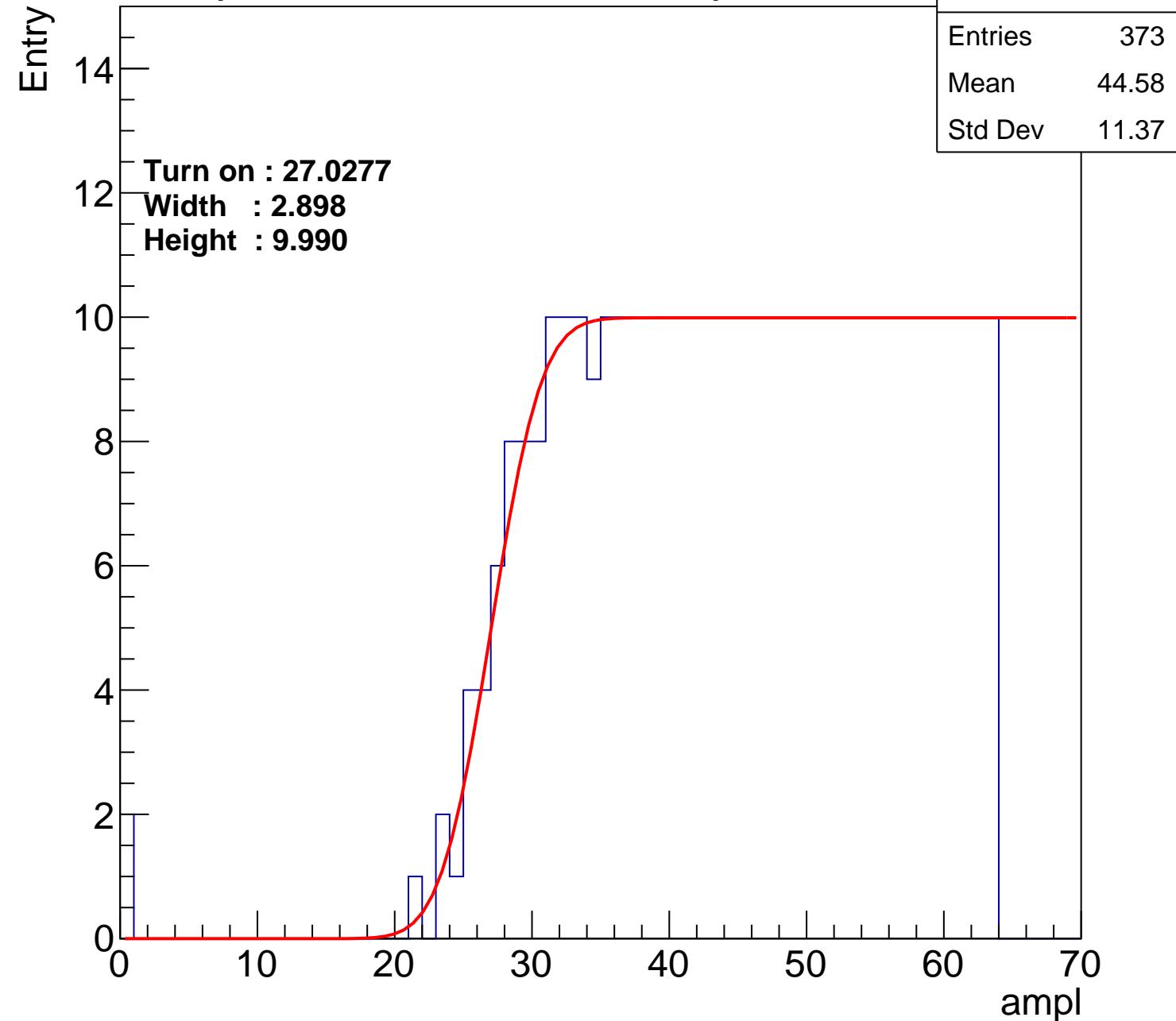
Width : 2.898

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.53
Std Dev	12

Turn on : 25.0290

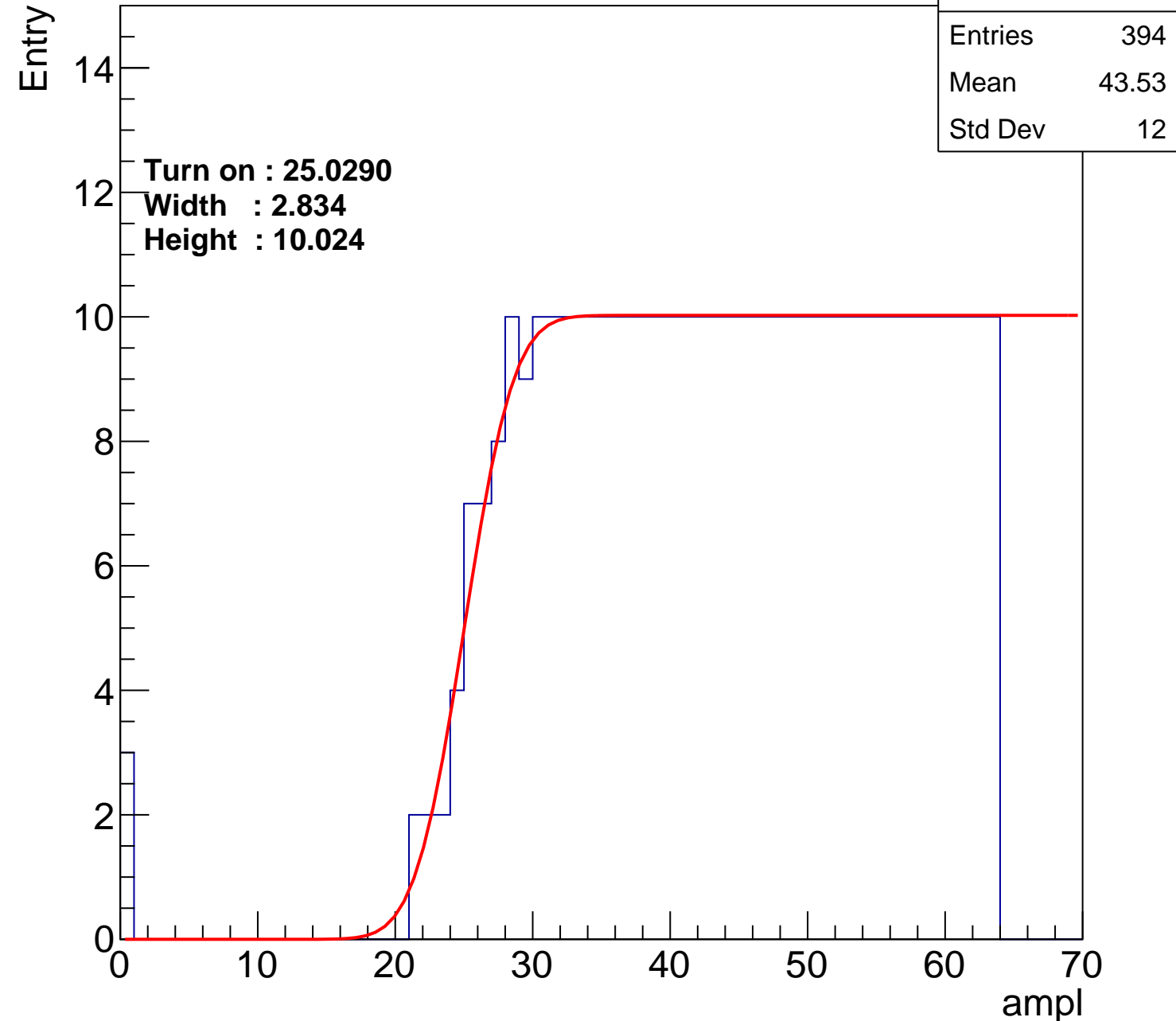
Width : 2.834

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.35
Std Dev	12.08

Turn on : 25.4164

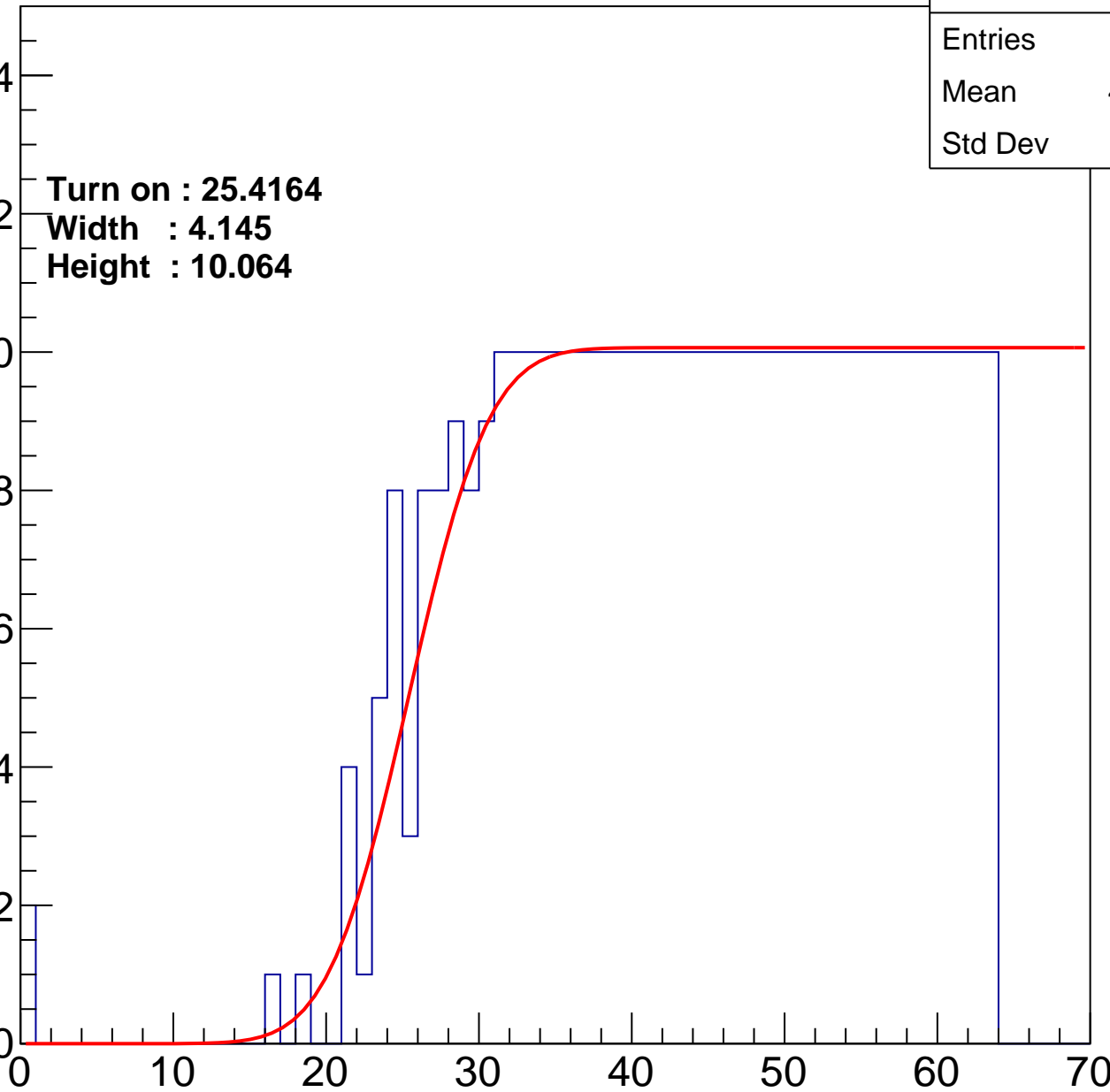
Width : 4.145

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch19

calib_packv5_042523_0143.root, FC#12, port B1

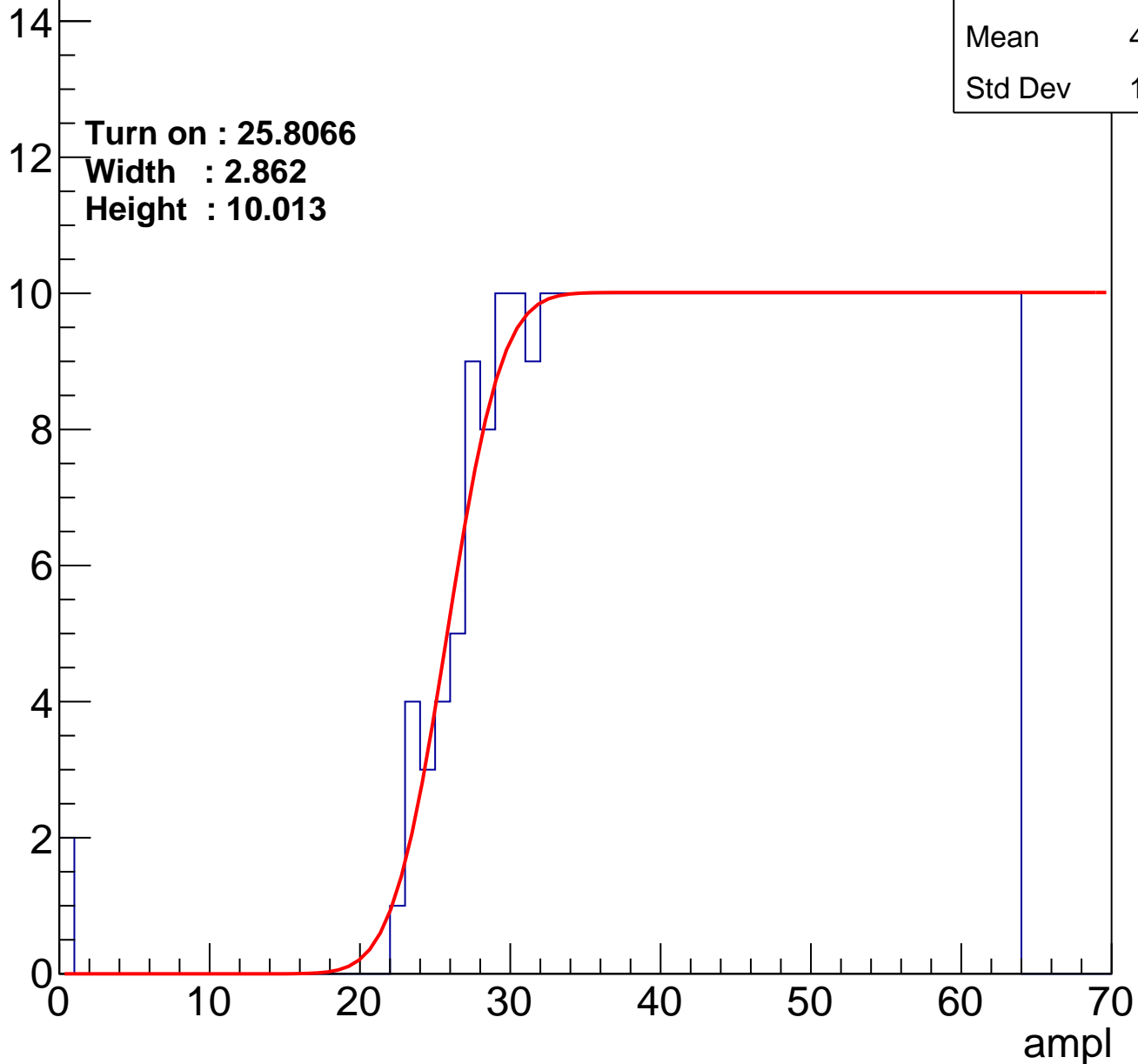
Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.8066

Width : 2.862

Height : 10.013

Entry



B0L102S, U4-ch20

calib_packv5_042523_0143.root, FC#12, port B1

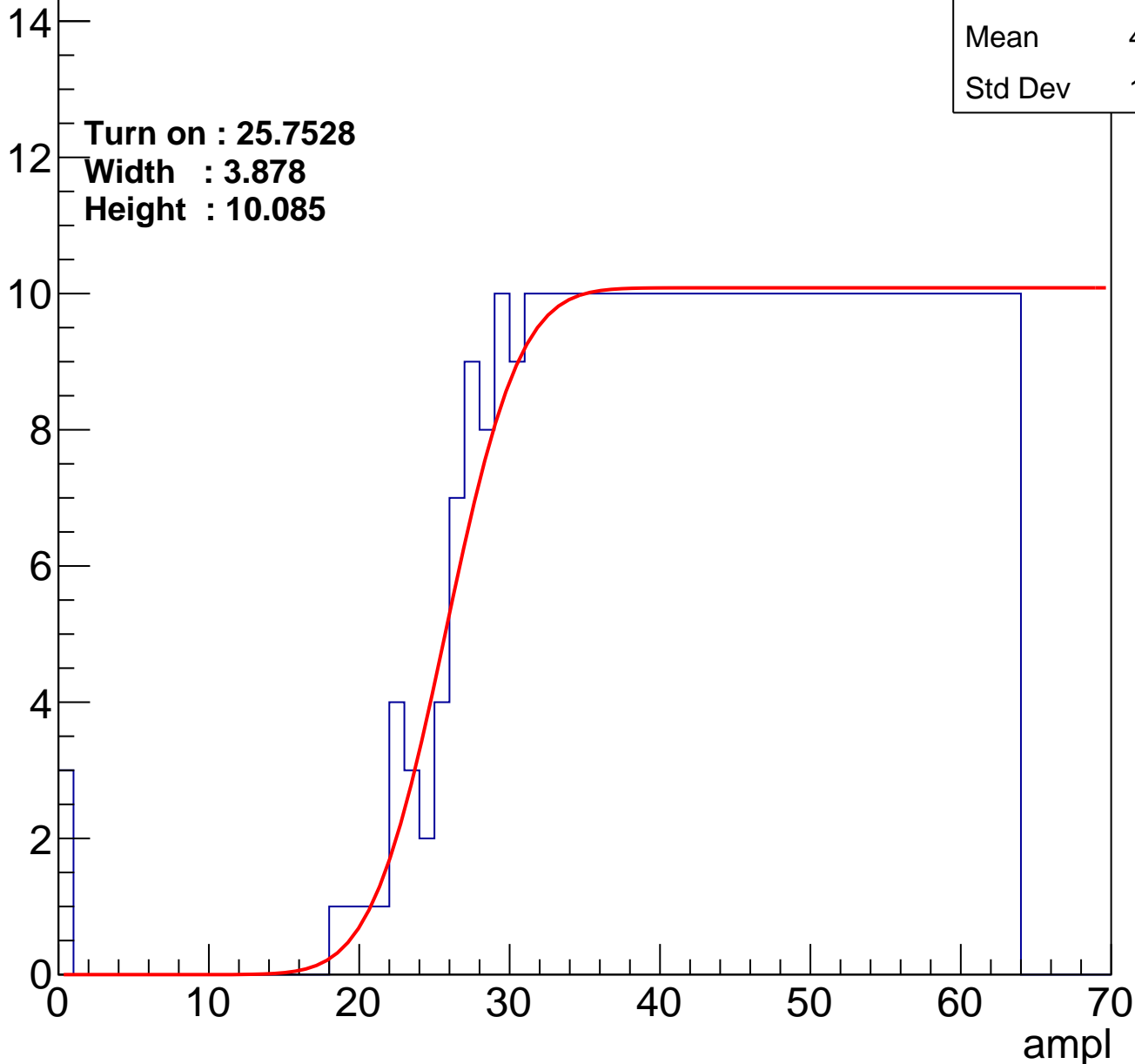
Entries	393
Mean	43.52
Std Dev	12.09

Turn on : 25.7528

Width : 3.878

Height : 10.085

Entry



B0L102S, U4-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.6464

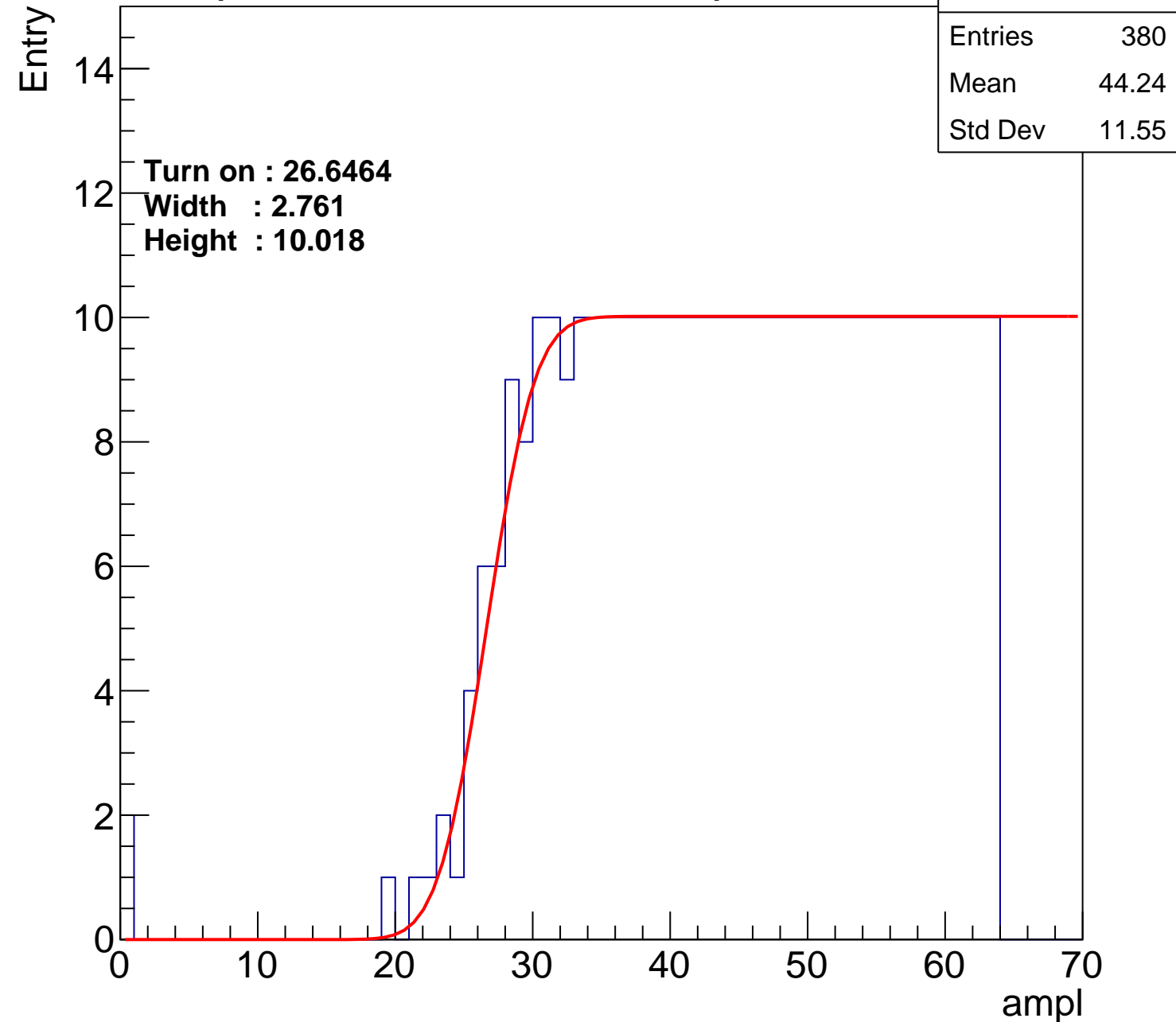
Width : 2.761

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.47
Std Dev	12.06

Turn on : 24.7778

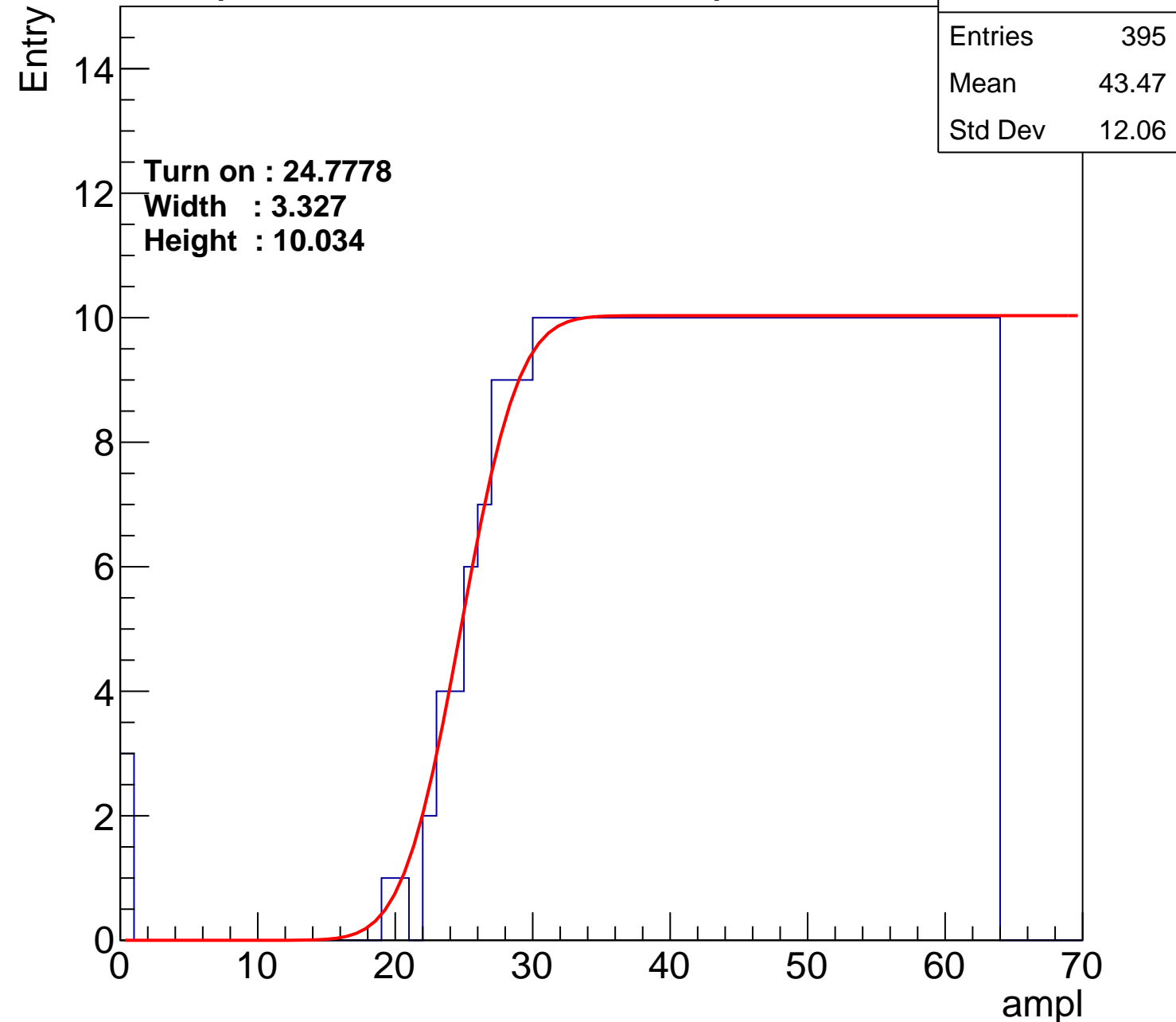
Width : 3.327

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.3
Std Dev	11.63

Turn on : 26.7403

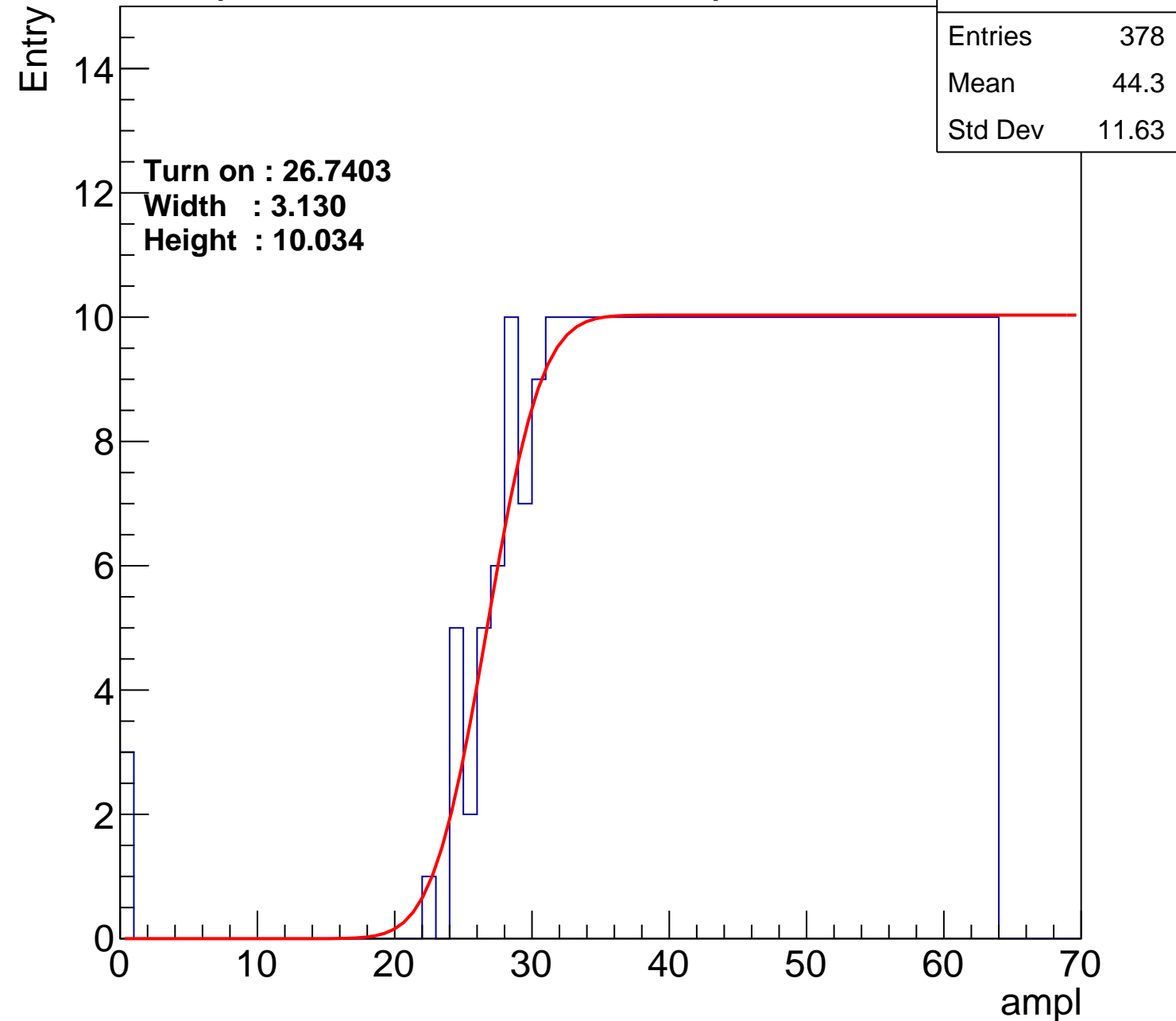
Width : 3.130

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 26.7985

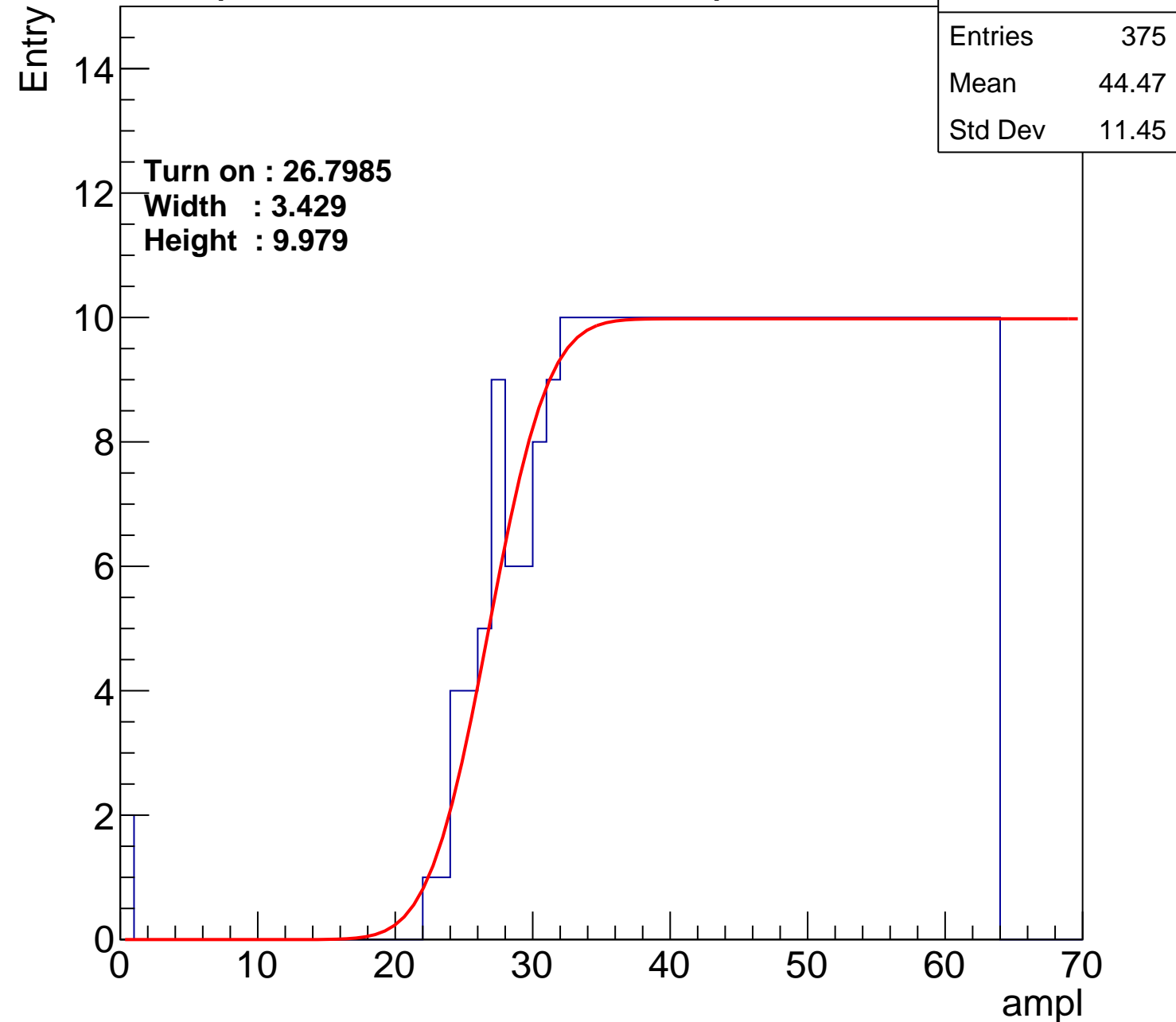
Width : 3.429

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.2
Std Dev	11.68

Turn on : 26.3895

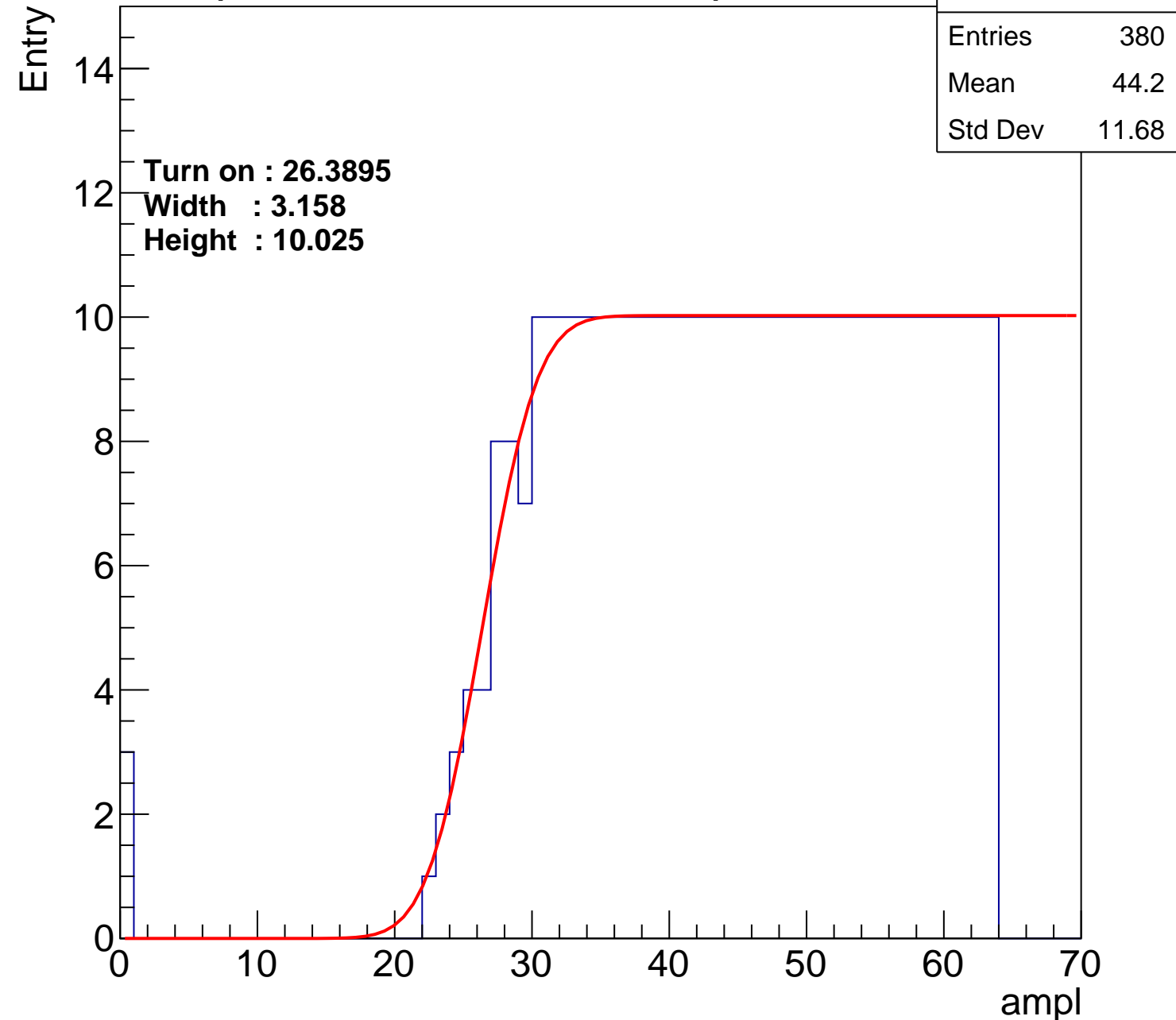
Width : 3.158

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch26

calib_packv5_042523_0143.root, FC#12, port B1

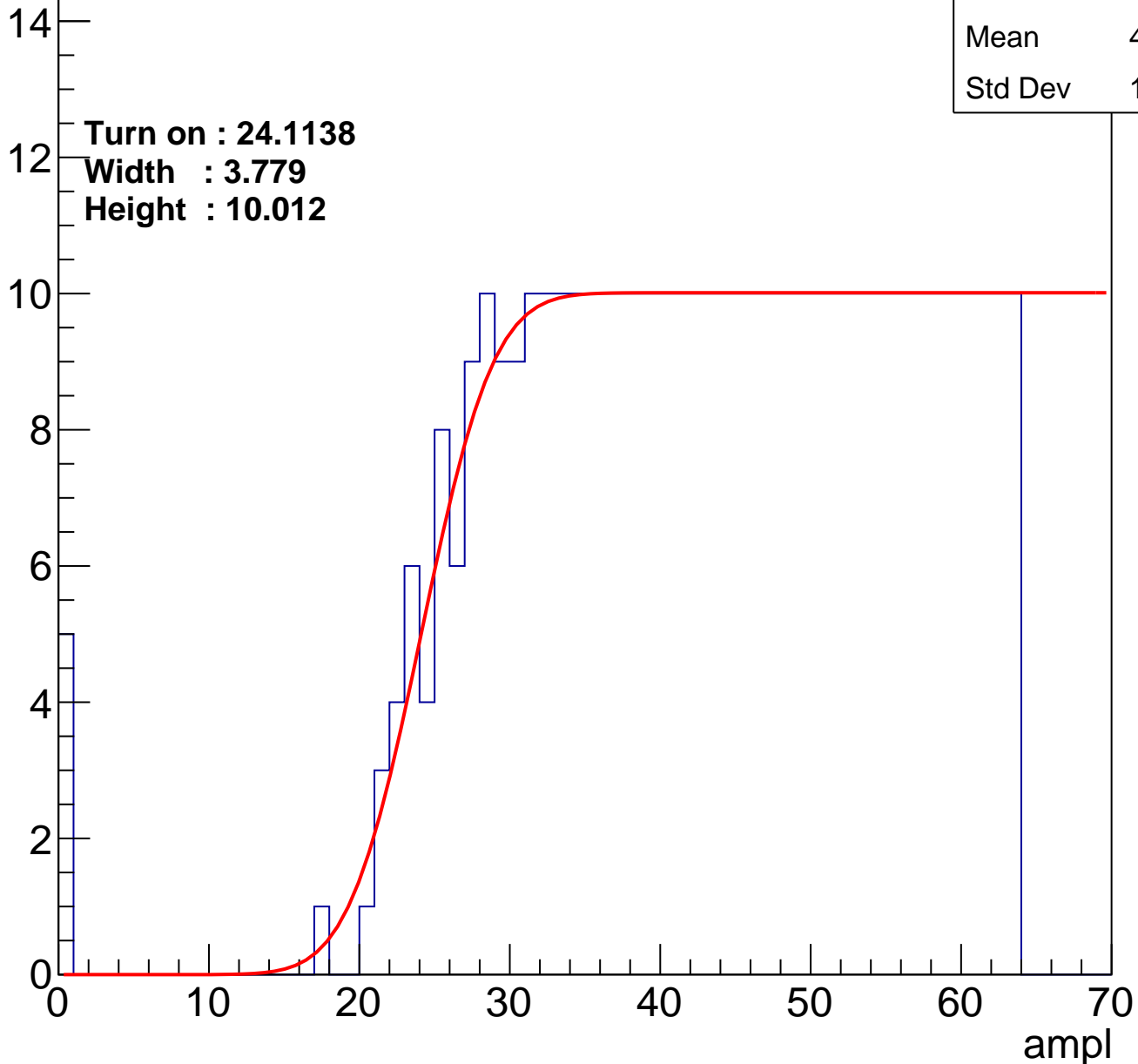
Entries	405
Mean	42.82
Std Dev	12.66

Turn on : 24.1138

Width : 3.779

Height : 10.012

Entry



B0L102S, U4-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.64
Std Dev	11.34

Turn on : 27.3294

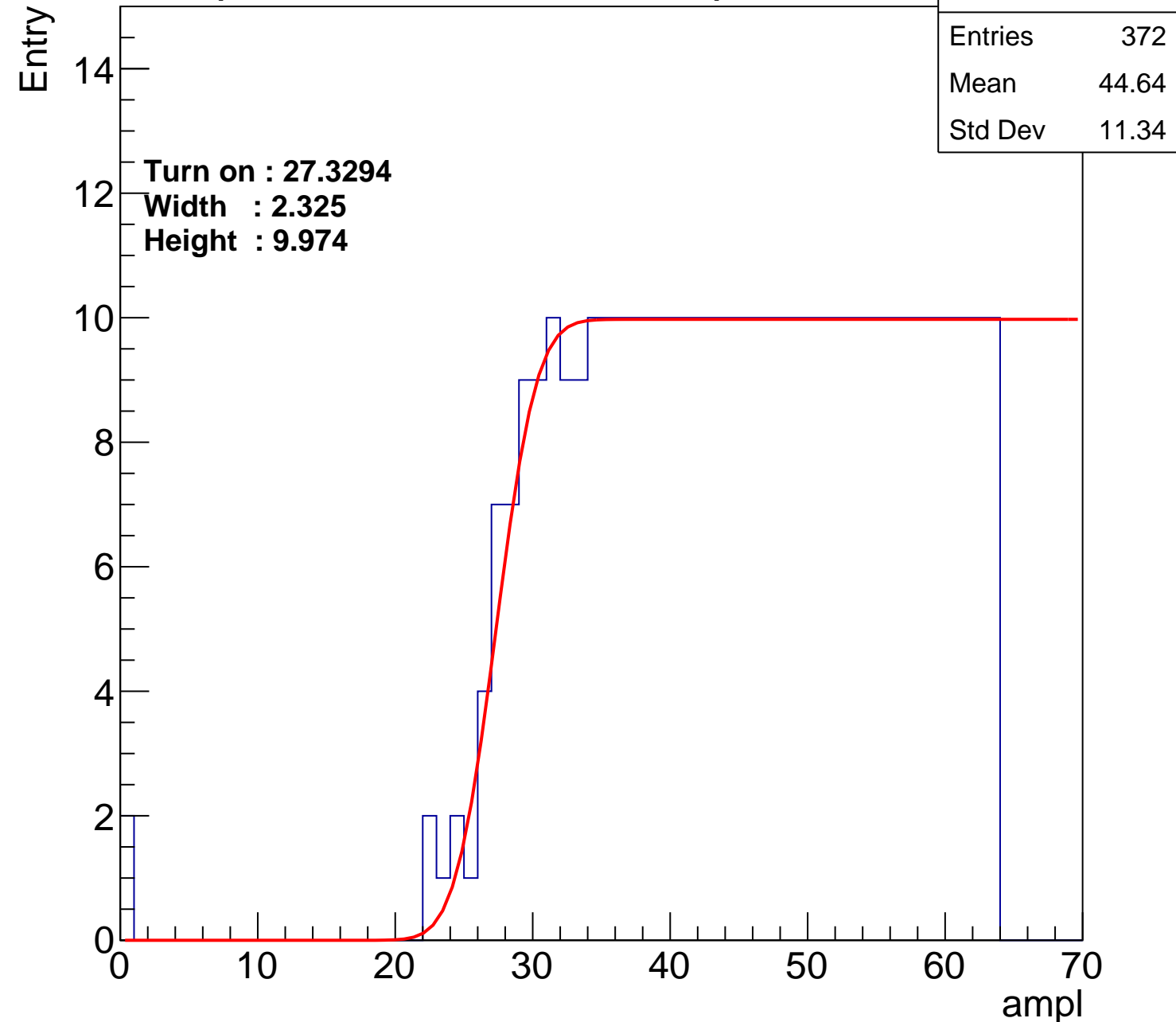
Width : 2.325

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.1
Std Dev	11.79

Turn on : 26.4582

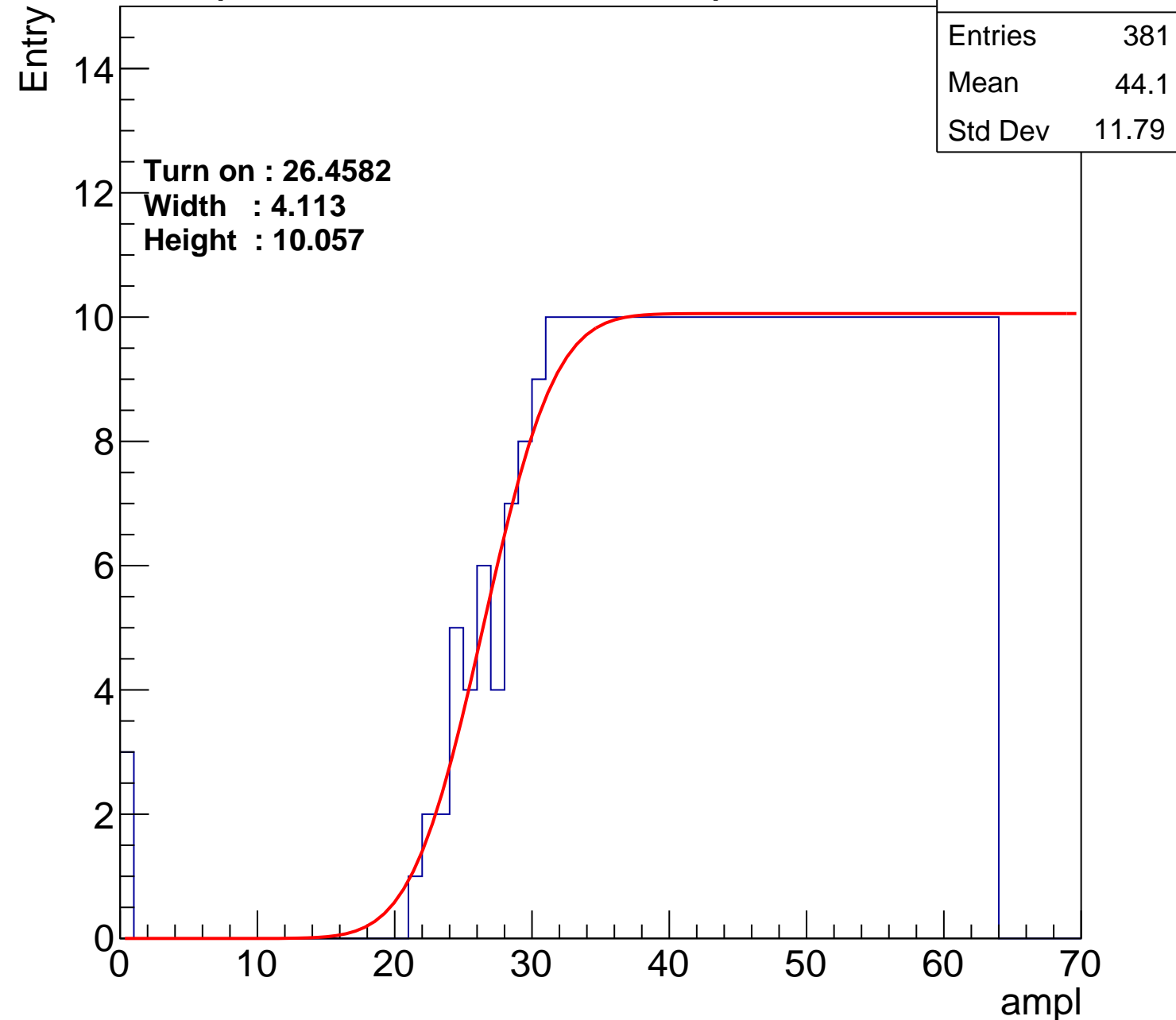
Width : 4.113

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.57
Std Dev	12

Turn on : 24.8368

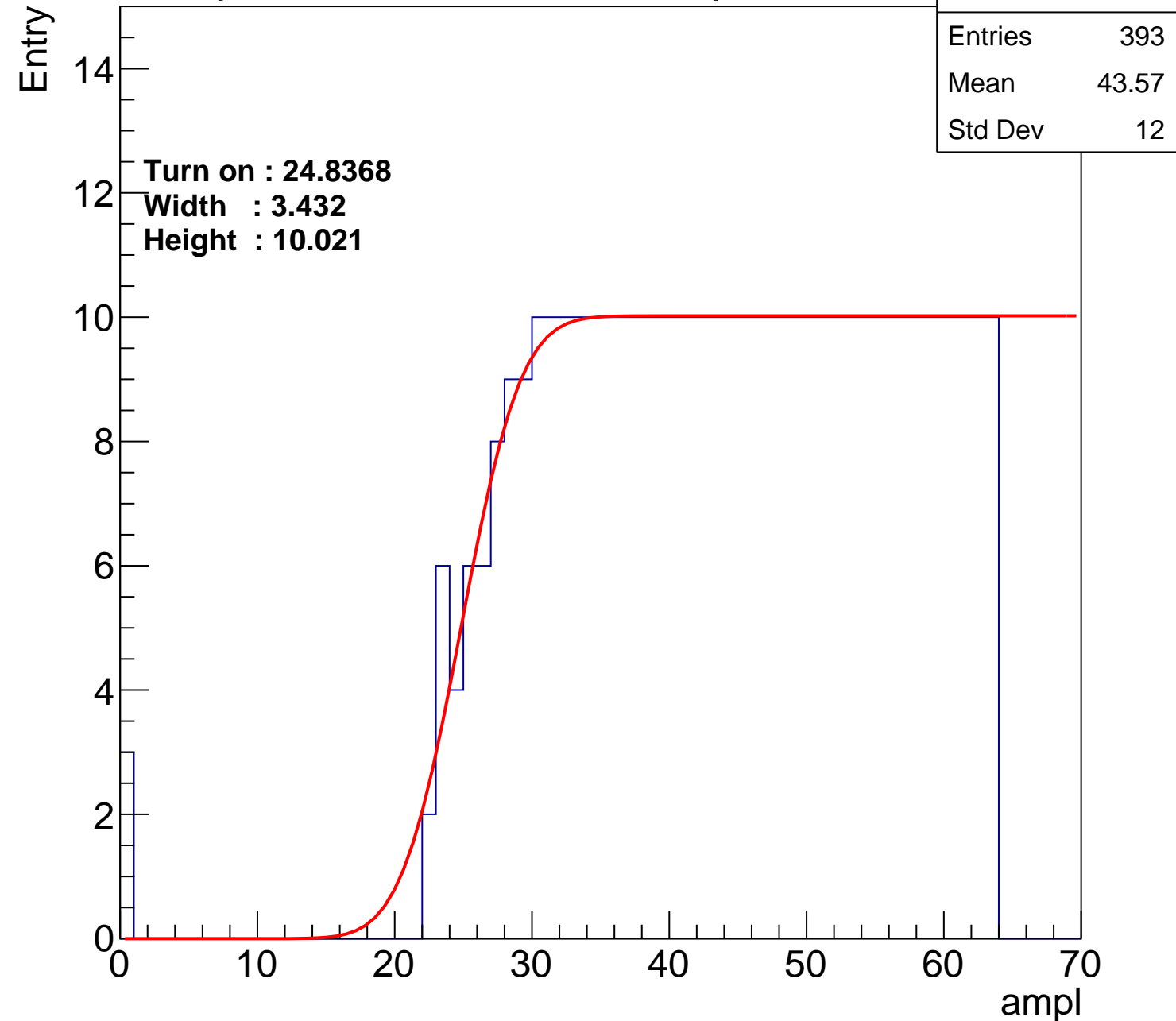
Width : 3.432

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.06
Std Dev	11.7

Turn on : 25.8688

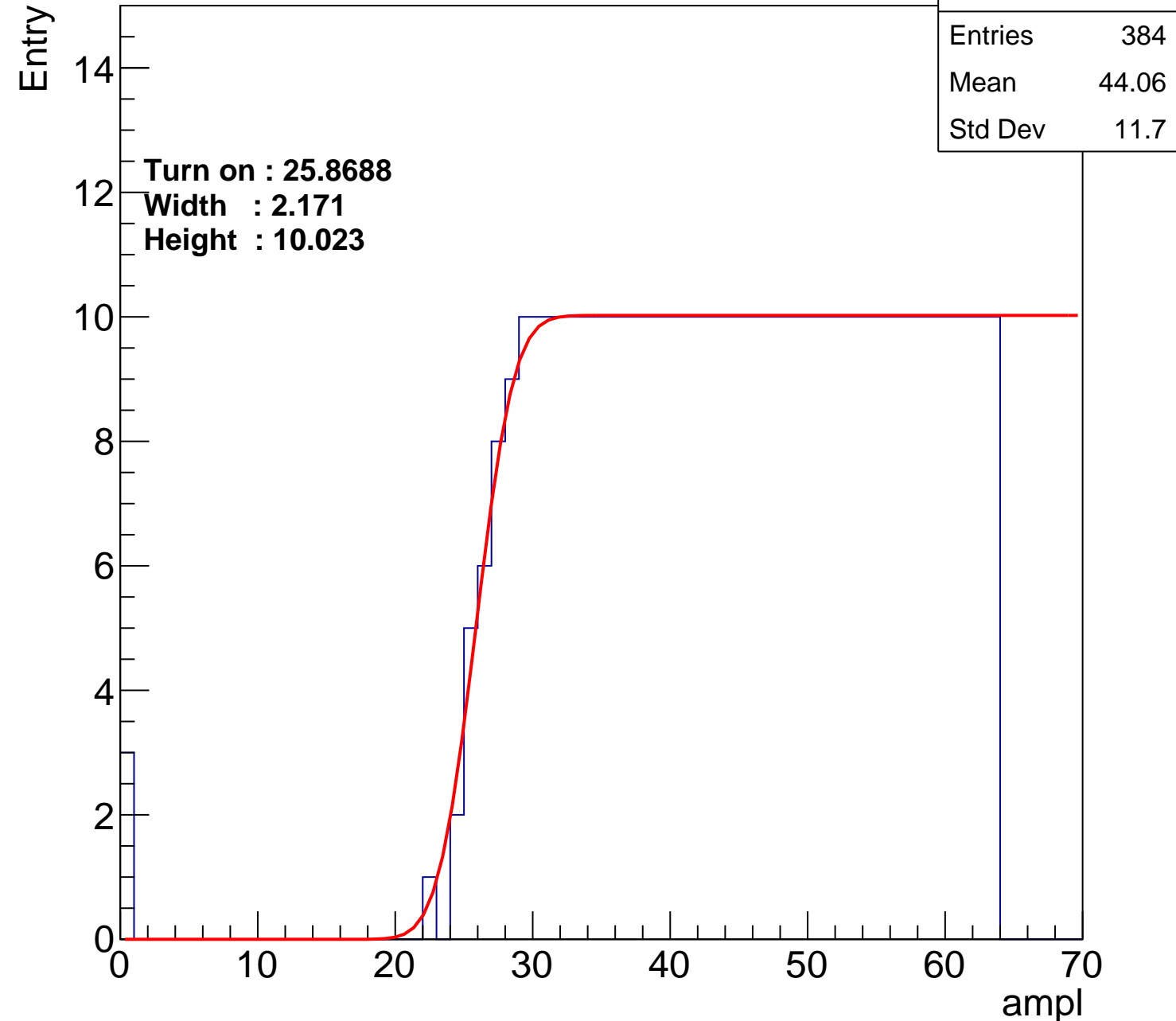
Width : 2.171

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch31

calib_packv5_042523_0143.root, FC#12, port B1

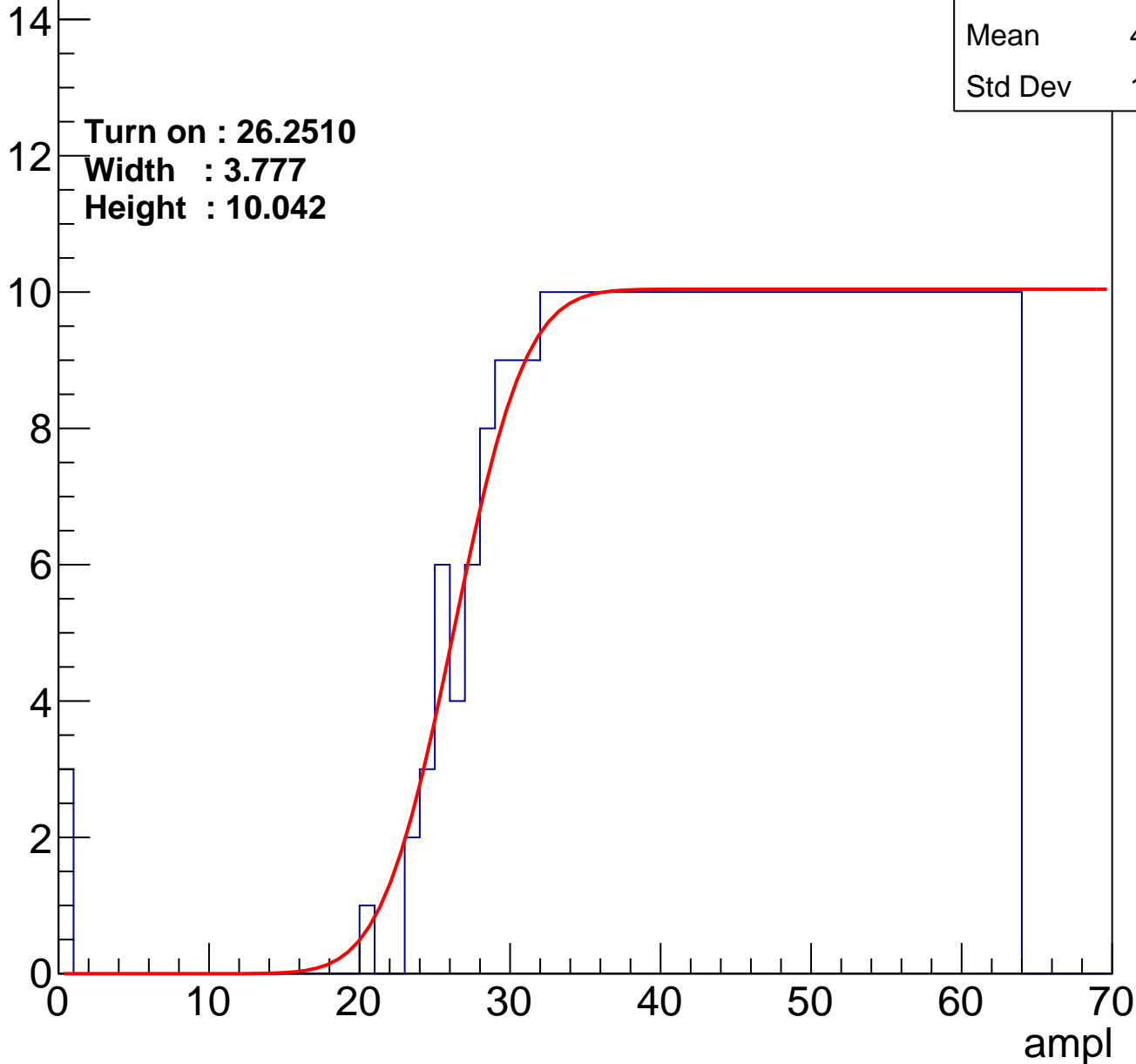
Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.2510

Width : 3.777

Height : 10.042

Entry



B0L102S, U4-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.16
Std Dev	12.22

Turn on : 24.6964

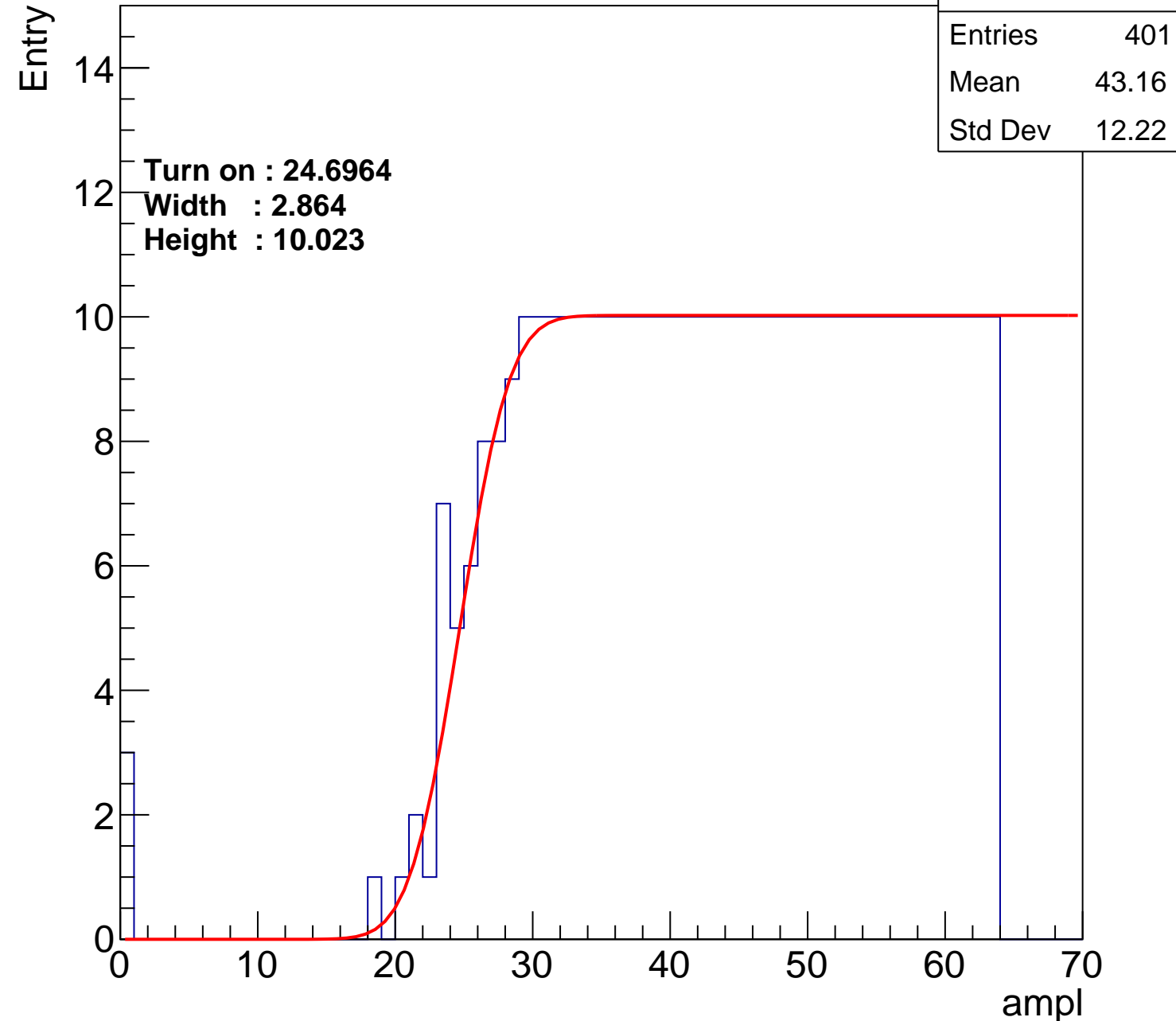
Width : 2.864

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.7859

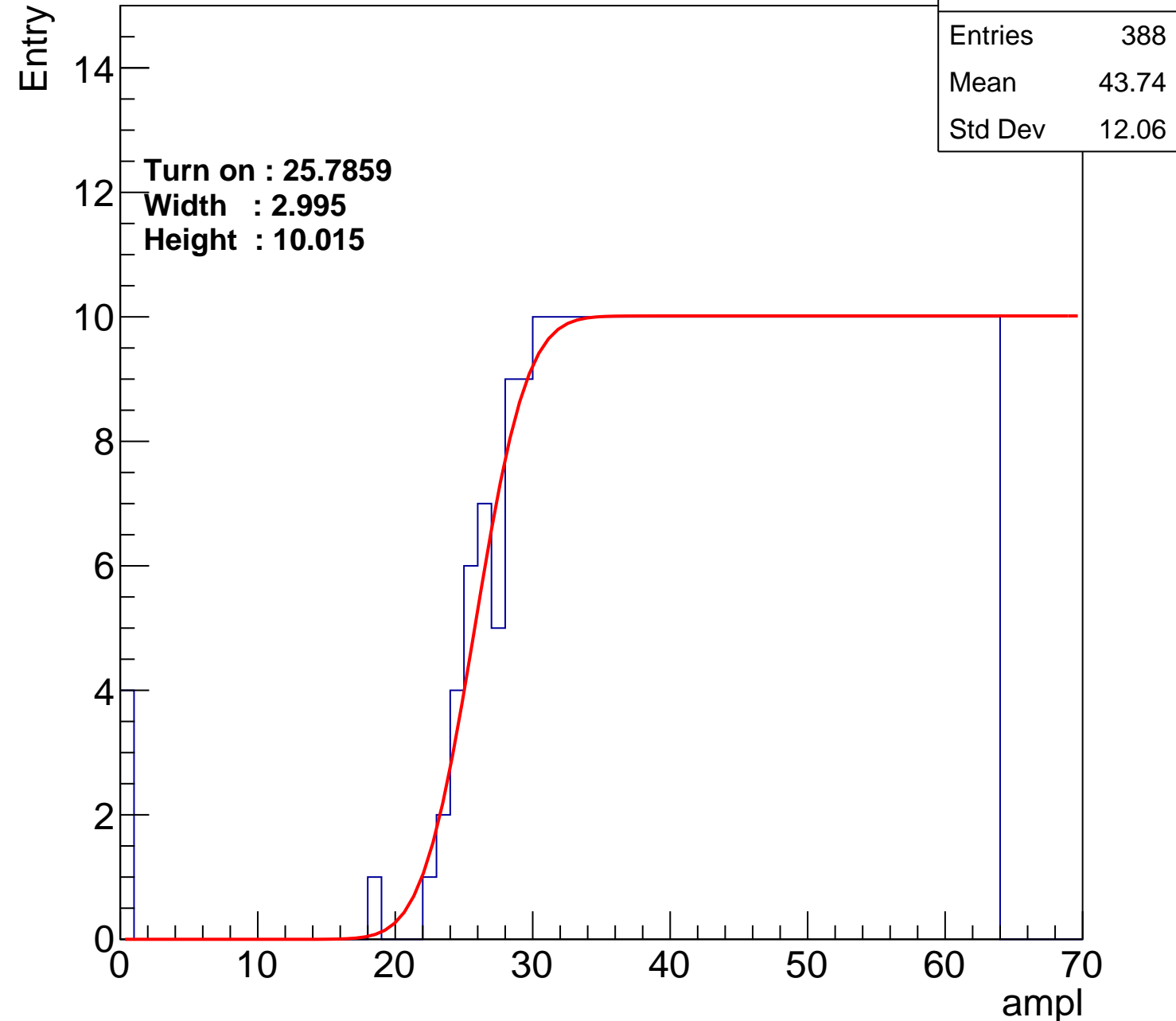
Width : 2.995

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.49
Std Dev	11.5

Turn on : 26.9383

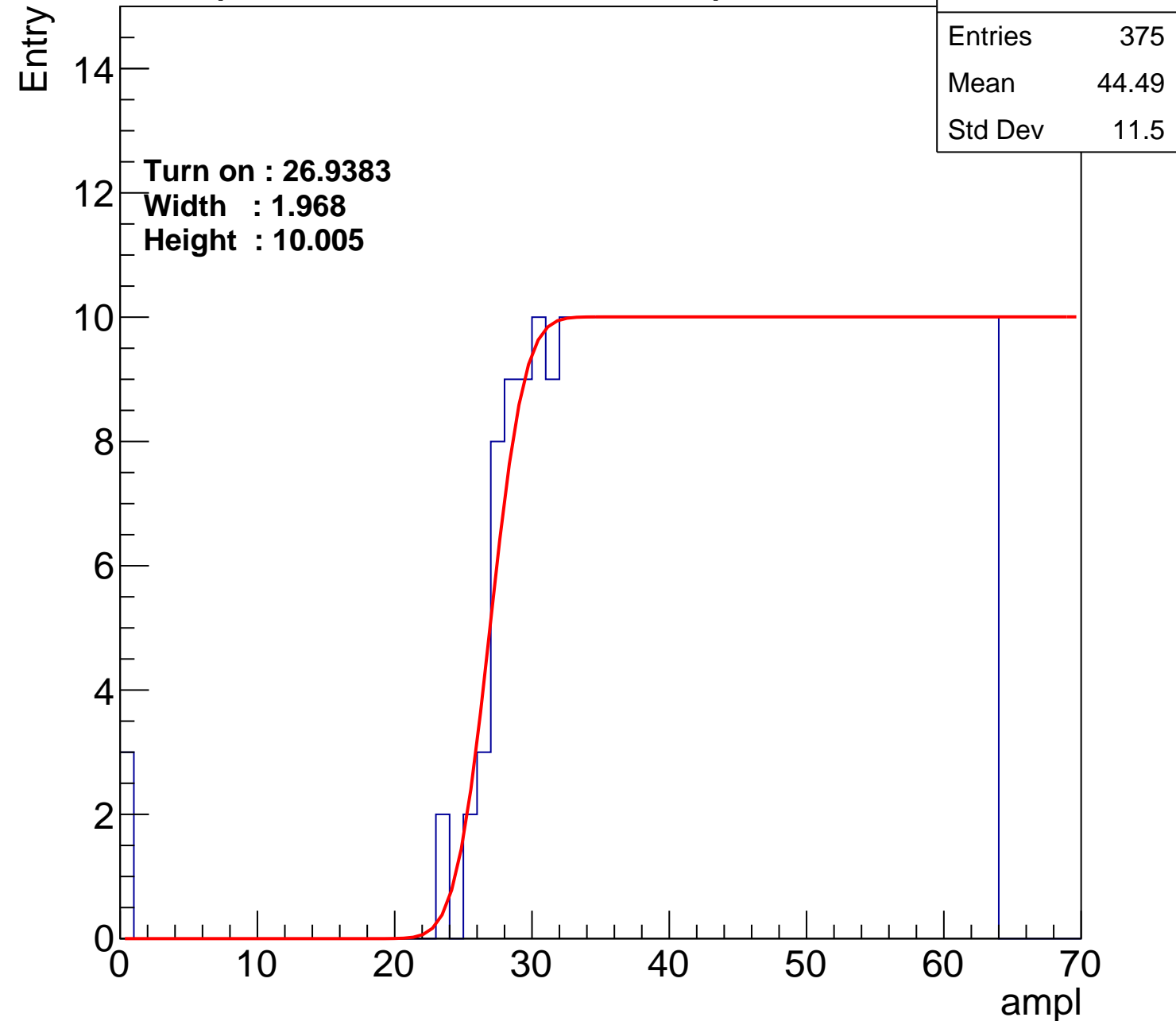
Width : 1.968

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.21
Std Dev	12.32

Turn on : 24.4627

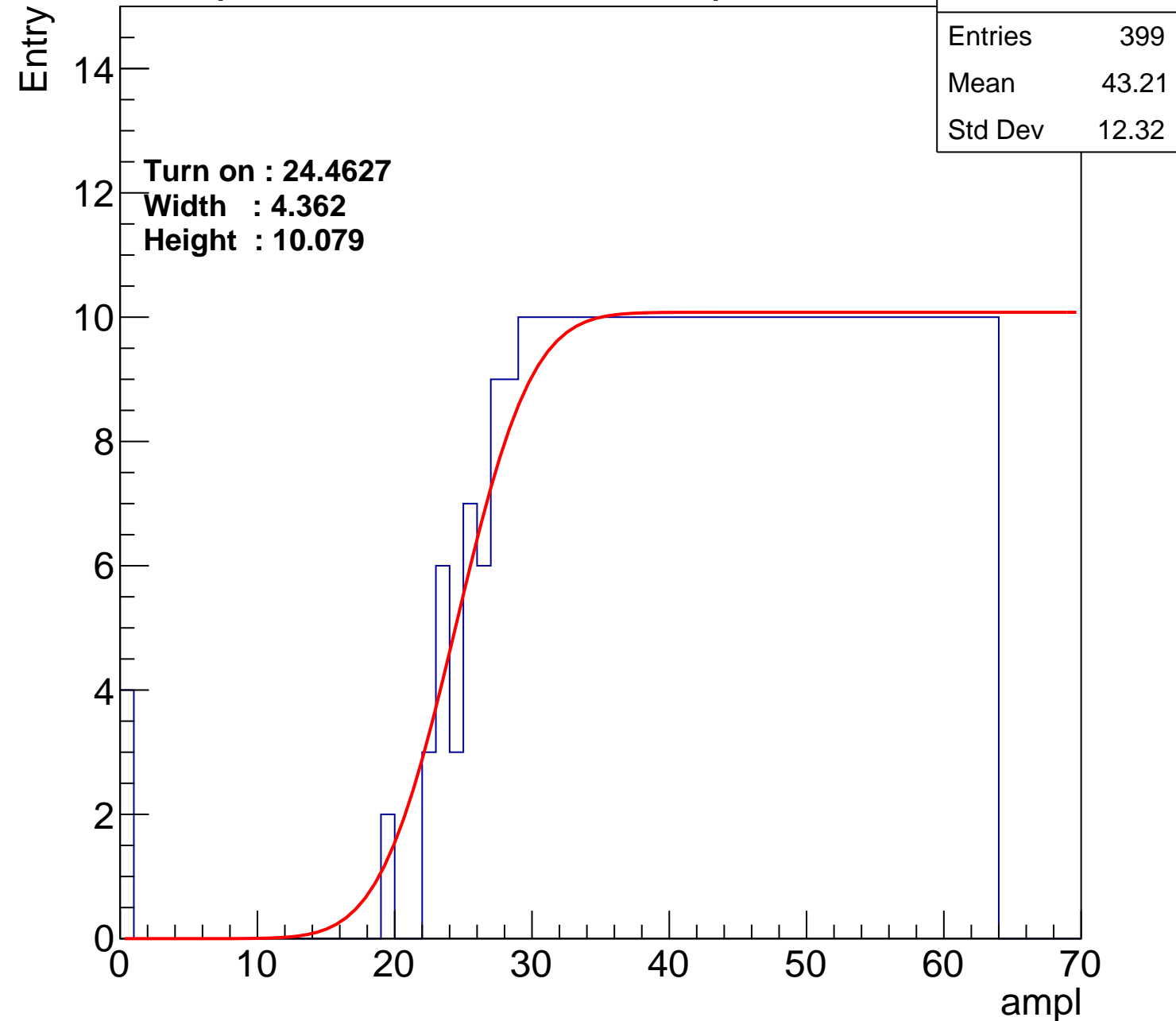
Width : 4.362

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 25.8042

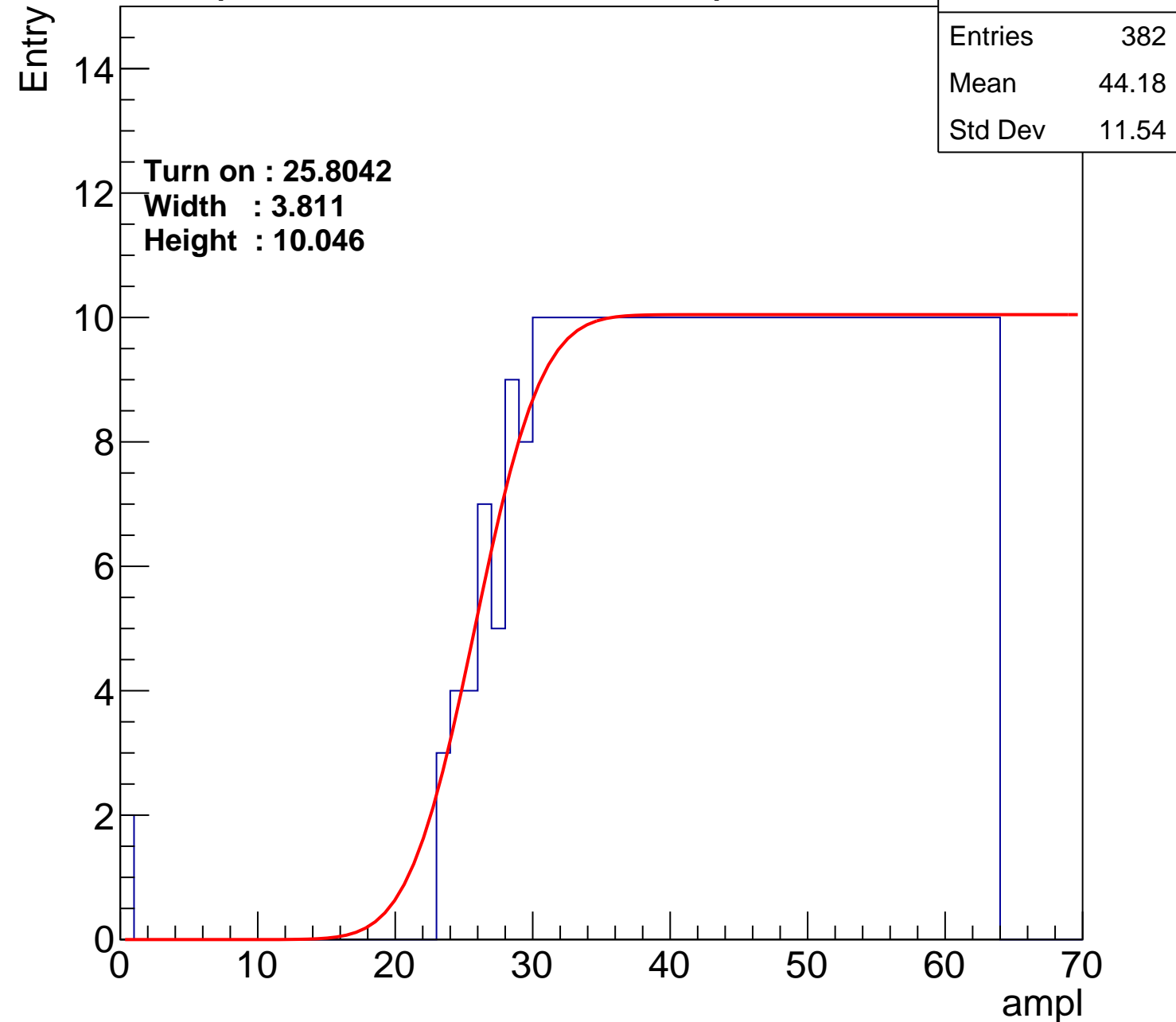
Width : 3.811

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.6094

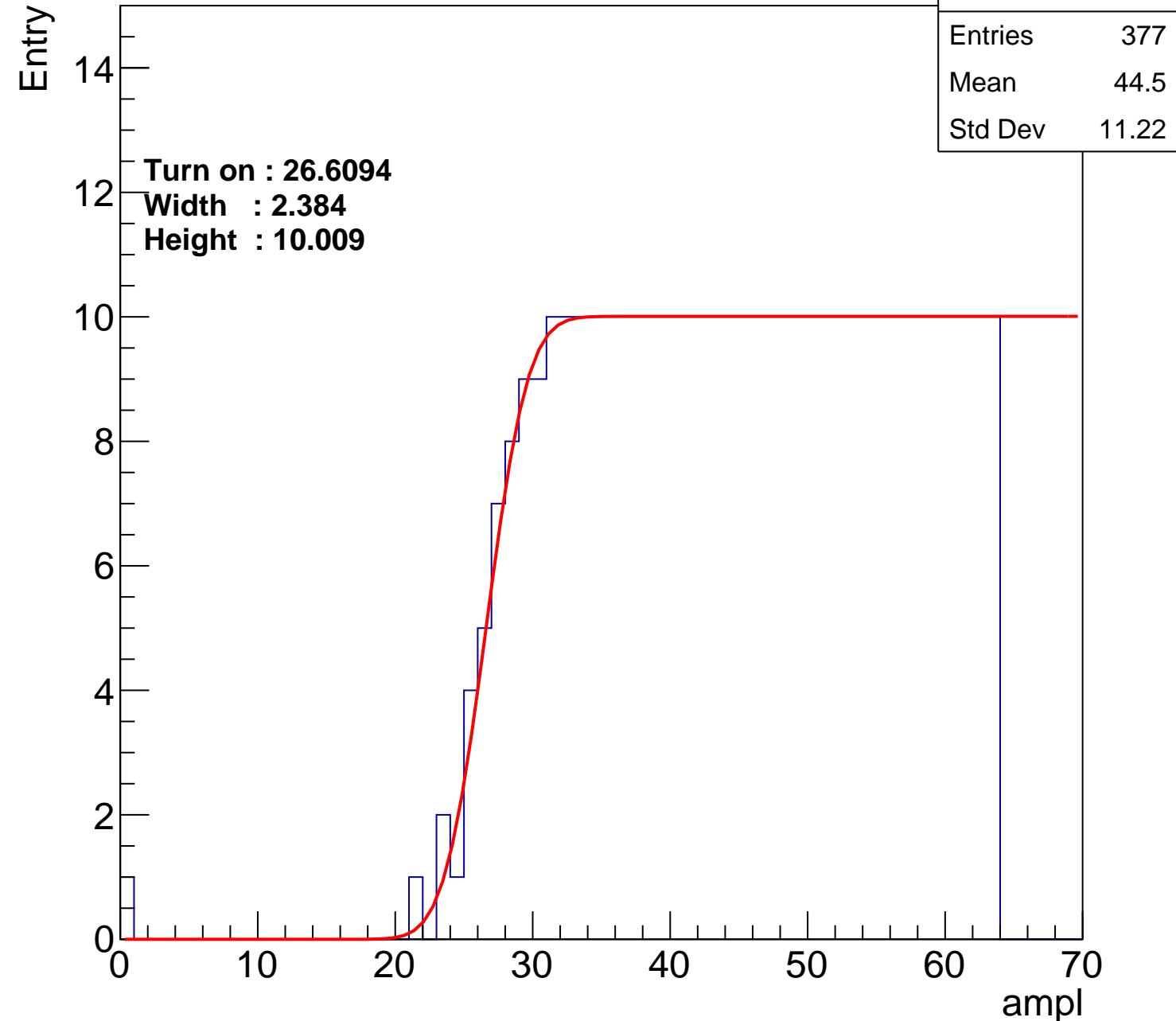
Width : 2.384

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch38

calib_packv5_042523_0143.root, FC#12, port B1

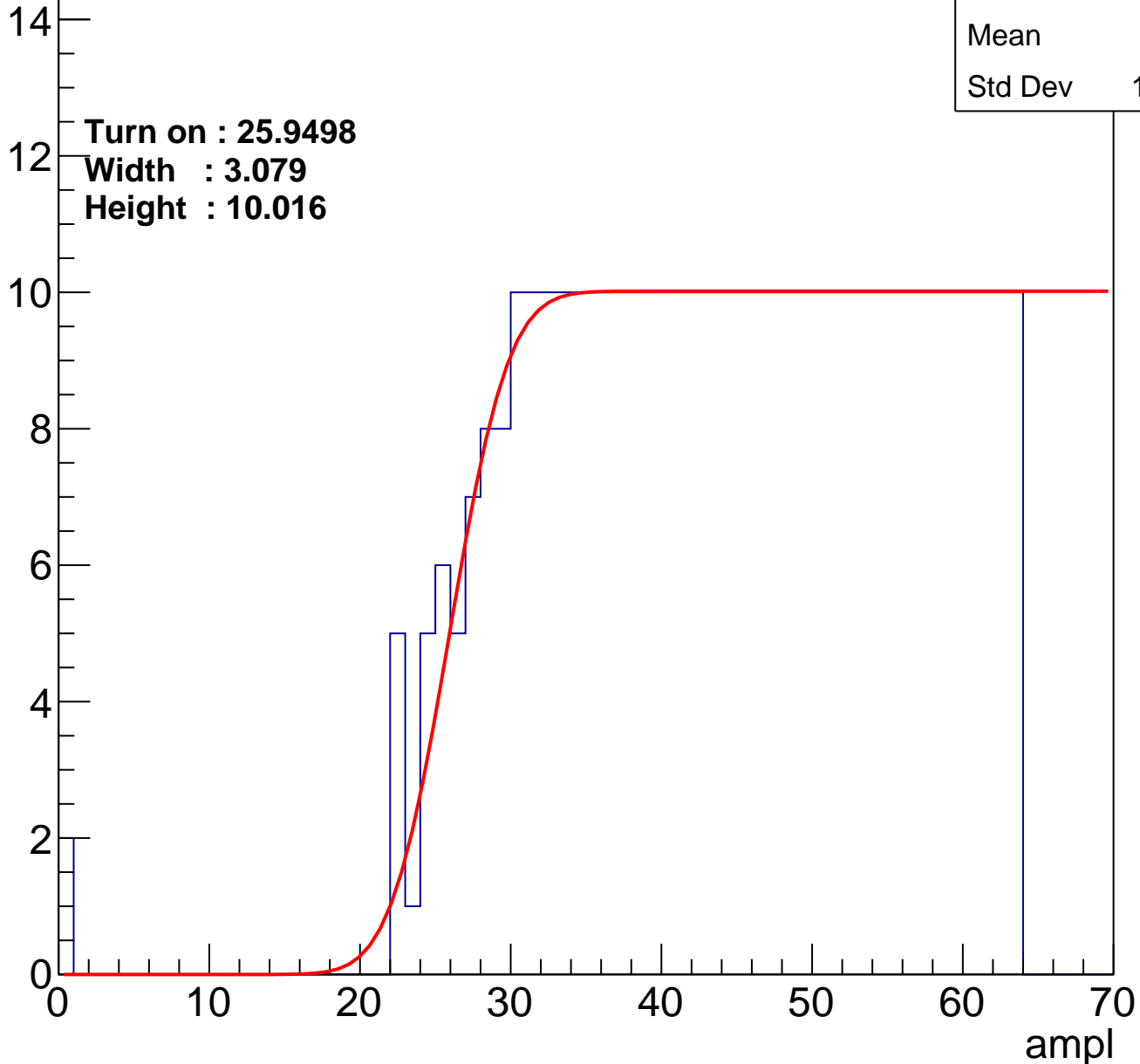
Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 25.9498

Width : 3.079

Height : 10.016

Entry



B0L102S, U4-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.25
Std Dev	12.09

Turn on : 26.8536

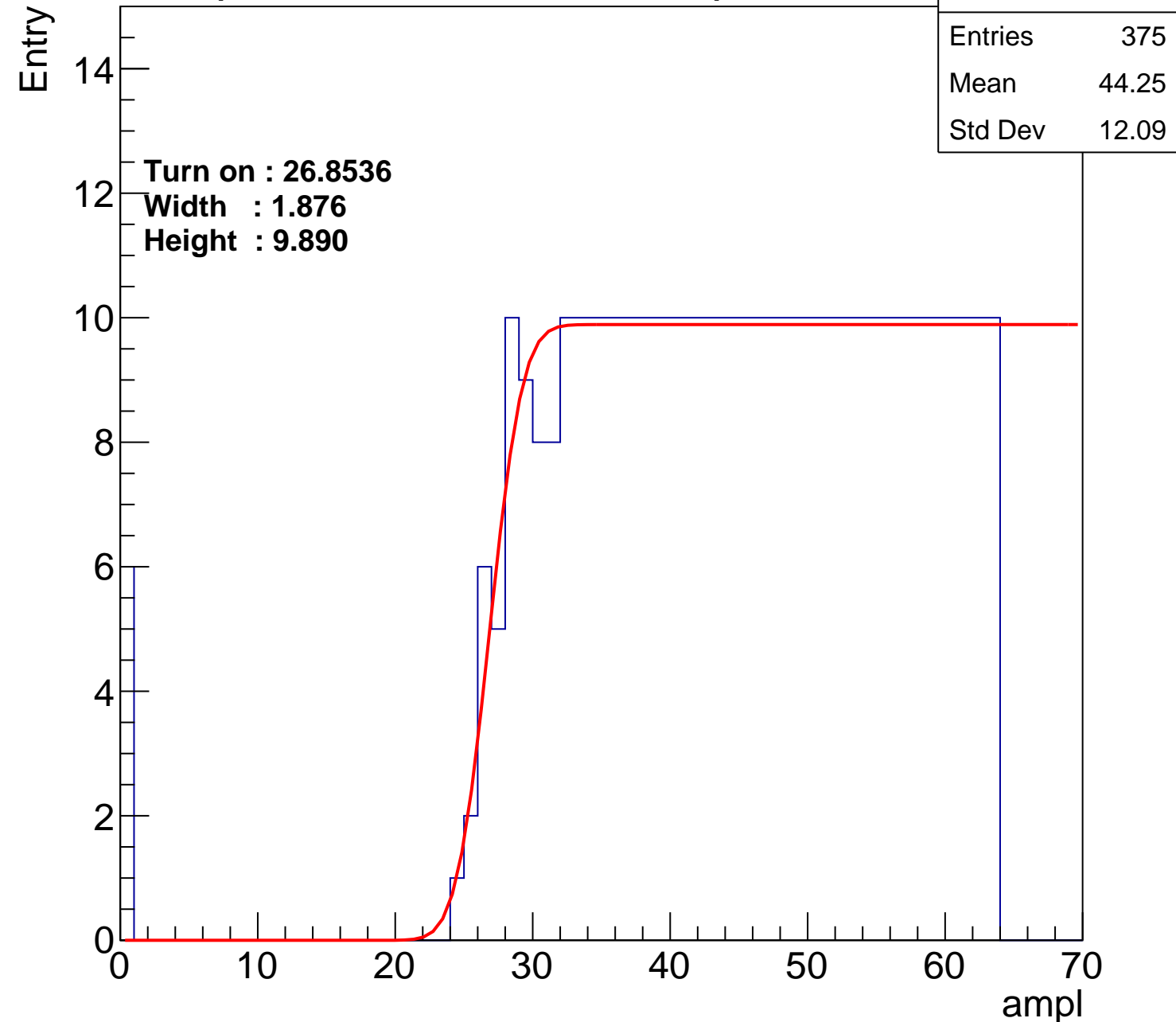
Width : 1.876

Height : 9.890

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.84
Std Dev	11.65

Turn on : 26.3492

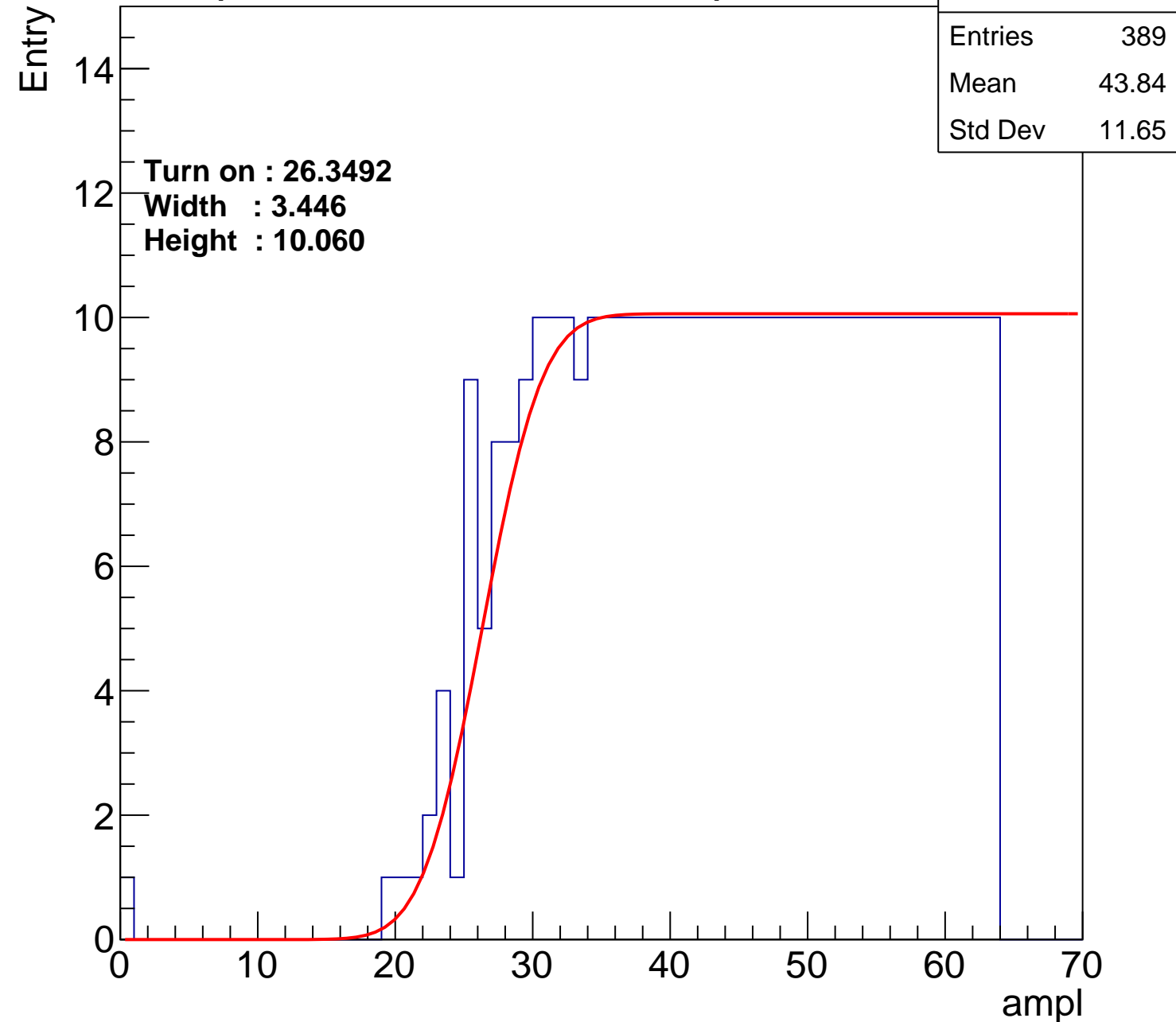
Width : 3.446

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.54
Std Dev	12.04

Turn on : 25.3478

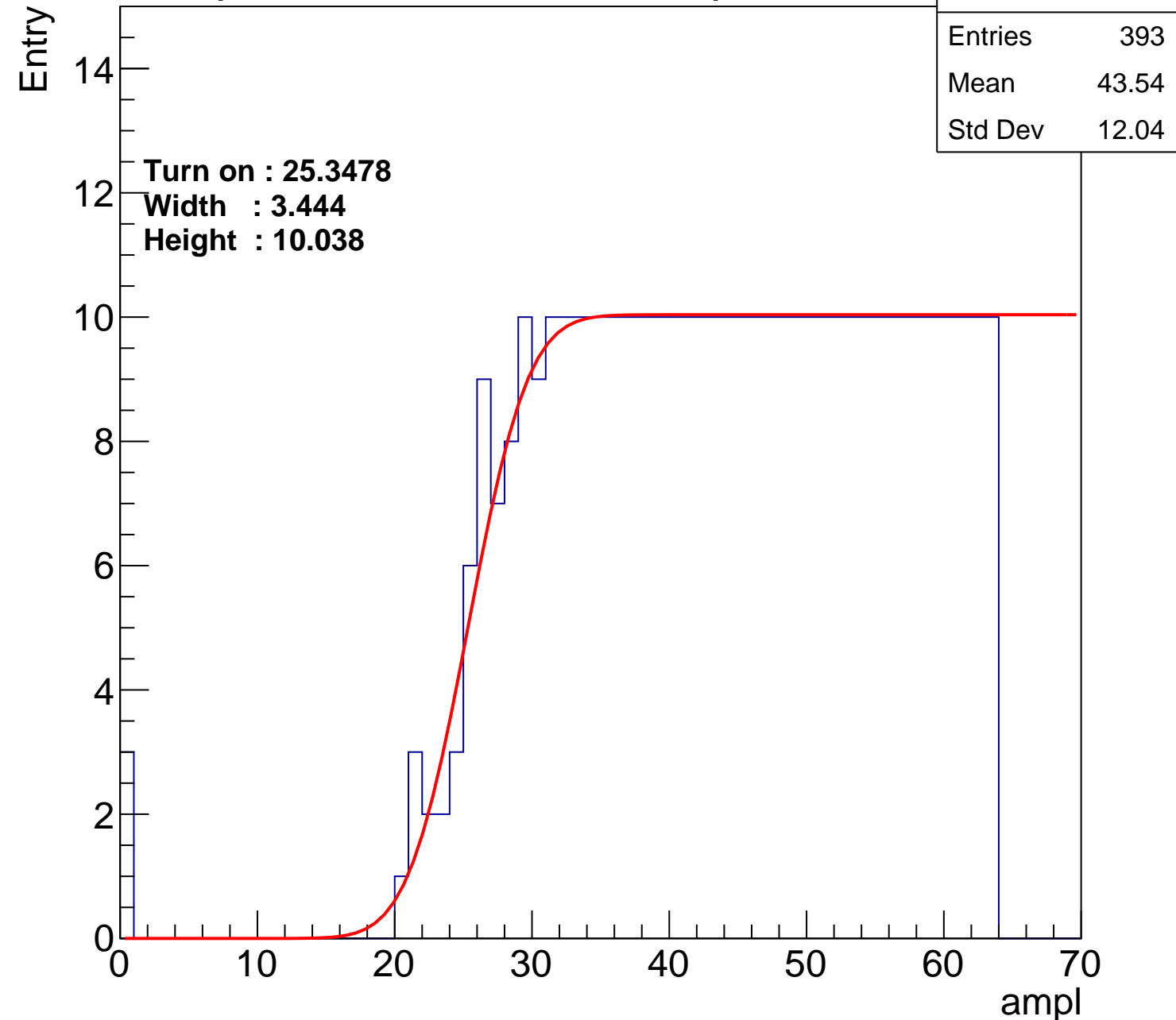
Width : 3.444

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.53
Std Dev	12.08

Turn on : 25.1859

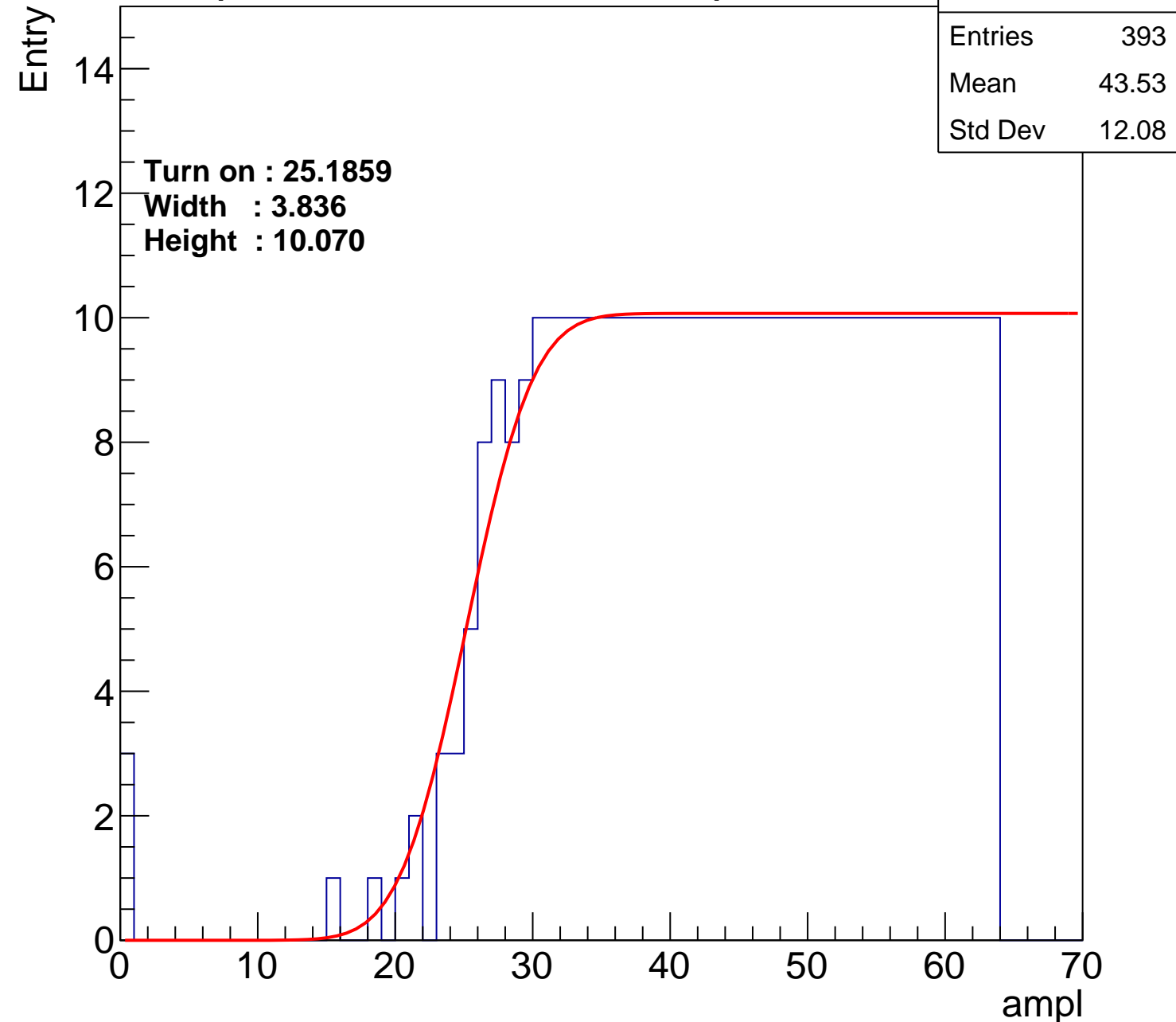
Width : 3.836

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.77
Std Dev	12.07

Turn on : 25.9057

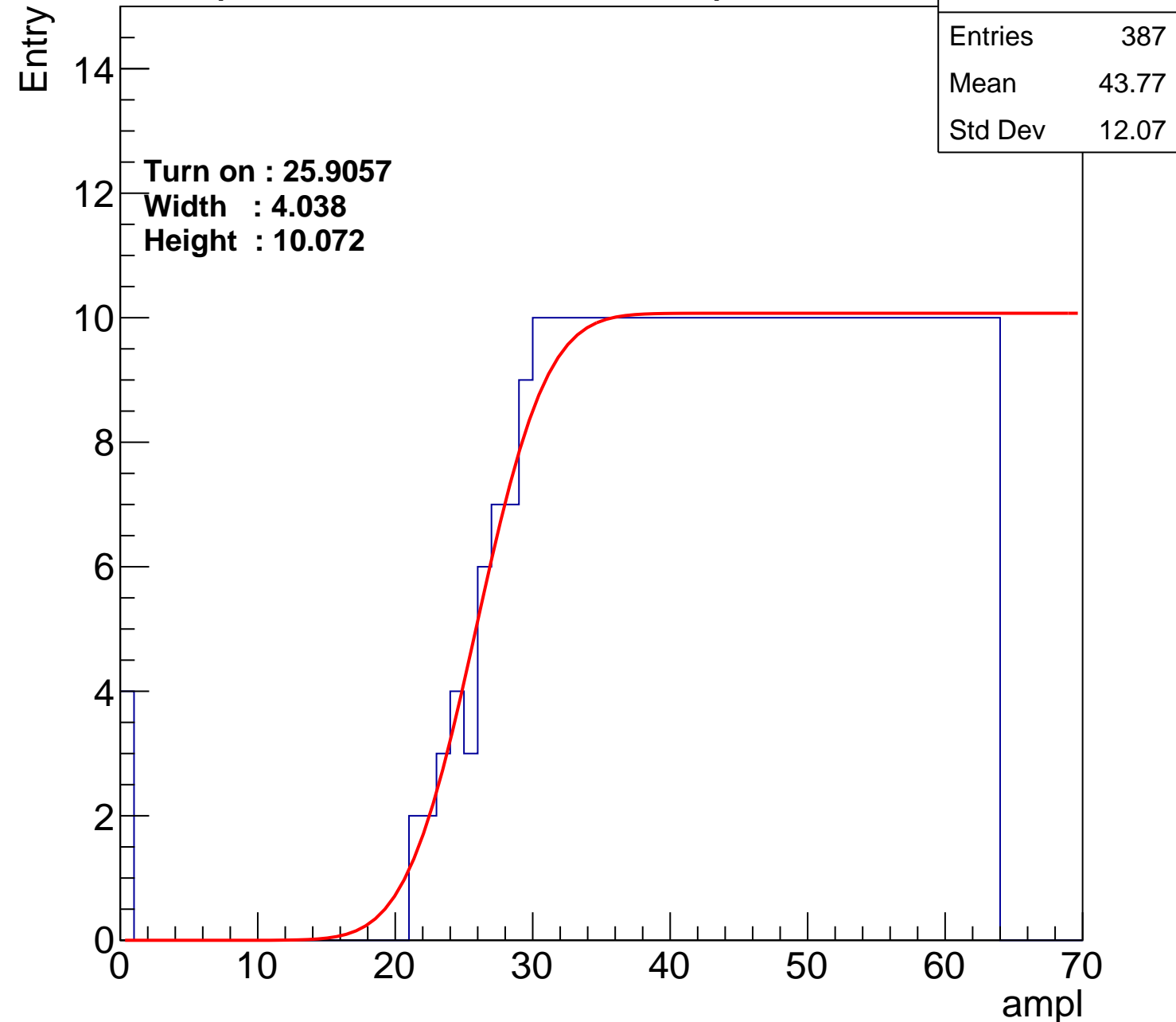
Width : 4.038

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.9
Std Dev	11.78

Turn on : 26.0739

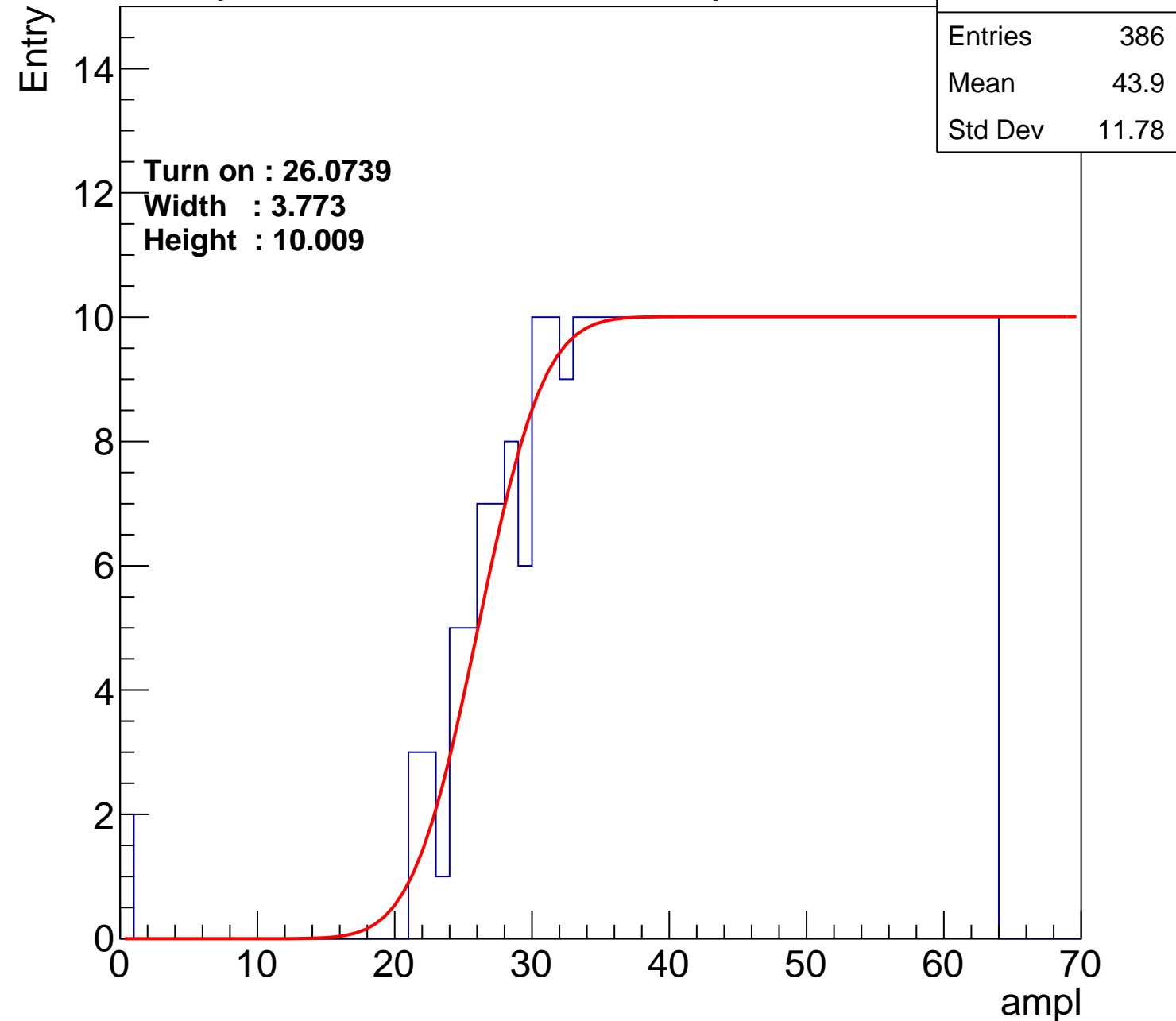
Width : 3.773

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.04
Std Dev	11.83

Turn on : 26.1427

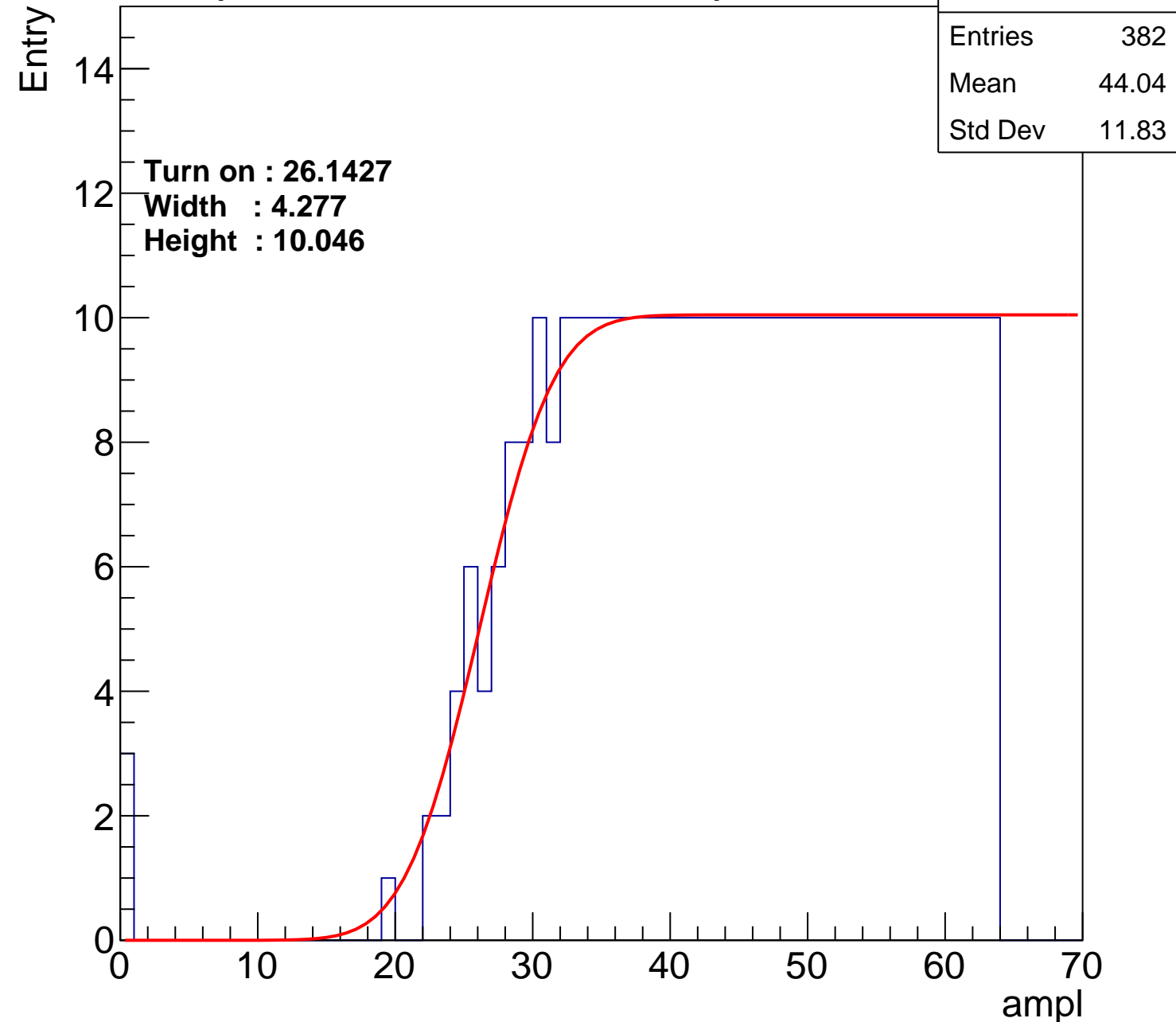
Width : 4.277

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.72
Std Dev	12.24

Turn on : 22.9756

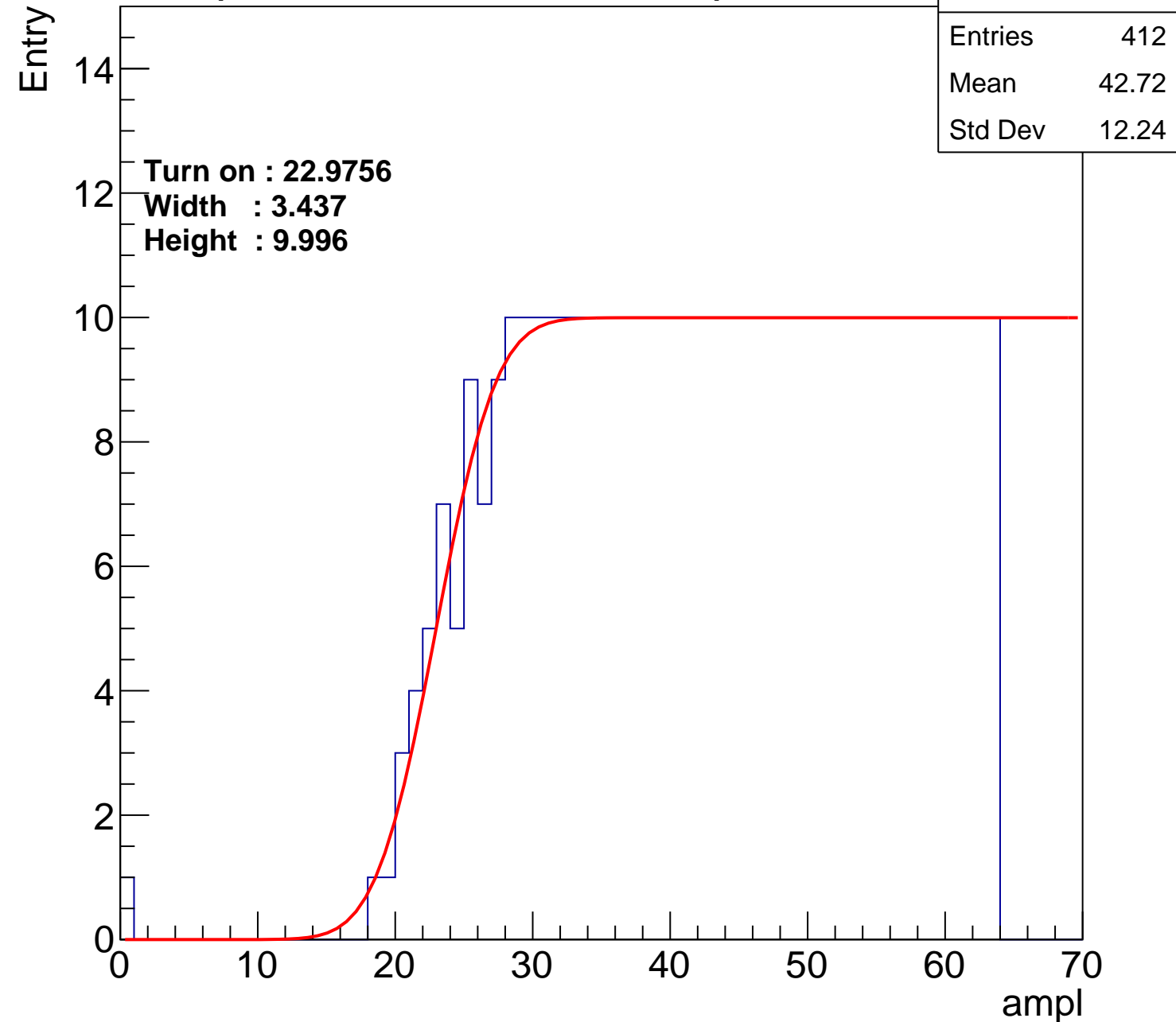
Width : 3.437

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.04
Std Dev	12.09

Turn on : 27.0833

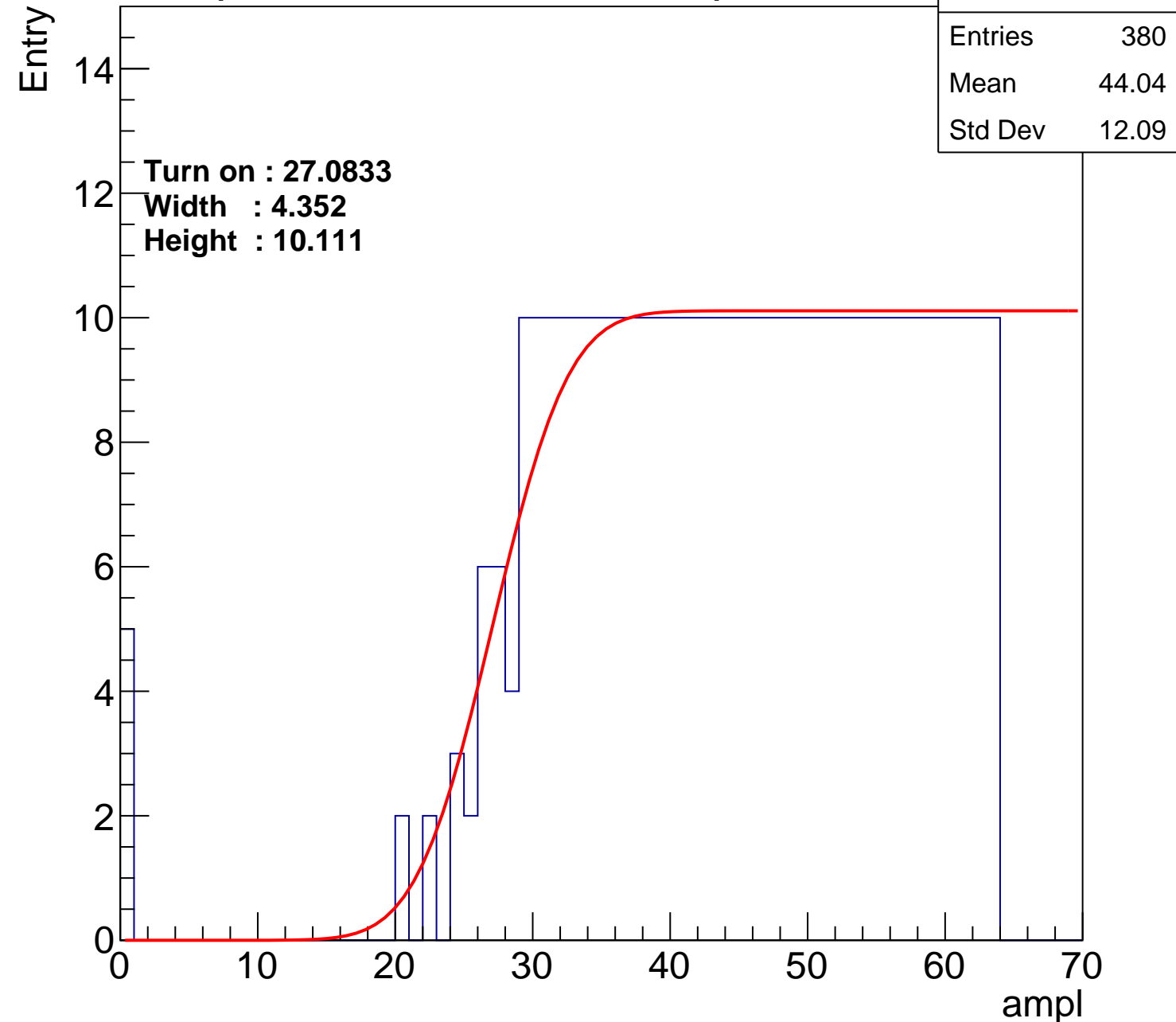
Width : 4.352

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.14
Std Dev	11.74

Turn on : 26.8597

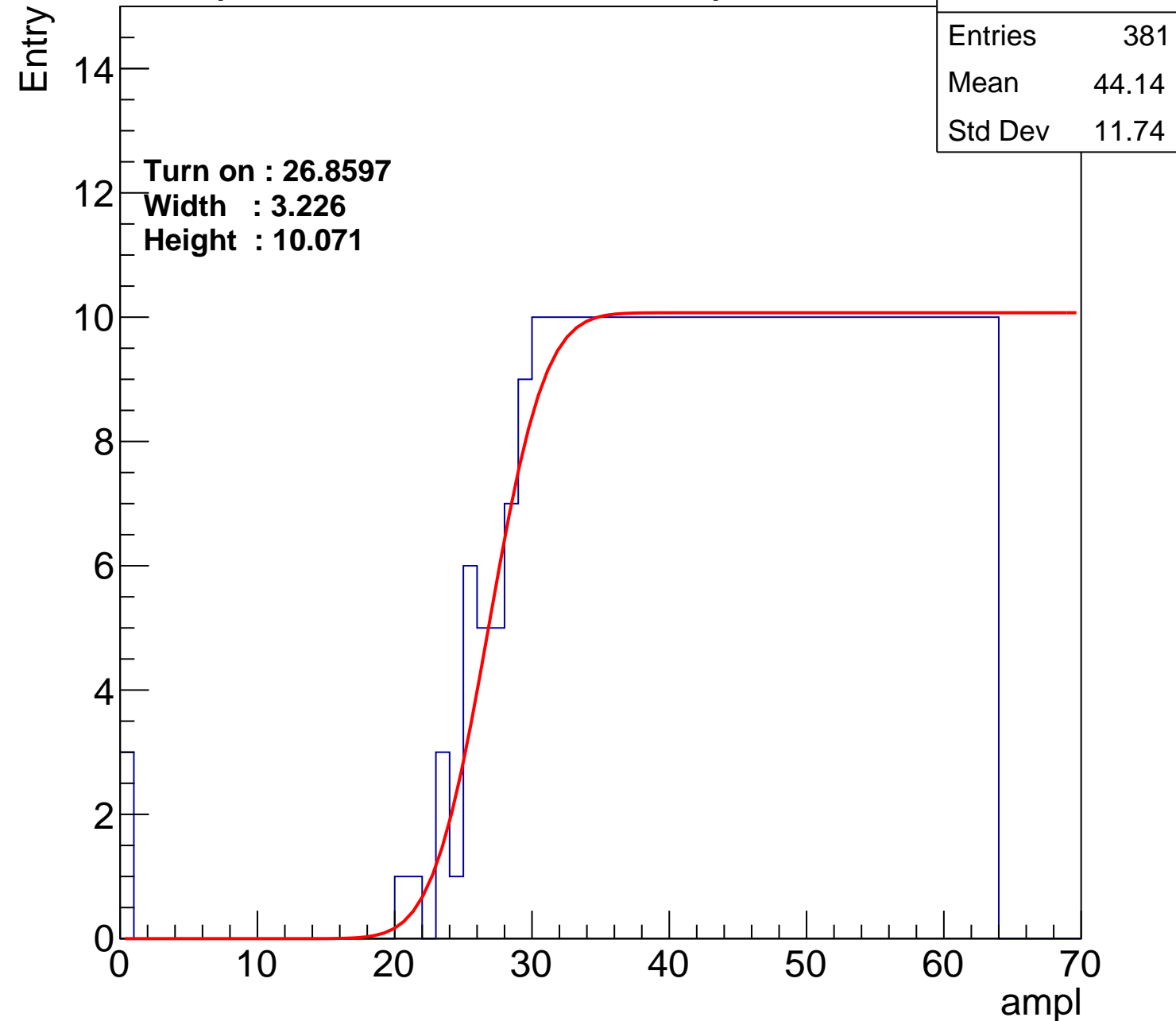
Width : 3.226

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.83
Std Dev	12.2

Turn on : 26.2603

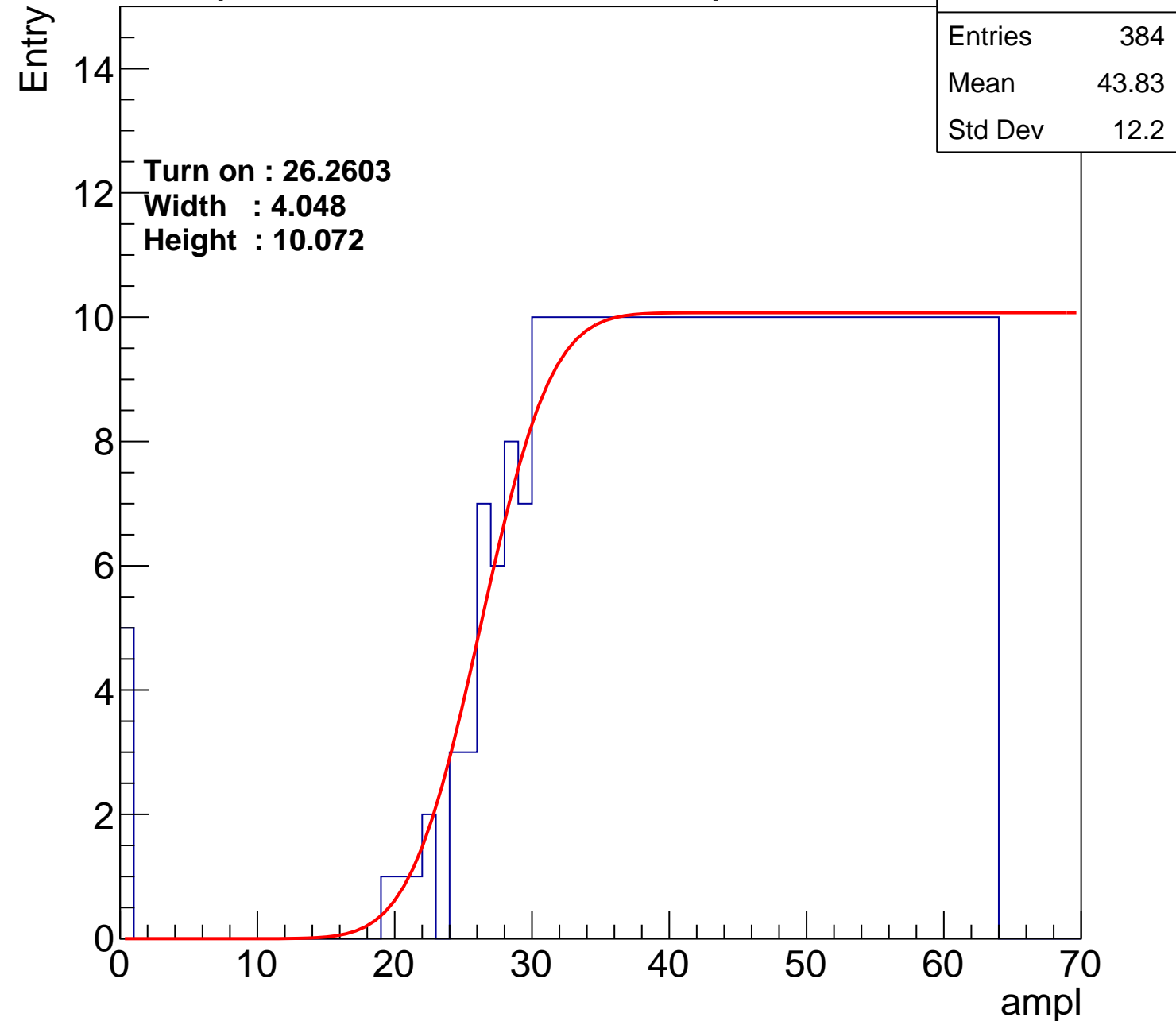
Width : 4.048

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.77
Std Dev	11.71

Turn on : 25.4800

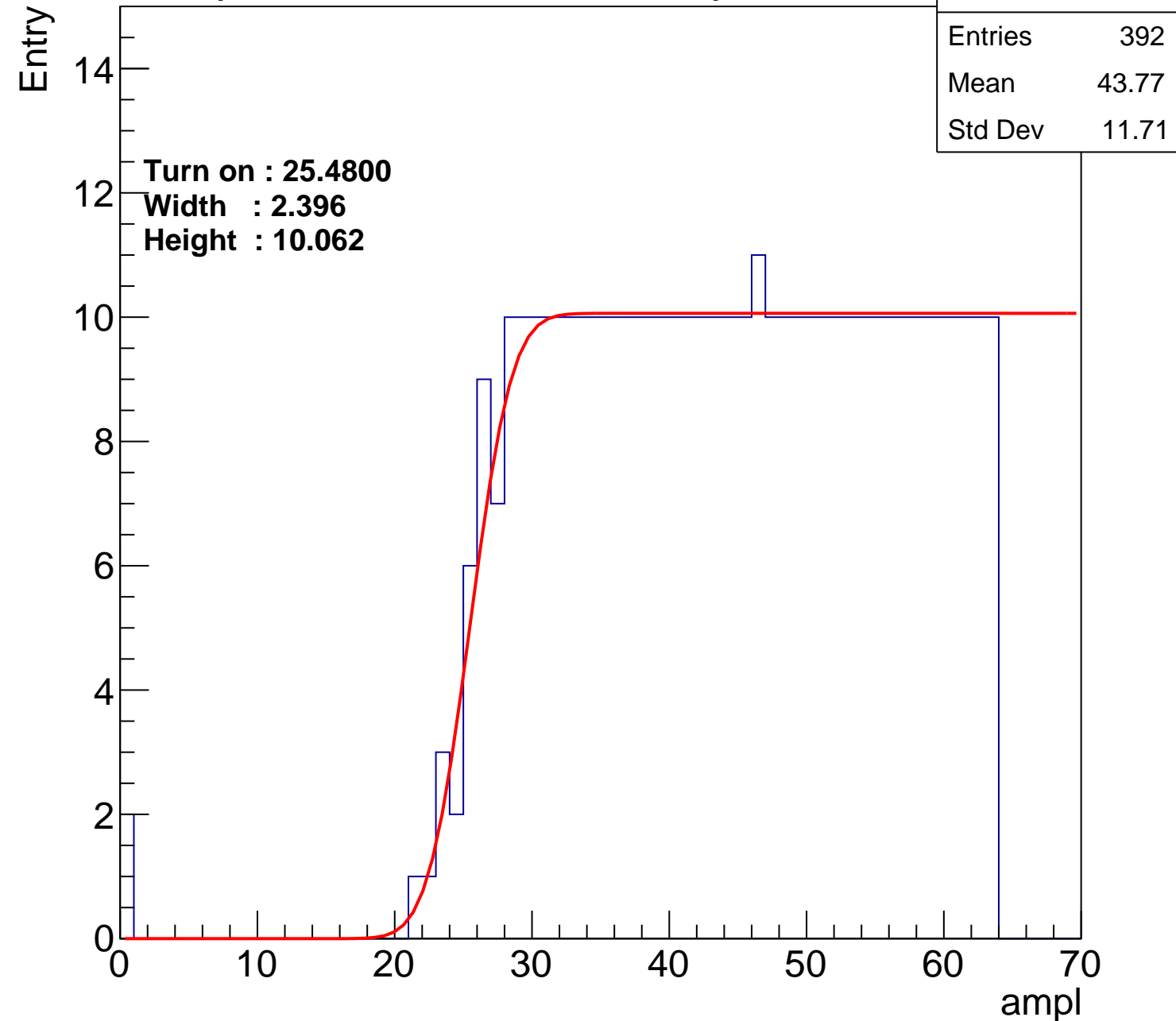
Width : 2.396

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.71
Std Dev	11.89

Turn on : 25.3488

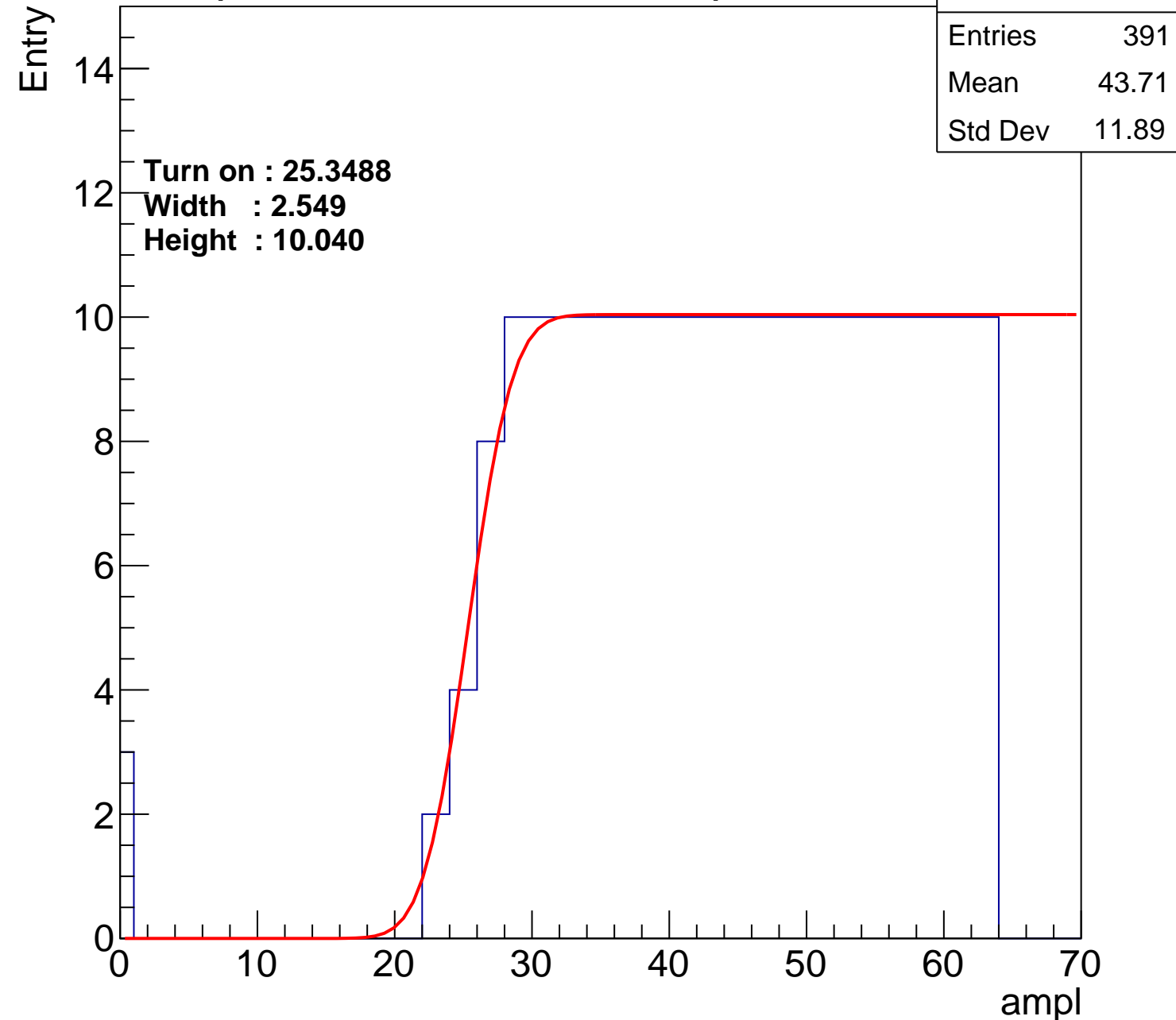
Width : 2.549

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.44
Std Dev	12.26

Turn on : 25.4672

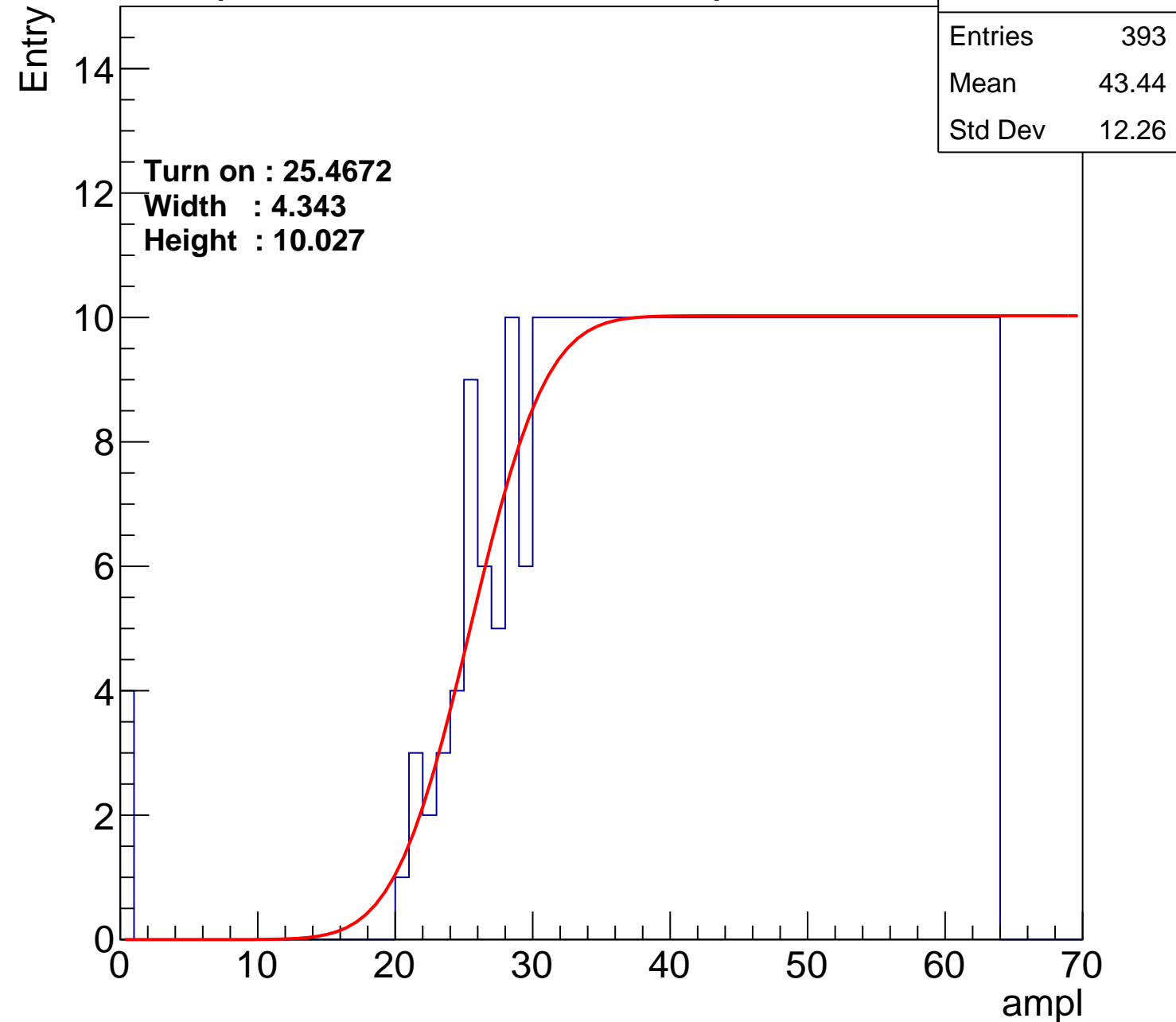
Width : 4.343

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.2
Std Dev	11.87

Turn on : 27.0183

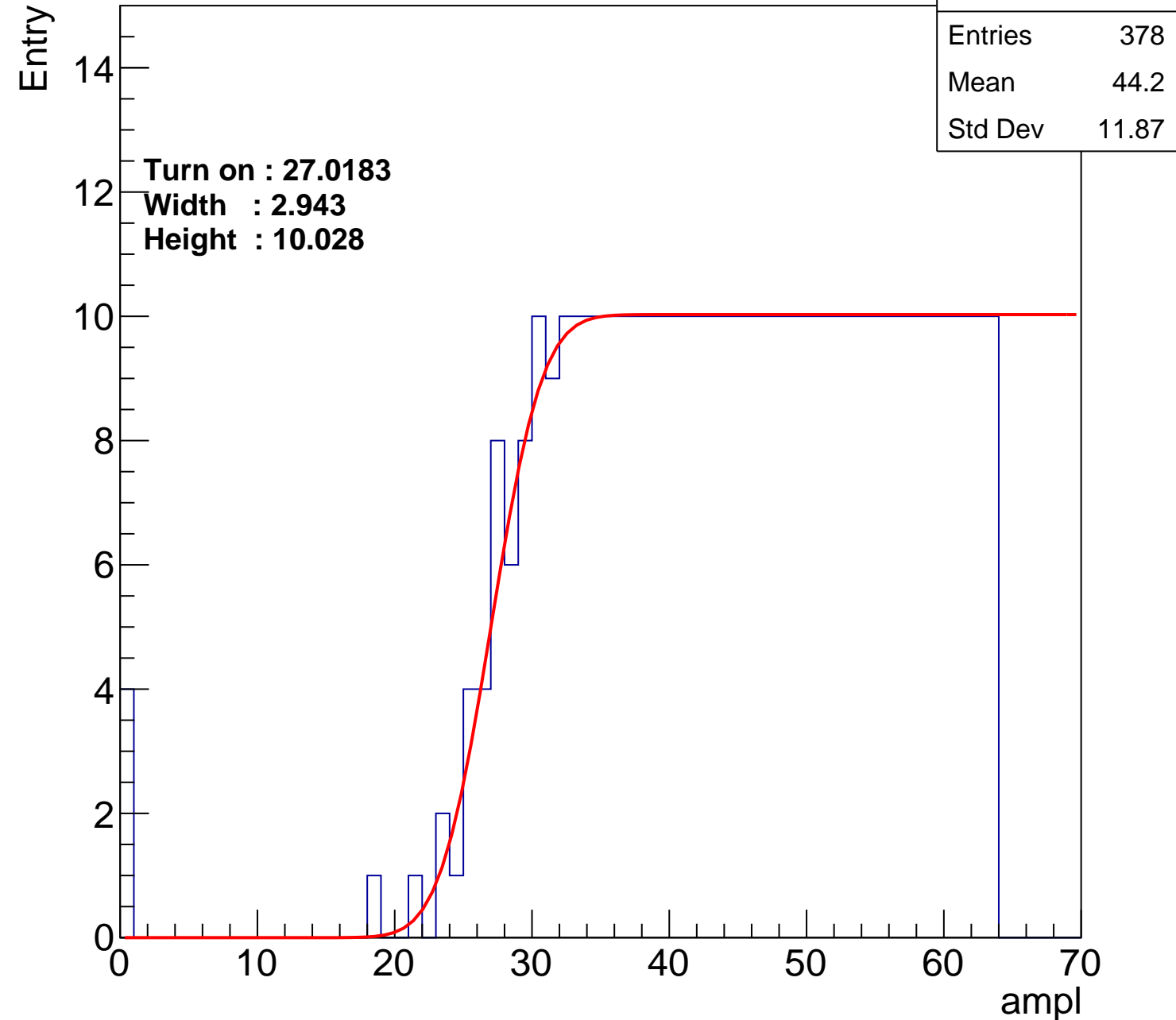
Width : 2.943

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.2362

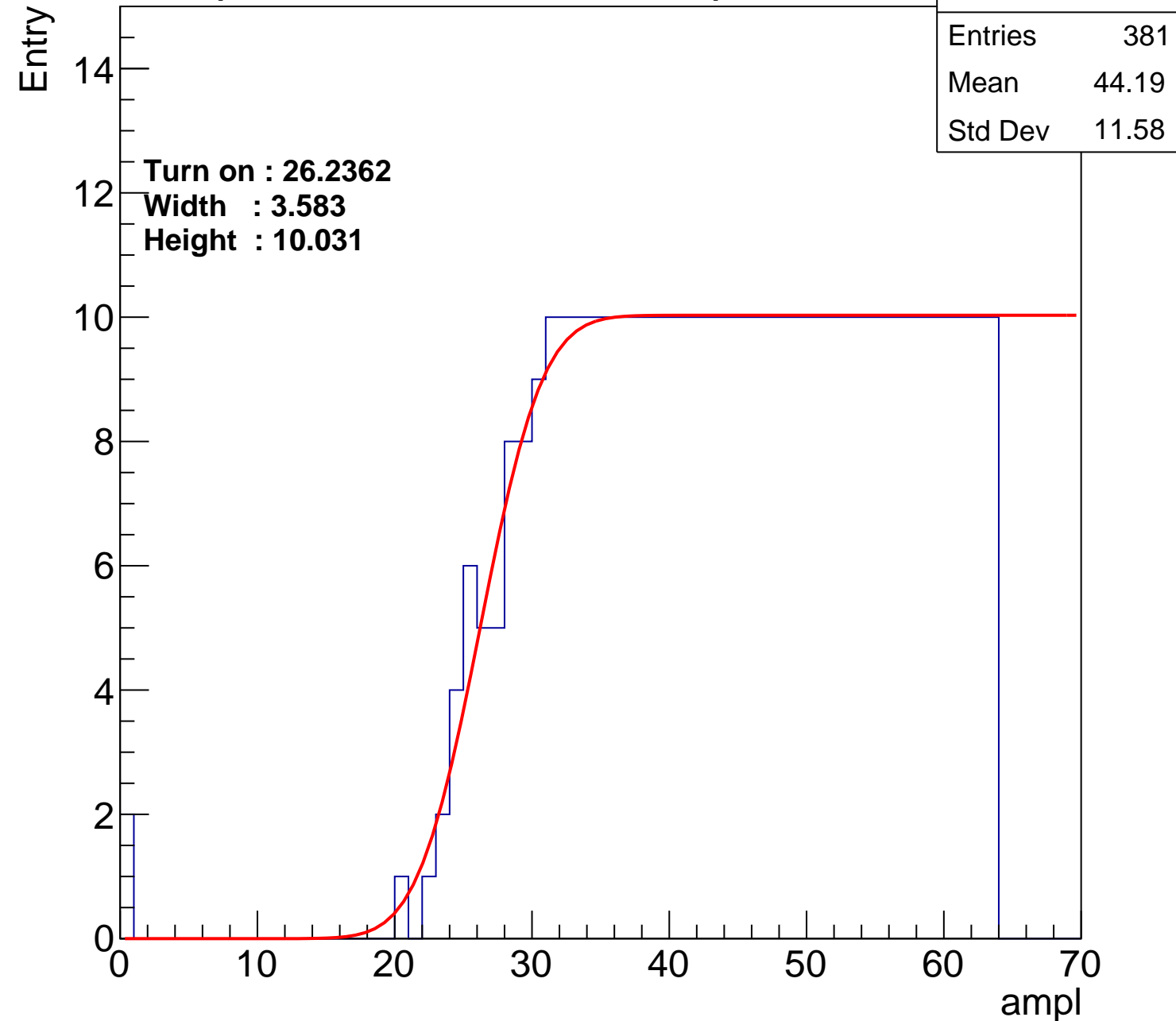
Width : 3.583

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.04
Std Dev	11.96

Turn on : 26.3888

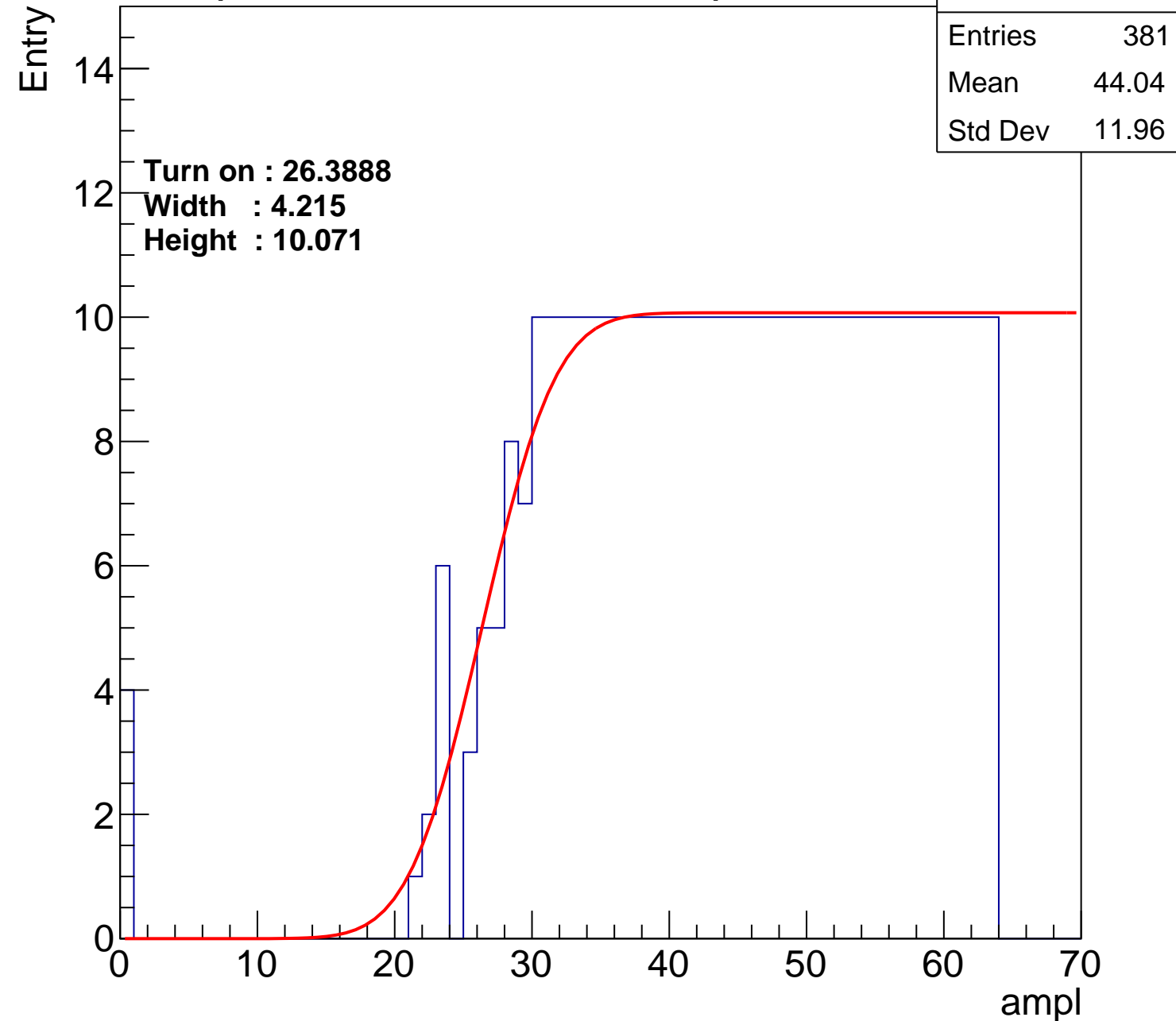
Width : 4.215

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.67
Std Dev	12.23

Turn on : 26.2527

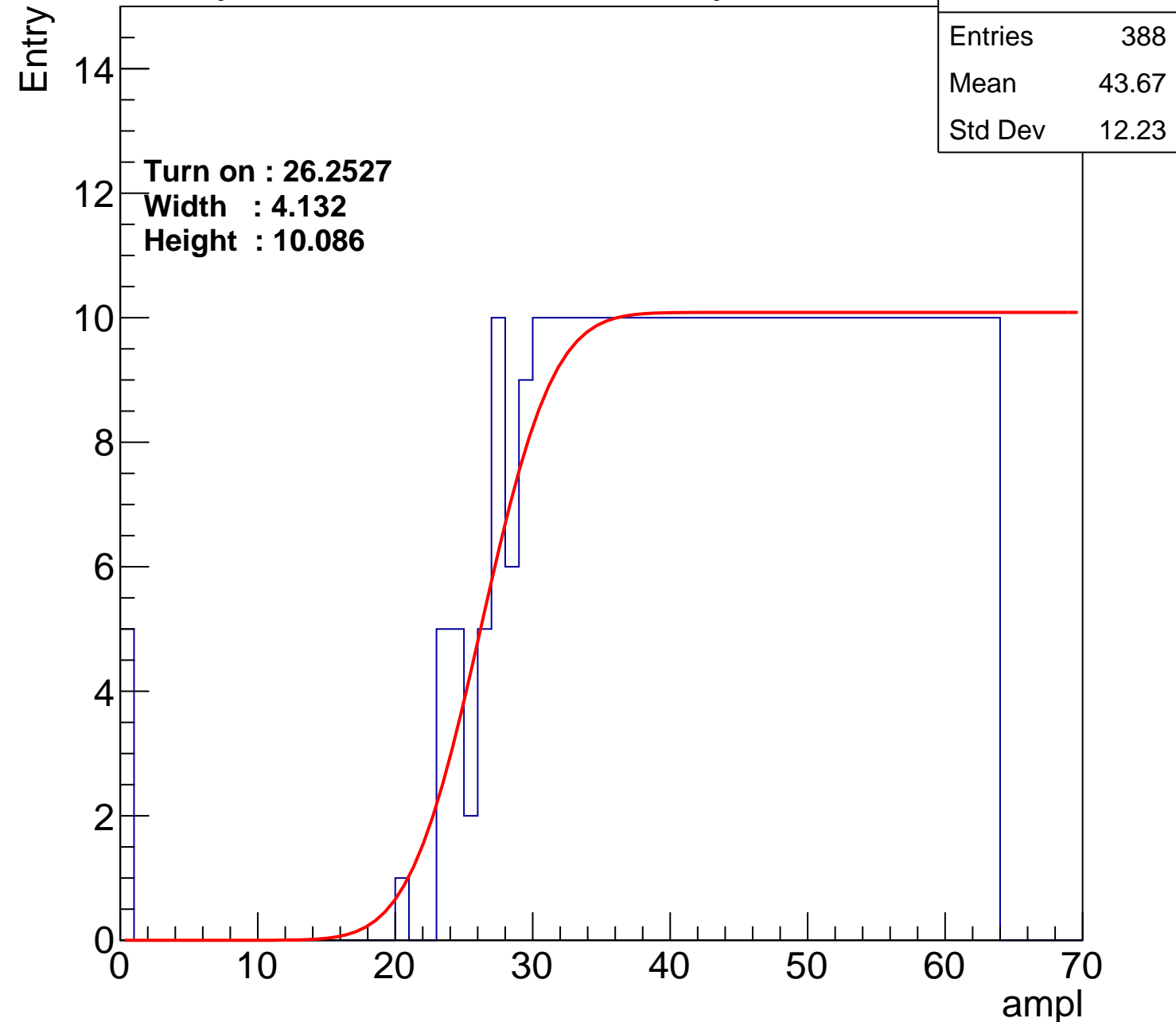
Width : 4.132

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.57
Std Dev	11.99

Turn on : 25.2454

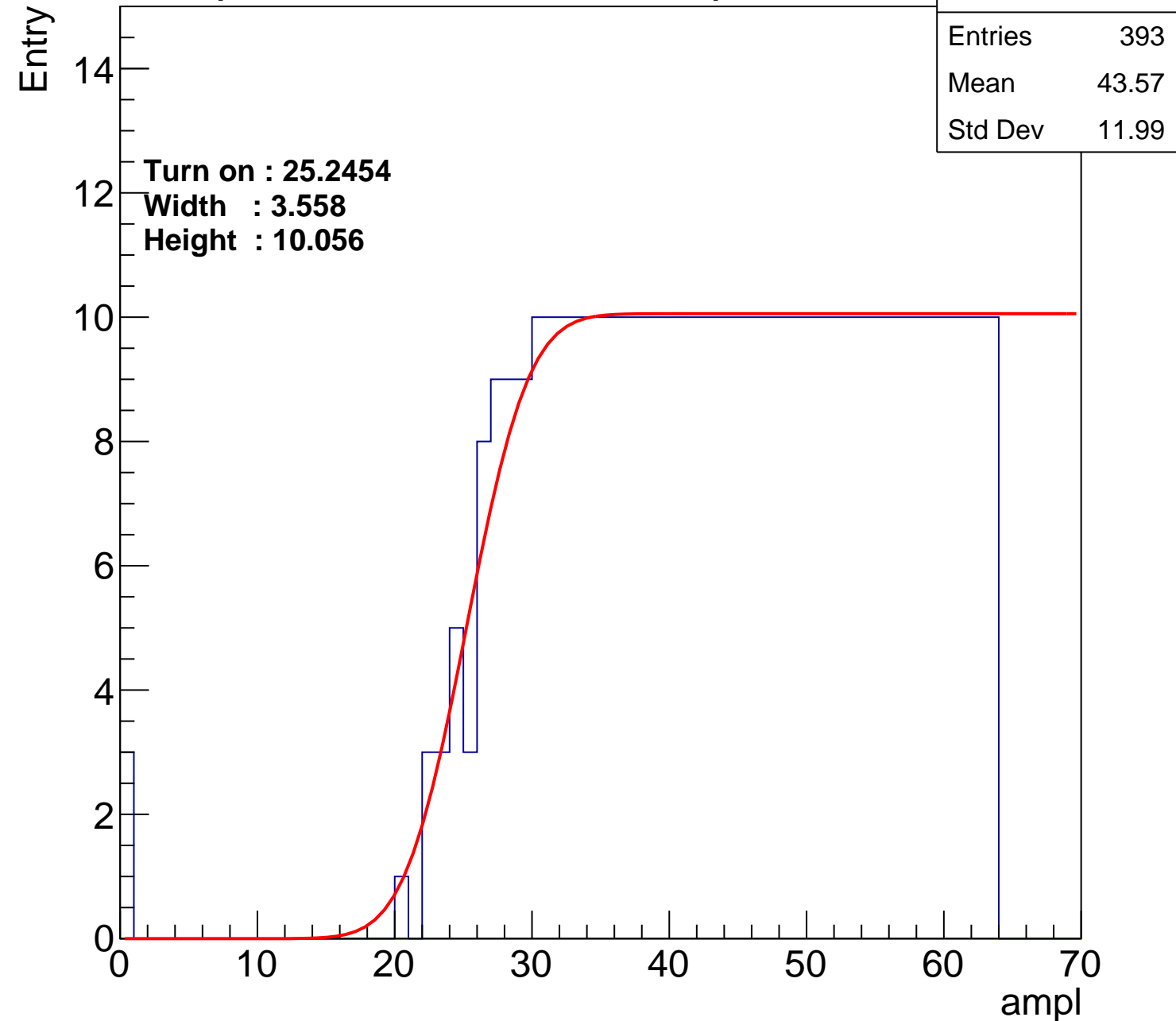
Width : 3.558

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.07
Std Dev	11.67

Turn on : 25.9080

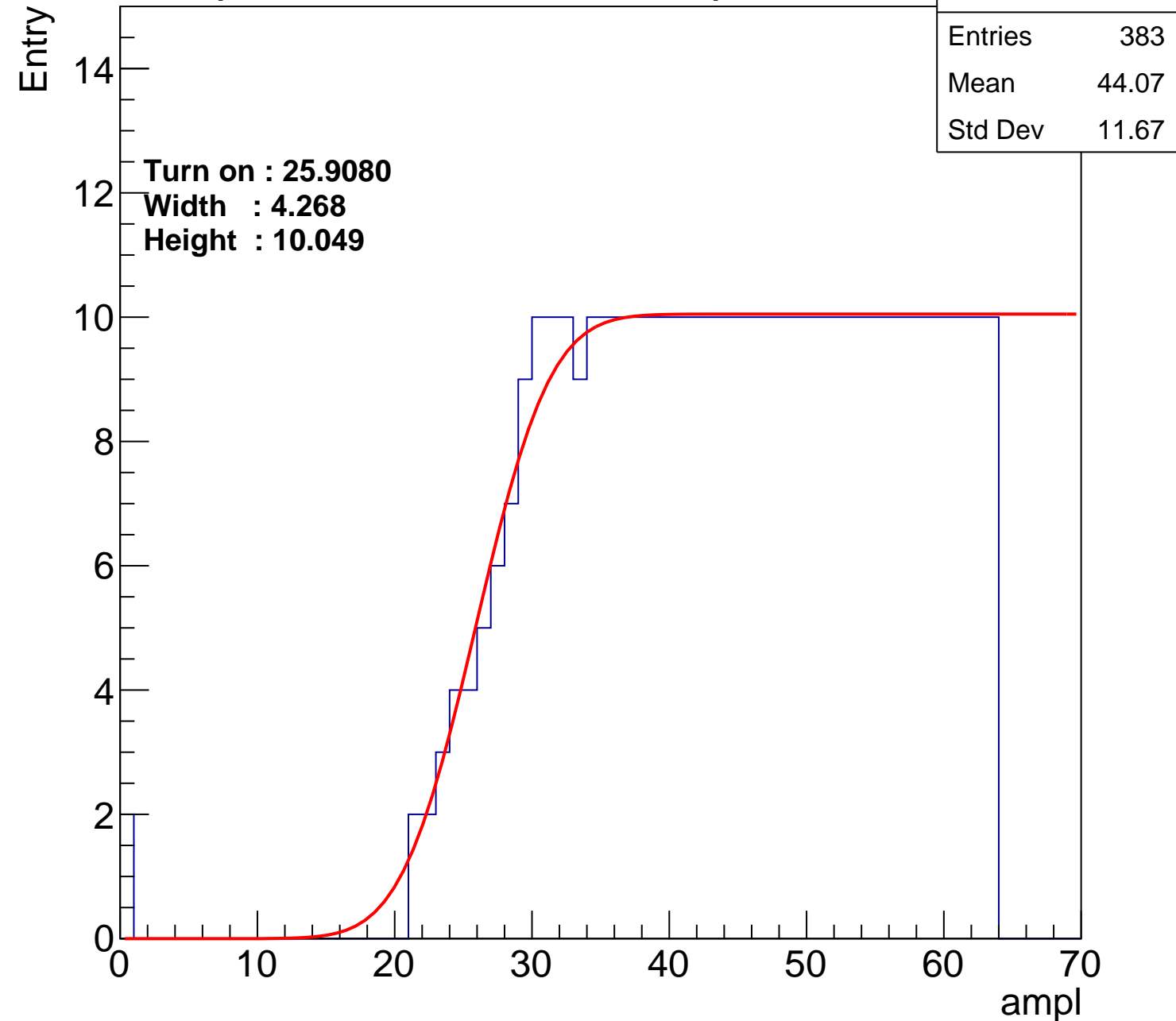
Width : 4.268

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	43.87
Std Dev	12.43

Turn on : 26.8045

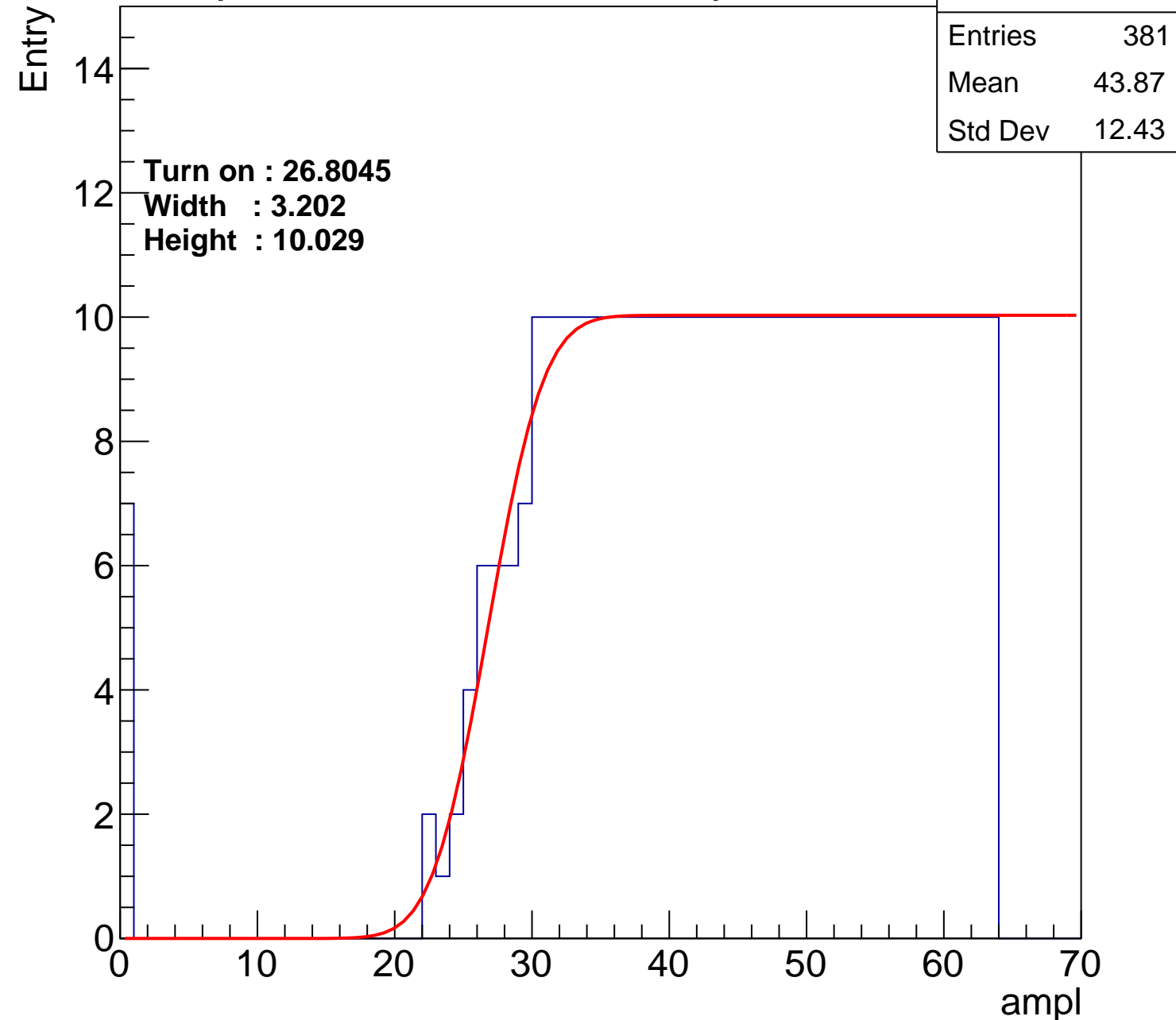
Width : 3.202

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.9
Std Dev	11.97

Turn on : 26.2561

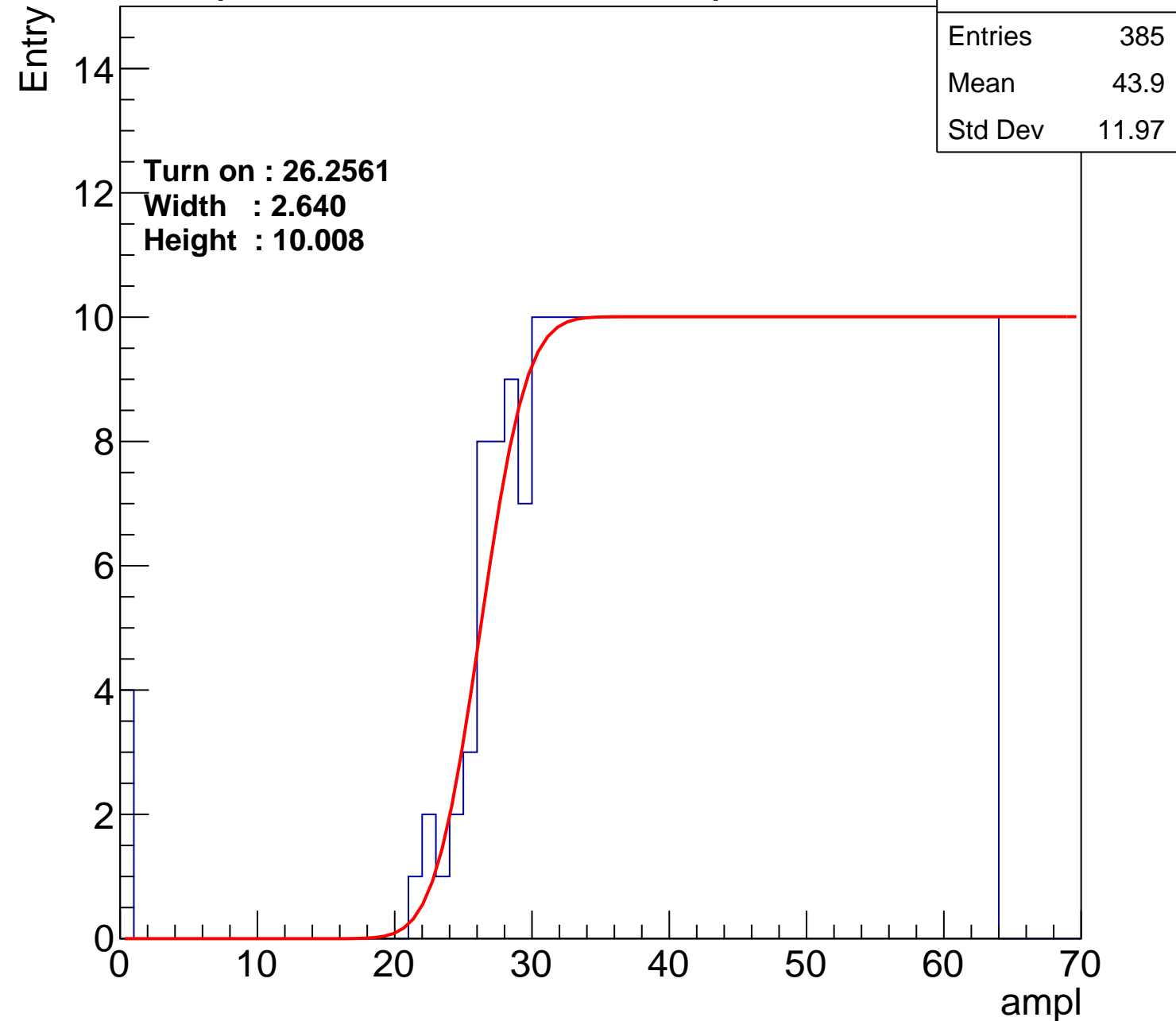
Width : 2.640

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 27.0109

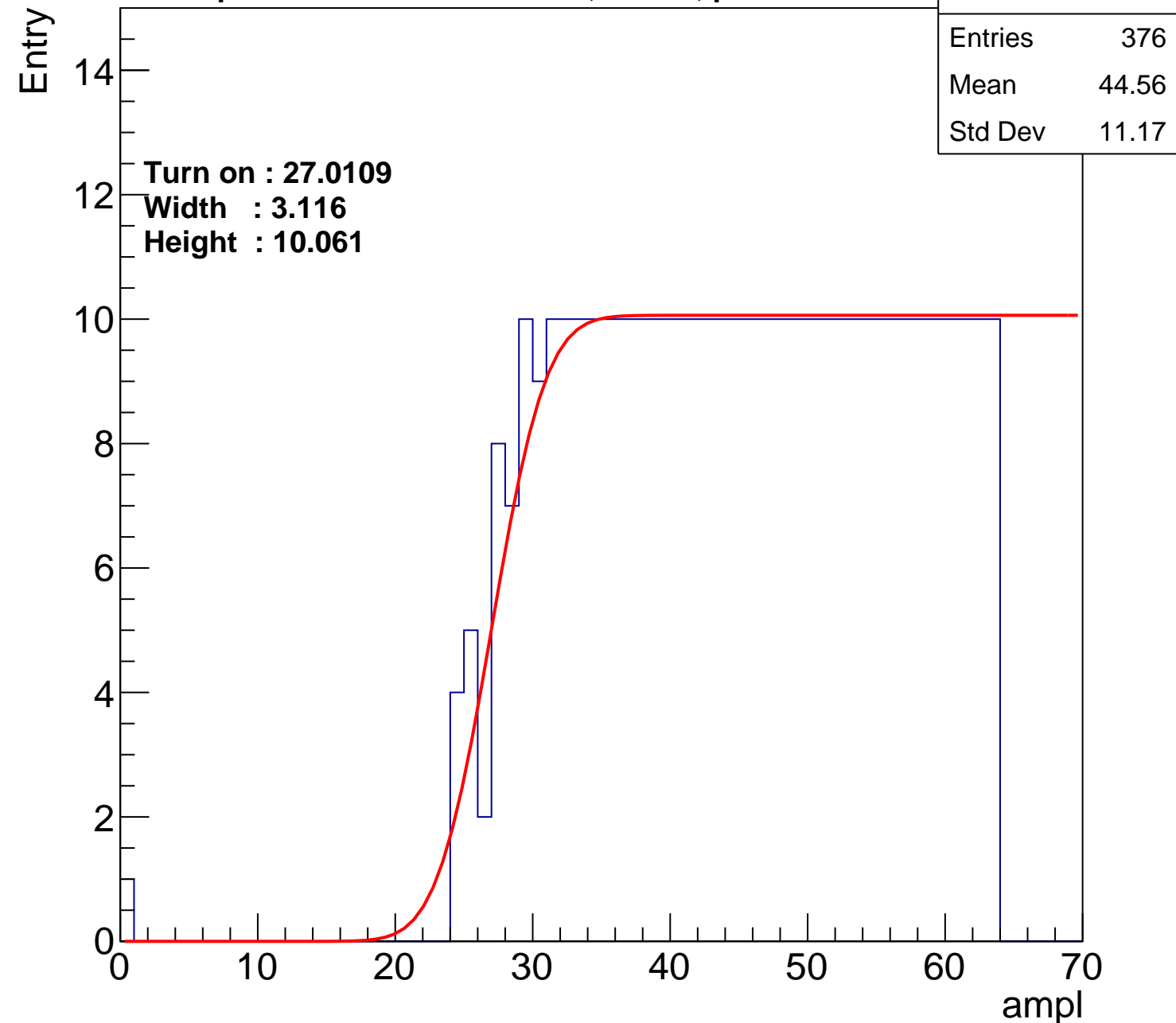
Width : 3.116

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	43.11
Std Dev	12.03

Turn on : 23.9244

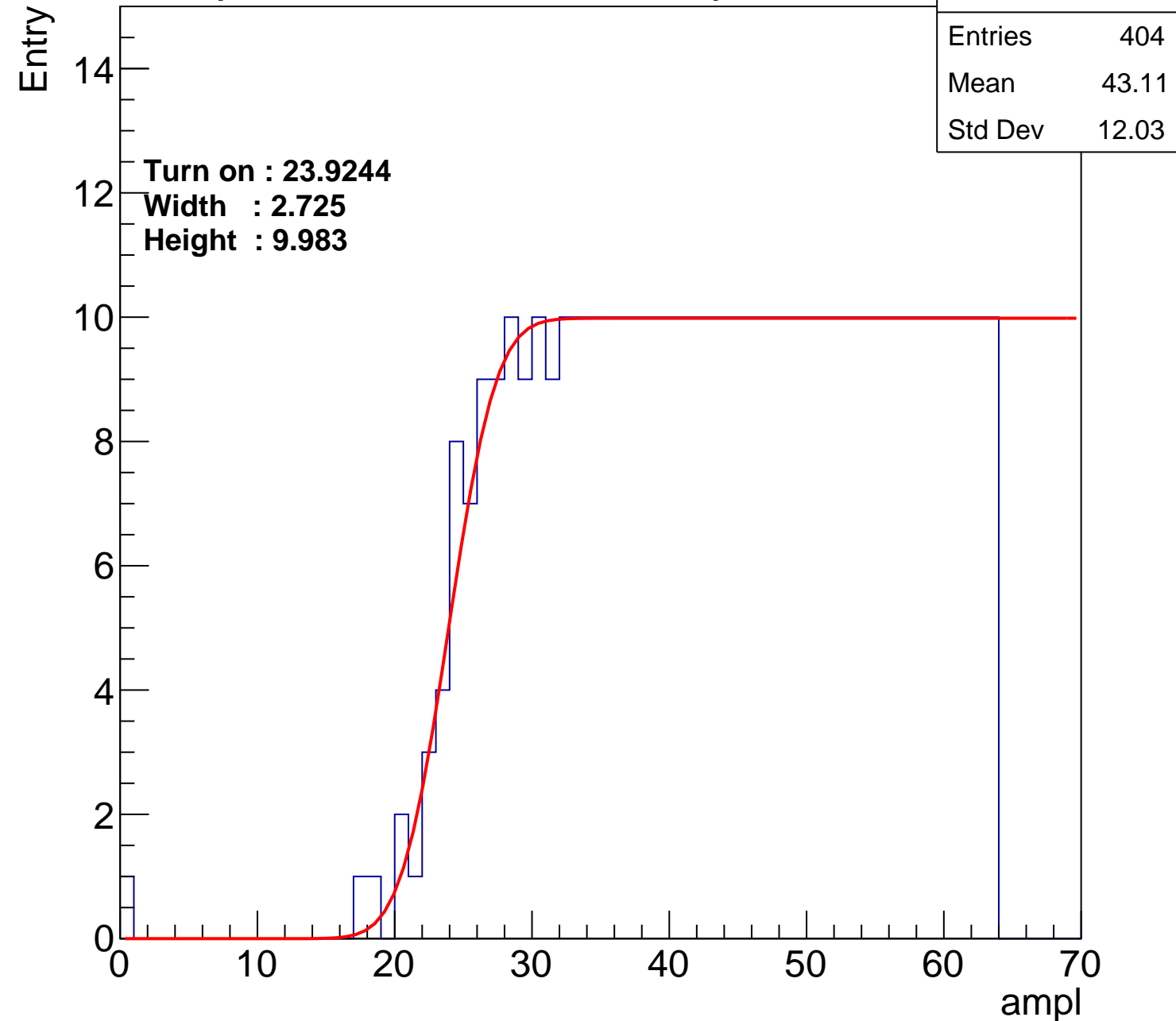
Width : 2.725

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.5
Std Dev	12.56

Turn on : 25.7262

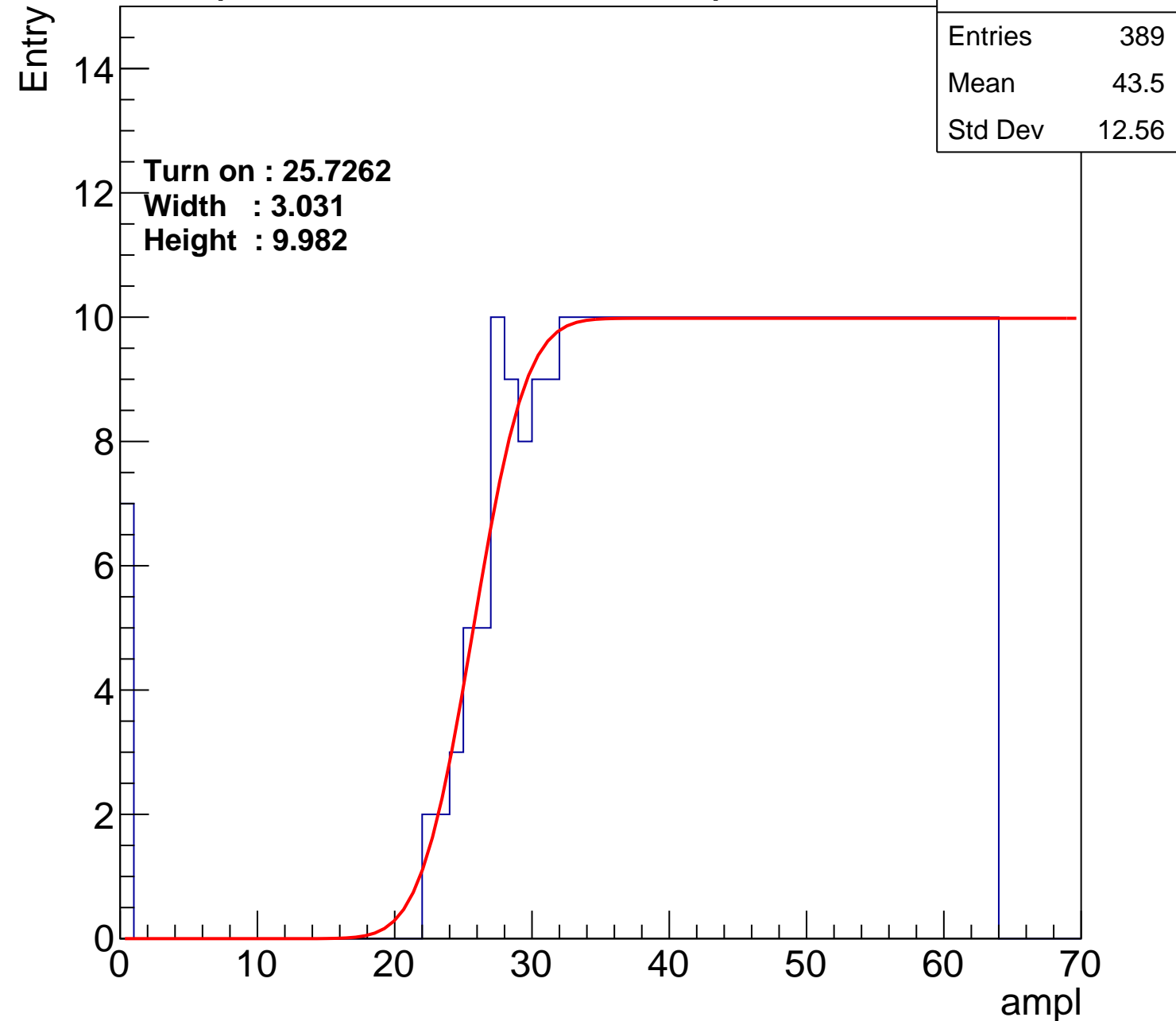
Width : 3.031

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	413
Mean	42.64
Std Dev	12.37

Turn on : 23.4962

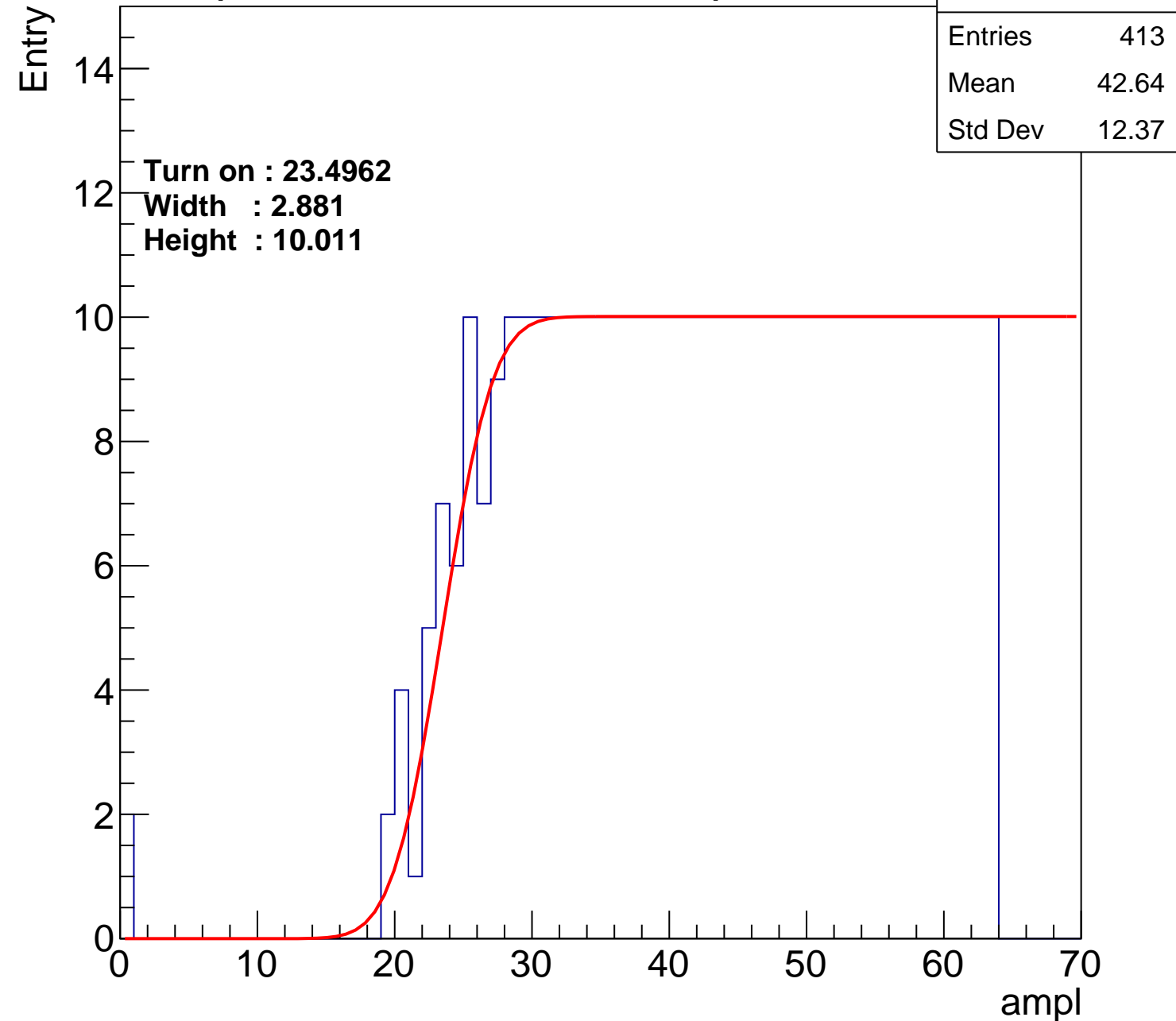
Width : 2.881

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 27.0472

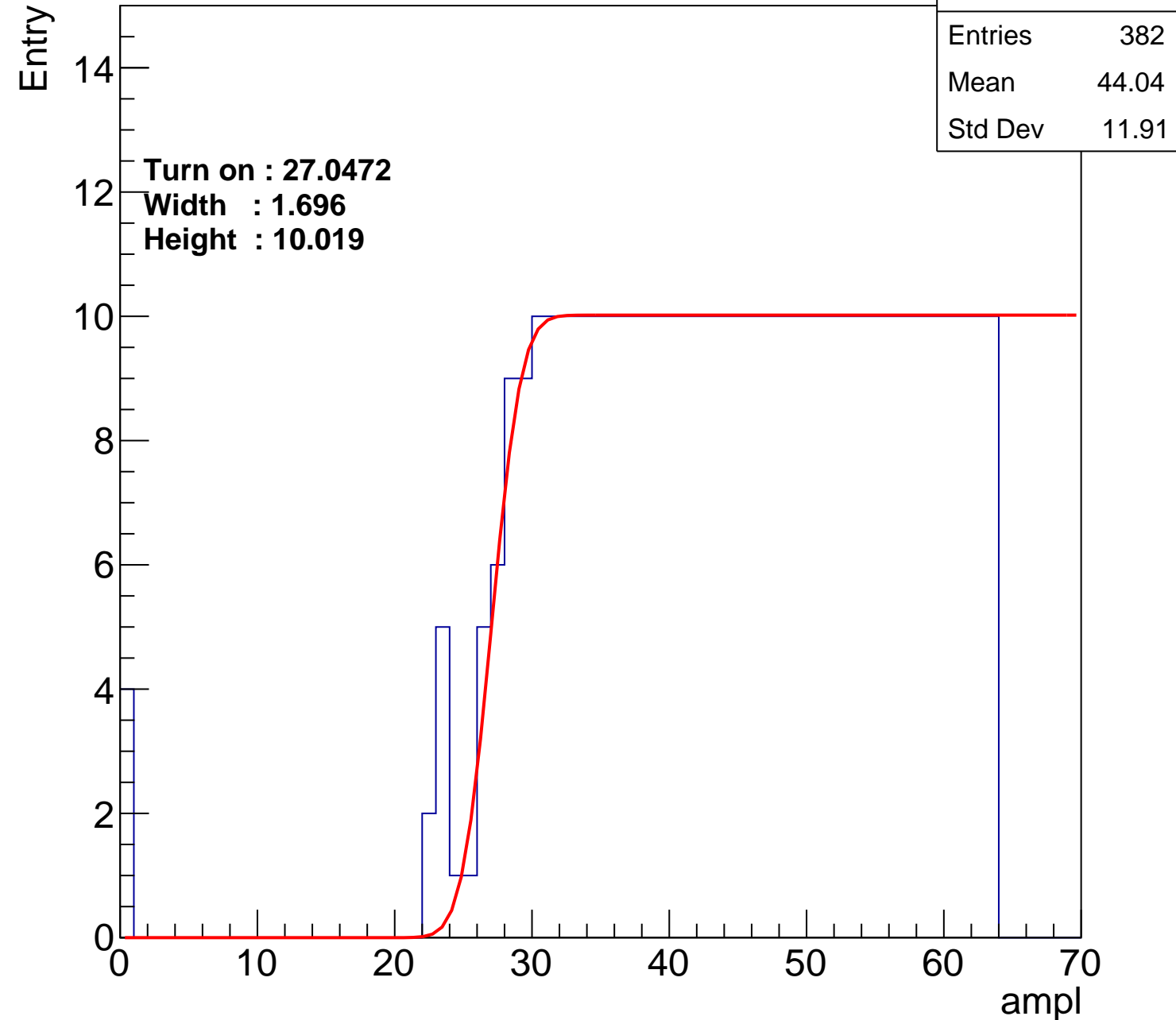
Width : 1.696

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch66

calib_packv5_042523_0143.root, FC#12, port B1

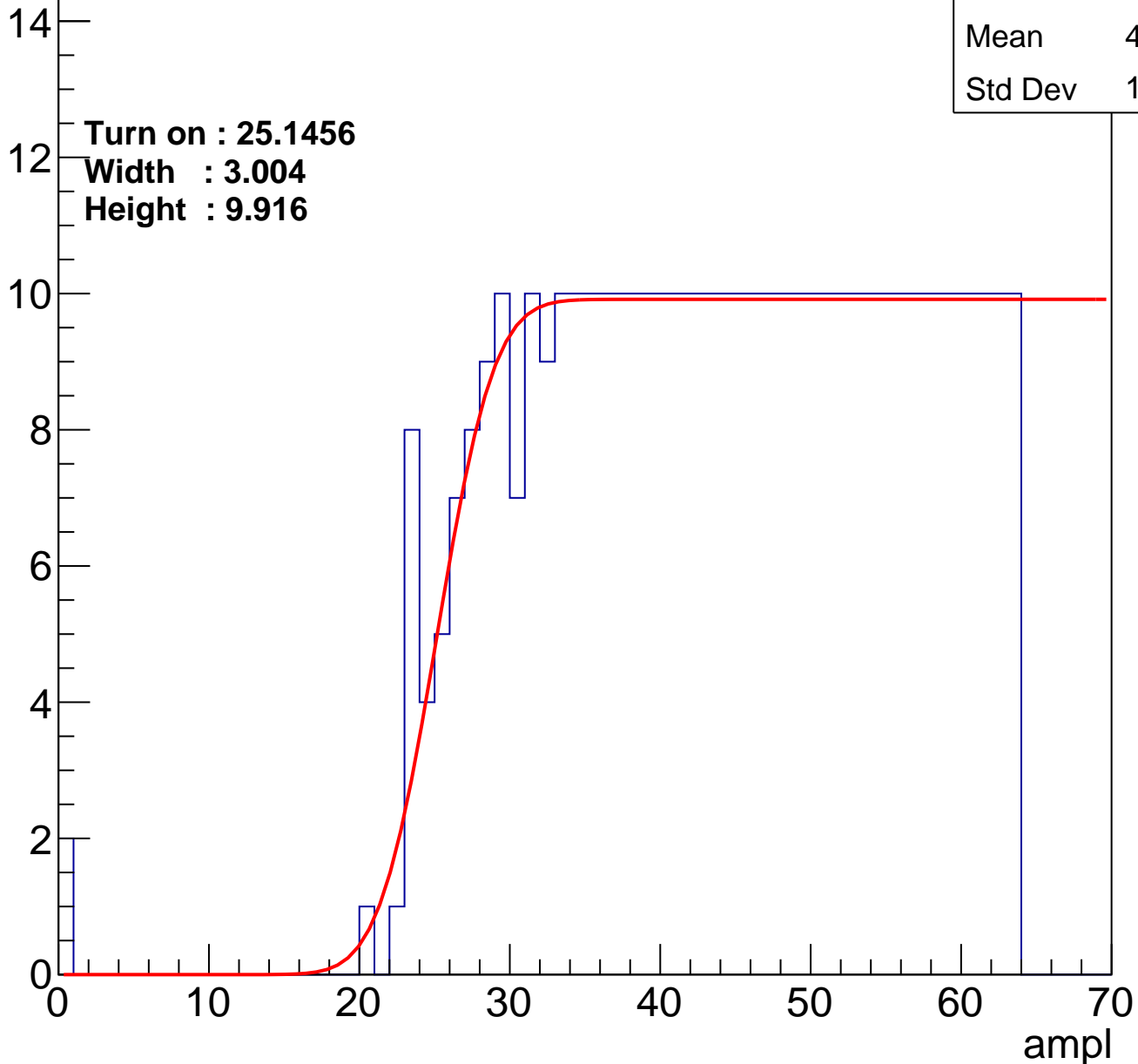
Entries	391
Mean	43.67
Std Dev	11.87

Turn on : 25.1456

Width : 3.004

Height : 9.916

Entry



B0L102S, U4-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.7580

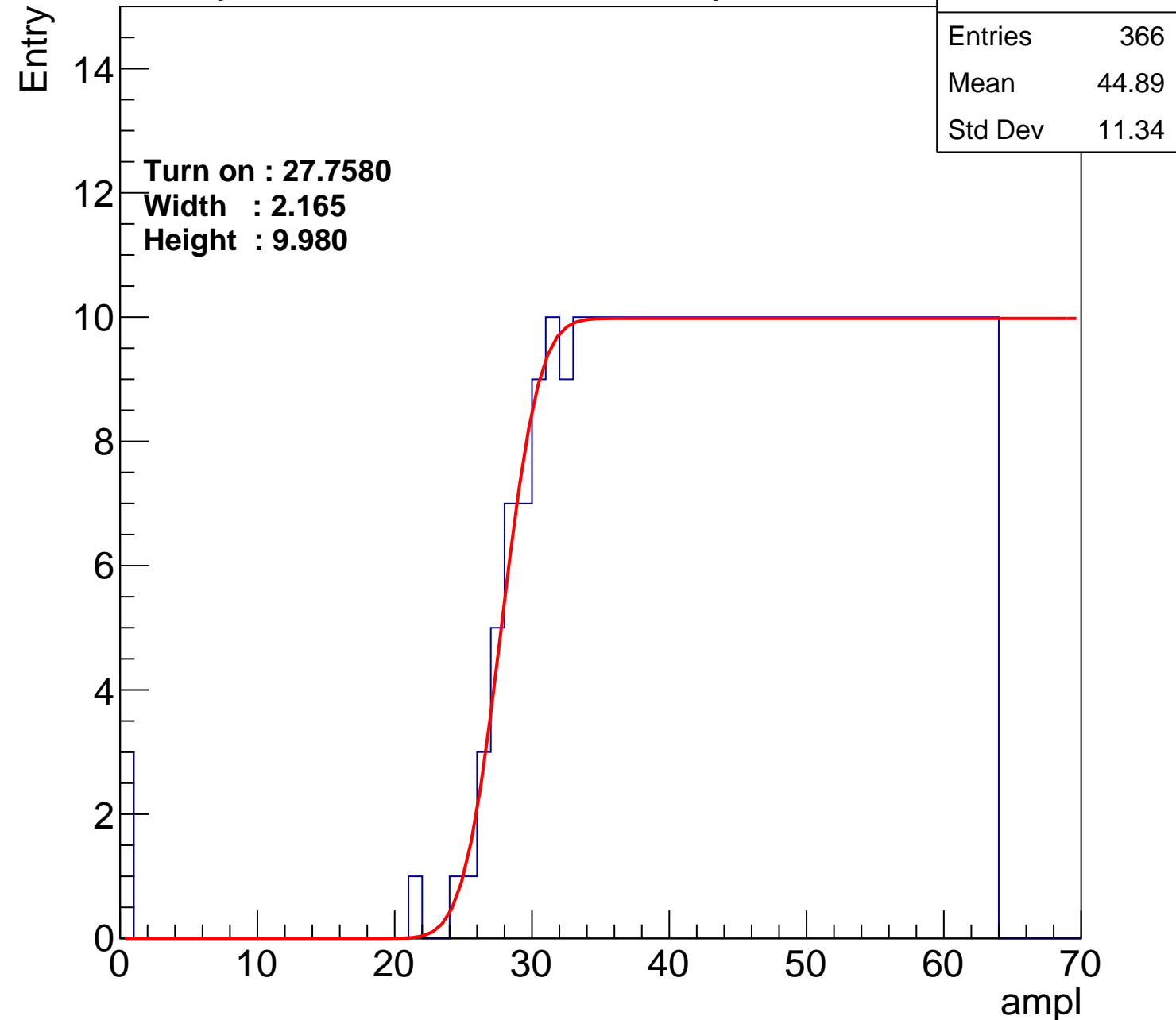
Width : 2.165

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.18
Std Dev	12.31

Turn on : 23.8091

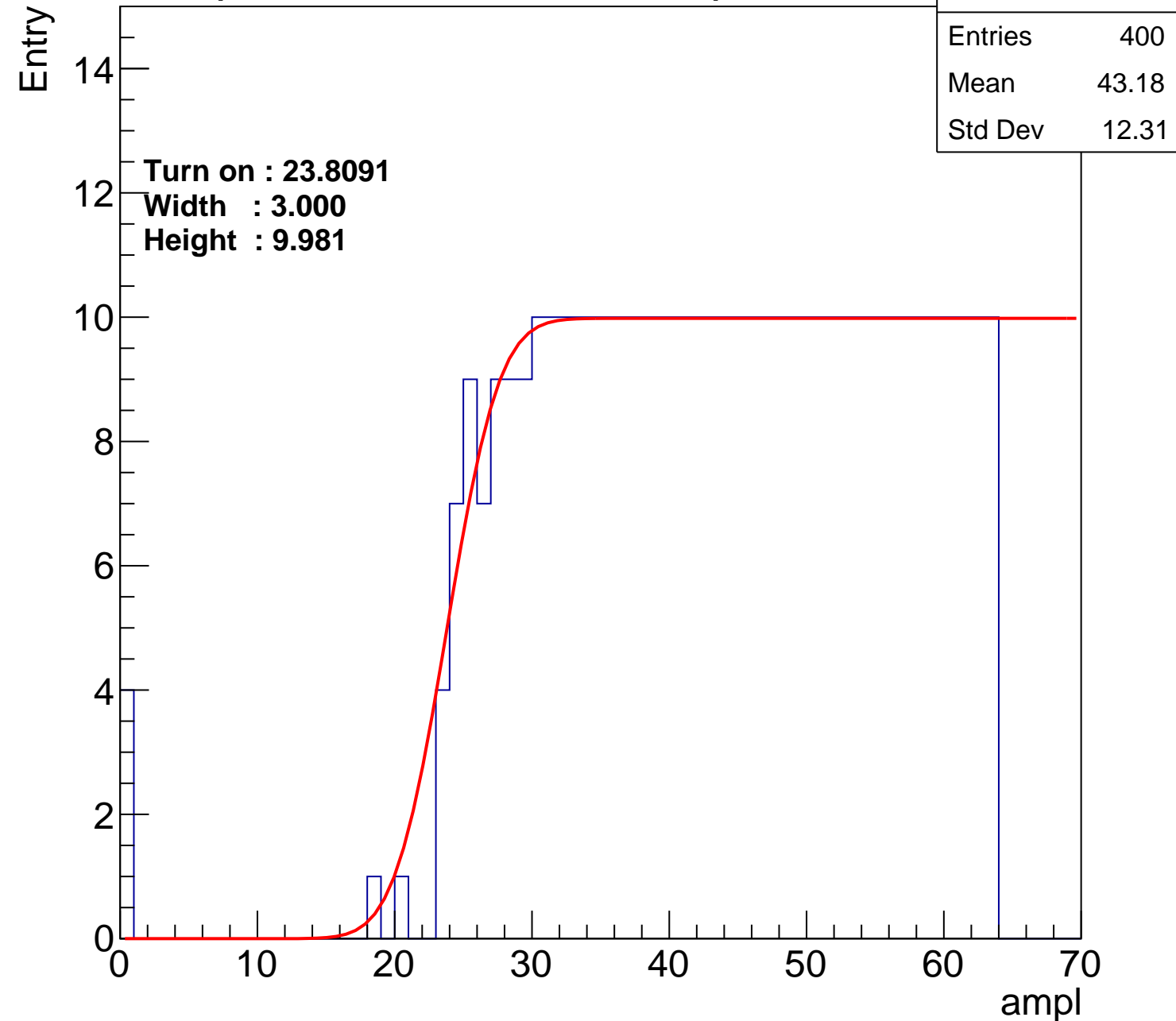
Width : 3.000

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.87
Std Dev	11.43

Turn on : 28.3233

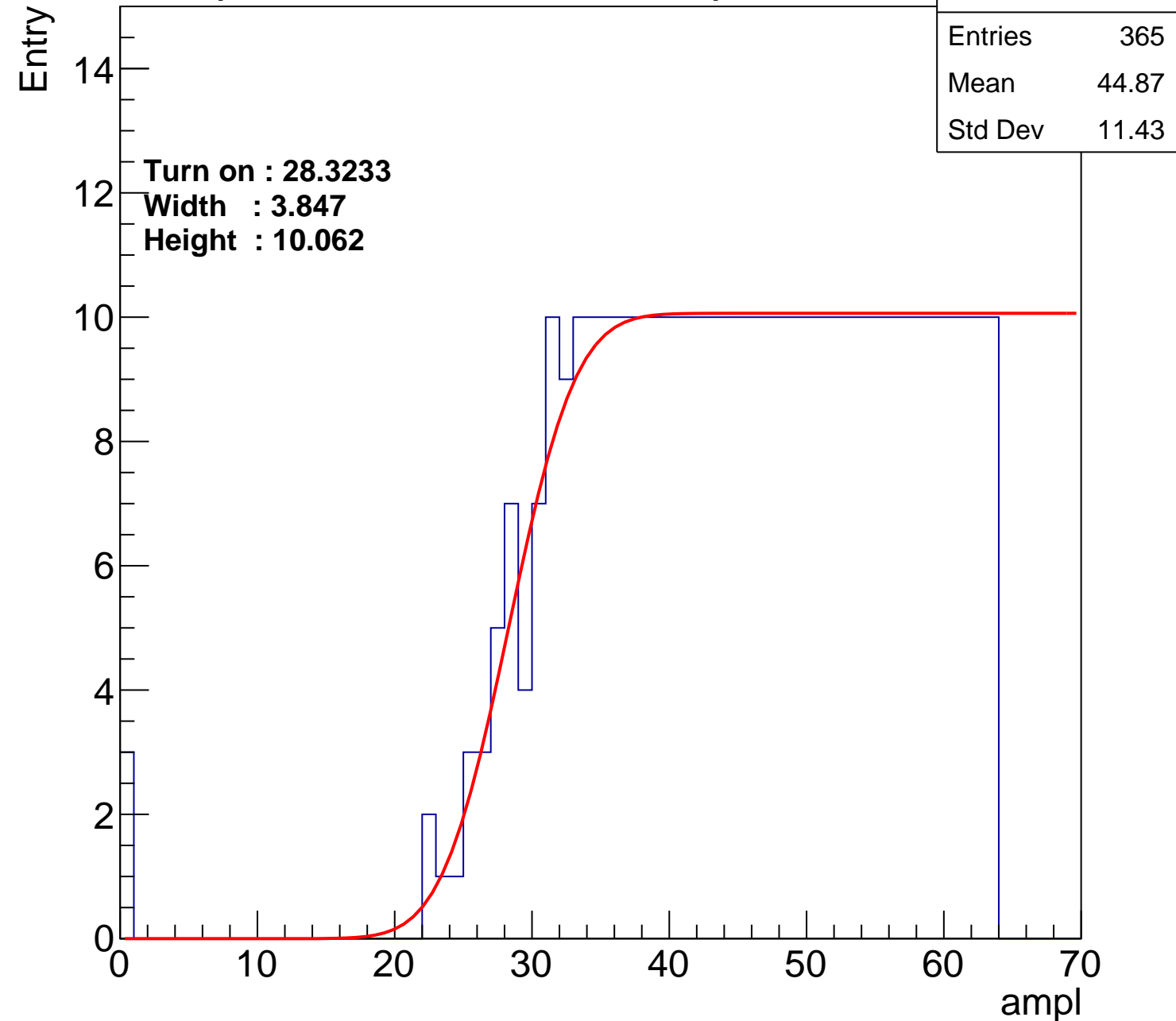
Width : 3.847

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.73
Std Dev	12.7

Turn on : 24.1971

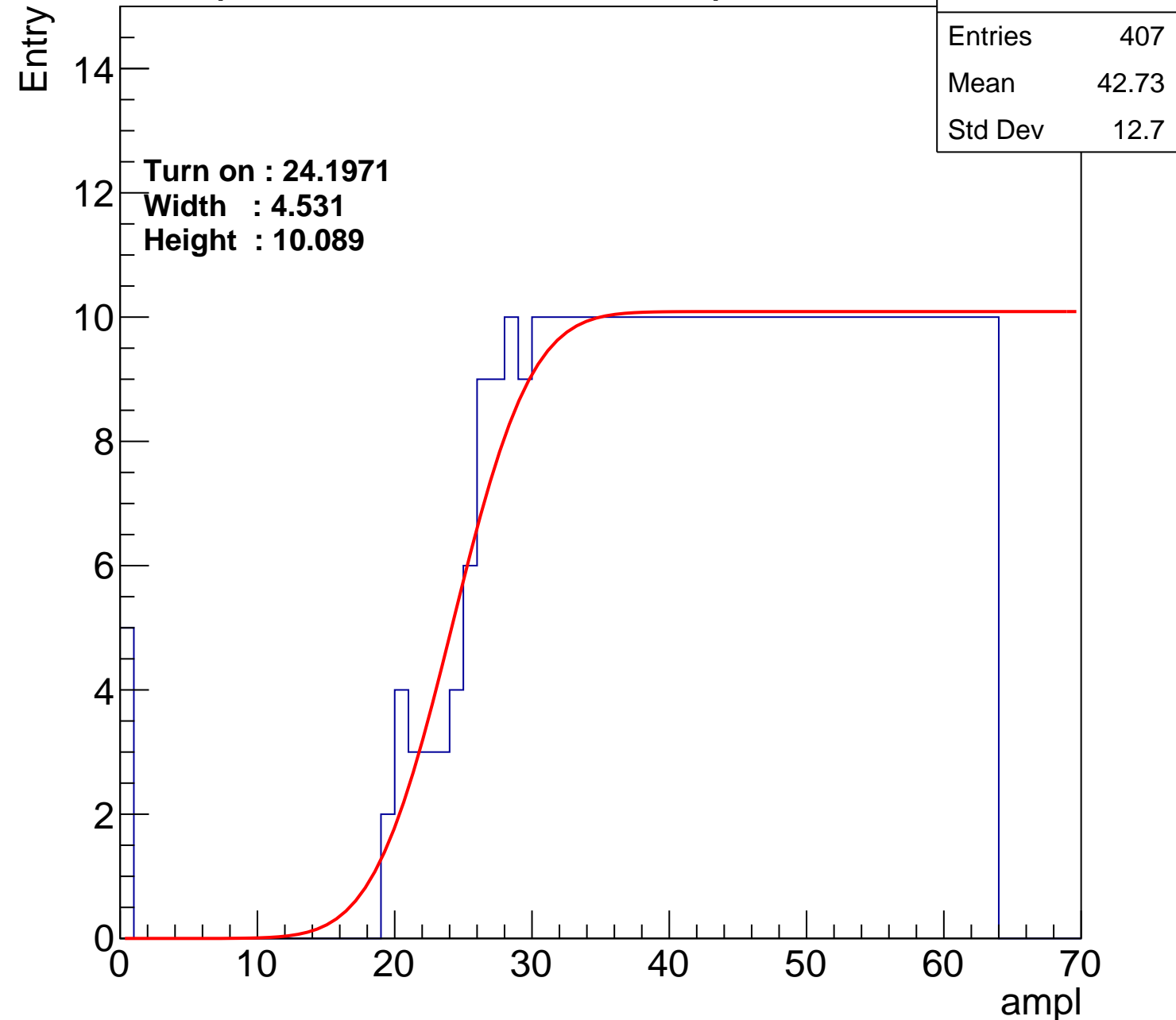
Width : 4.531

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 24.8779

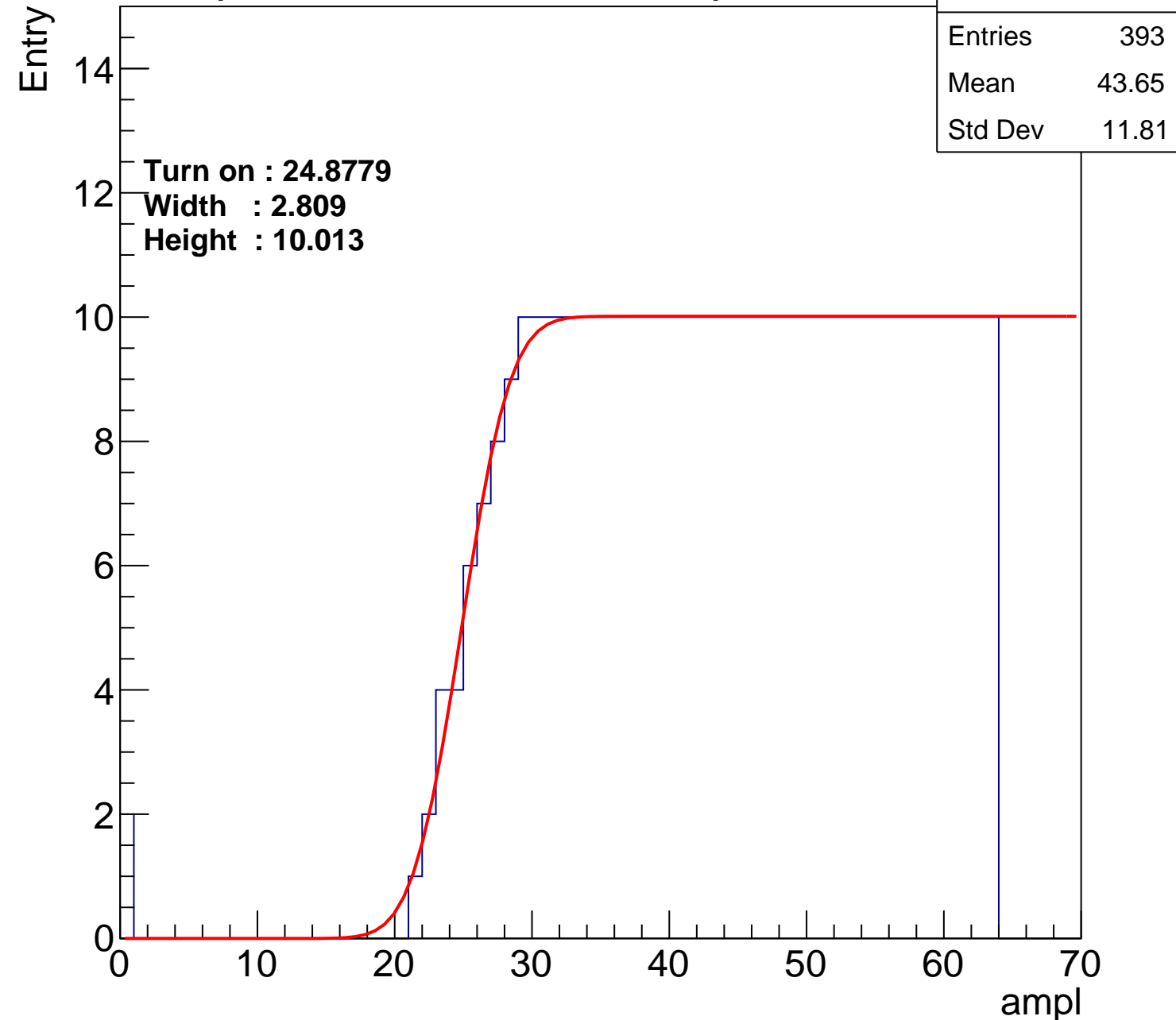
Width : 2.809

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.44
Std Dev	12.09

Turn on : 24.5298

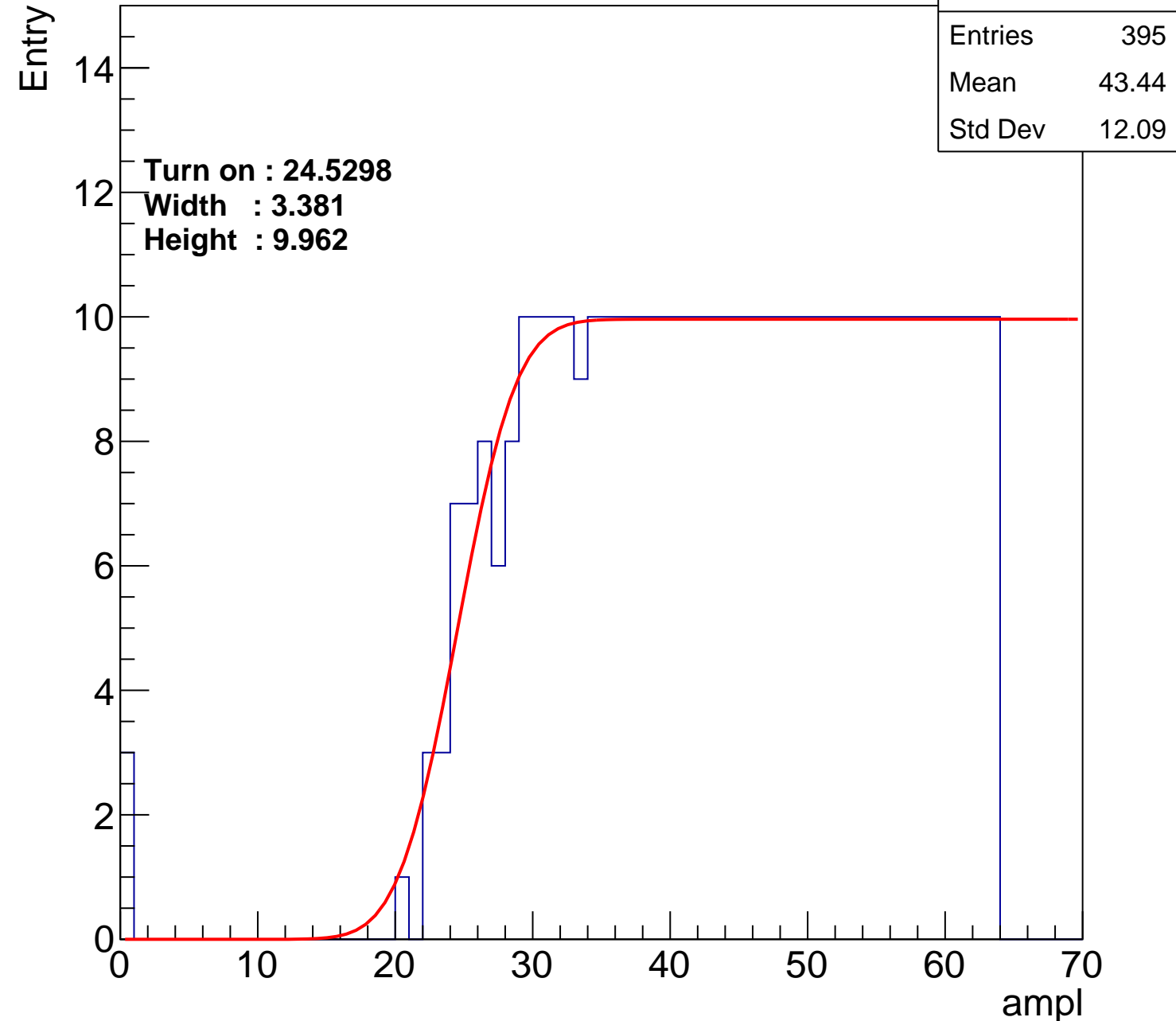
Width : 3.381

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.41
Std Dev	11.58

Turn on : 26.9841

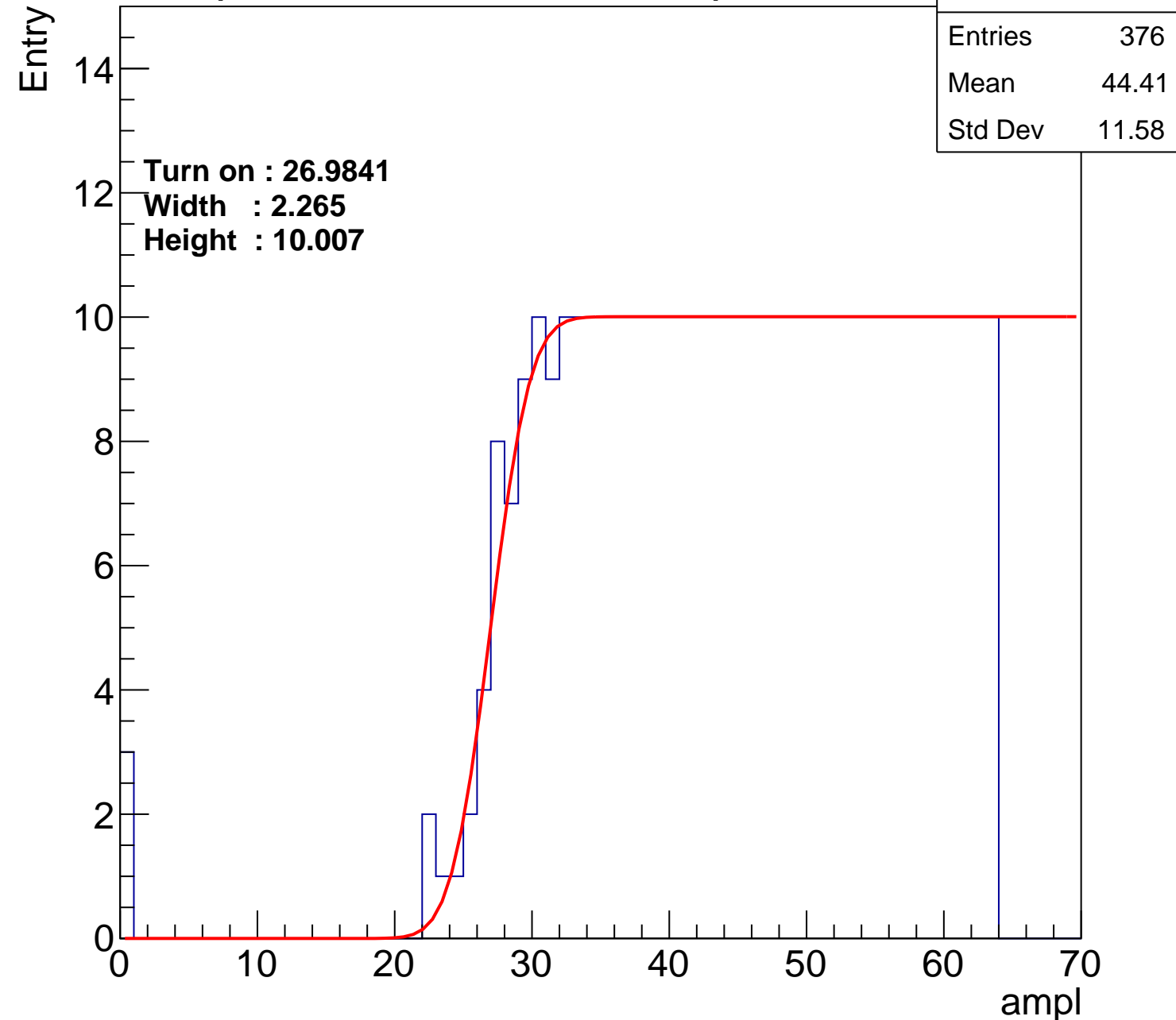
Width : 2.265

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.81
Std Dev	11.75

Turn on : 25.5481

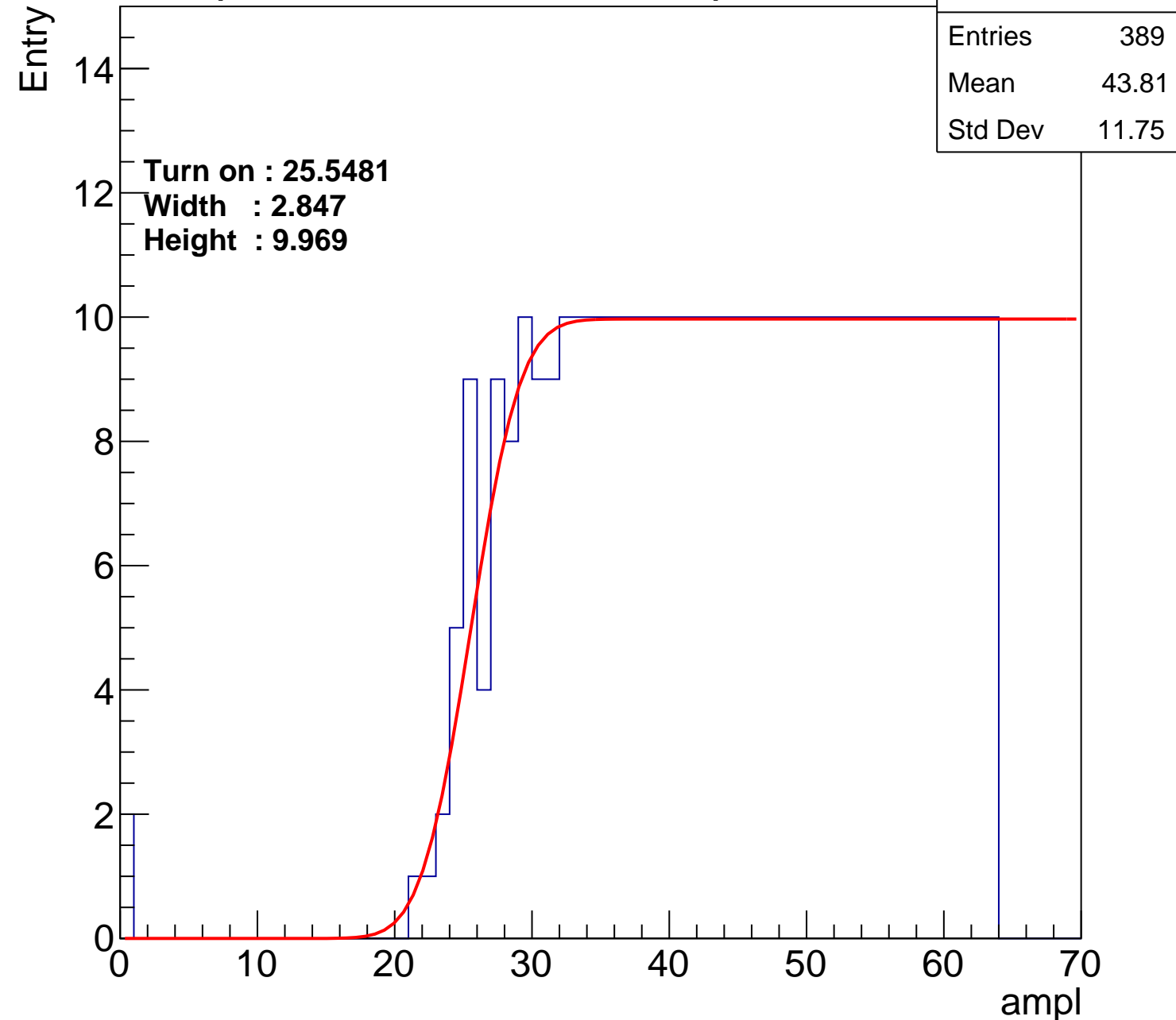
Width : 2.847

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.77
Std Dev	12.34

Turn on : 26.0304

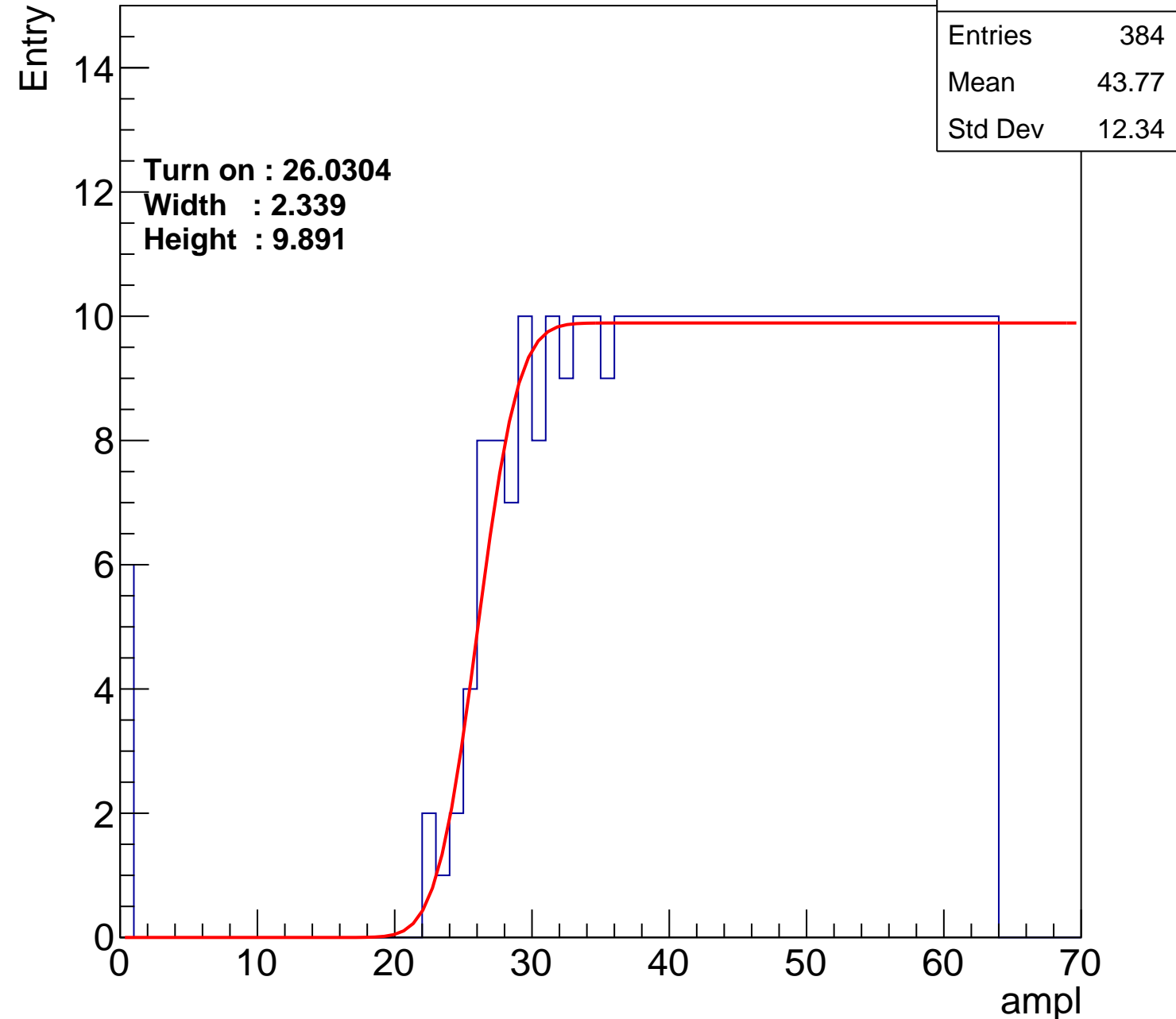
Width : 2.339

Height : 9.891

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.48
Std Dev	12.49

Turn on : 25.9020

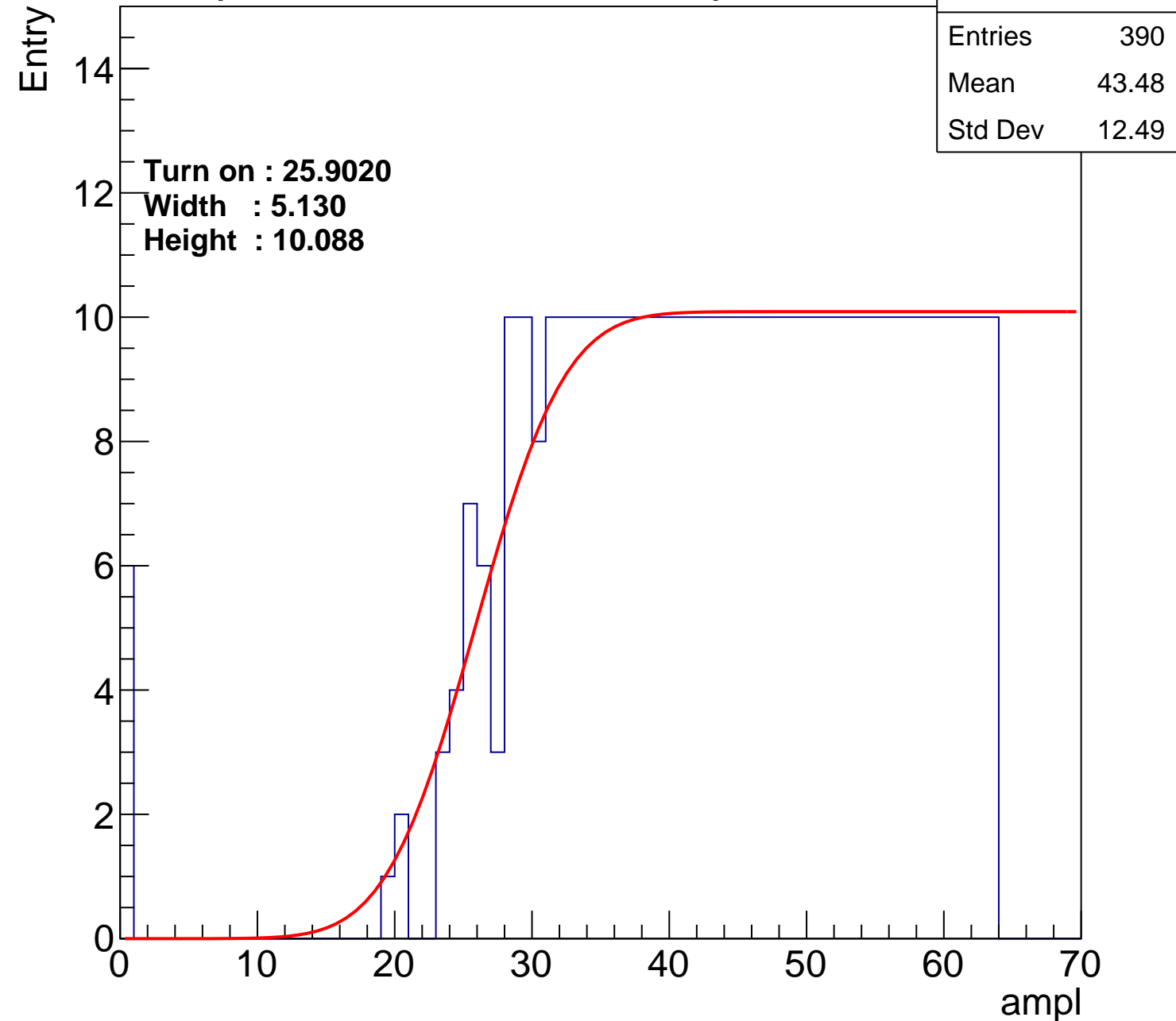
Width : 5.130

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 27.1058

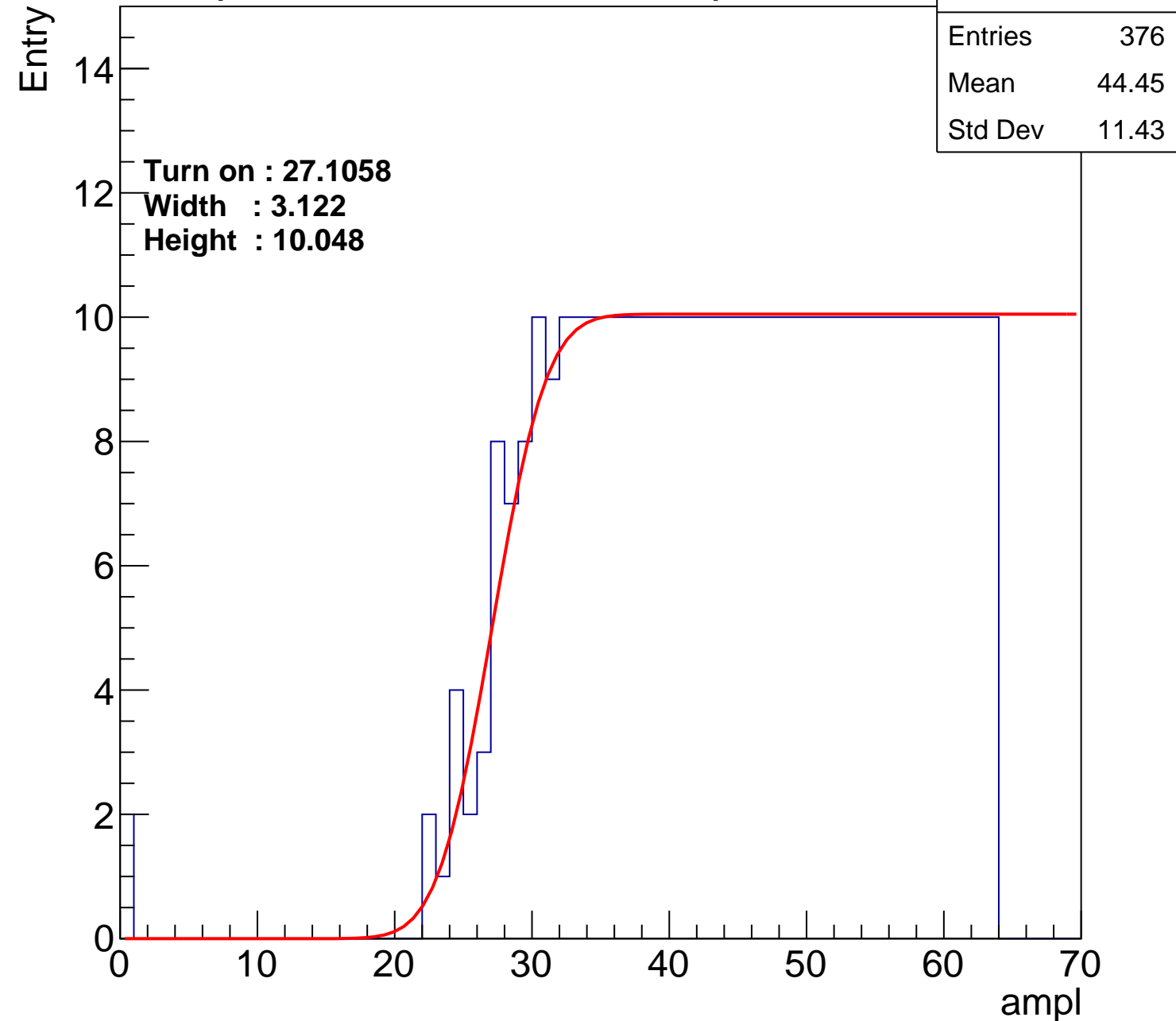
Width : 3.122

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.54
Std Dev	12.42

Turn on : 25.2703

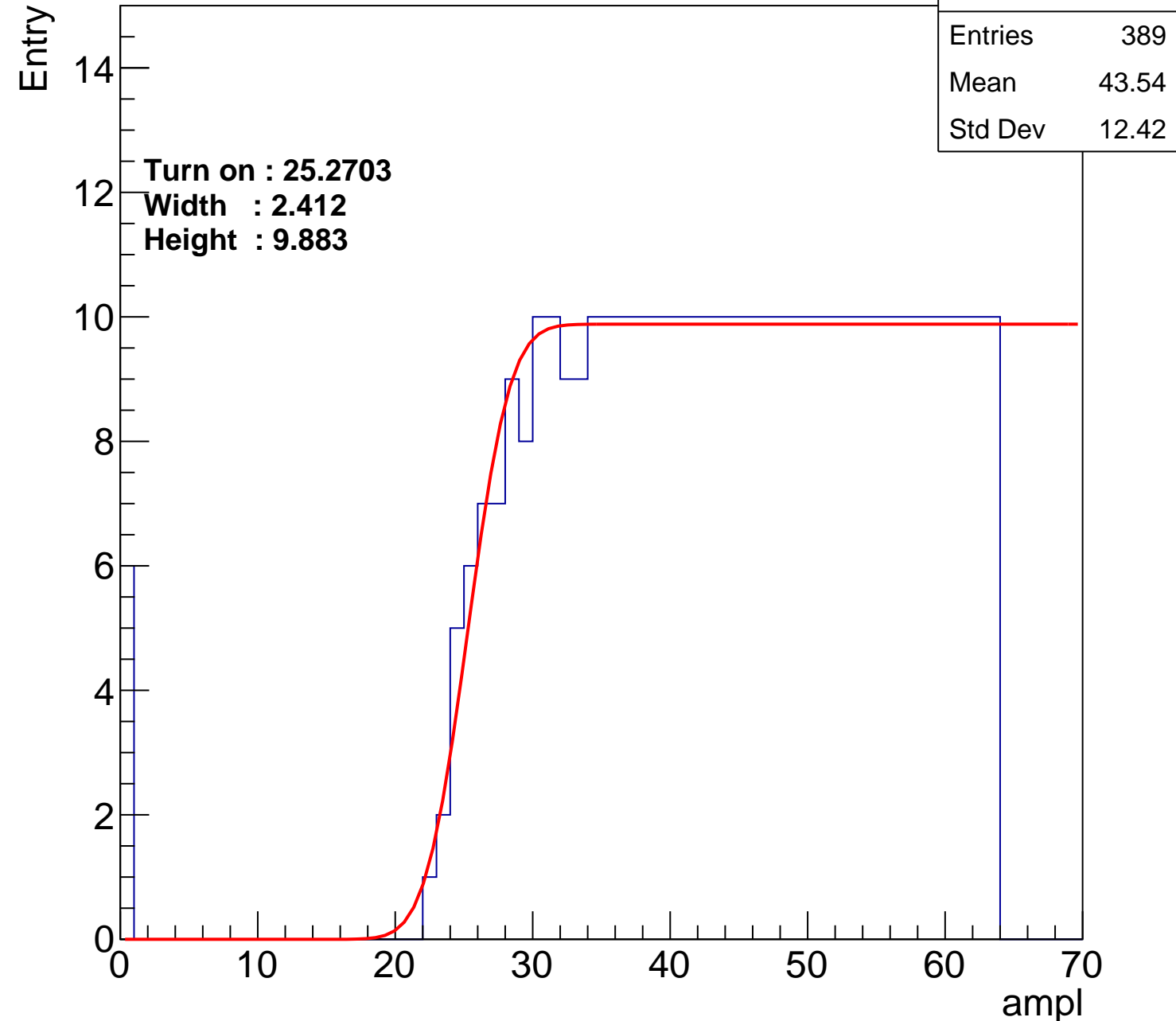
Width : 2.412

Height : 9.883

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch79

calib_packv5_042523_0143.root, FC#12, port B1

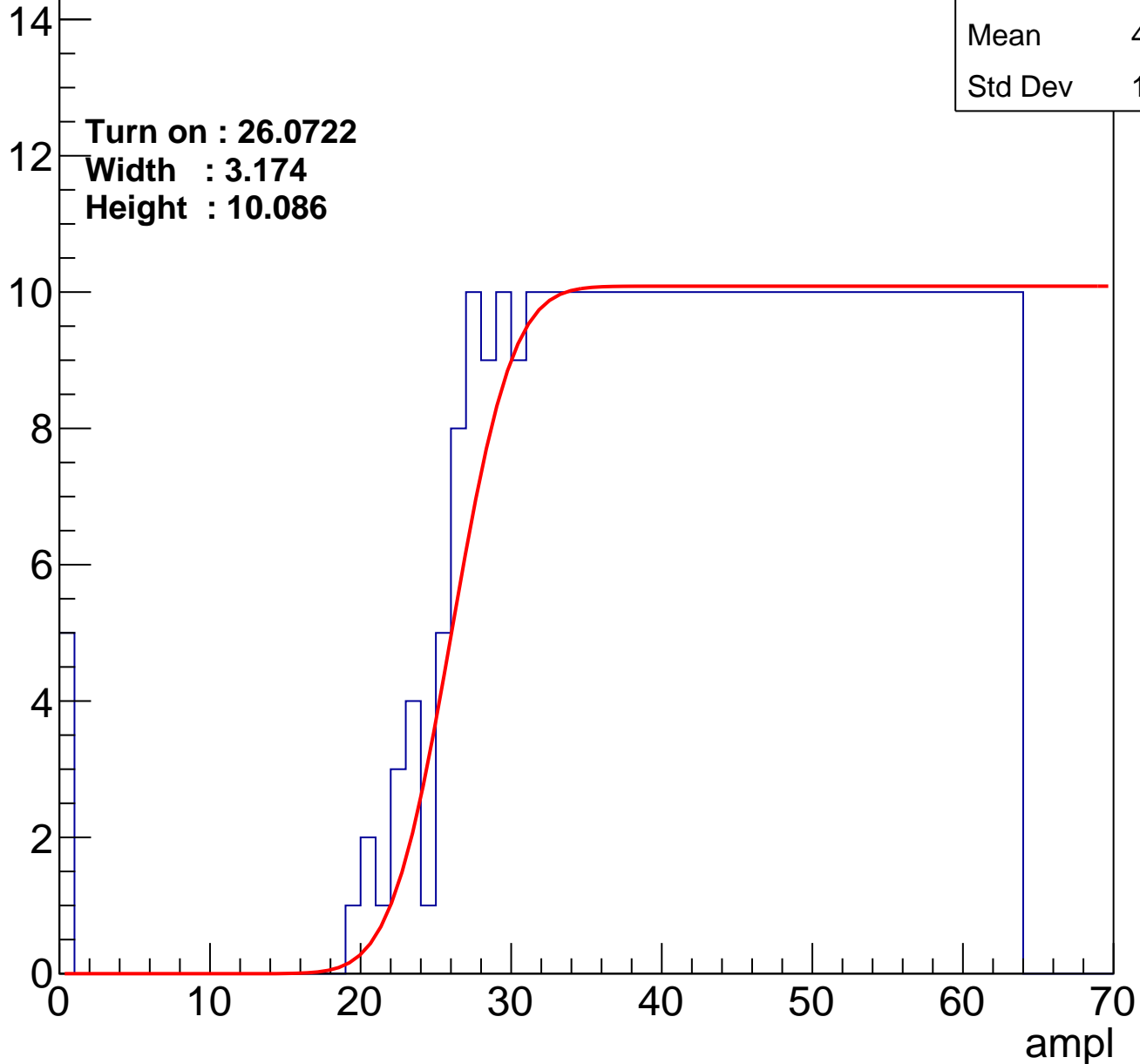
Entries	398
Mean	43.18
Std Dev	12.46

Turn on : 26.0722

Width : 3.174

Height : 10.086

Entry



B0L102S, U4-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.71
Std Dev	12.12

Turn on : 26.3591

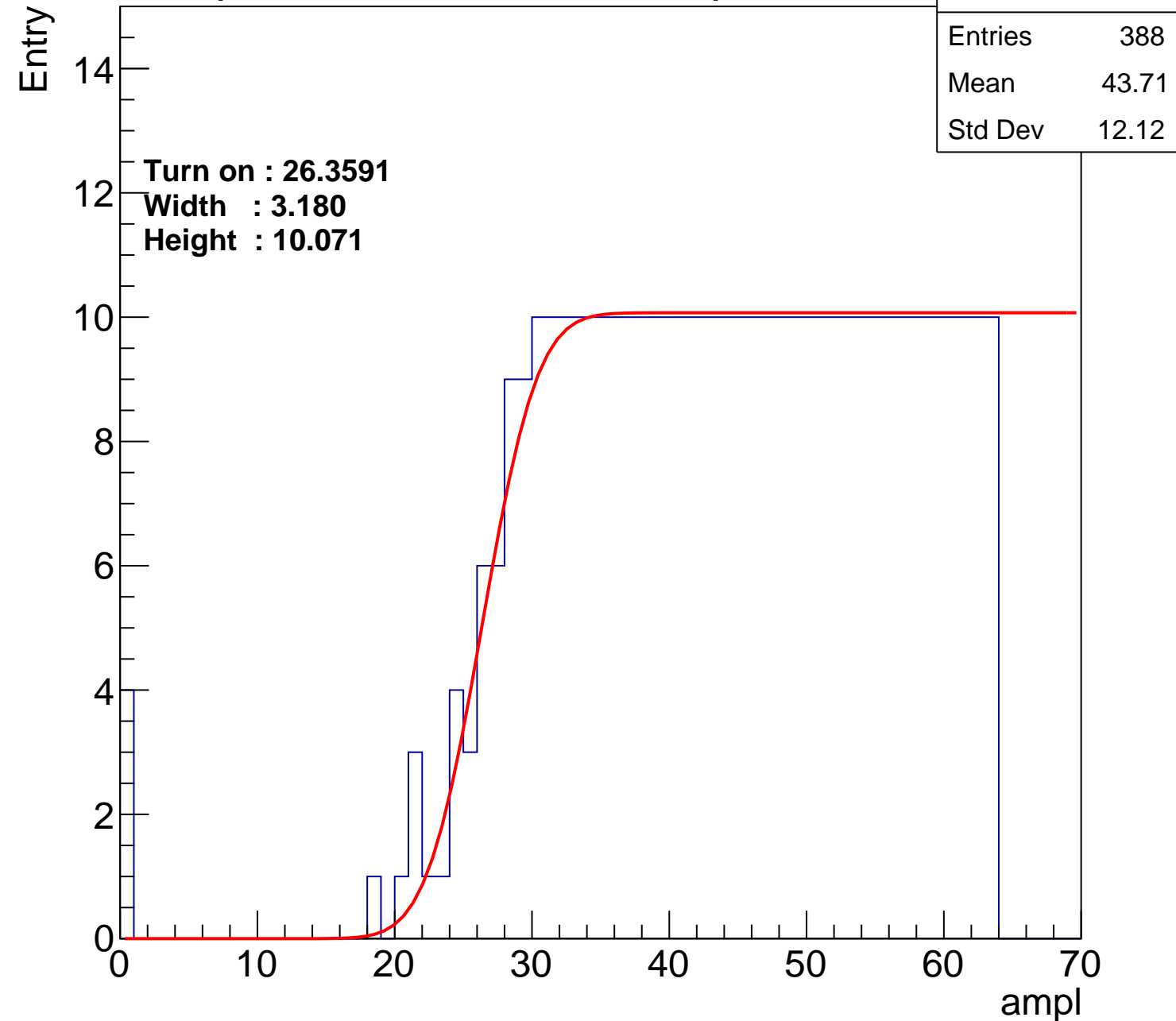
Width : 3.180

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.99
Std Dev	11.85

Turn on : 25.9316

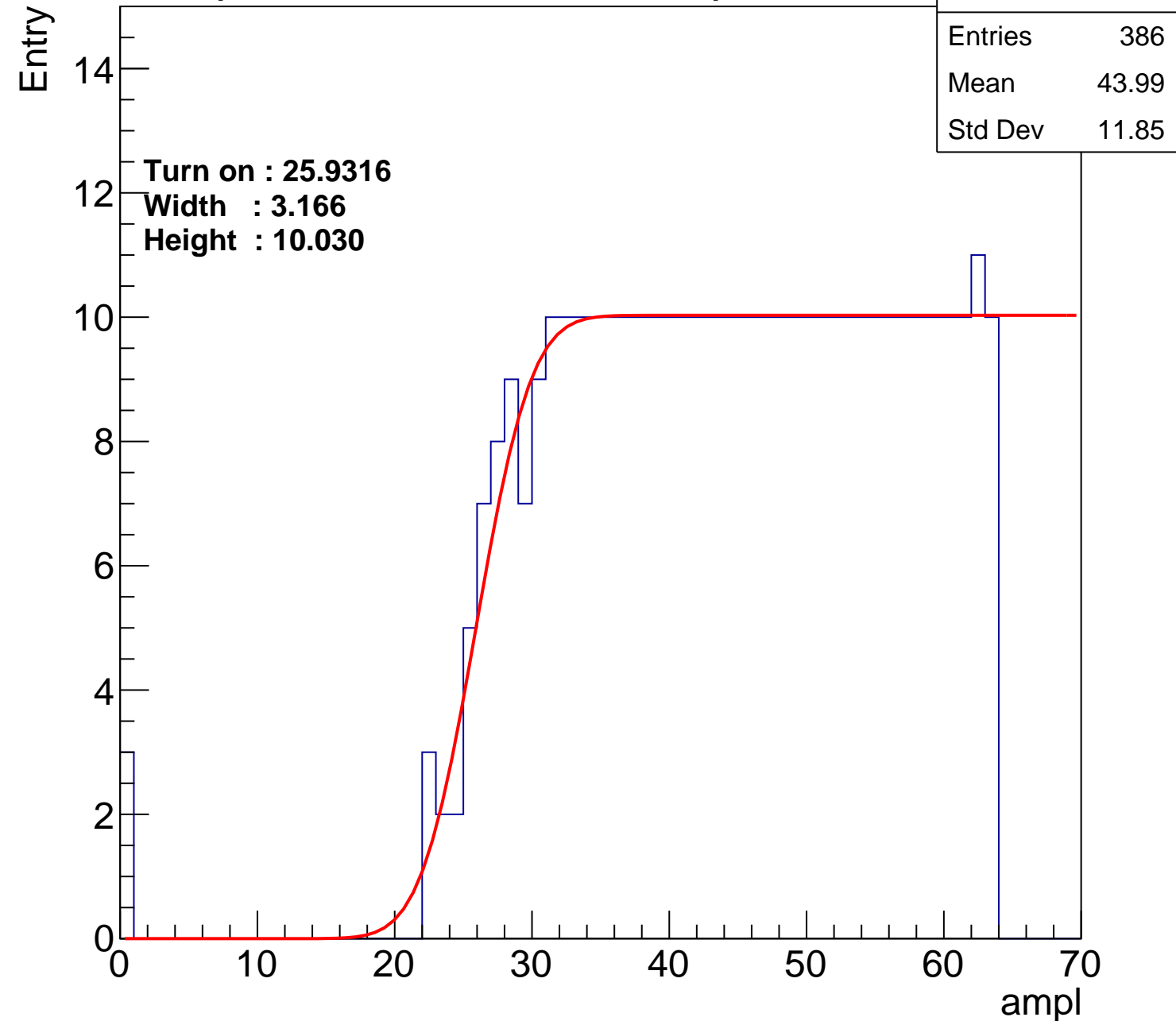
Width : 3.166

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.55
Std Dev	11.8

Turn on : 24.7873

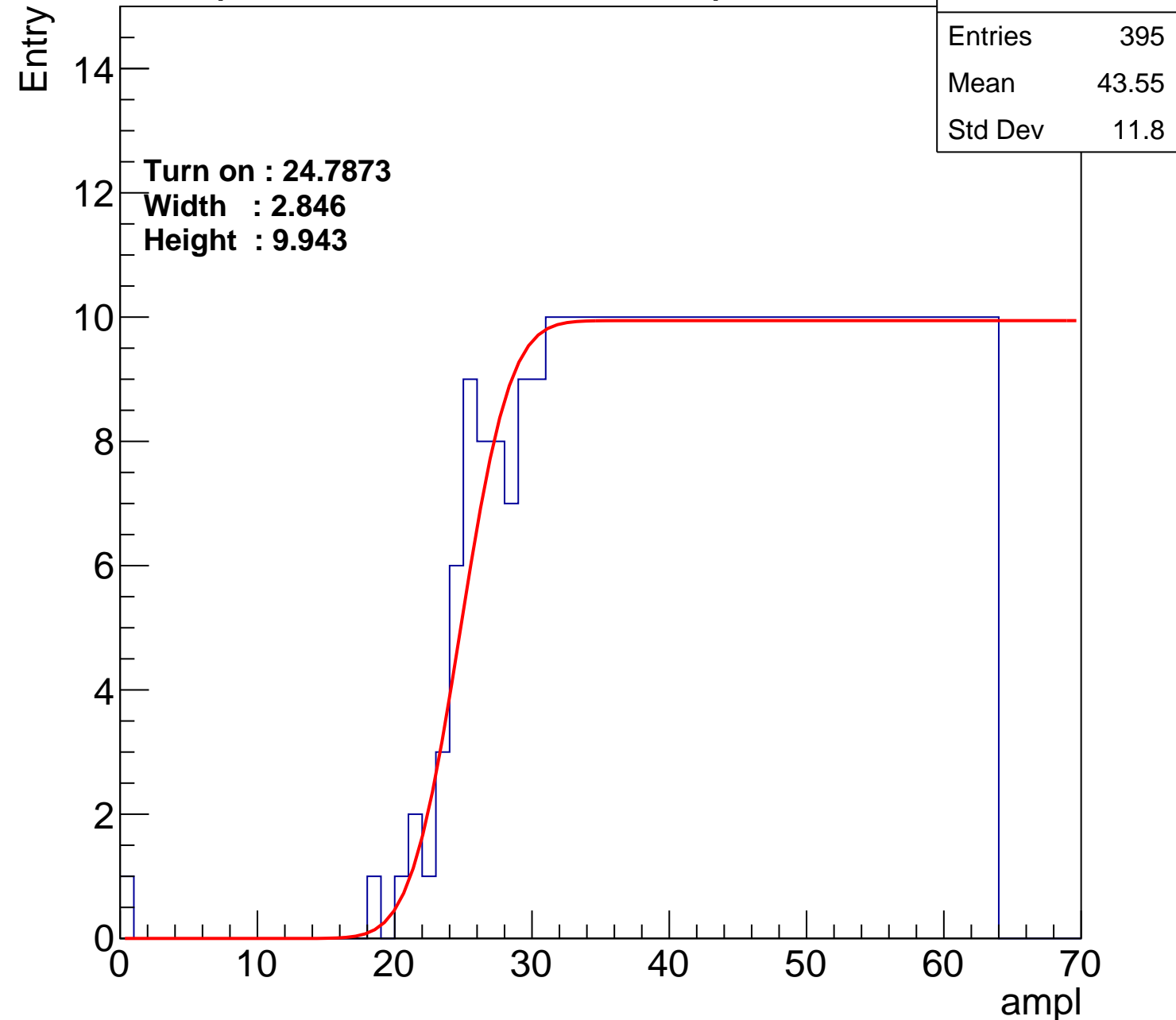
Width : 2.846

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.79
Std Dev	11.8

Turn on : 26.3643

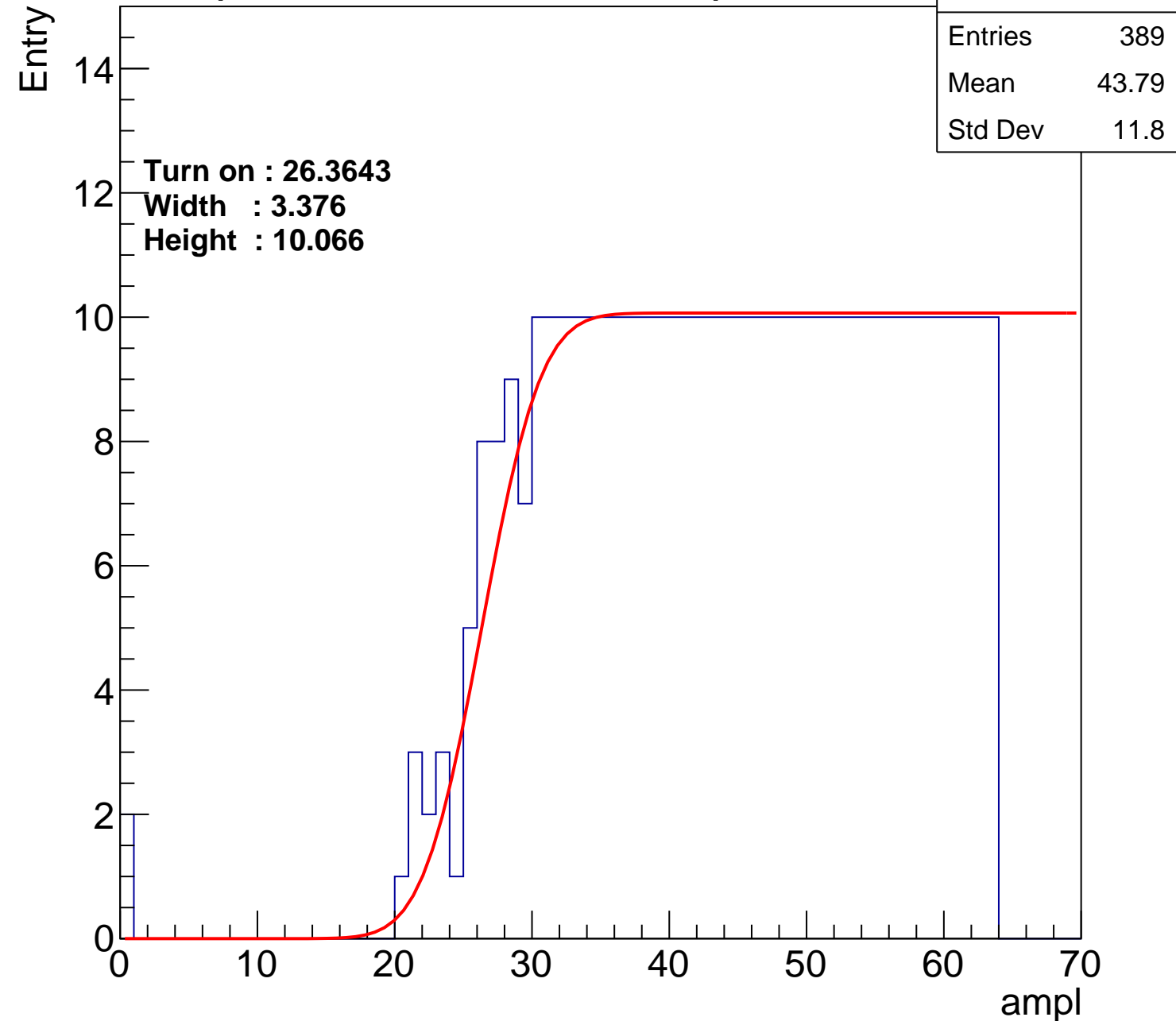
Width : 3.376

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.7539

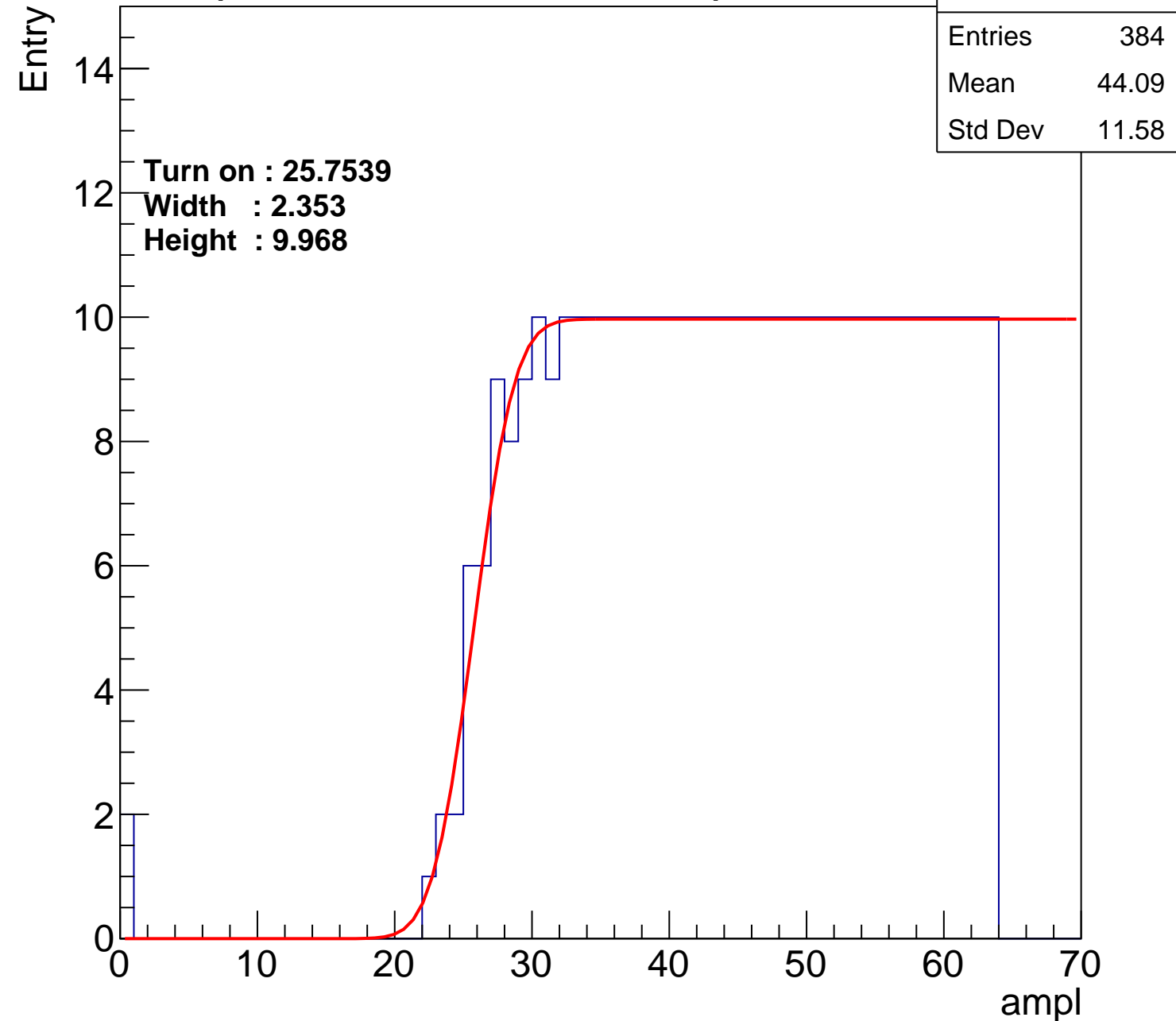
Width : 2.353

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.29
Std Dev	11.53

Turn on : 26.9655

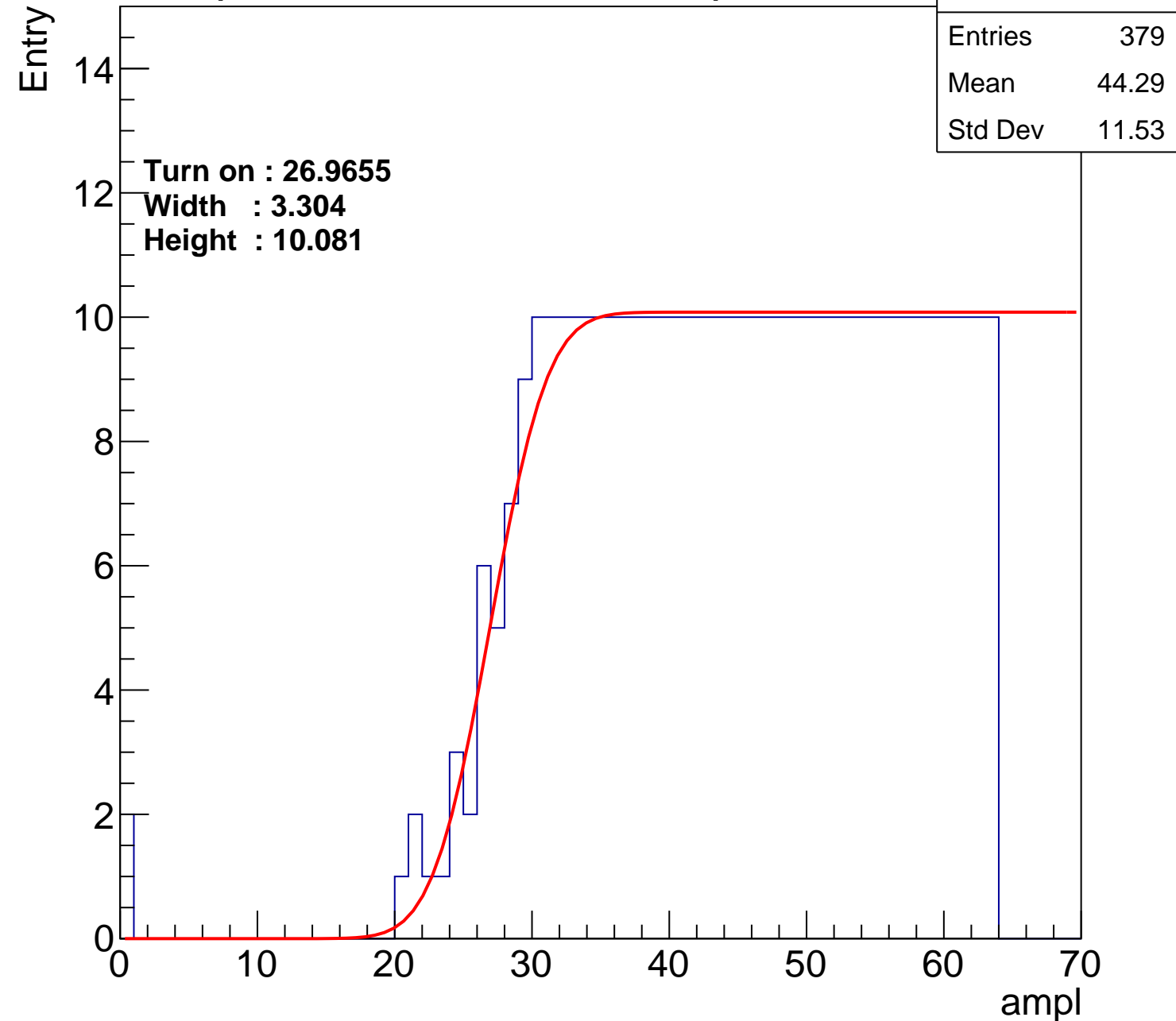
Width : 3.304

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.4318

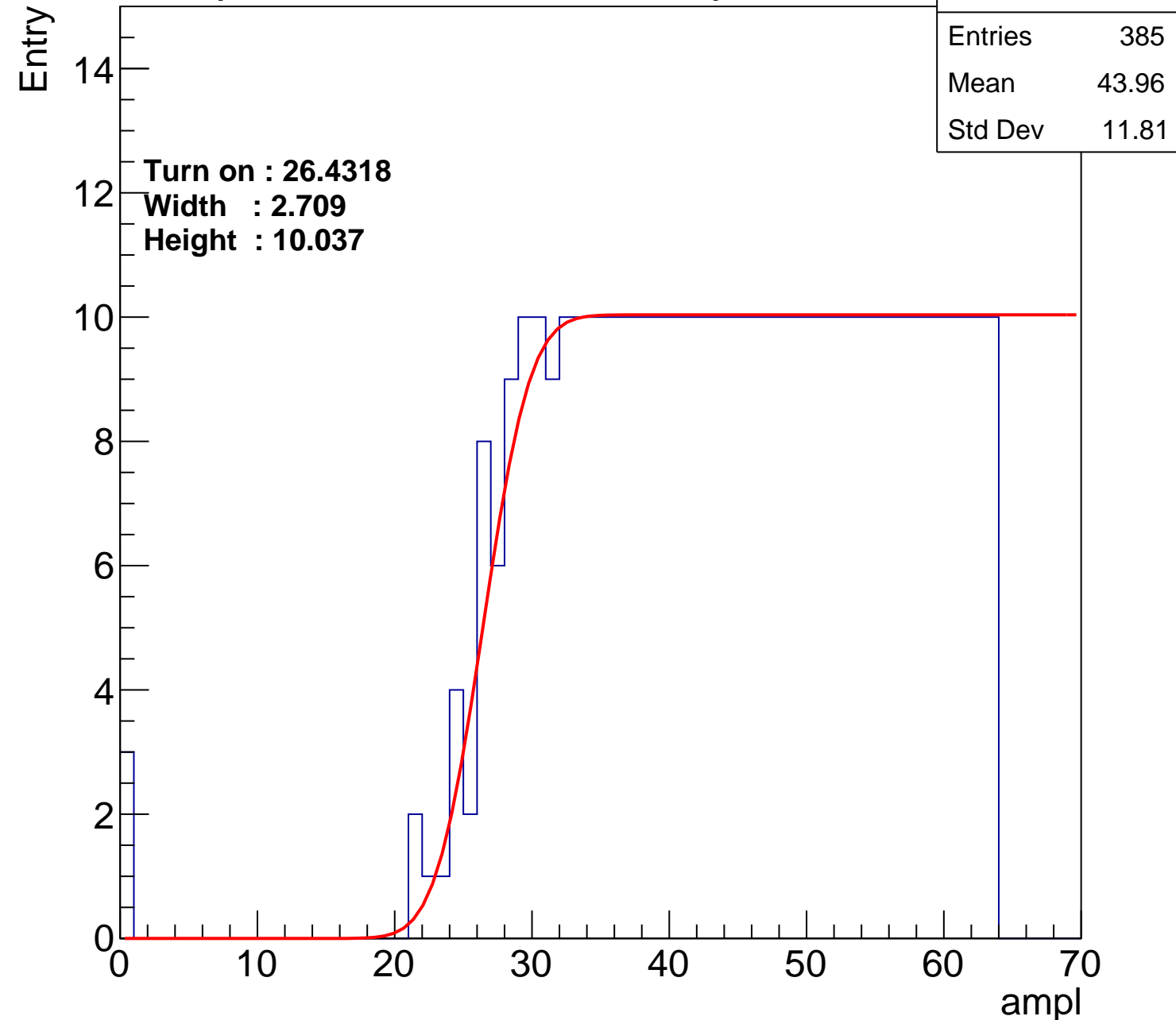
Width : 2.709

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.81
Std Dev	12.42

Turn on : 23.5150

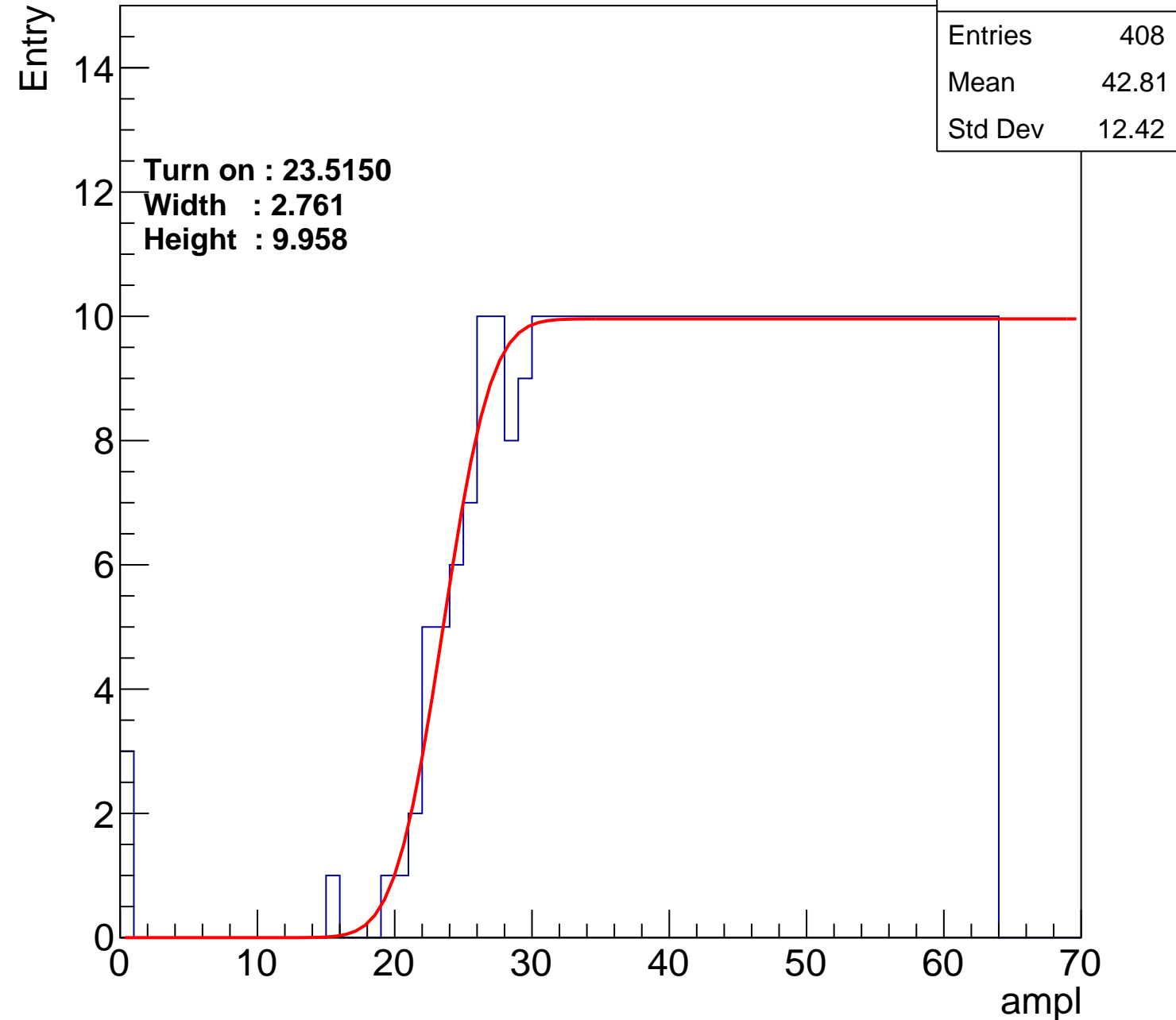
Width : 2.761

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.68
Std Dev	12.05

Turn on : 25.8052

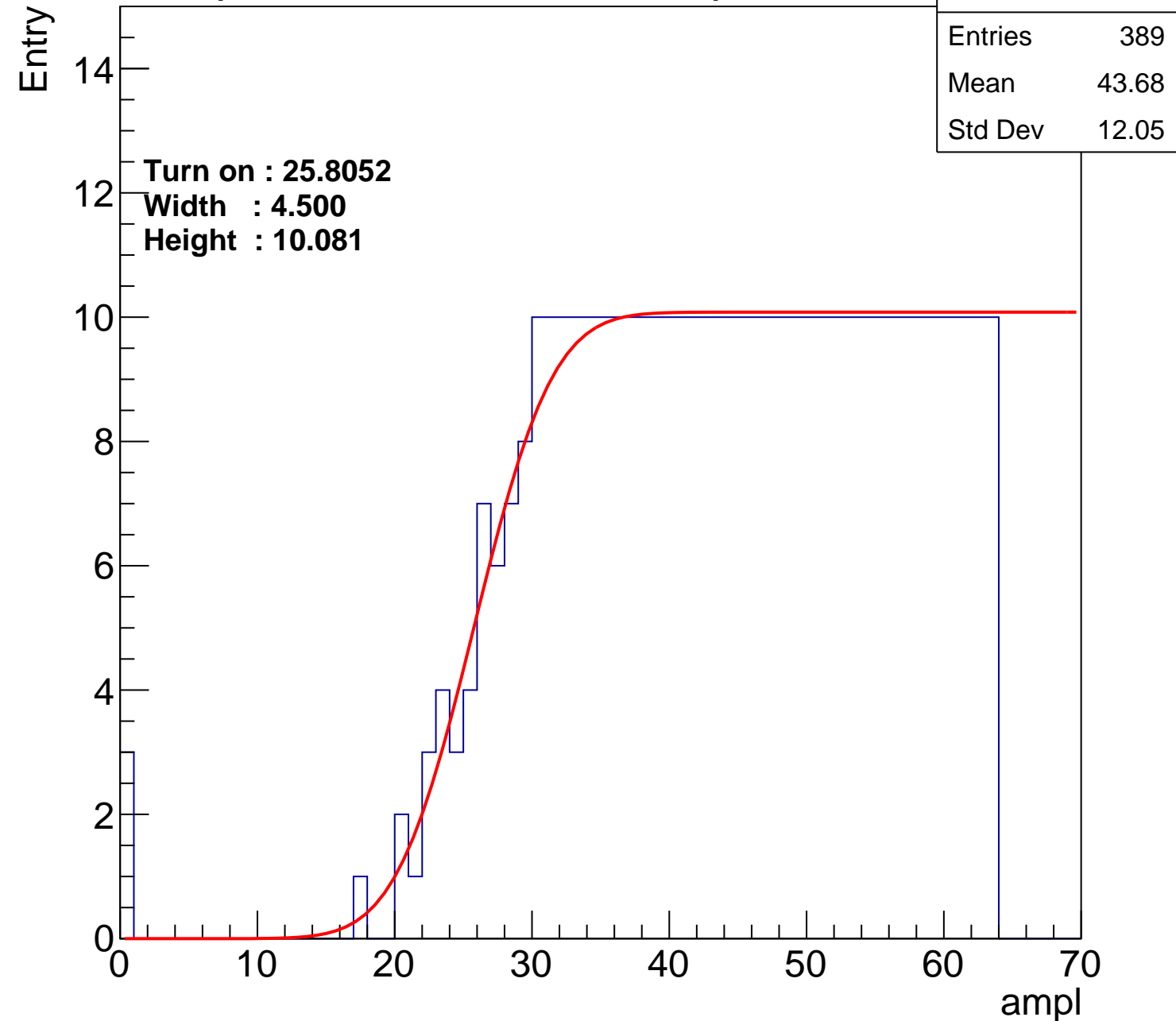
Width : 4.500

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.01
Std Dev	11.72

Turn on : 26.5494

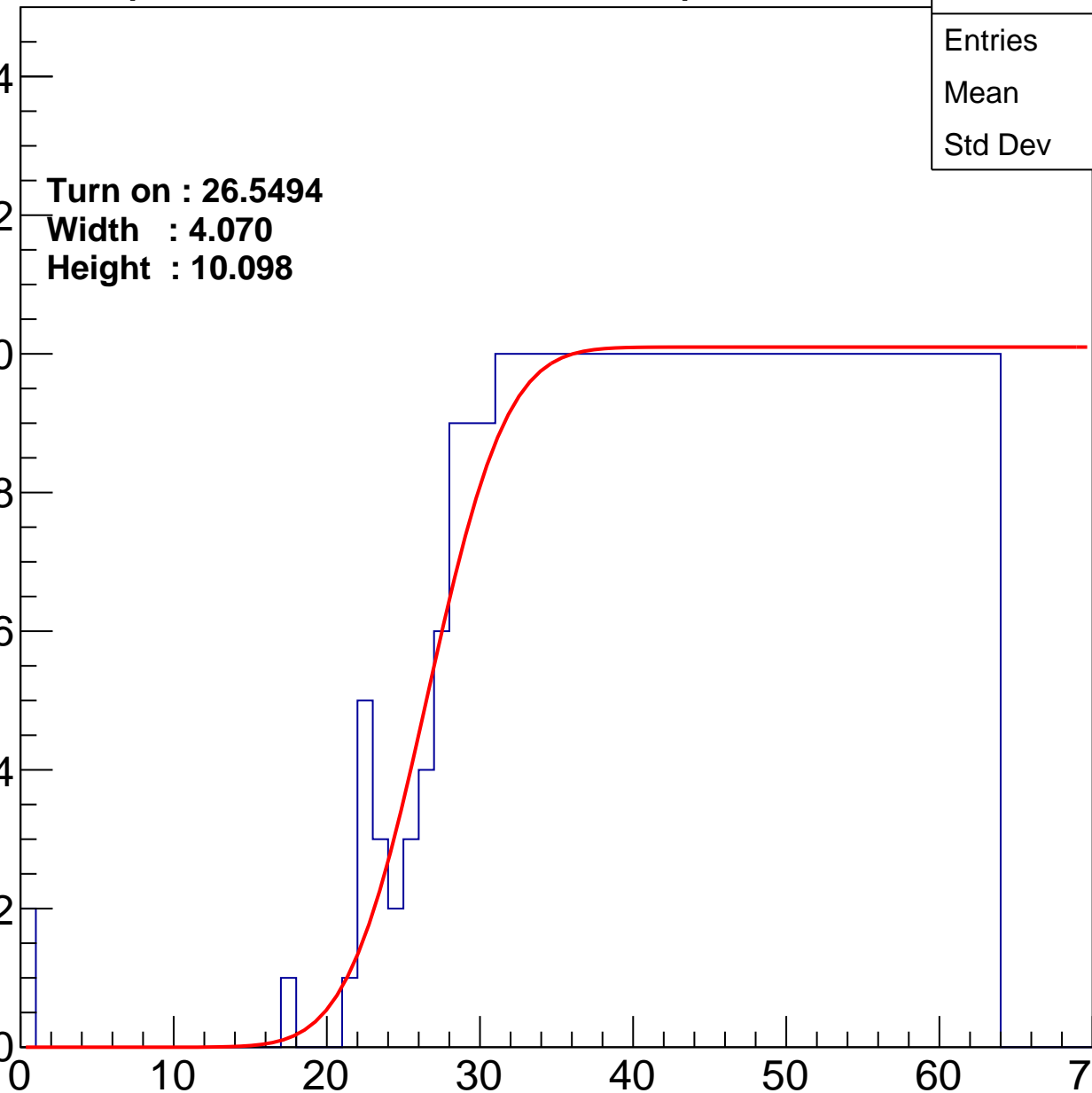
Width : 4.070

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.02
Std Dev	12.52

Turn on : 25.0407

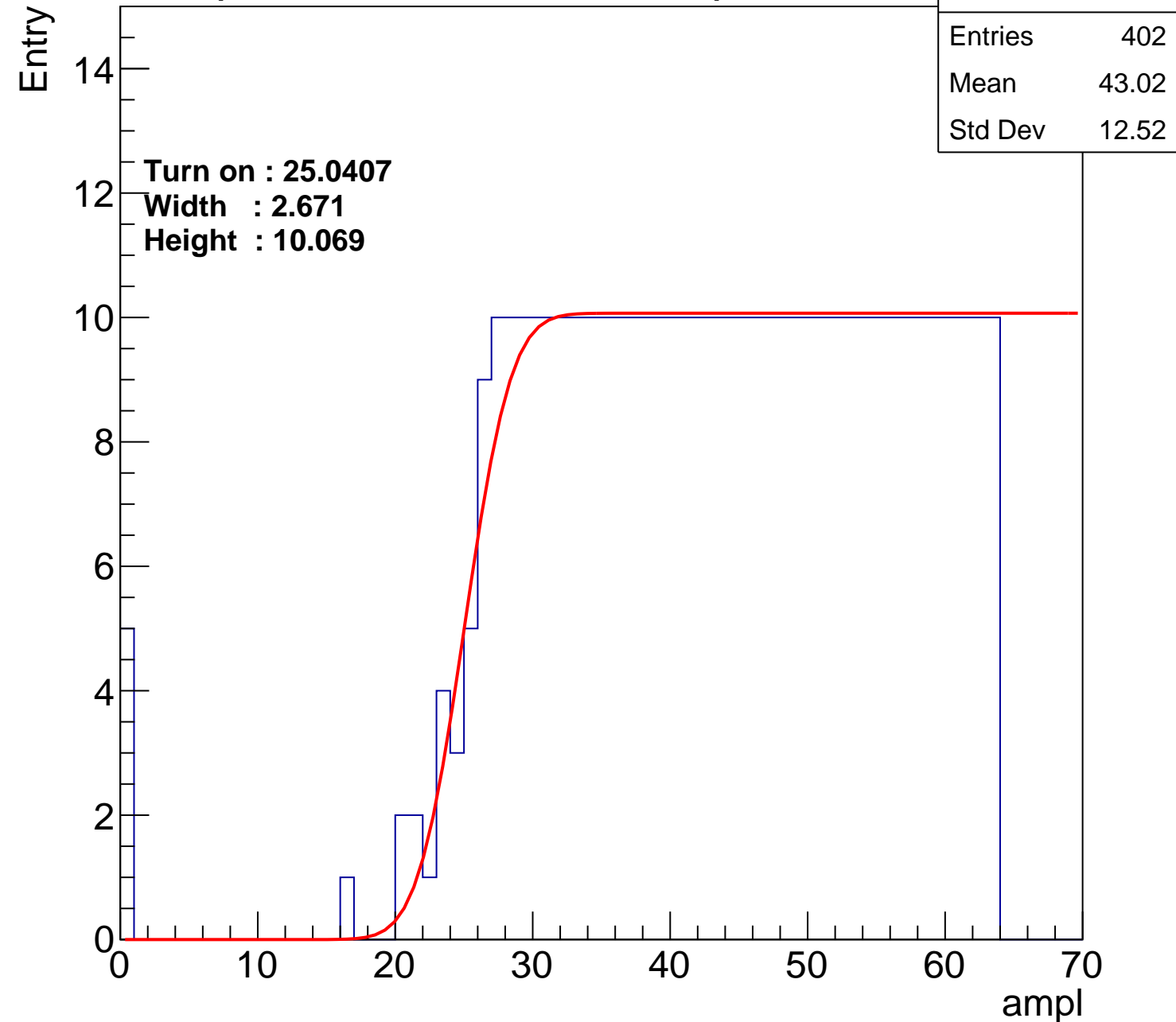
Width : 2.671

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.16
Std Dev	11.75

Turn on : 26.4774

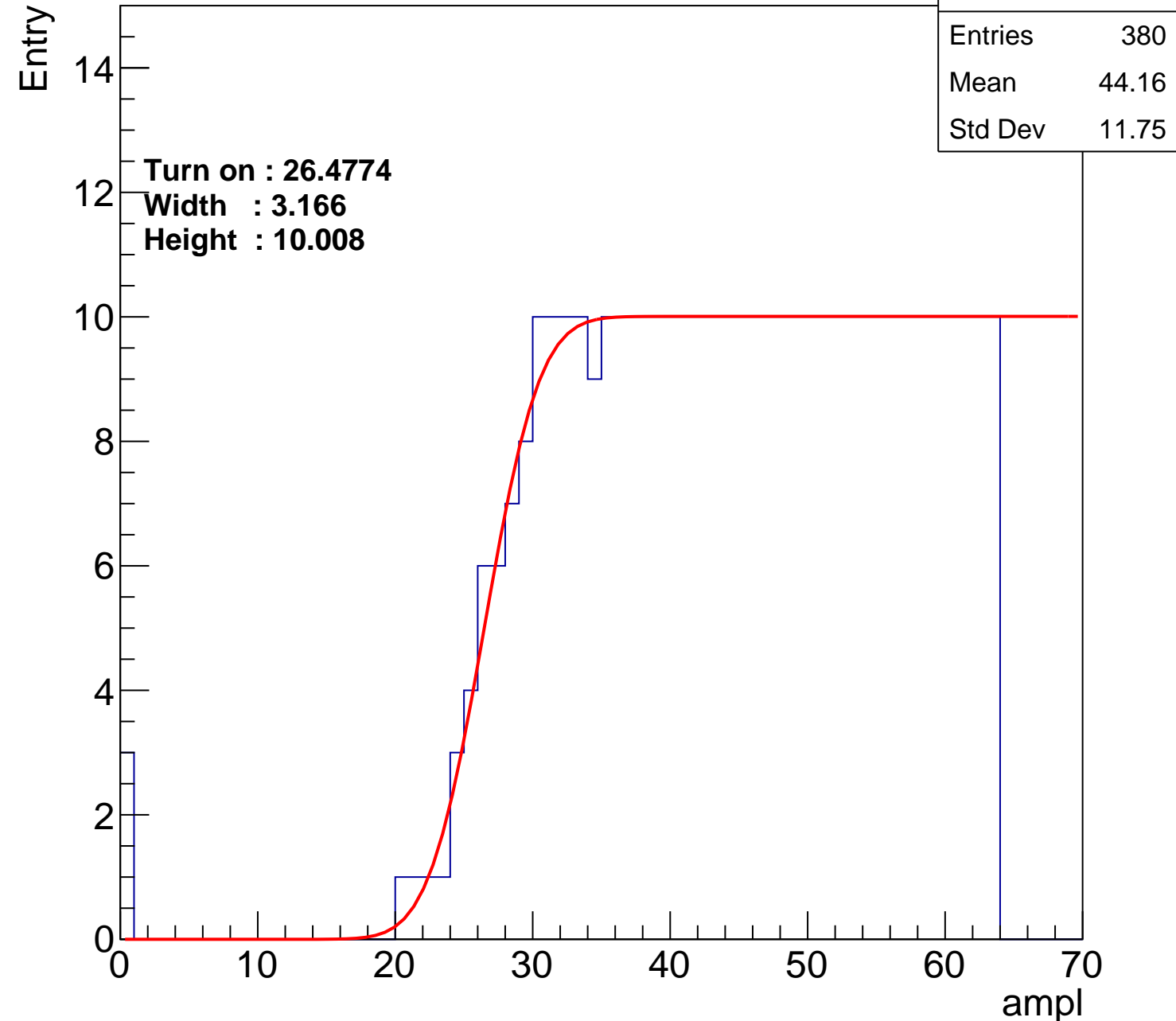
Width : 3.166

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.2
Std Dev	12.28

Turn on : 25.2522

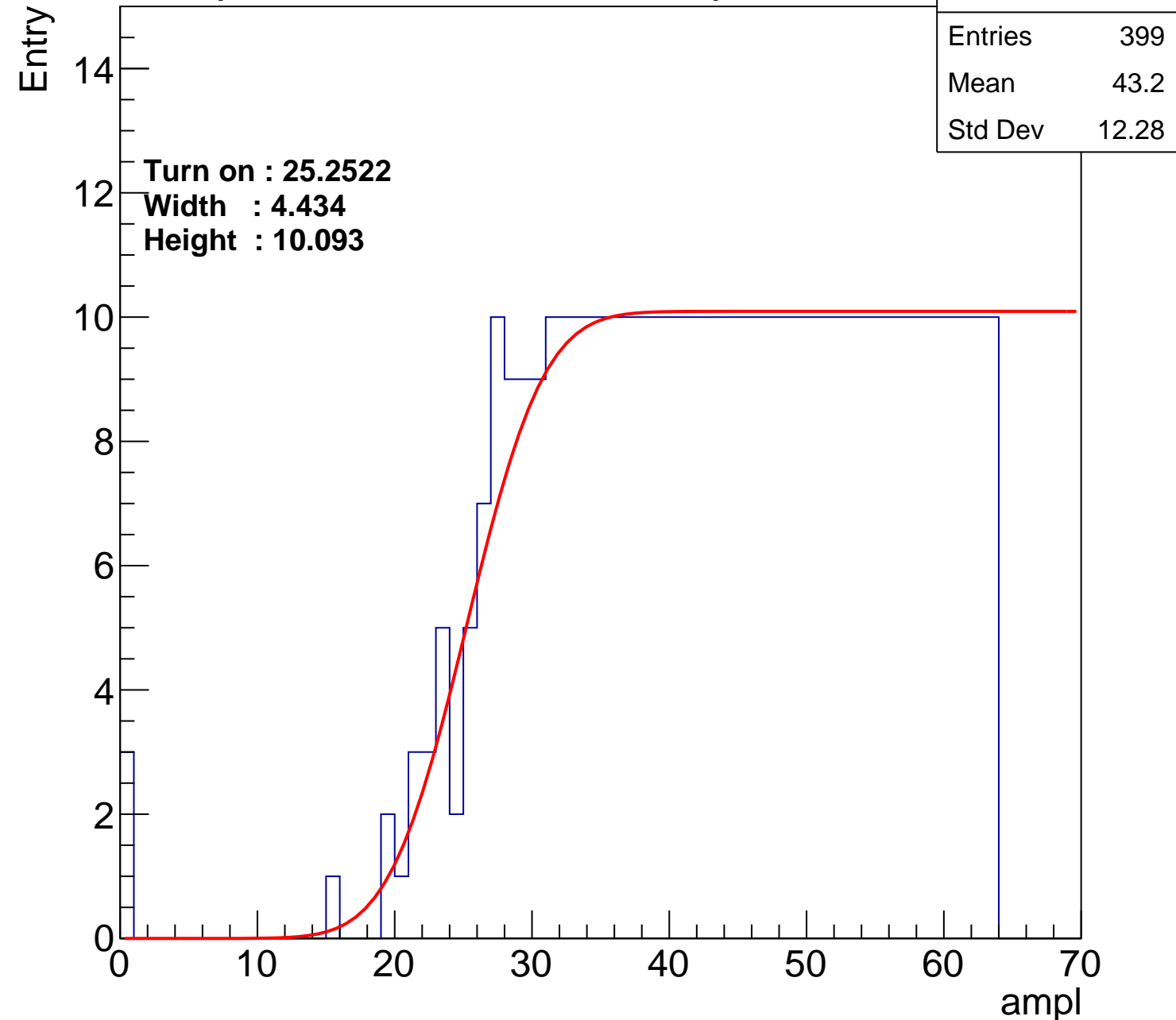
Width : 4.434

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.9
Std Dev	11.85

Turn on : 25.8478

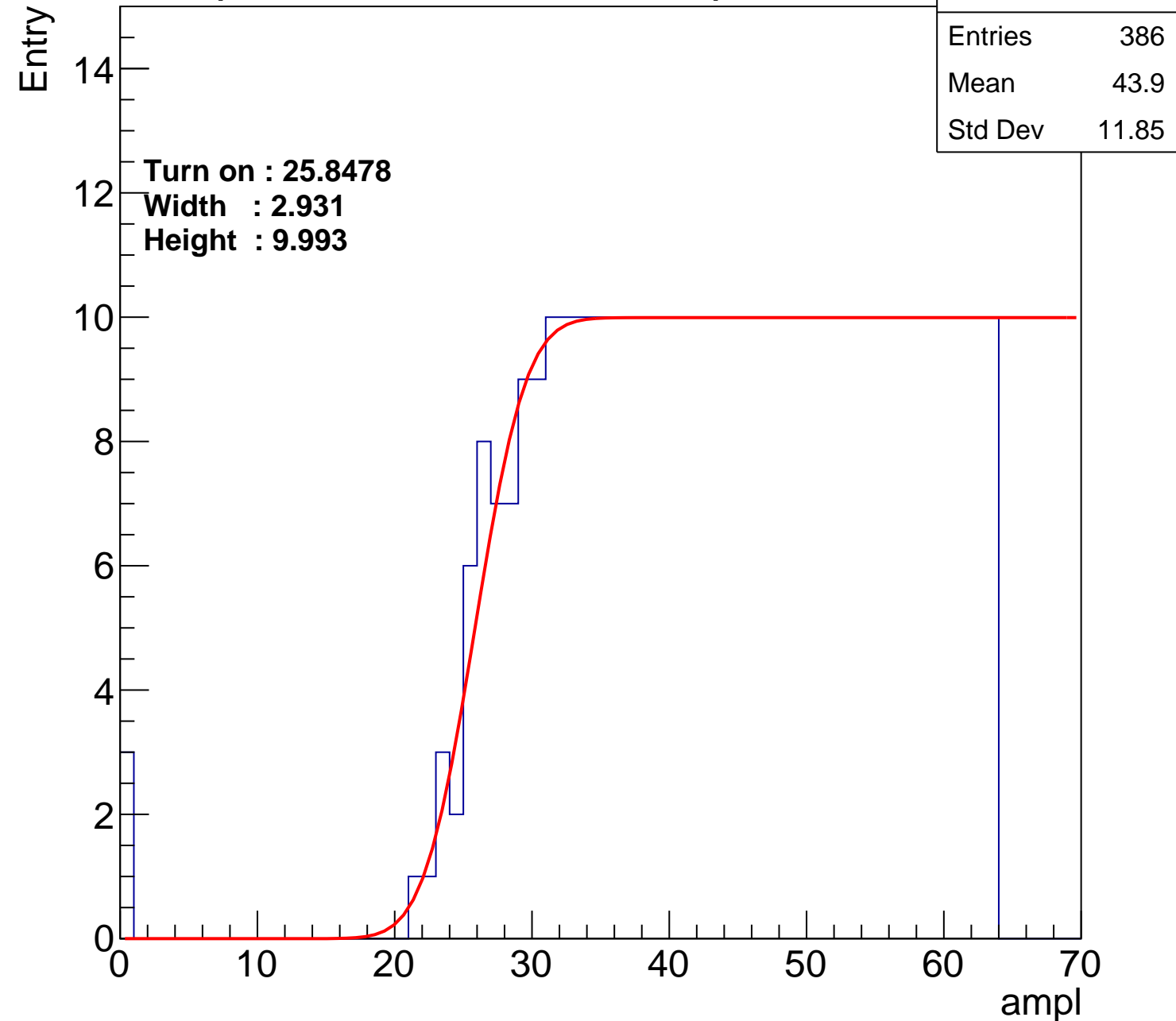
Width : 2.931

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.9707

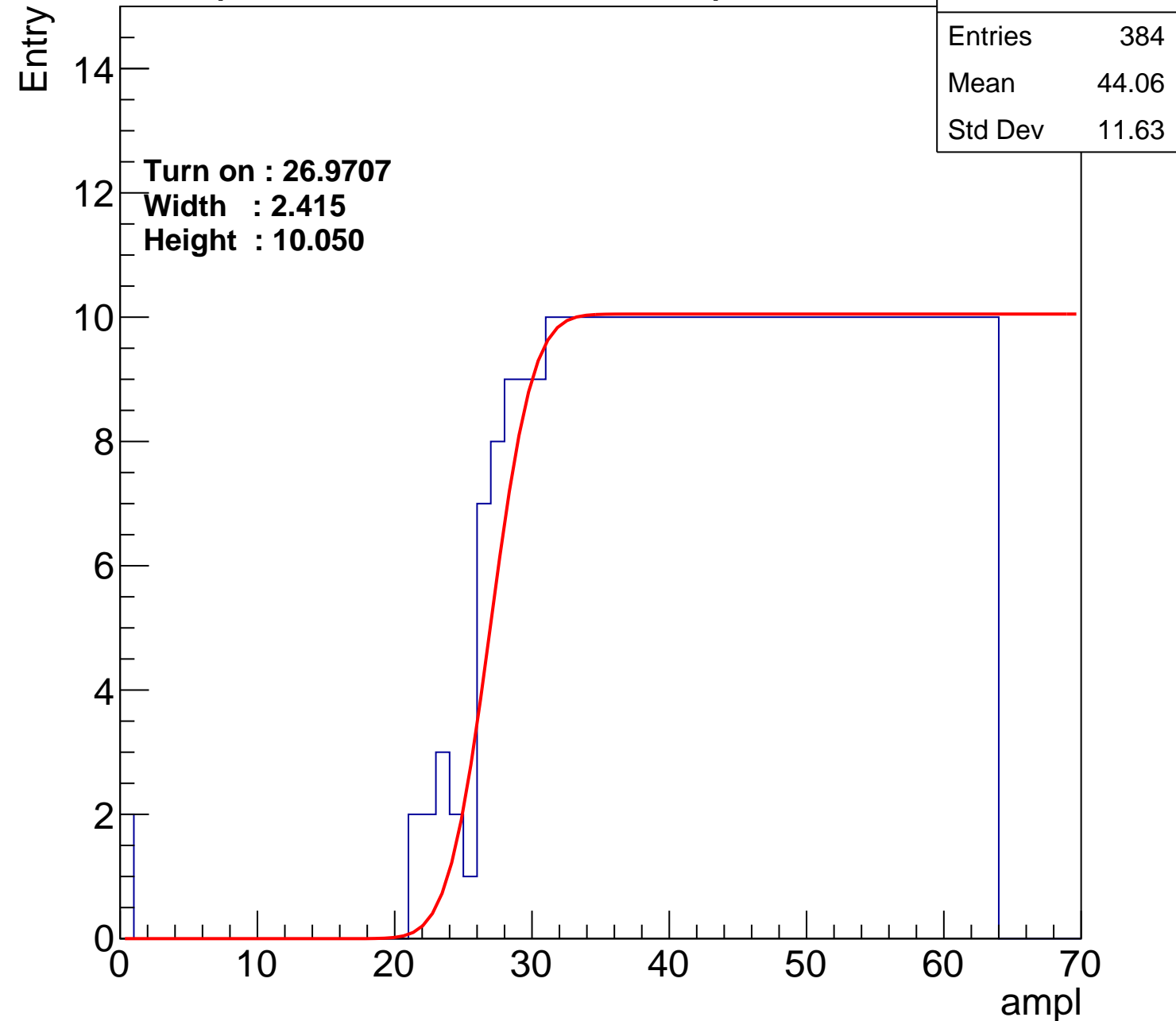
Width : 2.415

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.86
Std Dev	11.78

Turn on : 24.5363

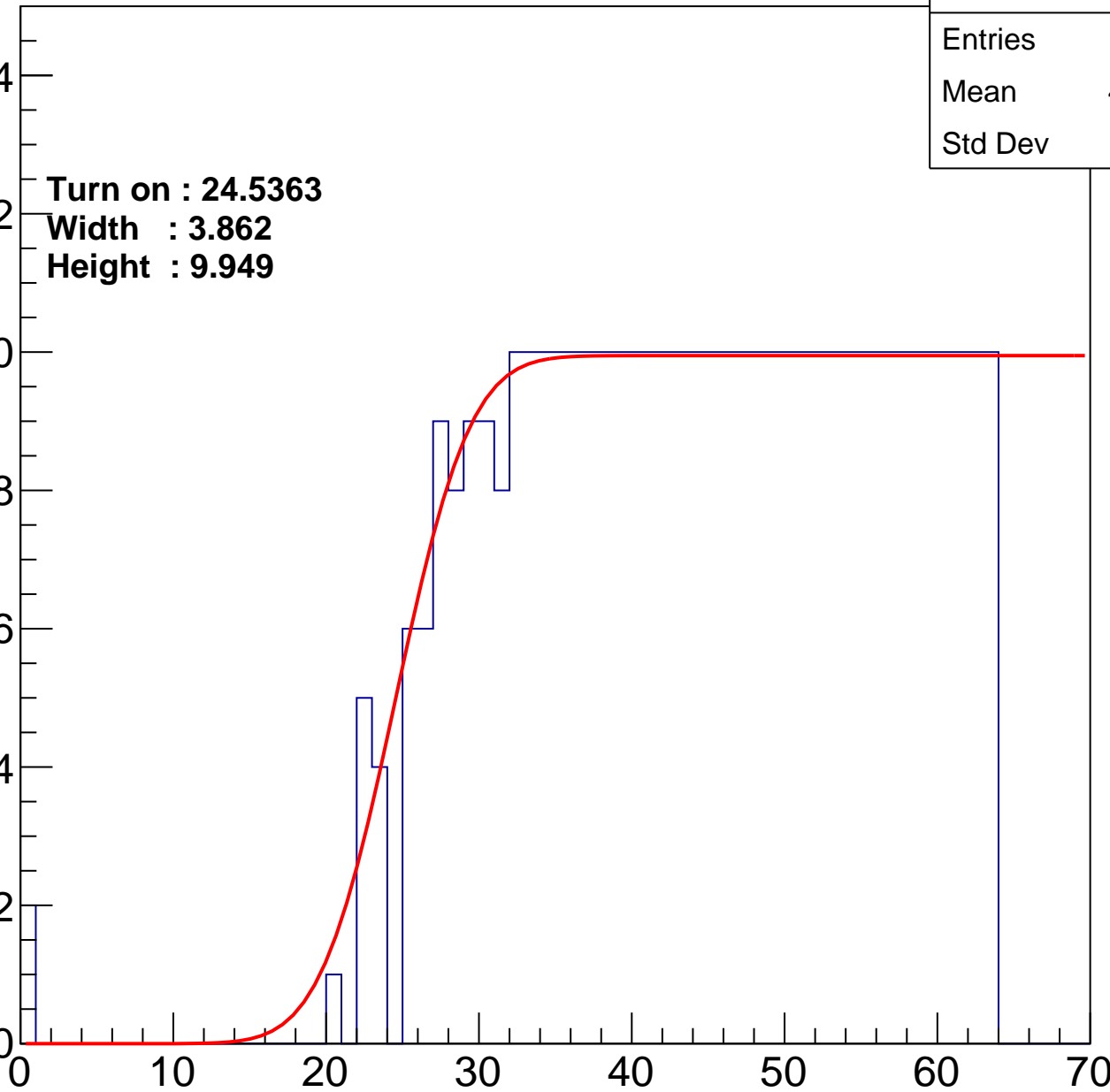
Width : 3.862

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.79
Std Dev	12.66

Turn on : 24.2633

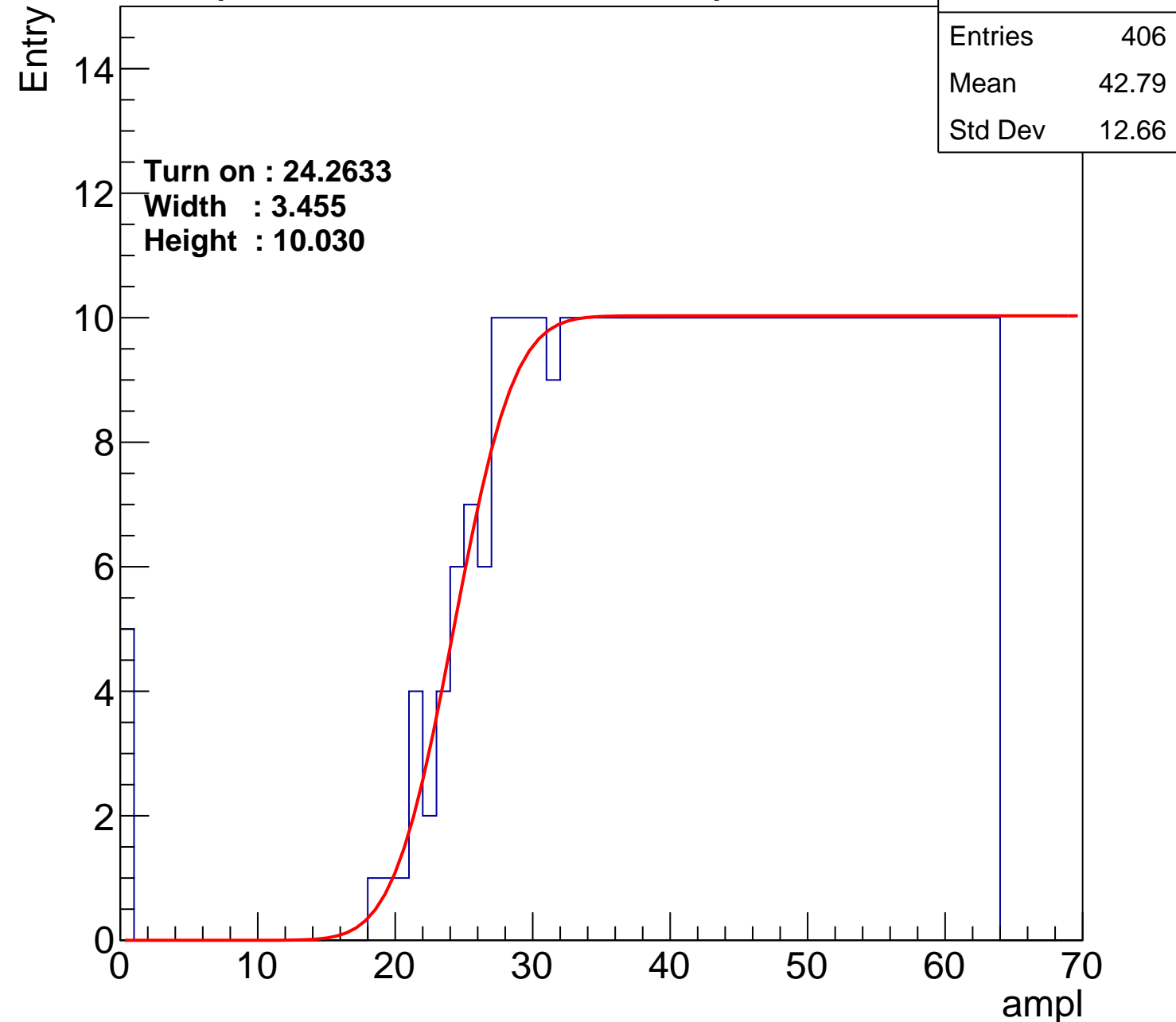
Width : 3.455

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.81
Std Dev	11.61

Turn on : 28.3742

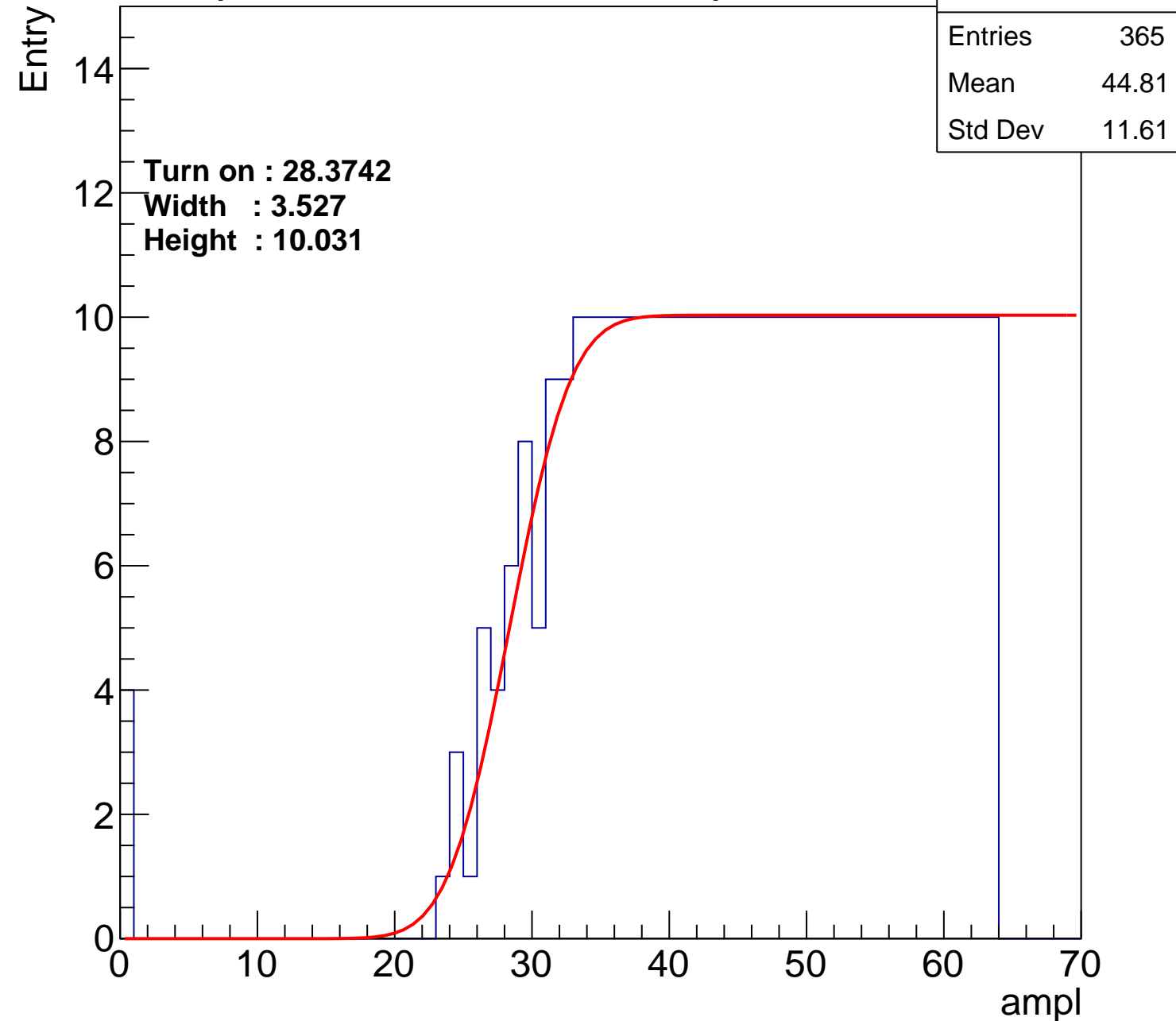
Width : 3.527

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.8
Std Dev	12.14

Turn on : 26.4605

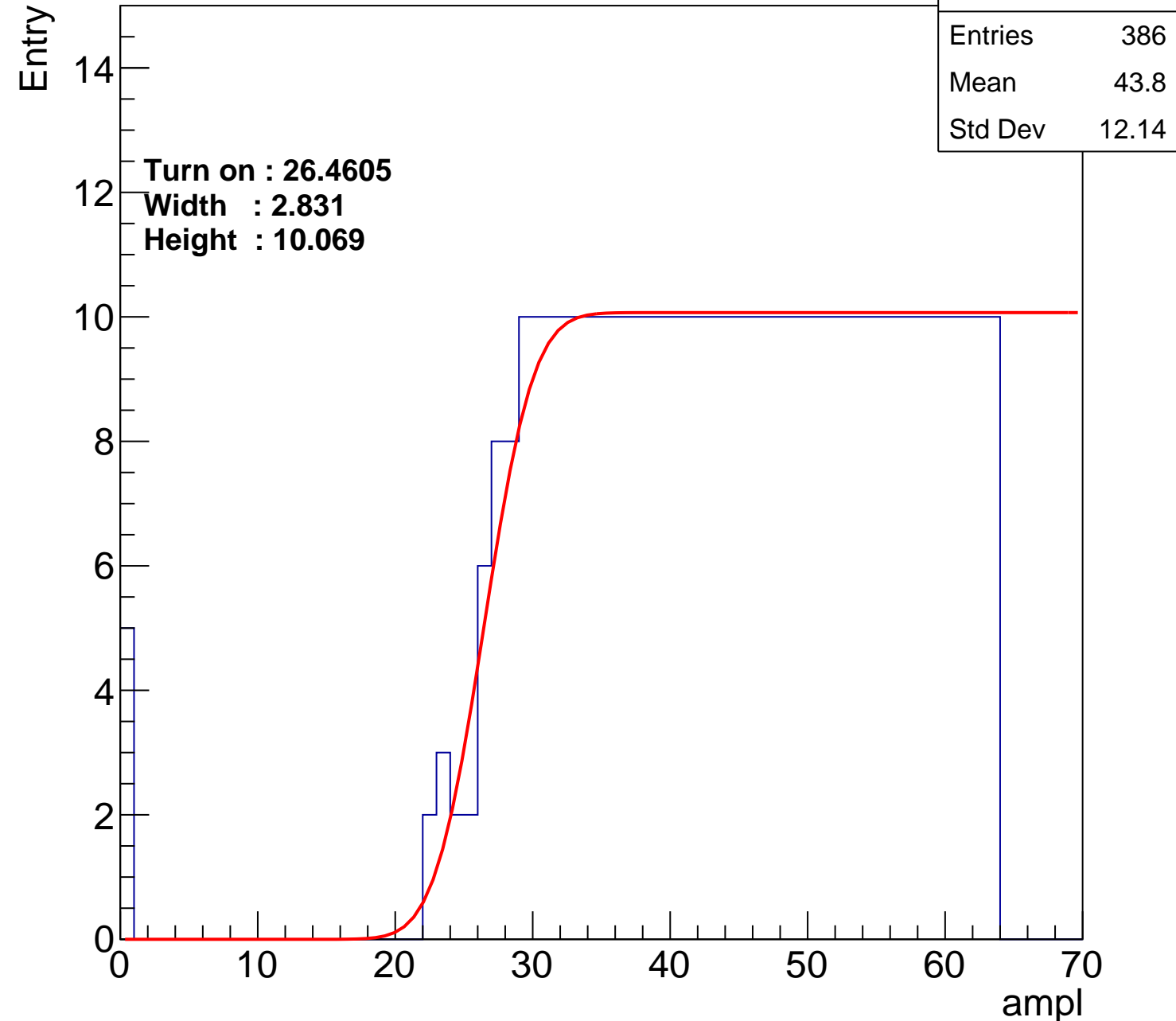
Width : 2.831

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch99

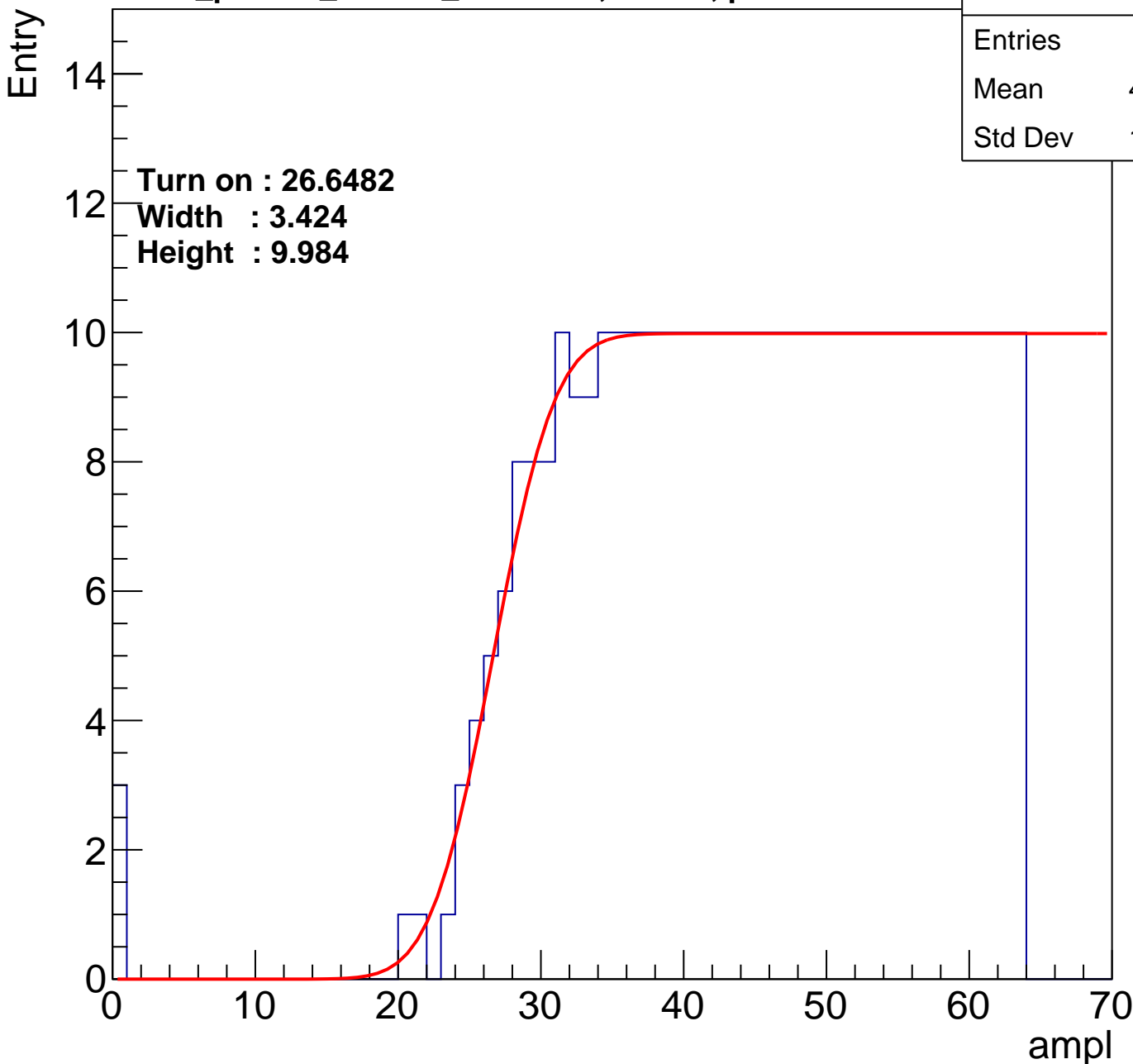
calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.33
Std Dev	11.69

Turn on : 26.6482

Width : 3.424

Height : 9.984



B0L102S, U4-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.05
Std Dev	12.52

Turn on : 25.0310

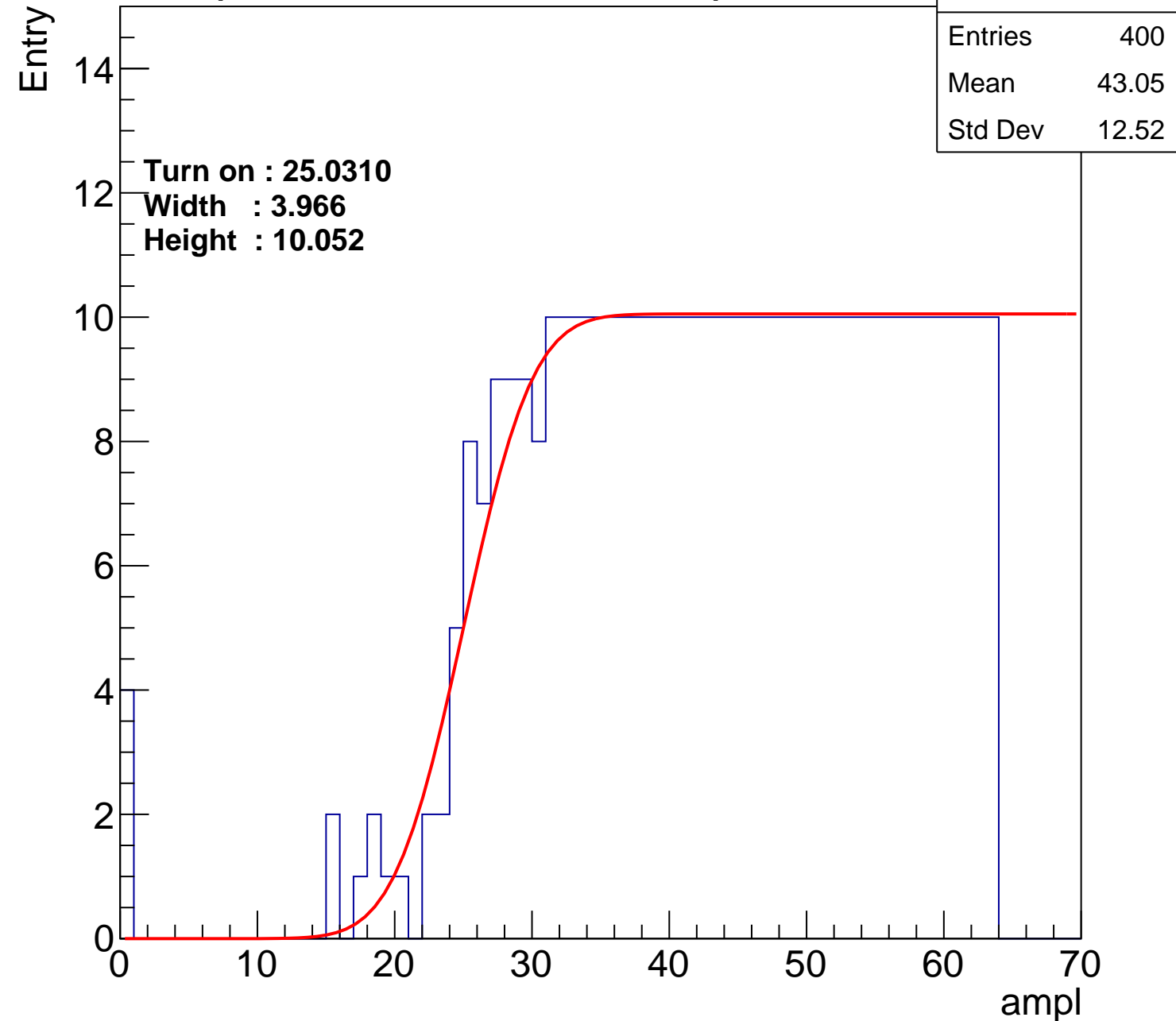
Width : 3.966

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.29
Std Dev	12.19

Turn on : 24.6545

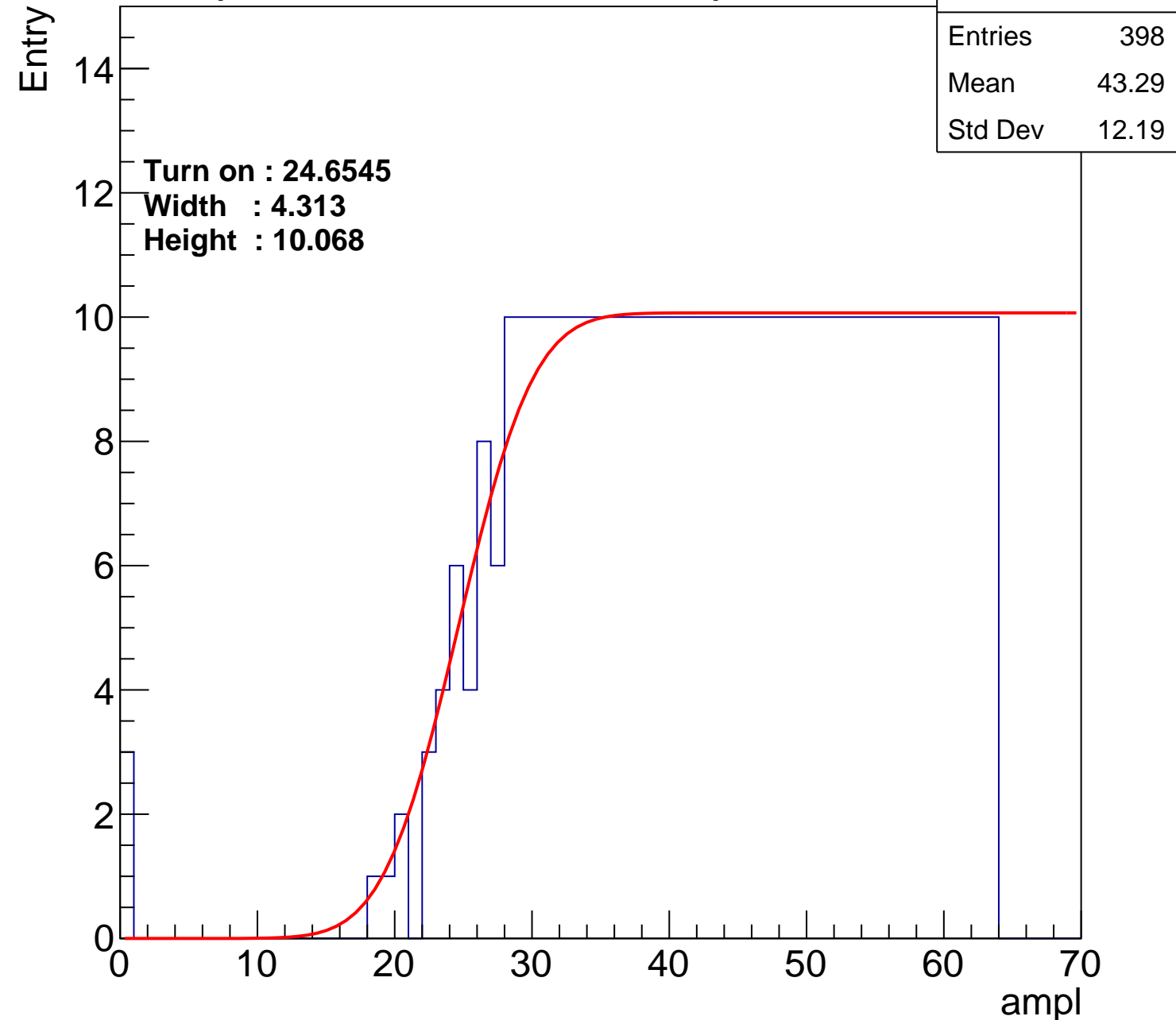
Width : 4.313

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.87
Std Dev	12.36

Turn on : 24.3088

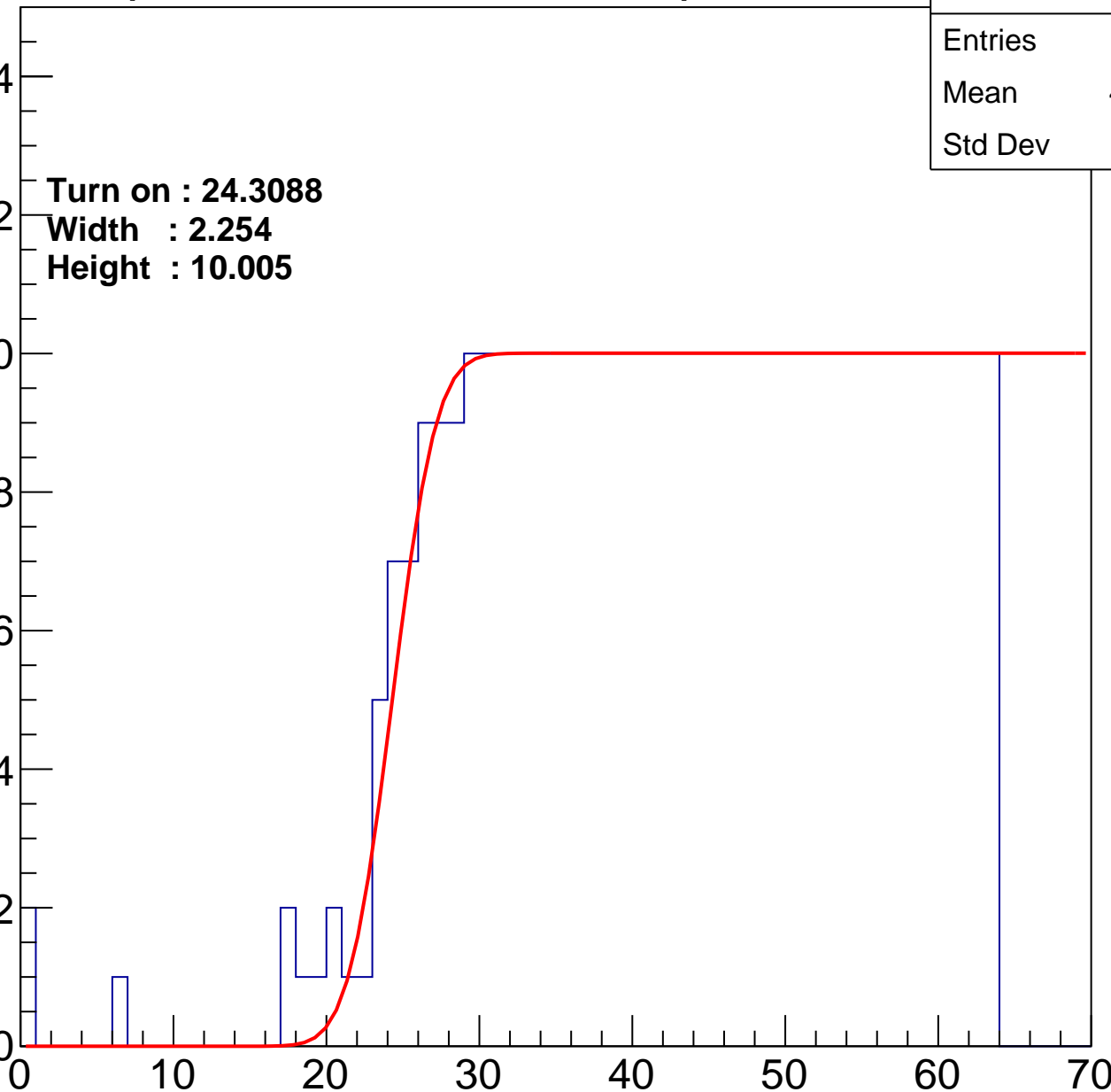
Width : 2.254

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.76
Std Dev	12.04

Turn on : 26.2410

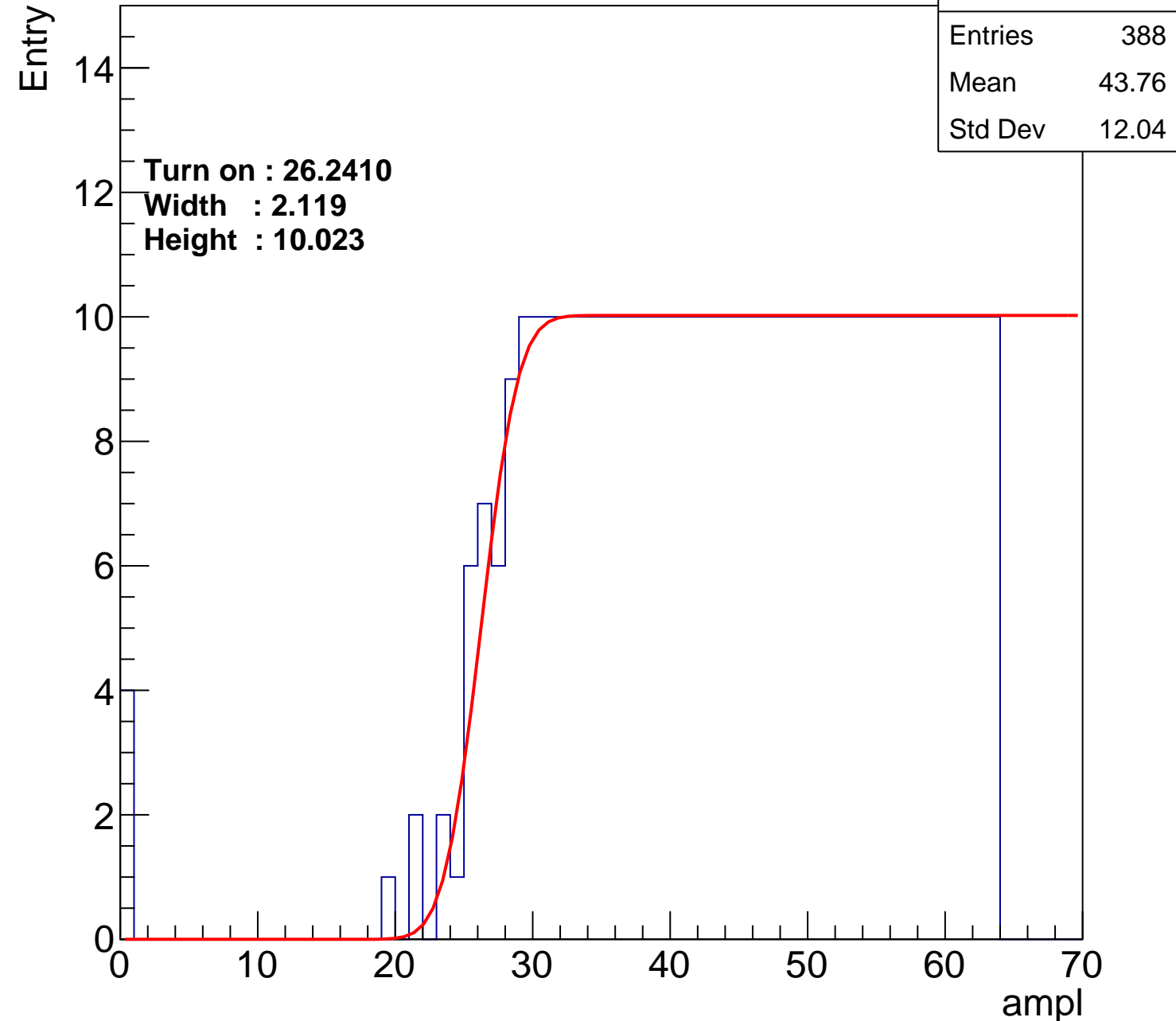
Width : 2.119

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch104

calib_packv5_042523_0143.root, FC#12, port B1

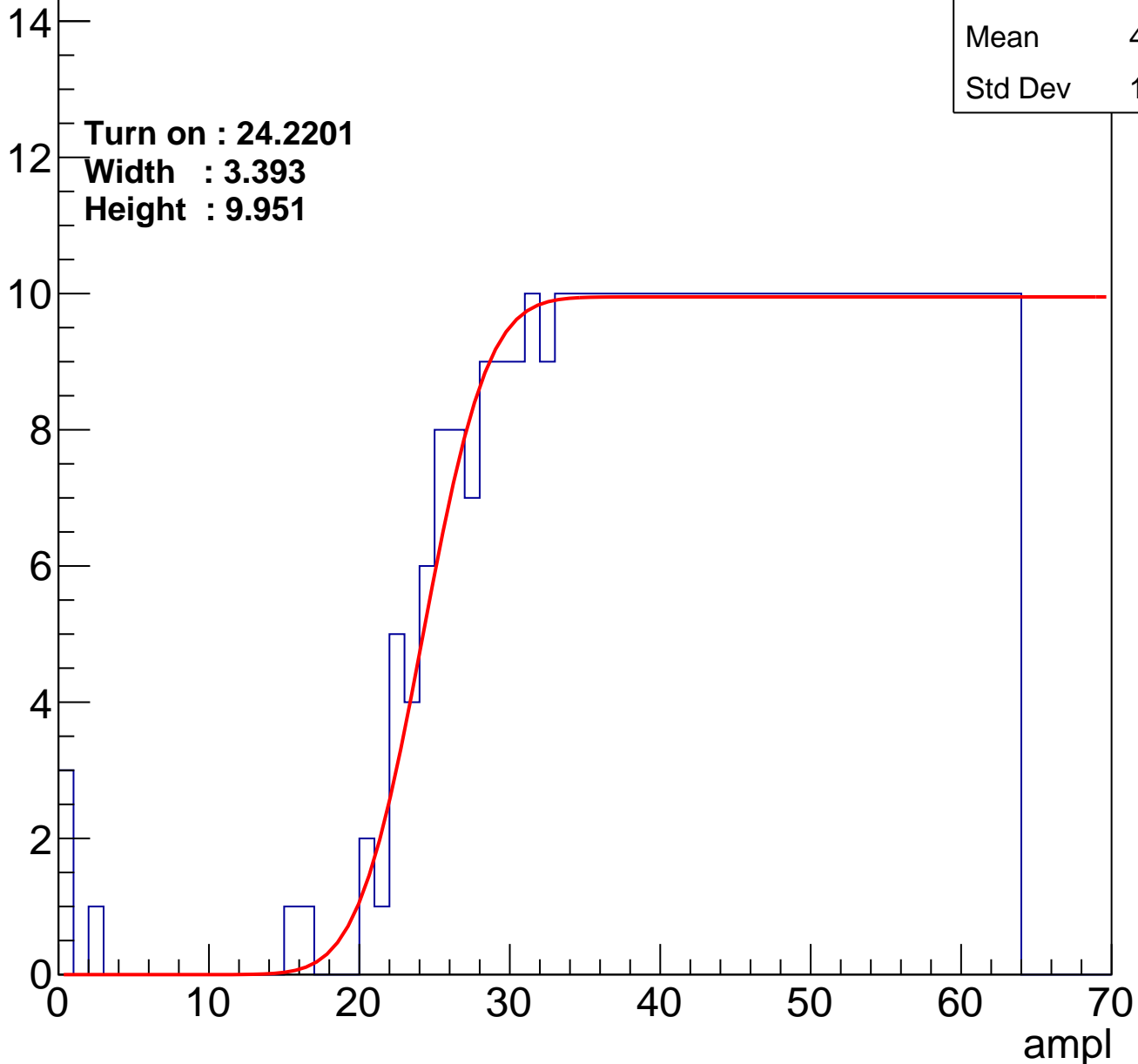
Entries	403
Mean	42.92
Std Dev	12.54

Turn on : 24.2201

Width : 3.393

Height : 9.951

Entry



B0L102S, U4-ch105

calib_packv5_042523_0143.root, FC#12, port B1

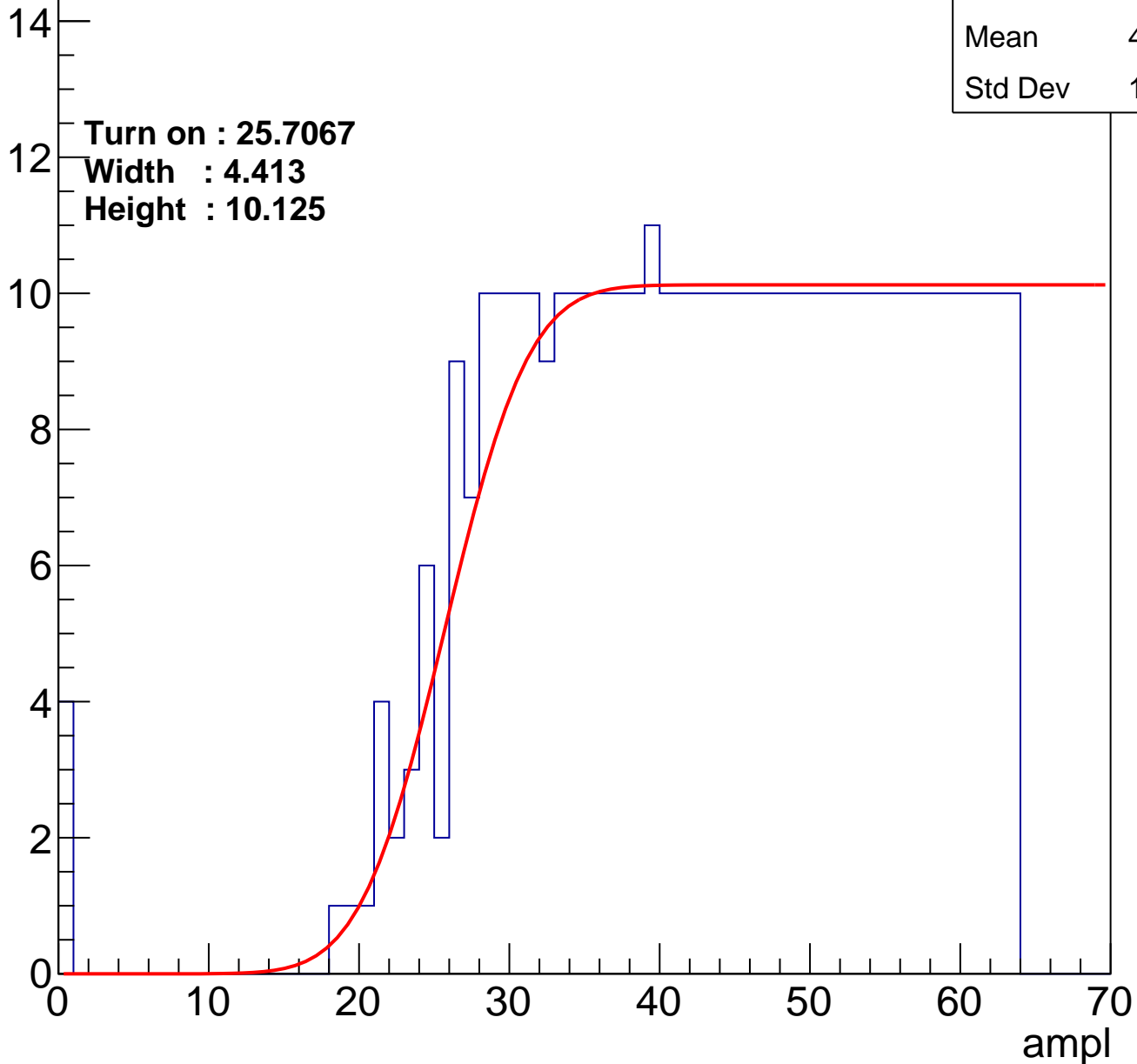
Entries	400
Mean	43.15
Std Dev	12.38

Turn on : 25.7067

Width : 4.413

Height : 10.125

Entry



B0L102S, U4-ch106

calib_packv5_042523_0143.root, FC#12, port B1

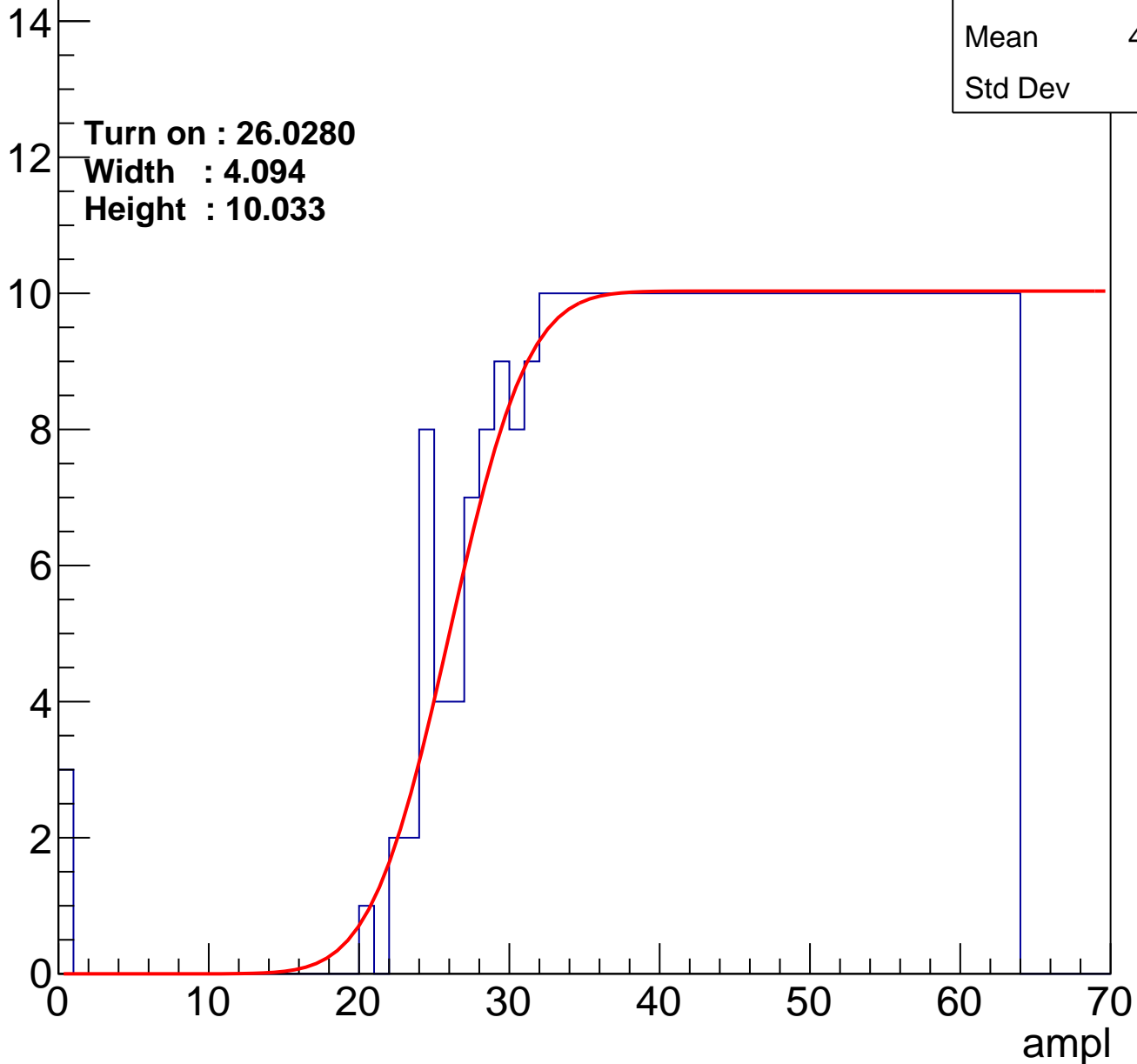
Entries	385
Mean	43.89
Std Dev	11.9

Turn on : 26.0280

Width : 4.094

Height : 10.033

Entry



B0L102S, U4-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.76
Std Dev	11.78

Turn on : 25.7410

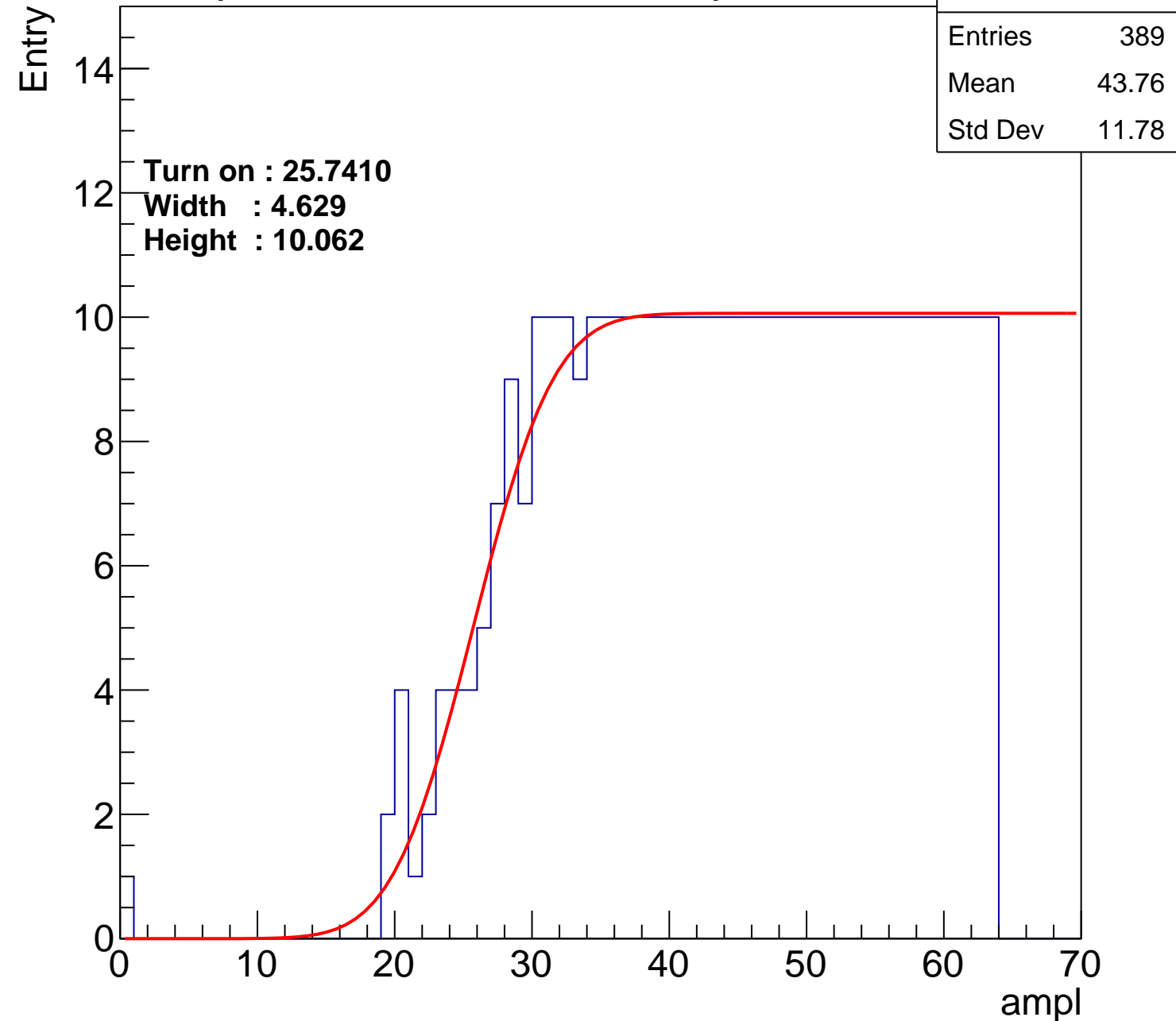
Width : 4.629

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	43.08
Std Dev	12.24

Turn on : 23.8463

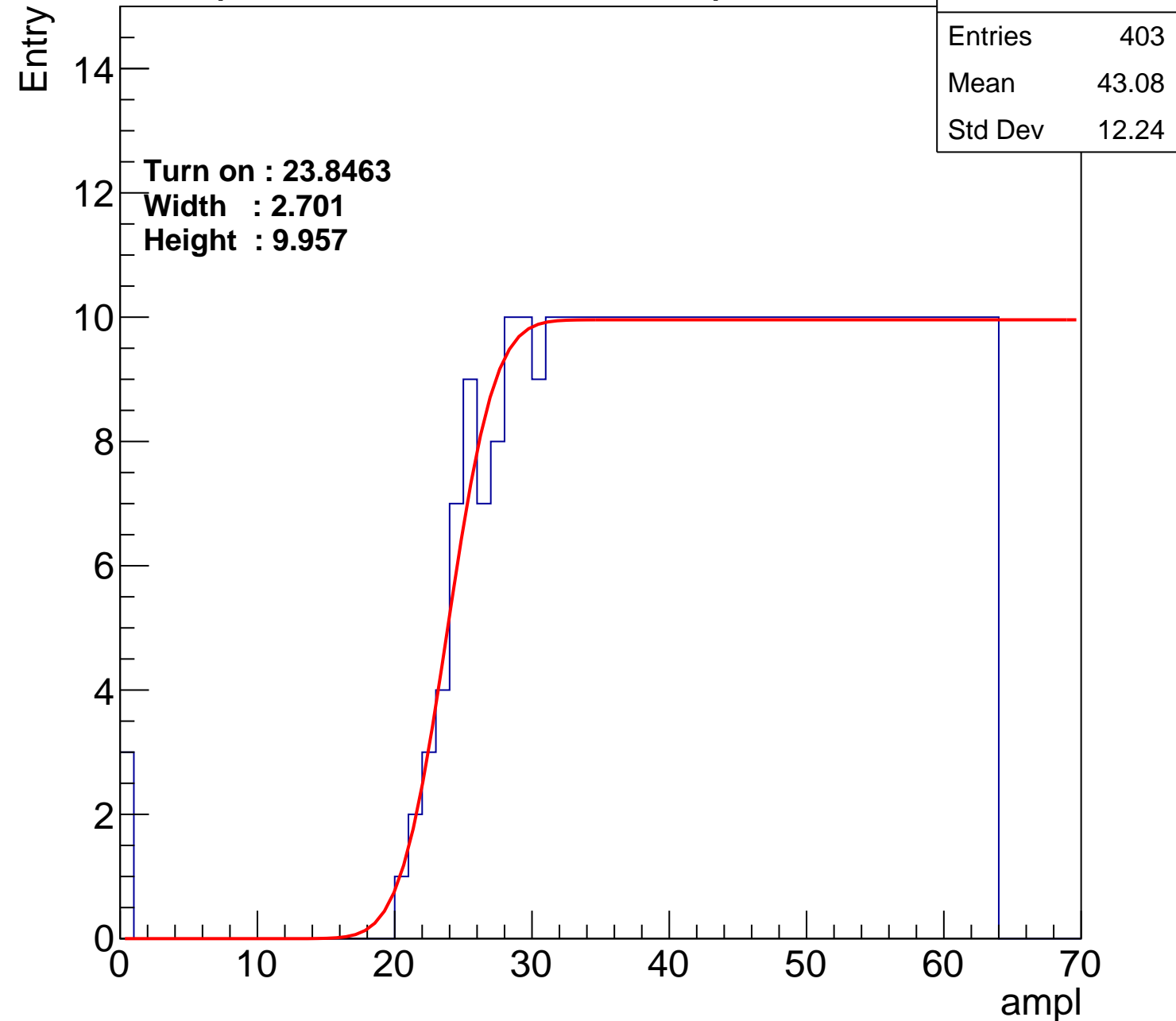
Width : 2.701

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch109

calib_packv5_042523_0143.root, FC#12, port B1

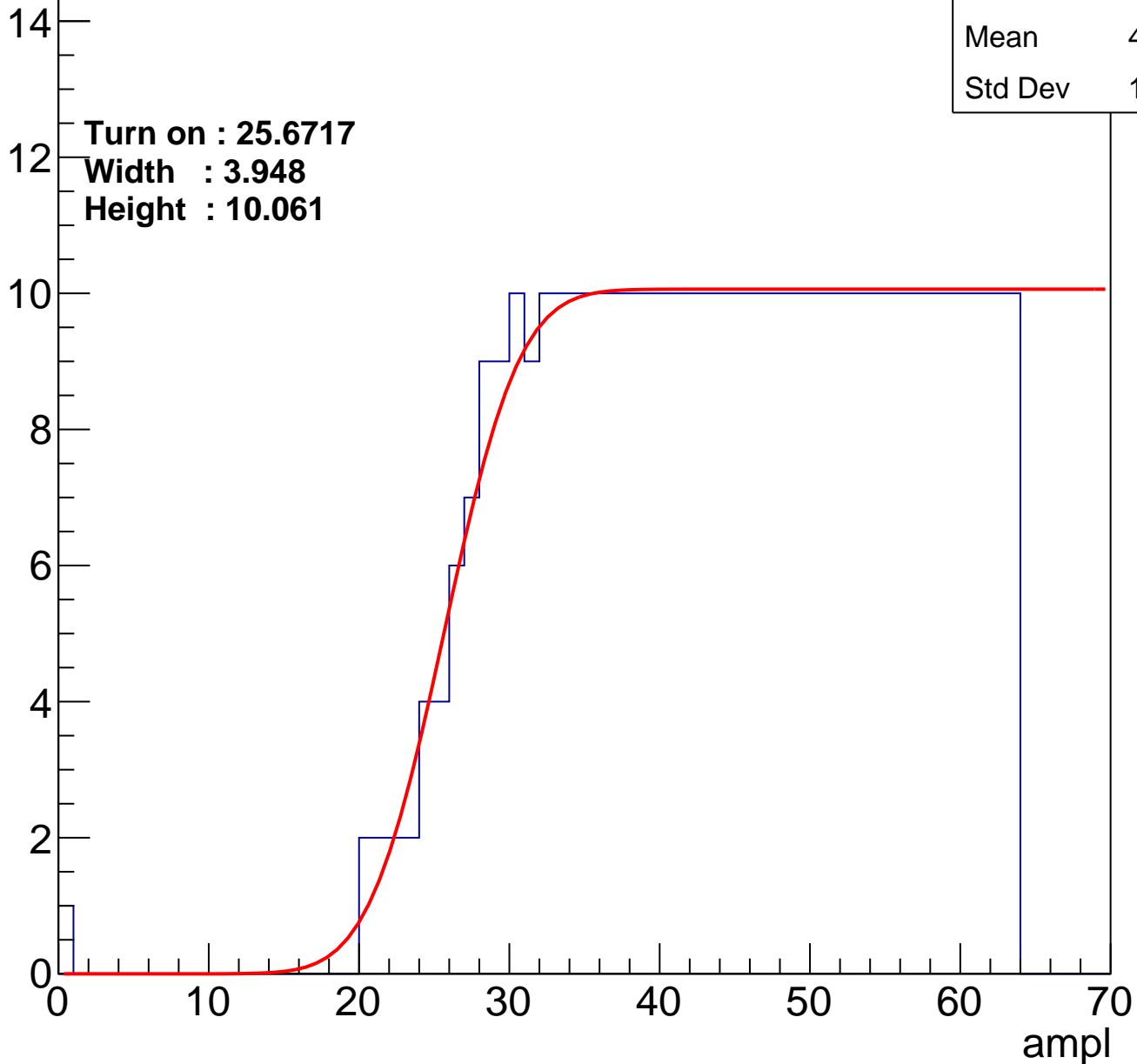
Entries	387
Mean	43.94
Std Dev	11.59

Turn on : 25.6717

Width : 3.948

Height : 10.061

Entry



B0L102S, U4-ch110

calib_packv5_042523_0143.root, FC#12, port B1

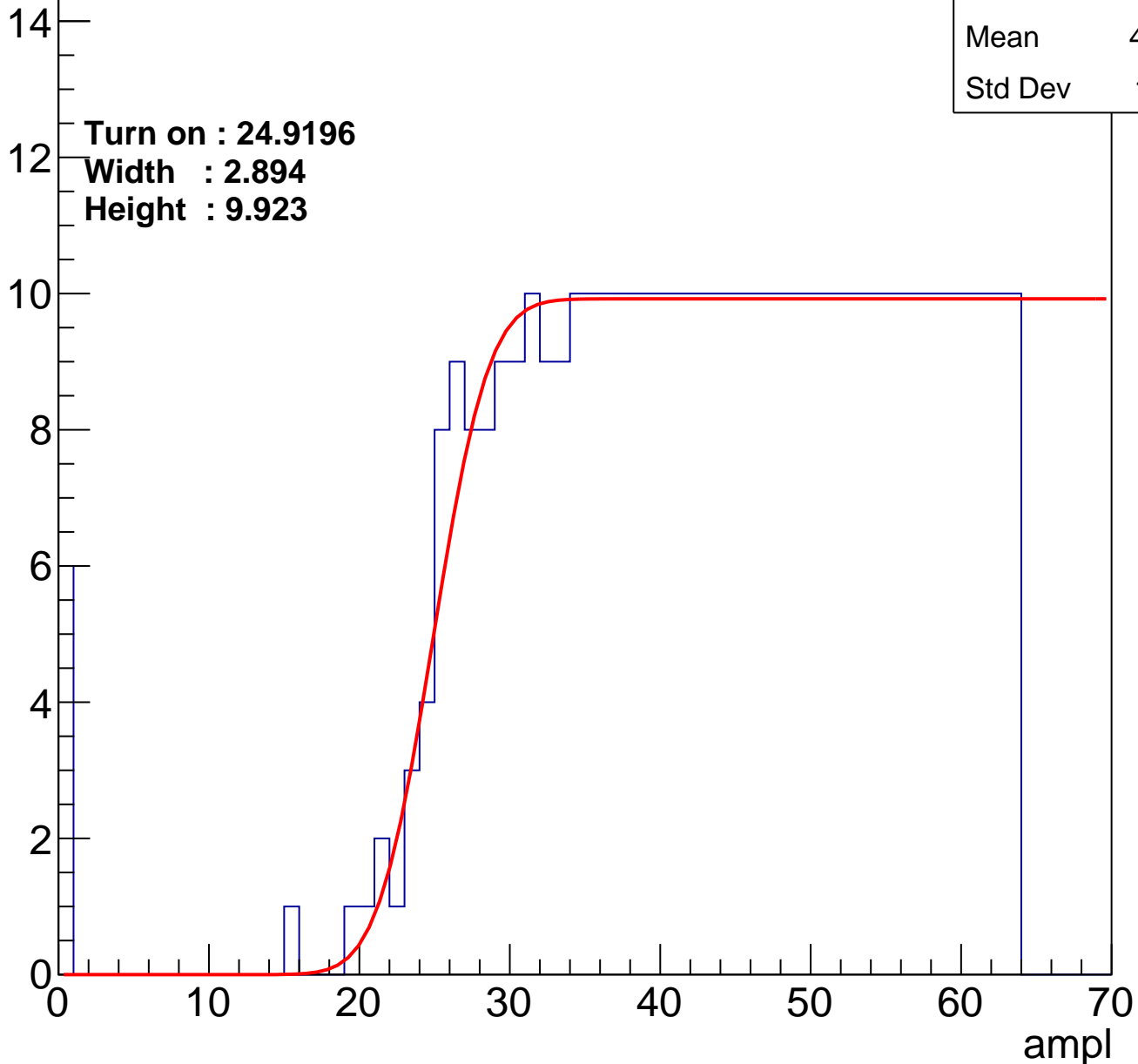
Entries	398
Mean	43.05
Std Dev	12.71

Turn on : 24.9196

Width : 2.894

Height : 9.923

Entry



B0L102S, U4-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.32
Std Dev	12.03

Turn on : 24.8283

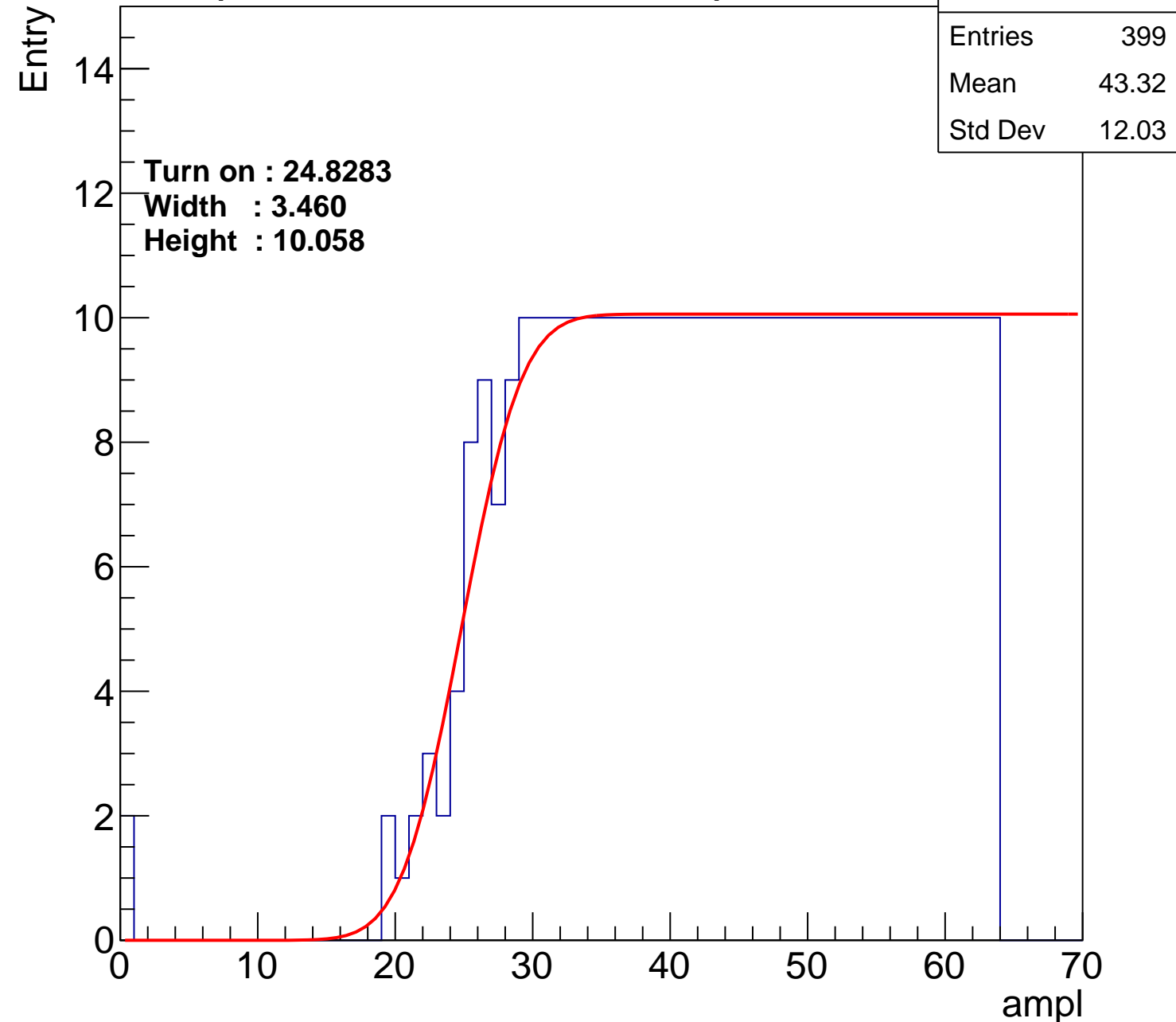
Width : 3.460

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch112

calib_packv5_042523_0143.root, FC#12, port B1

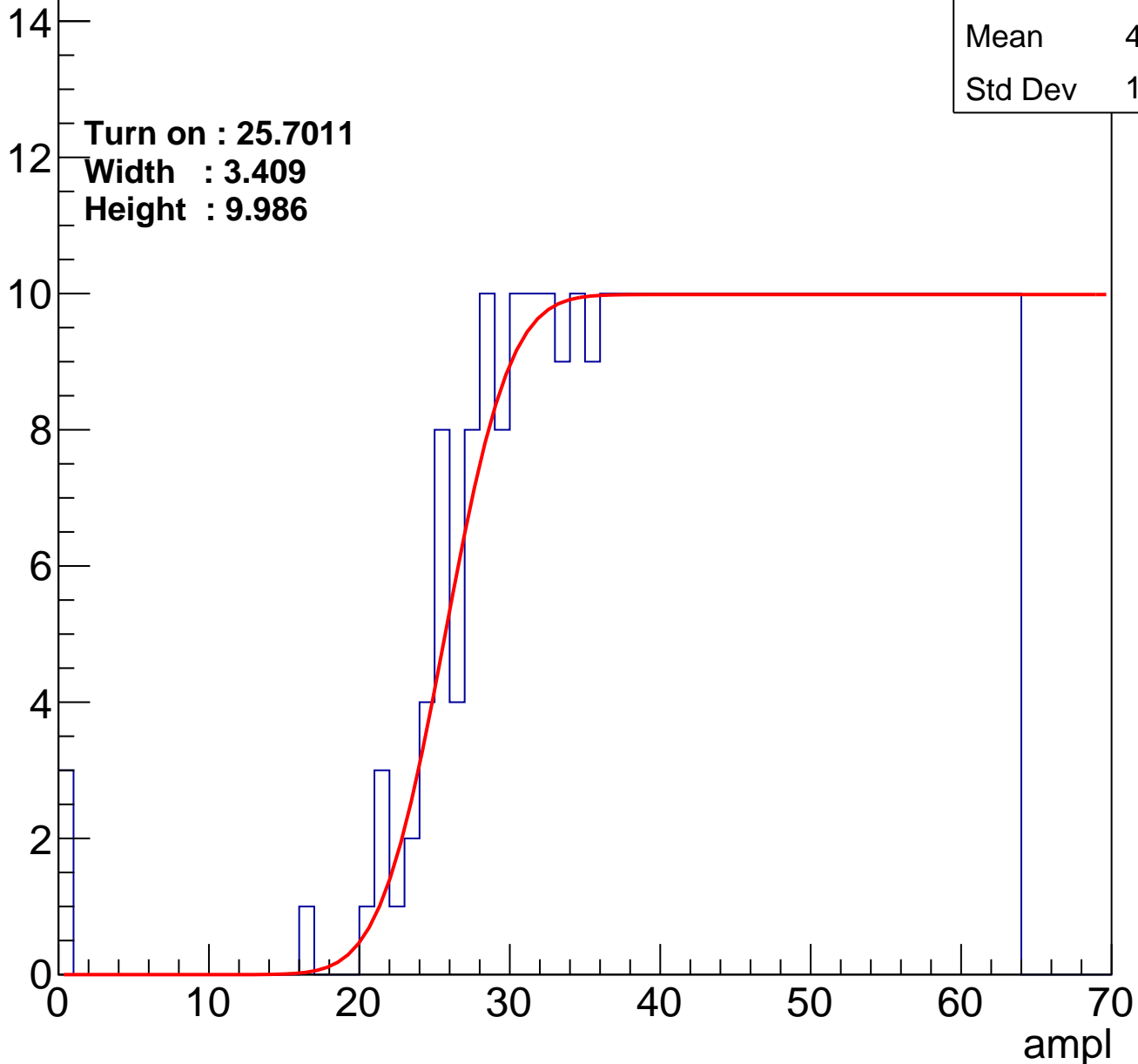
Entries	391
Mean	43.57
Std Dev	12.09

Turn on : 25.7011

Width : 3.409

Height : 9.986

Entry



B0L102S, U4-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.06
Std Dev	11.76

Turn on : 26.3030

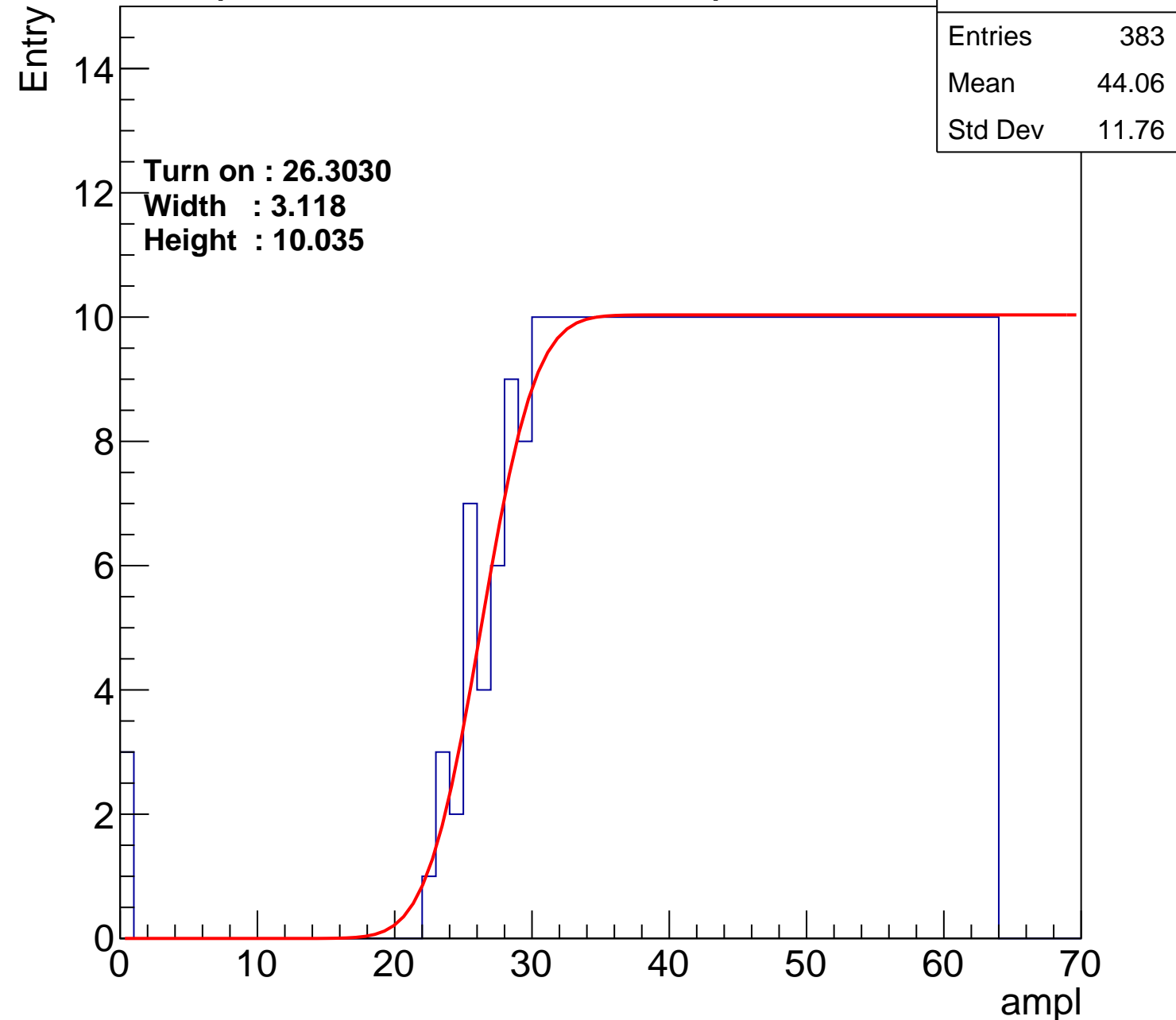
Width : 3.118

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch114

calib_packv5_042523_0143.root, FC#12, port B1

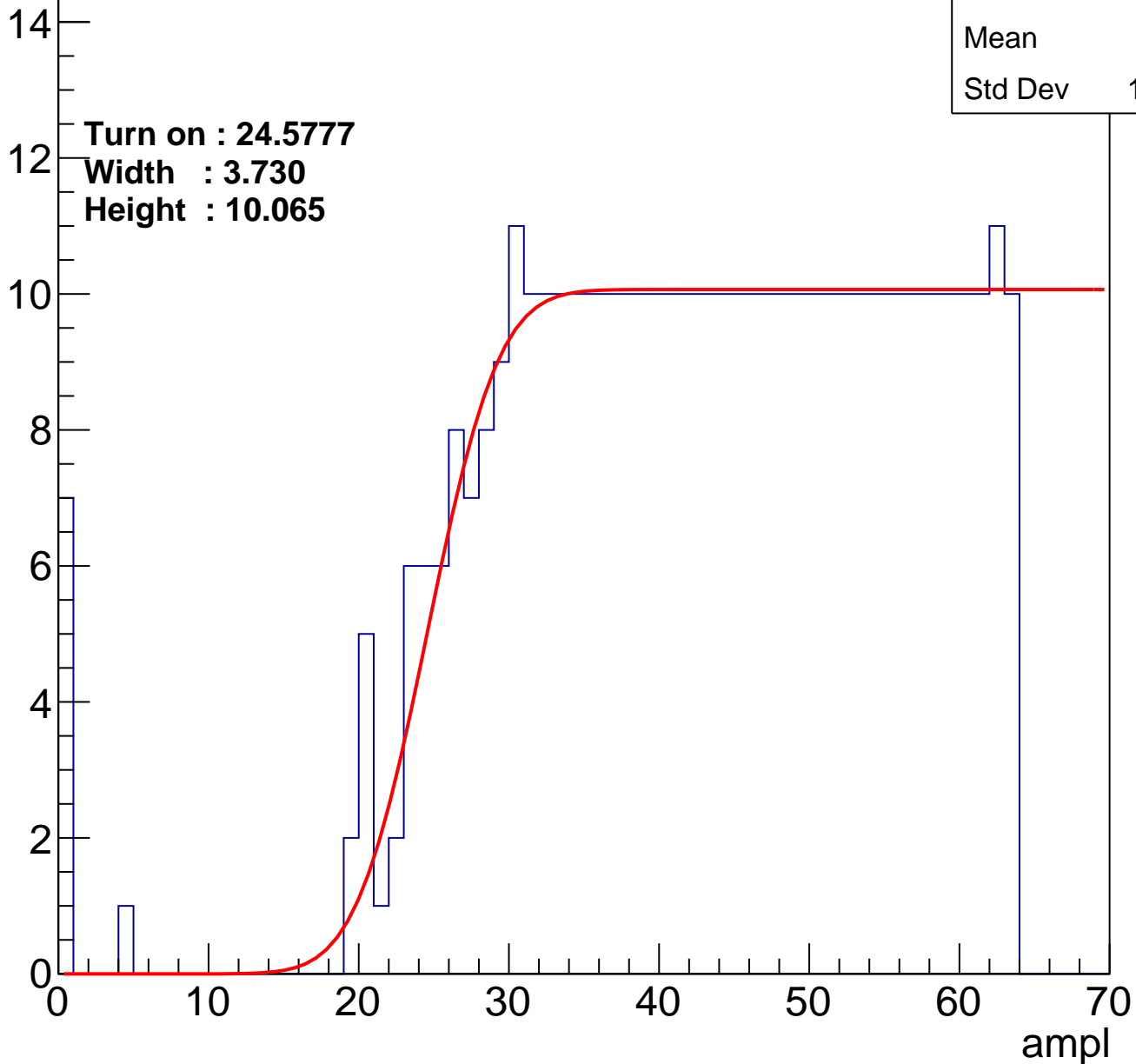
Entries	410
Mean	42.5
Std Dev	13.17

Turn on : 24.5777

Width : 3.730

Height : 10.065

Entry



B0L102S, U4-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.47
Std Dev	11.99

Turn on : 25.2426

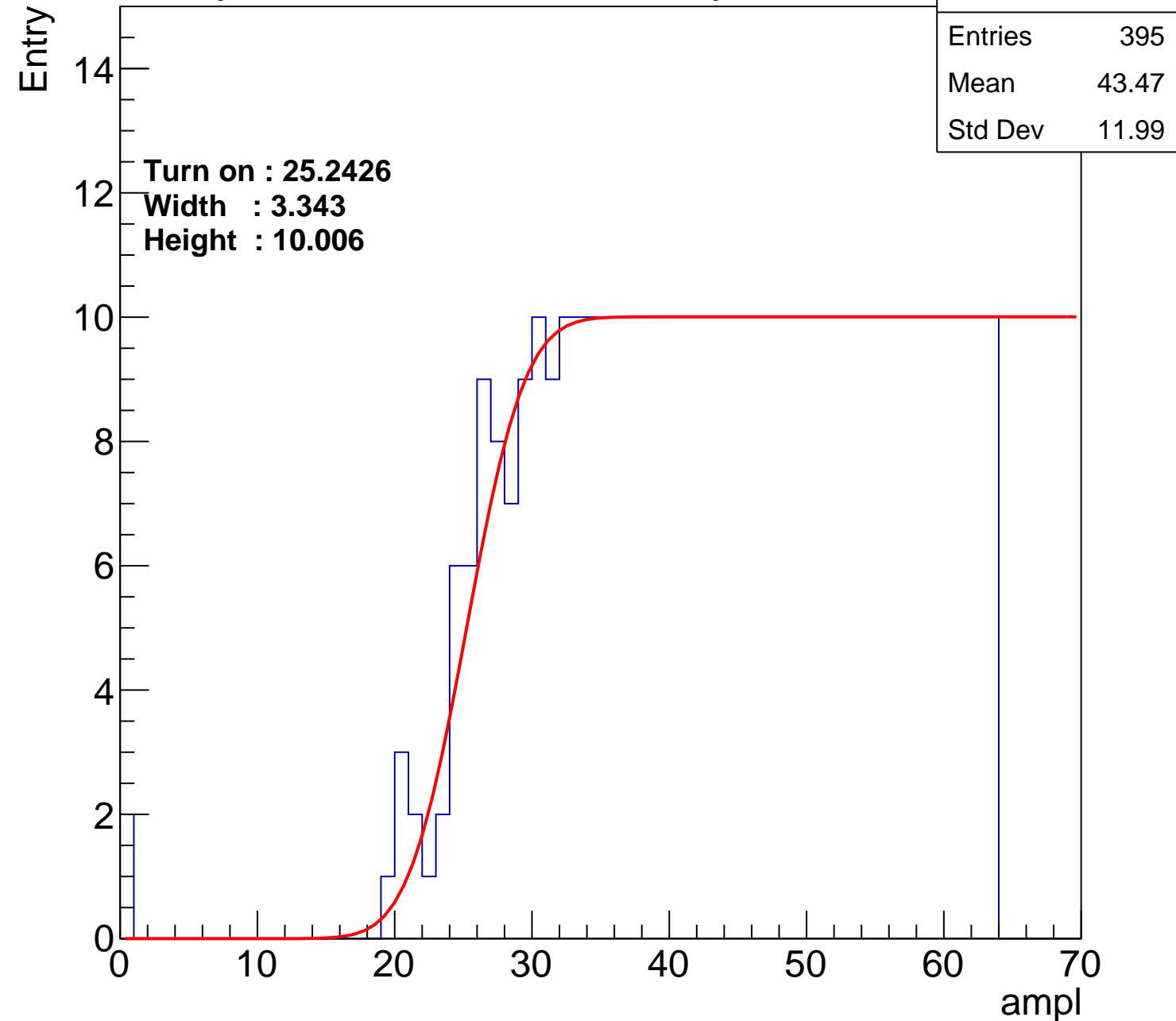
Width : 3.343

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch116

calib_packv5_042523_0143.root, FC#12, port B1

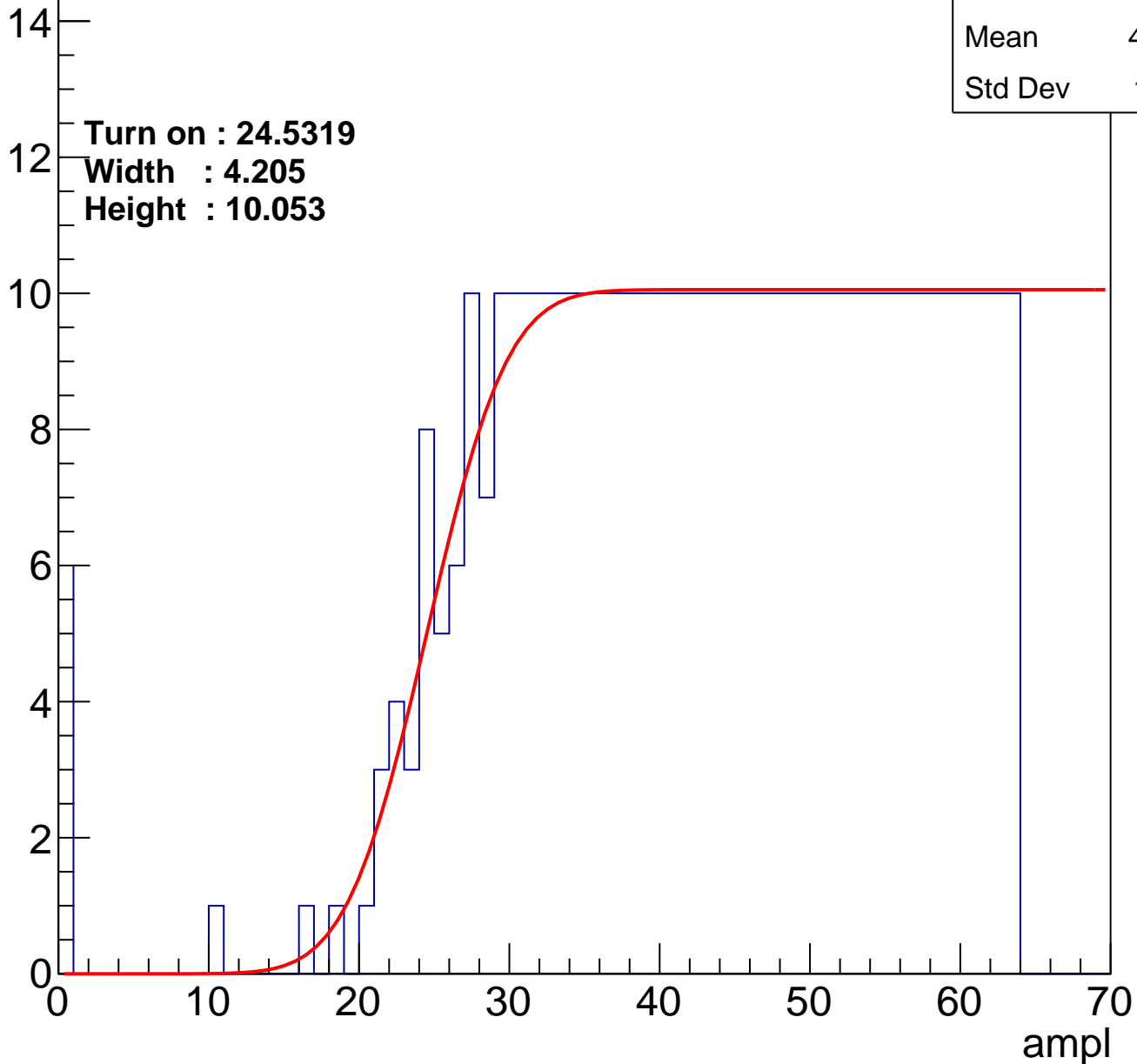
Entries	406
Mean	42.67
Std Dev	12.91

Turn on : 24.5319

Width : 4.205

Height : 10.053

Entry



B0L102S, U4-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.22
Std Dev	11.89

Turn on : 27.0931

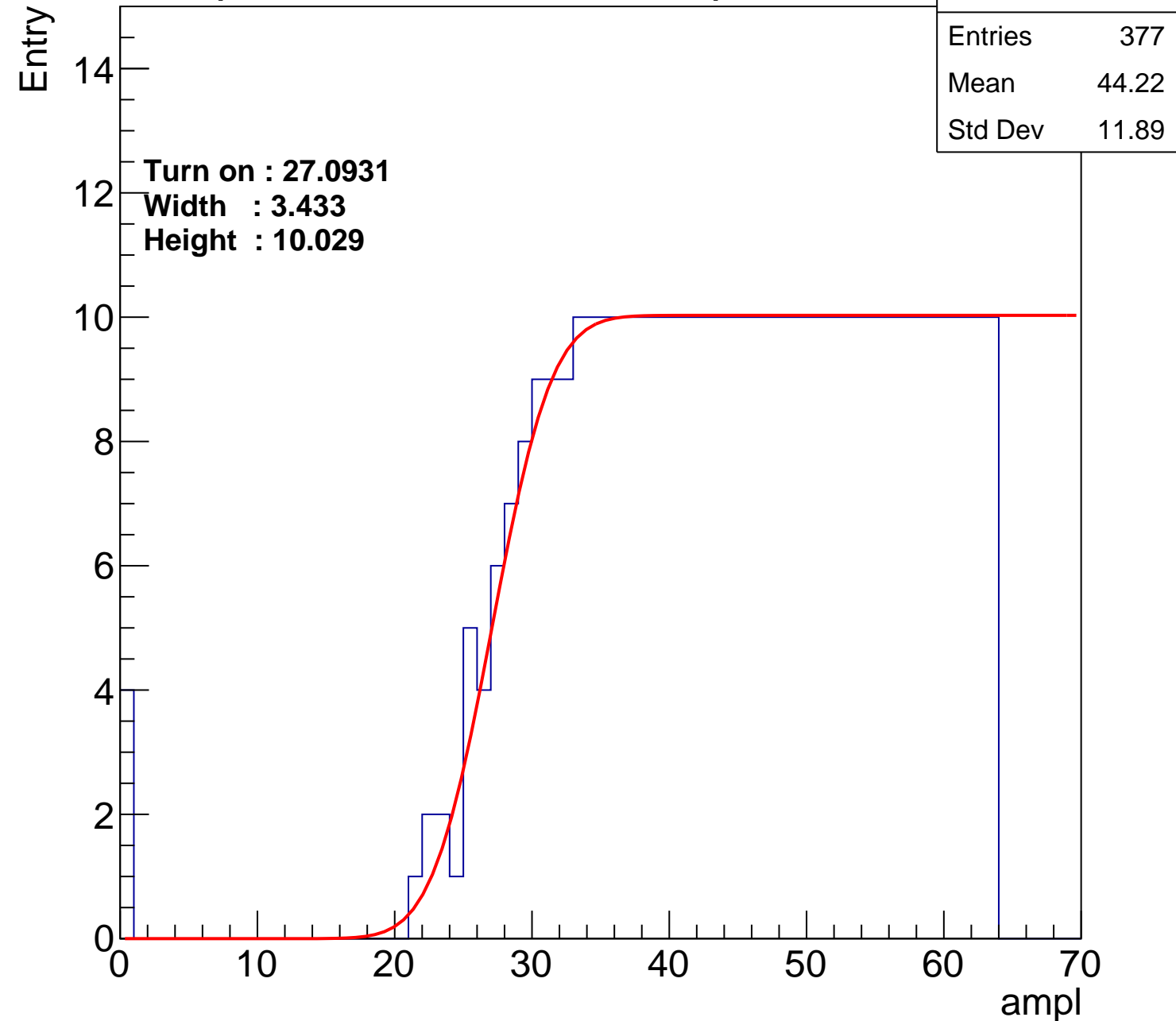
Width : 3.433

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	418
Mean	42.37
Std Dev	12.59

Turn on : 22.5859

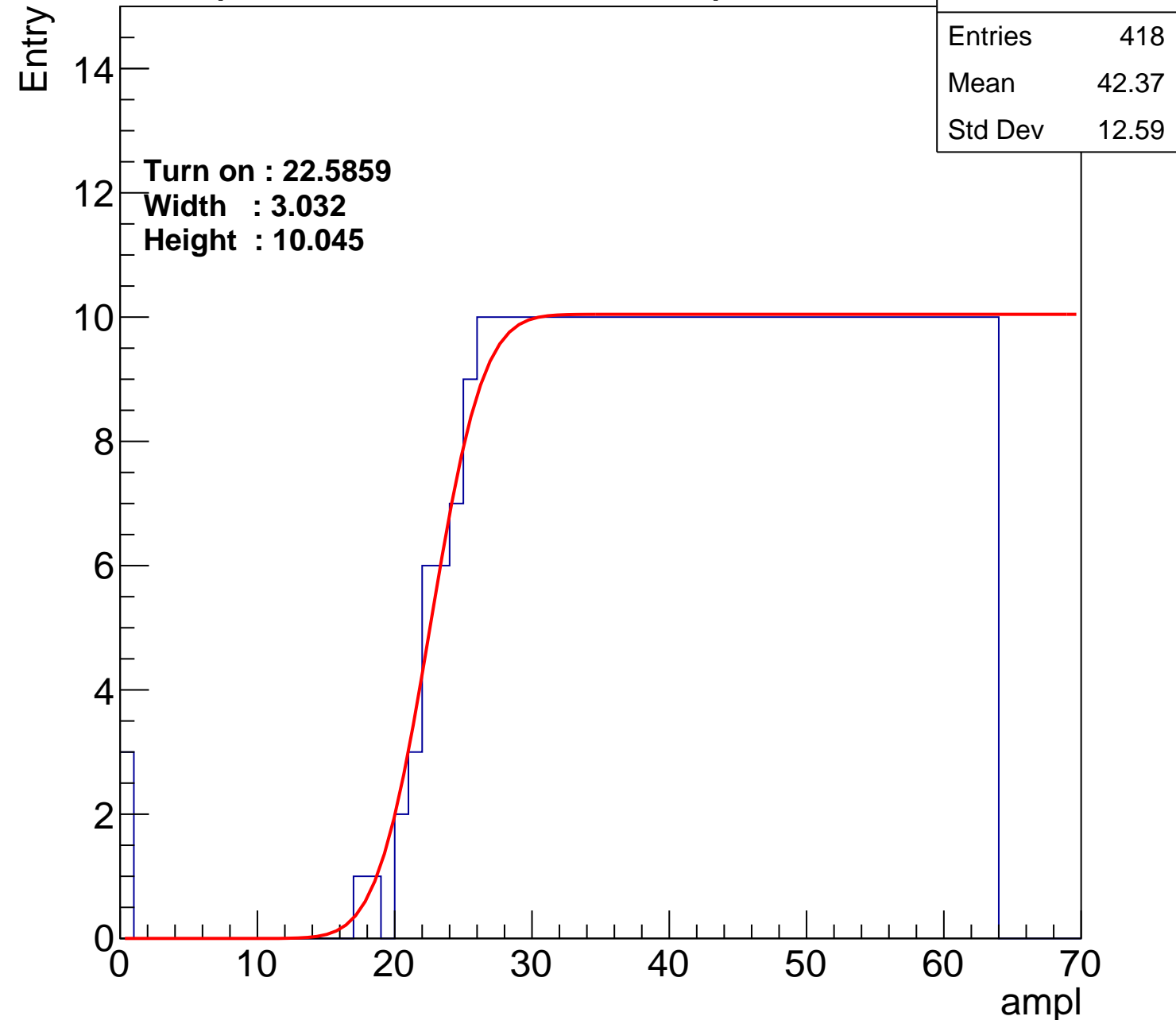
Width : 3.032

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.19
Std Dev	12.5

Turn on : 25.1330

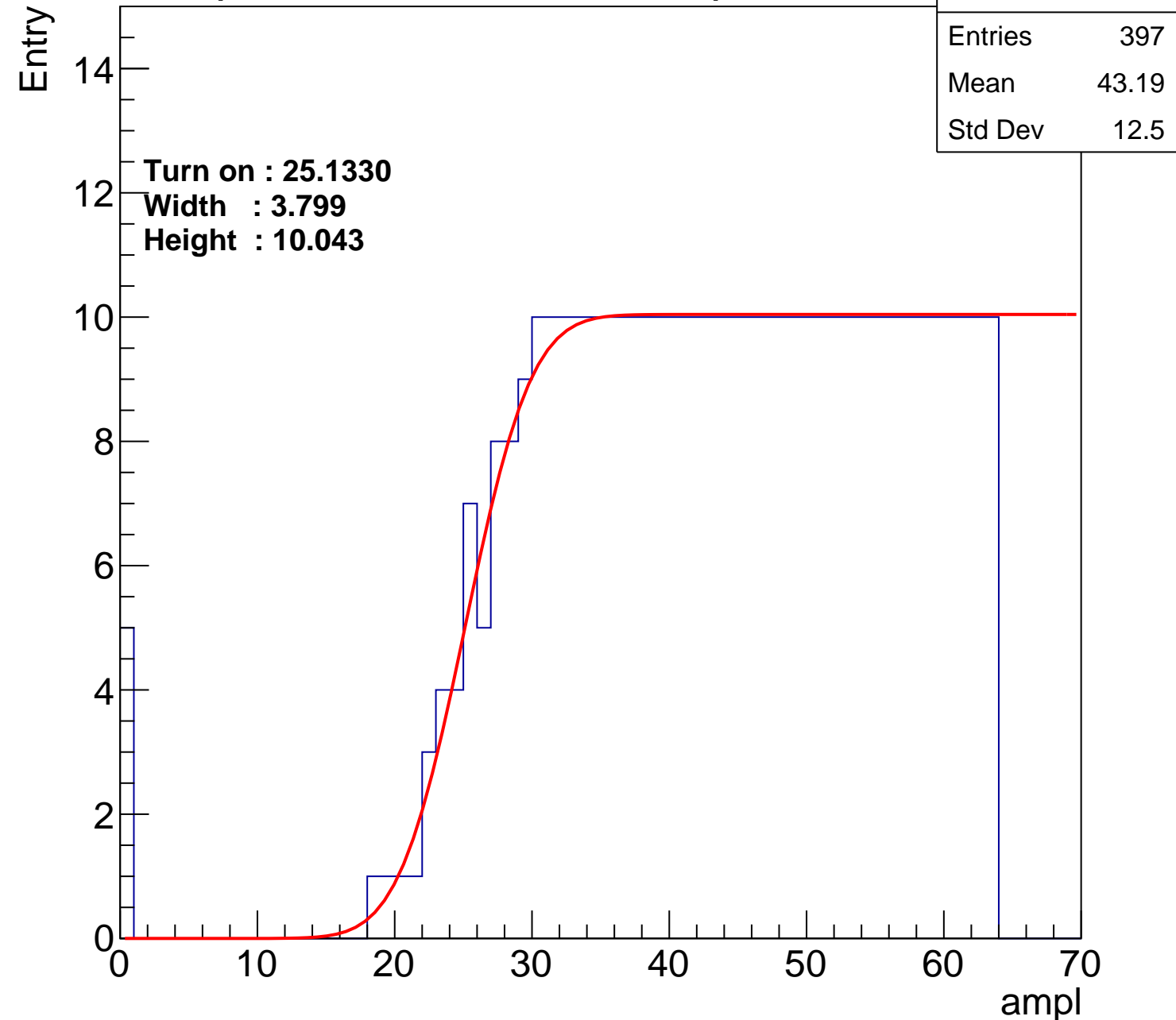
Width : 3.799

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.31
Std Dev	12.21

Turn on : 25.2425

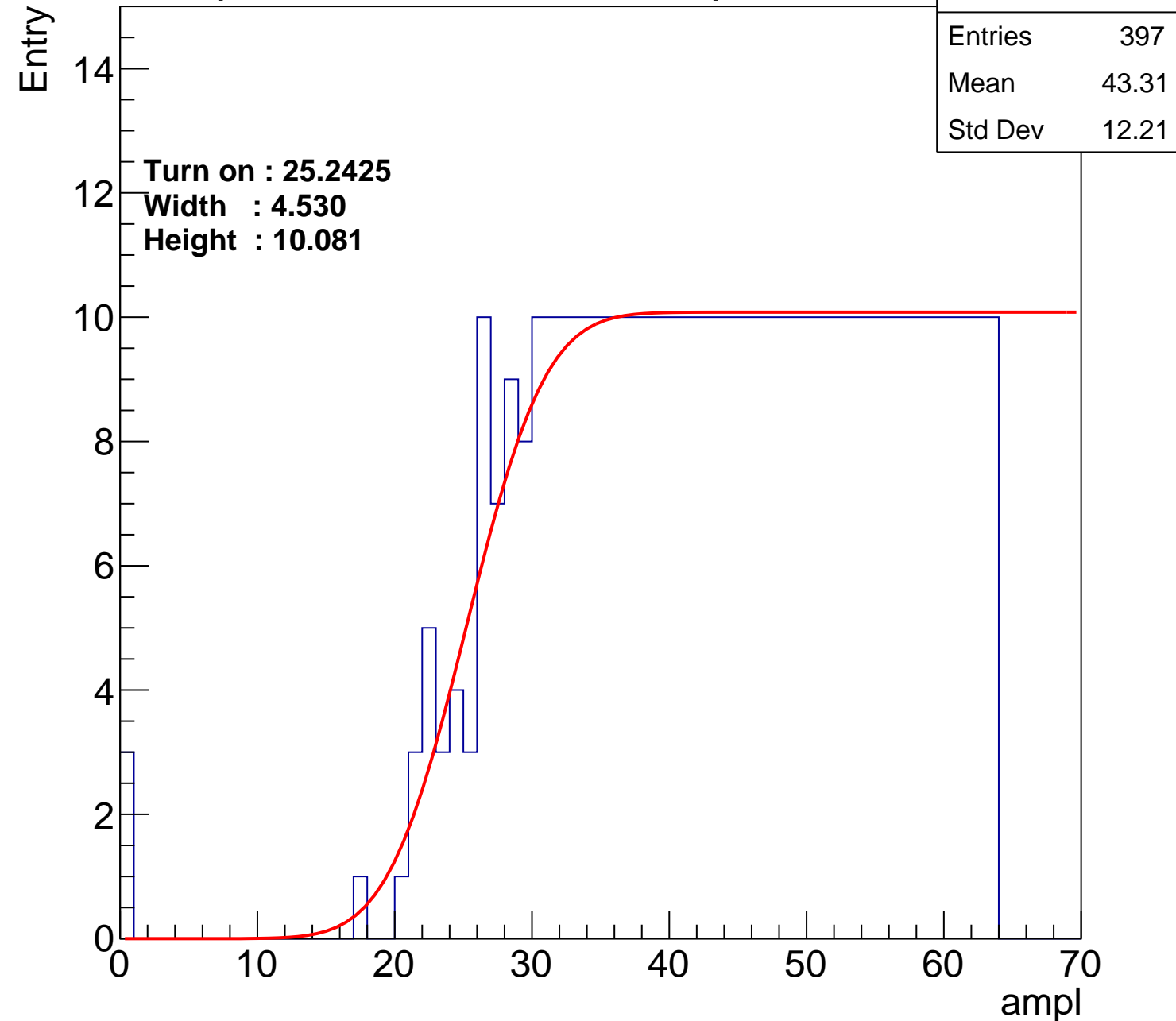
Width : 4.530

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch121

calib_packv5_042523_0143.root, FC#12, port B1

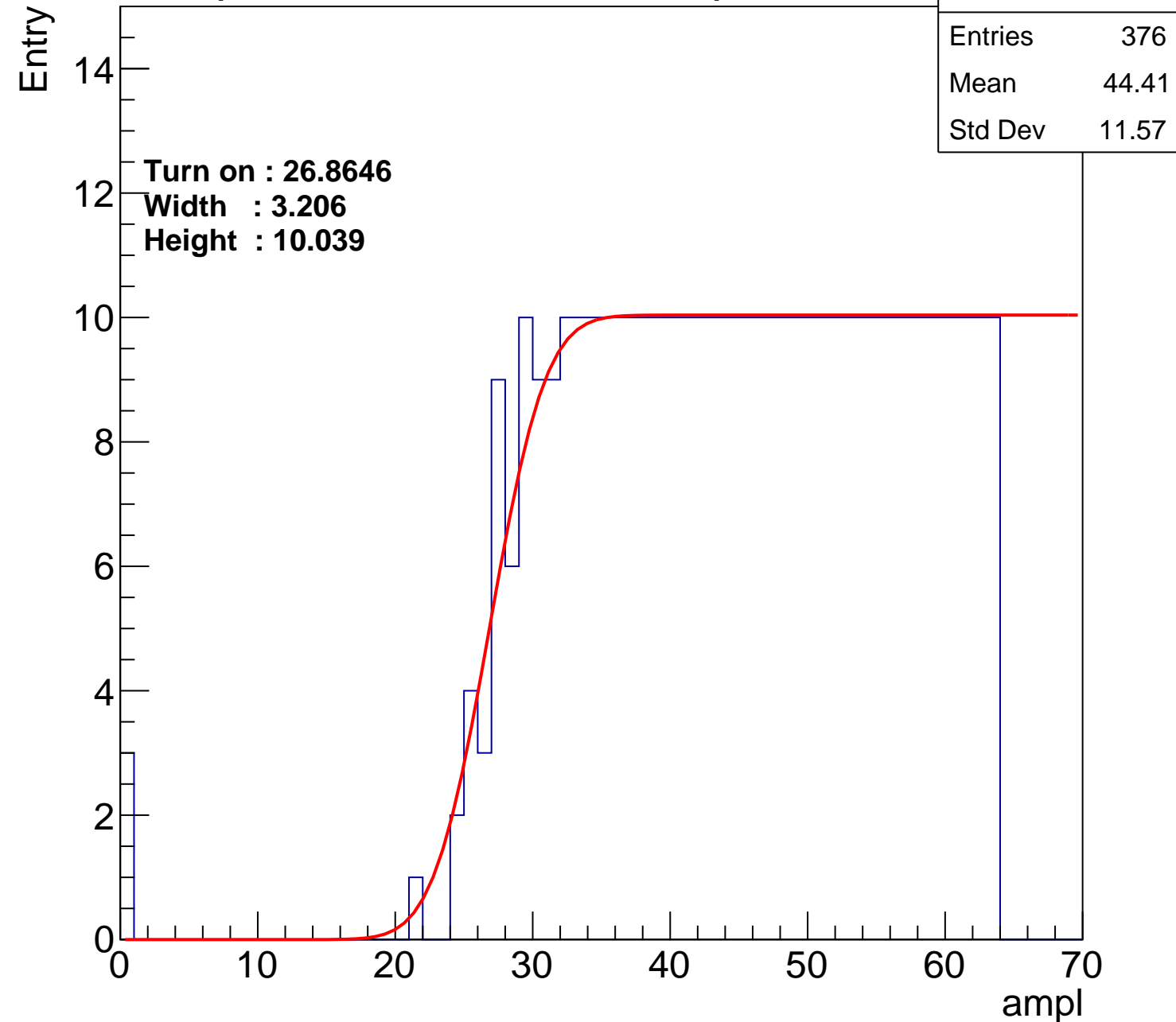
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8646
Width : 3.206
Height : 10.039

Entries	376
Mean	44.41
Std Dev	11.57

ampl



B0L102S, U4-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
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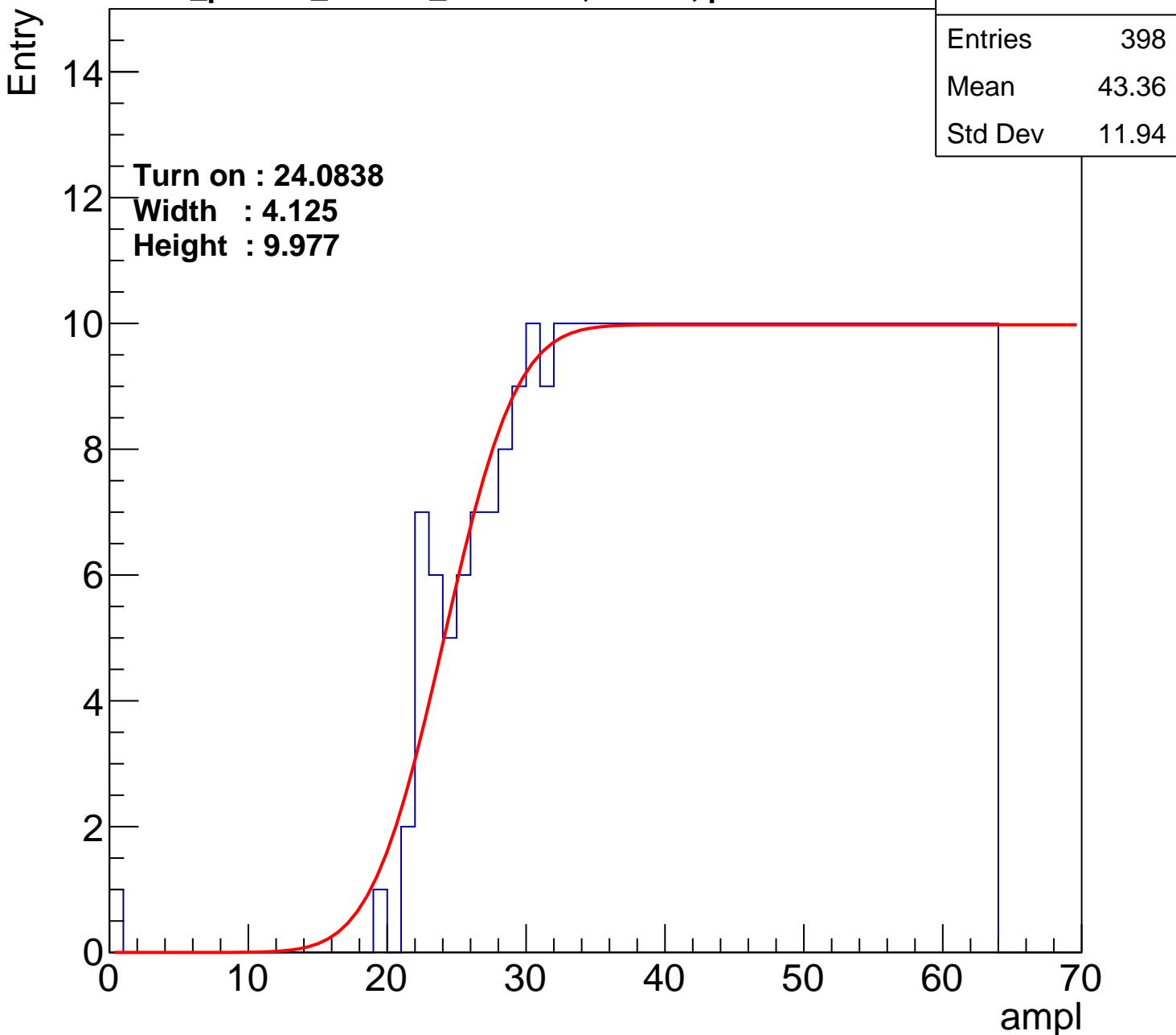
Mean	43.36
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Std Dev	11.94
---------	-------

Turn on : 24.0838

Width : 4.125

Height : 9.977



B0L102S, U4-ch123

calib_packv5_042523_0143.root, FC#12, port B1

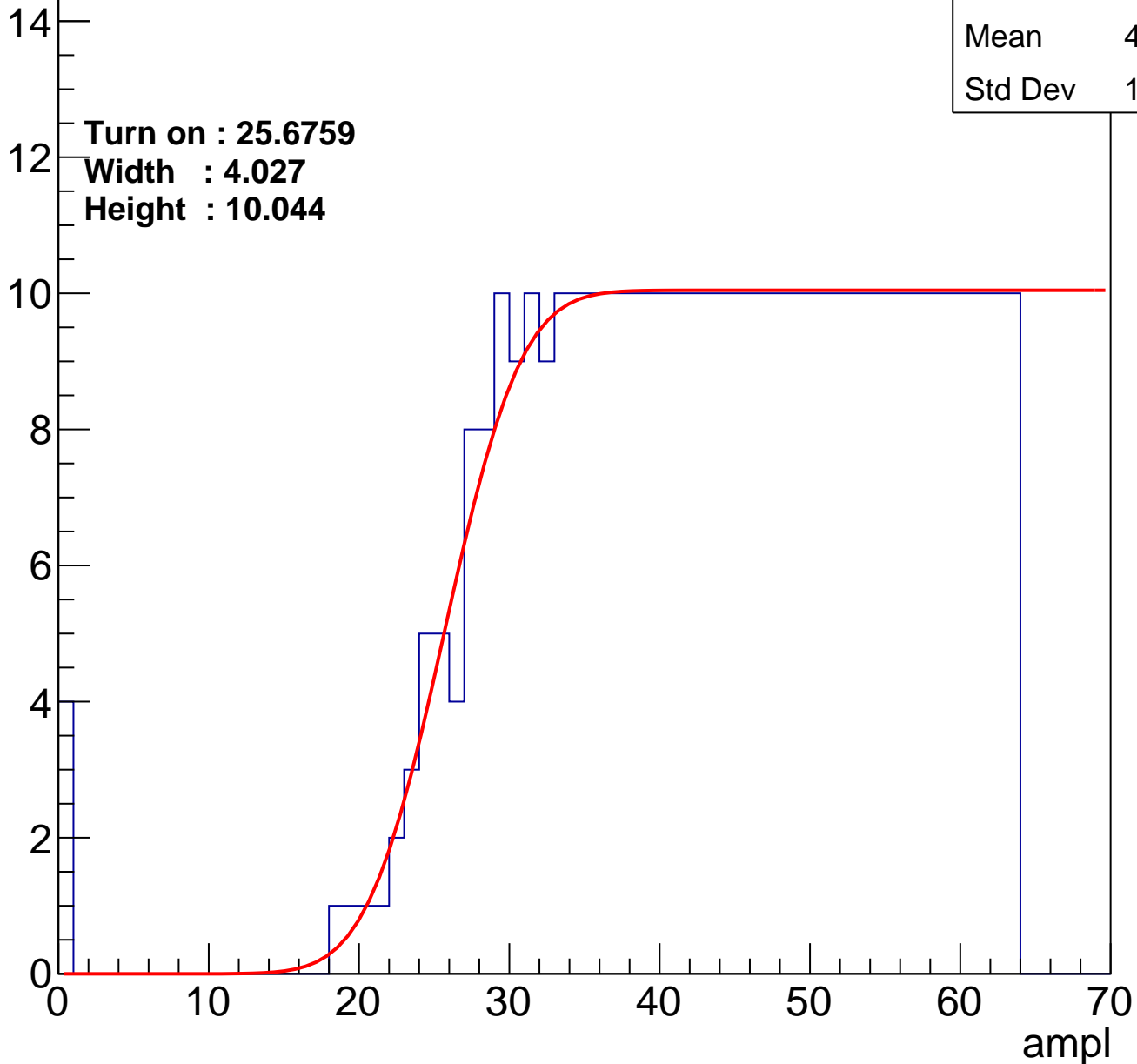
Entries	391
Mean	43.52
Std Dev	12.24

Turn on : 25.6759

Width : 4.027

Height : 10.044

Entry



B0L102S, U4-ch124

calib_packv5_042523_0143.root, FC#12, port B1

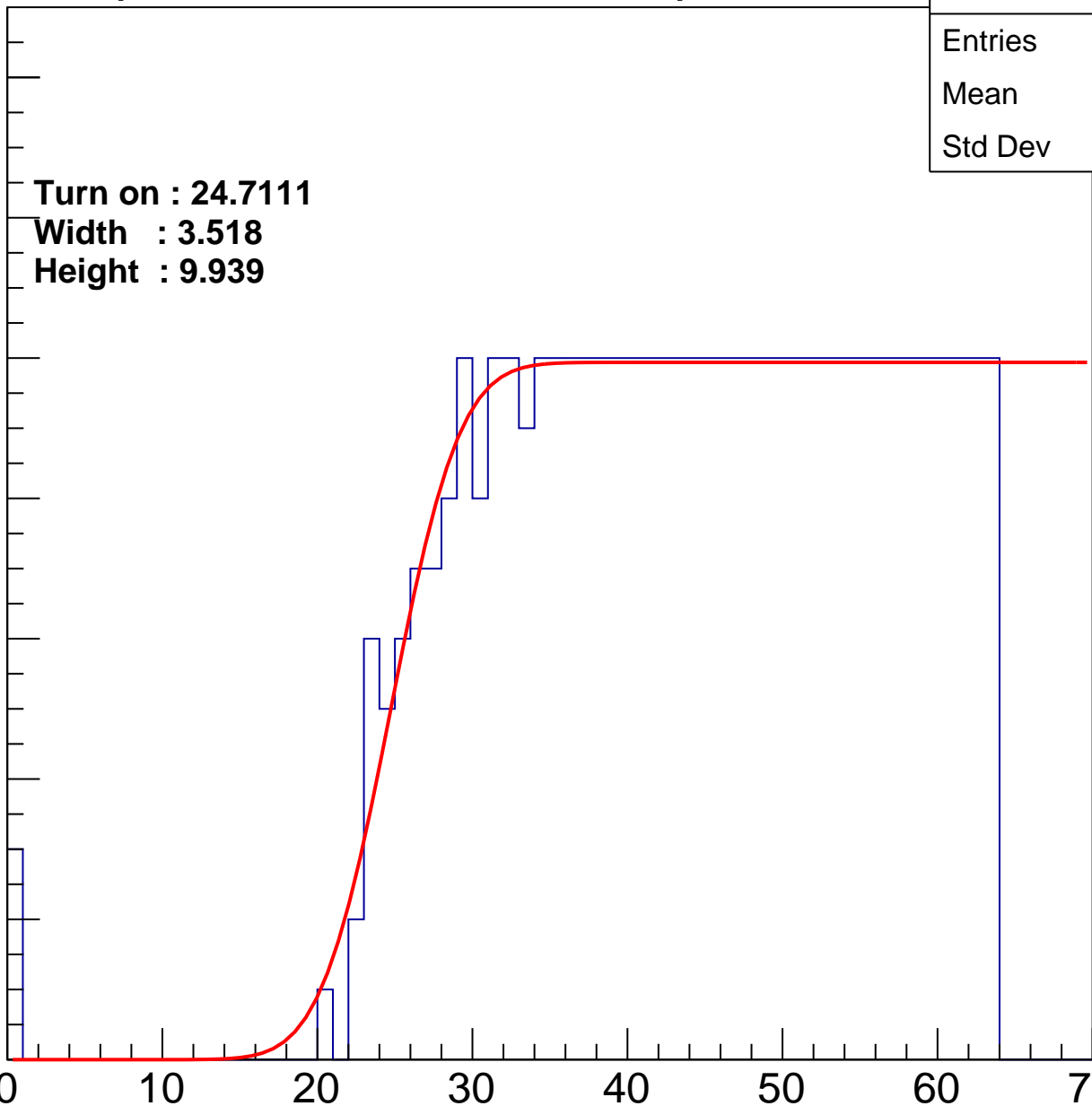
Entry

14
12
10
8
6
4
2
0

Turn on : 24.7111
Width : 3.518
Height : 9.939

Entries	392
Mean	43.56
Std Dev	12.06

ampl



B0L102S, U4-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.72
Std Dev	12.2

Turn on : 26.0585

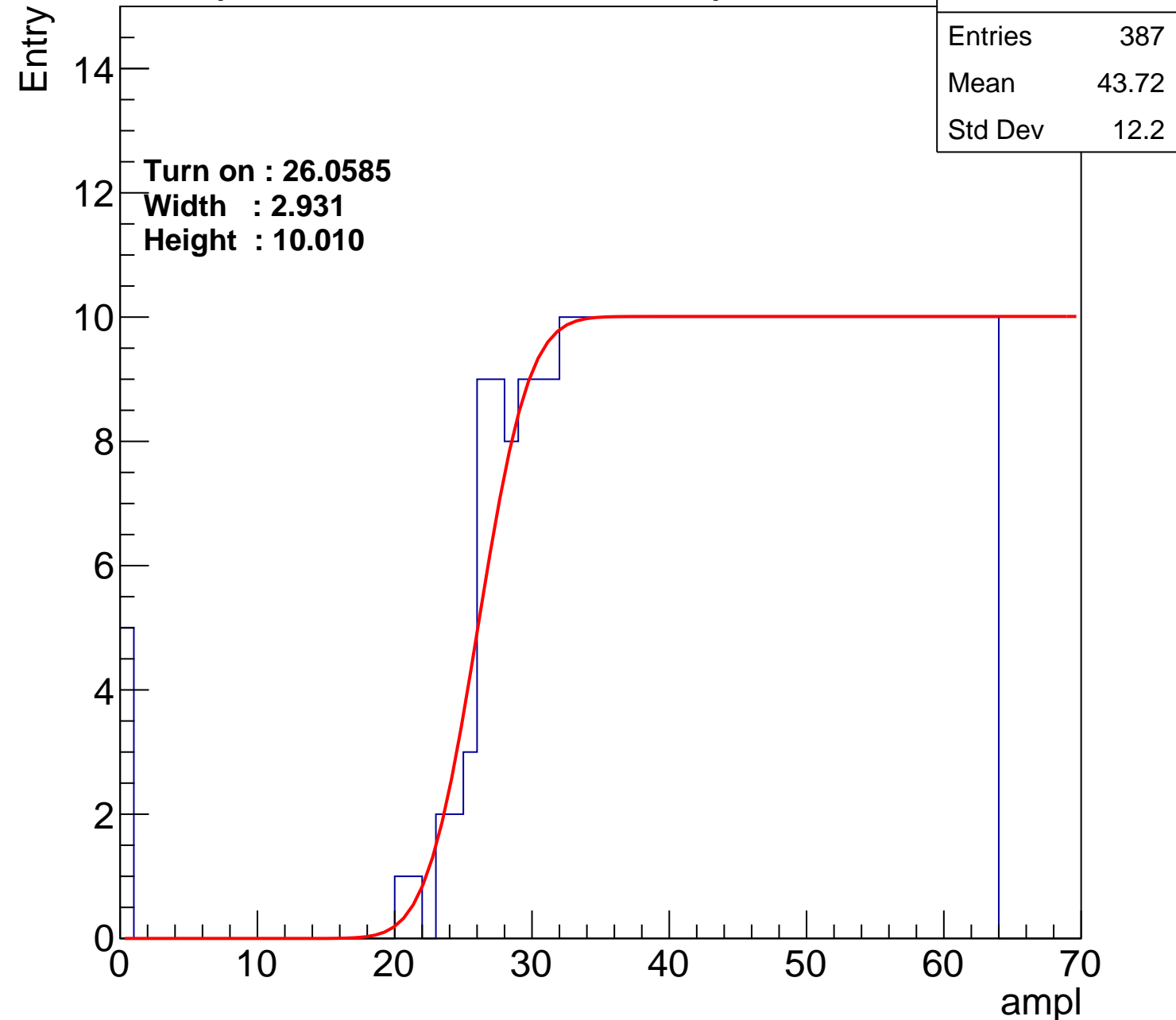
Width : 2.931

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch126

calib_packv5_042523_0143.root, FC#12, port B1

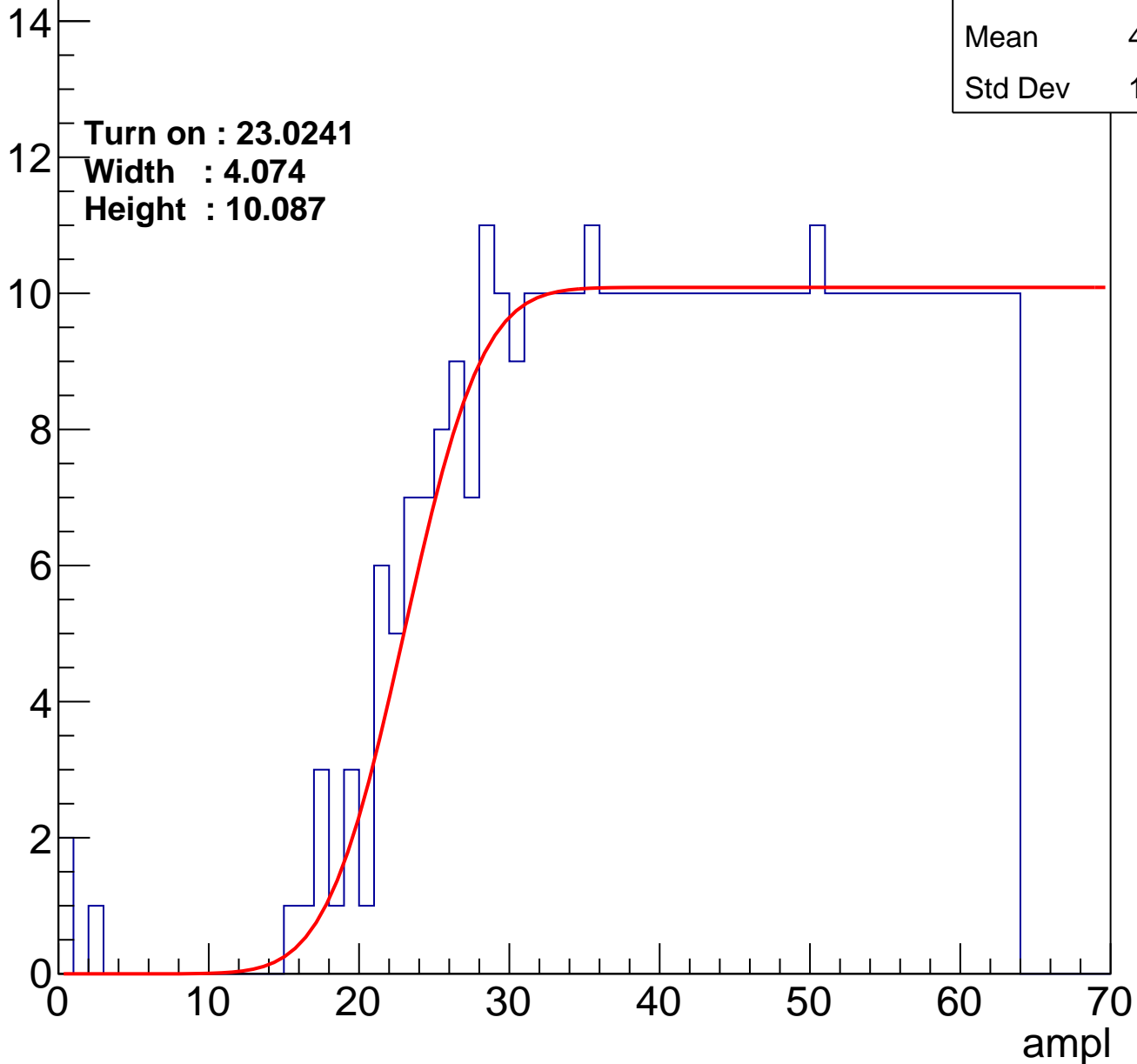
Entries	424
Mean	42.05
Std Dev	12.86

Turn on : 23.0241

Width : 4.074

Height : 10.087

Entry



B0L102S, U4-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.39
Std Dev	11.95

Turn on : 27.7320

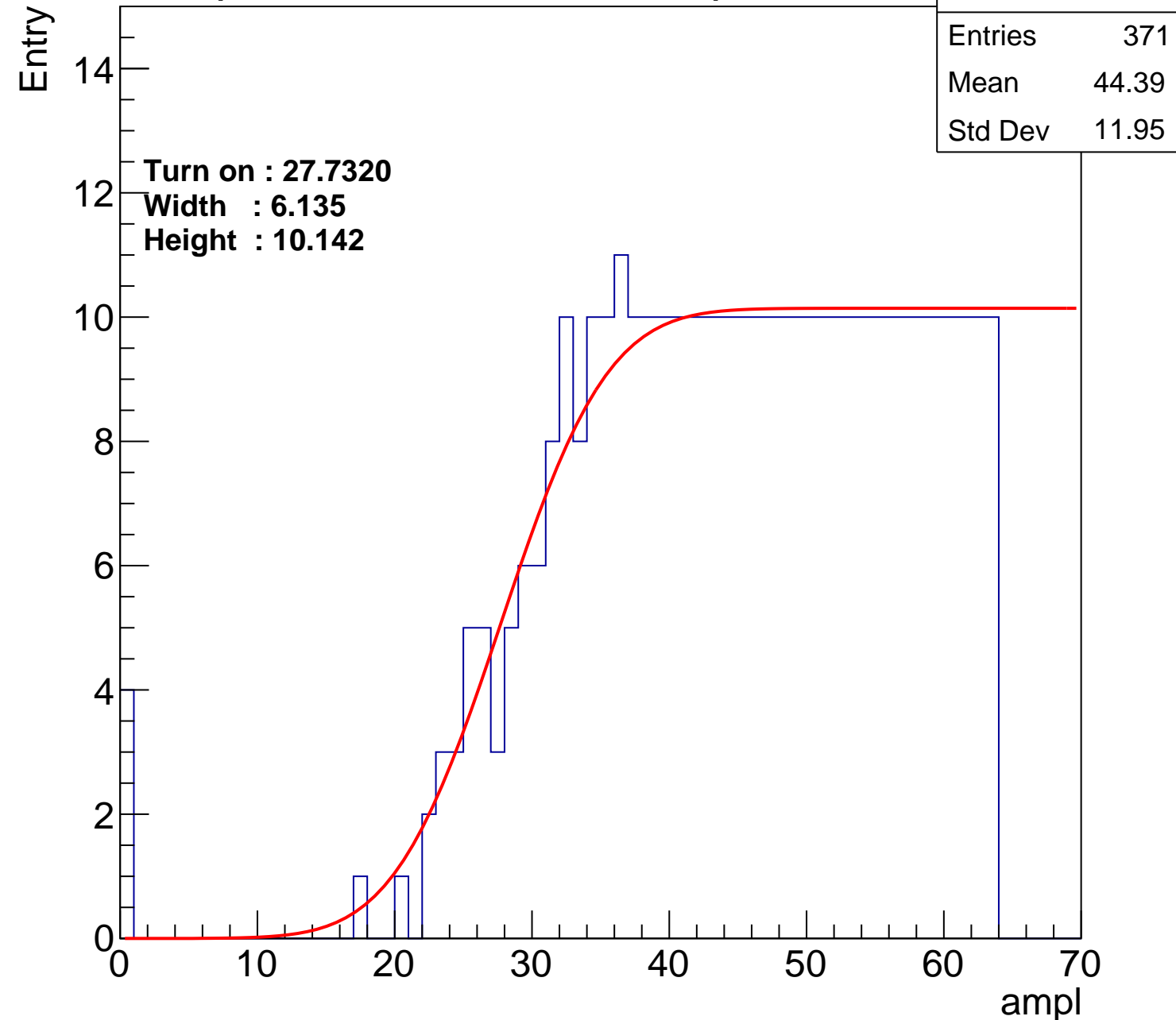
Width : 6.135

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U4-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.39
Std Dev	11.95

Turn on : 27.7320

Width : 6.135

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl

