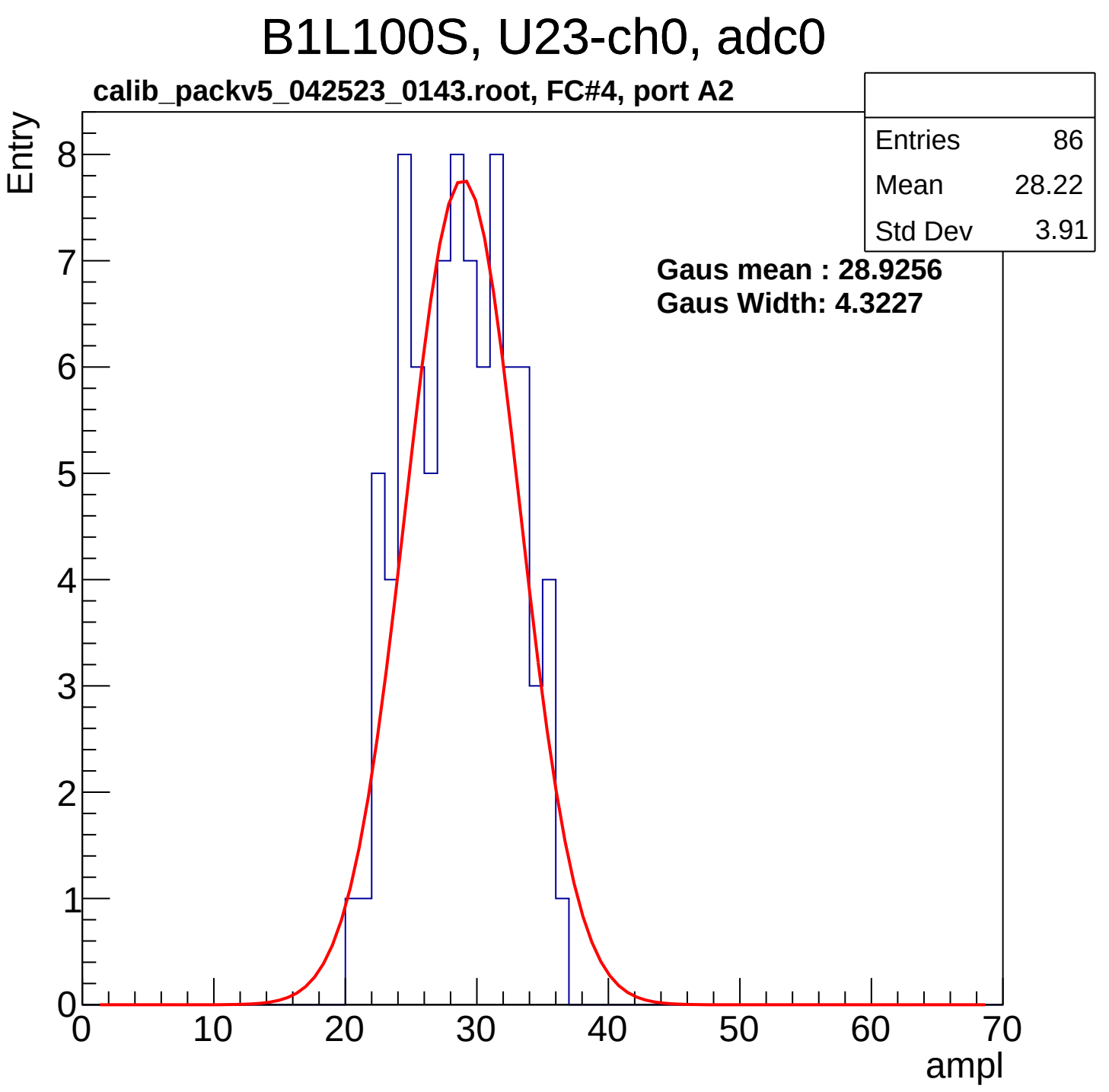
B1L100S, U23-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 28.22
Std Dev 3.91
Gaus mean : 28.9256
Gaus Width: 4.3227
Entry
ampl
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1
2
3
4
5
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7
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0
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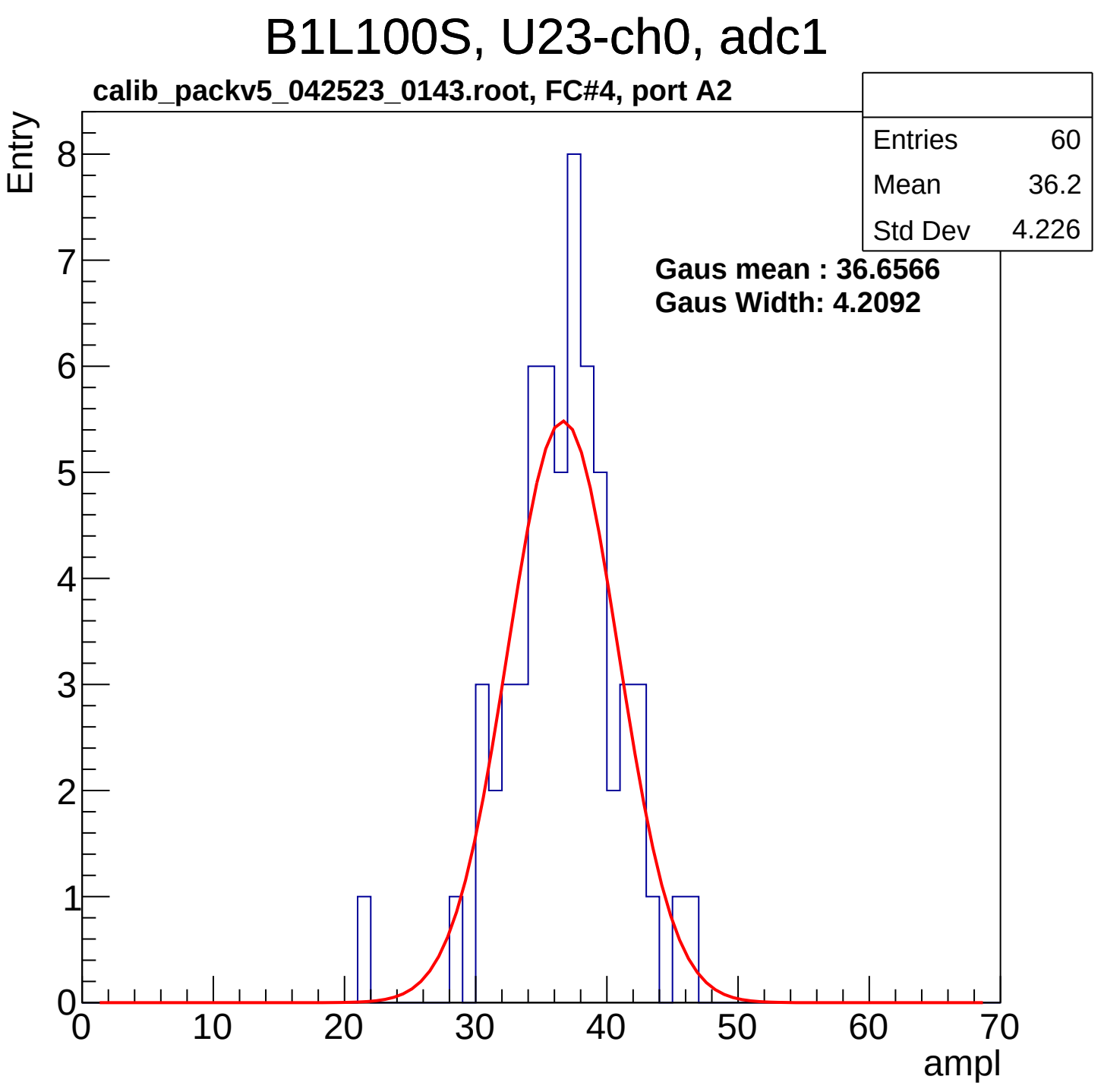
B1L100S, U23-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.2
Std Dev 4.226
Gaus mean : 36.6566
Gaus Width: 4.2092
Entry
ampl
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0
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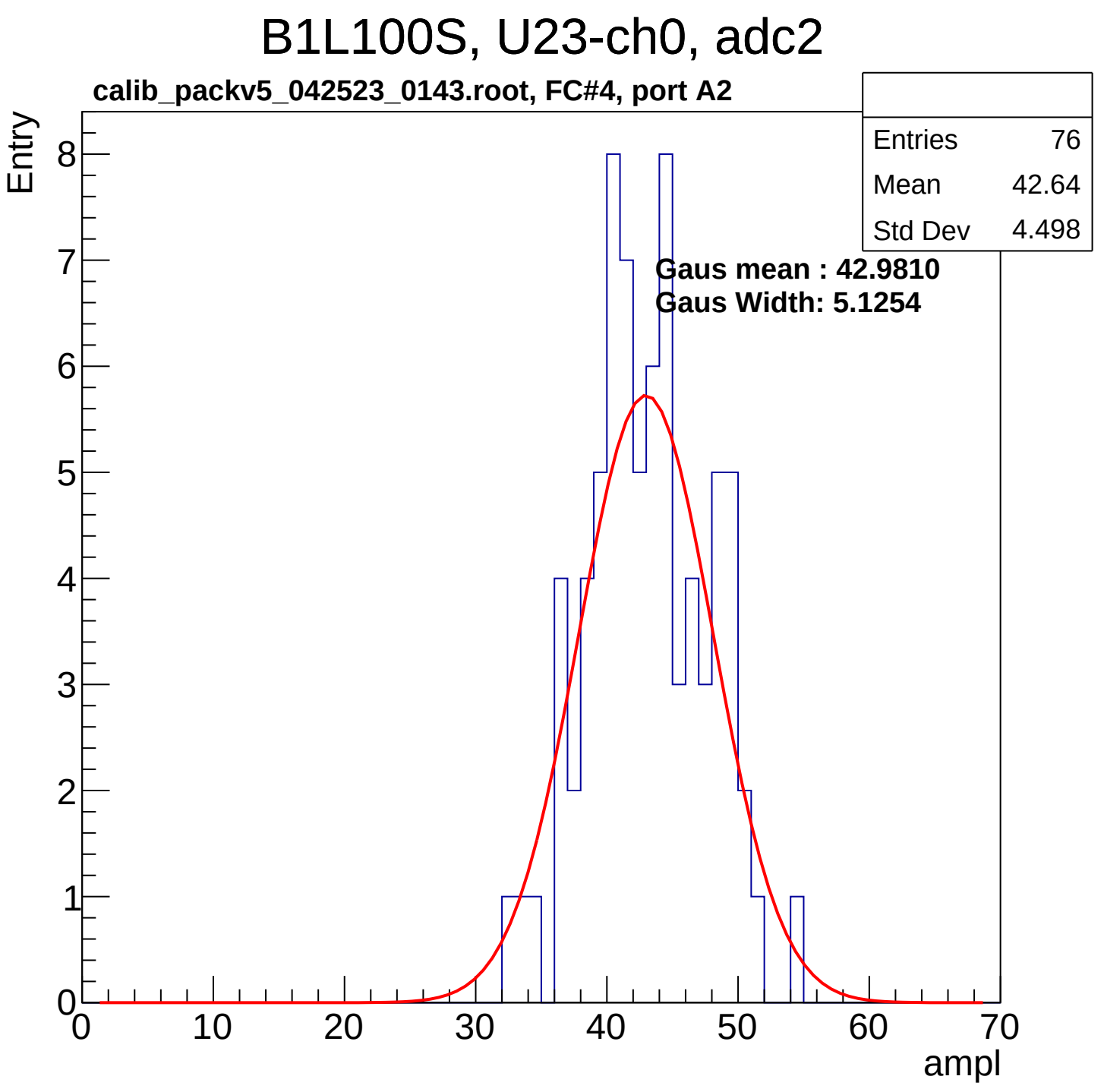
B1L100S, U23-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 42.64
Std Dev 4.498
Gaus mean : 42.9810
Gaus Width: 5.1254
Entry
ampl
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0
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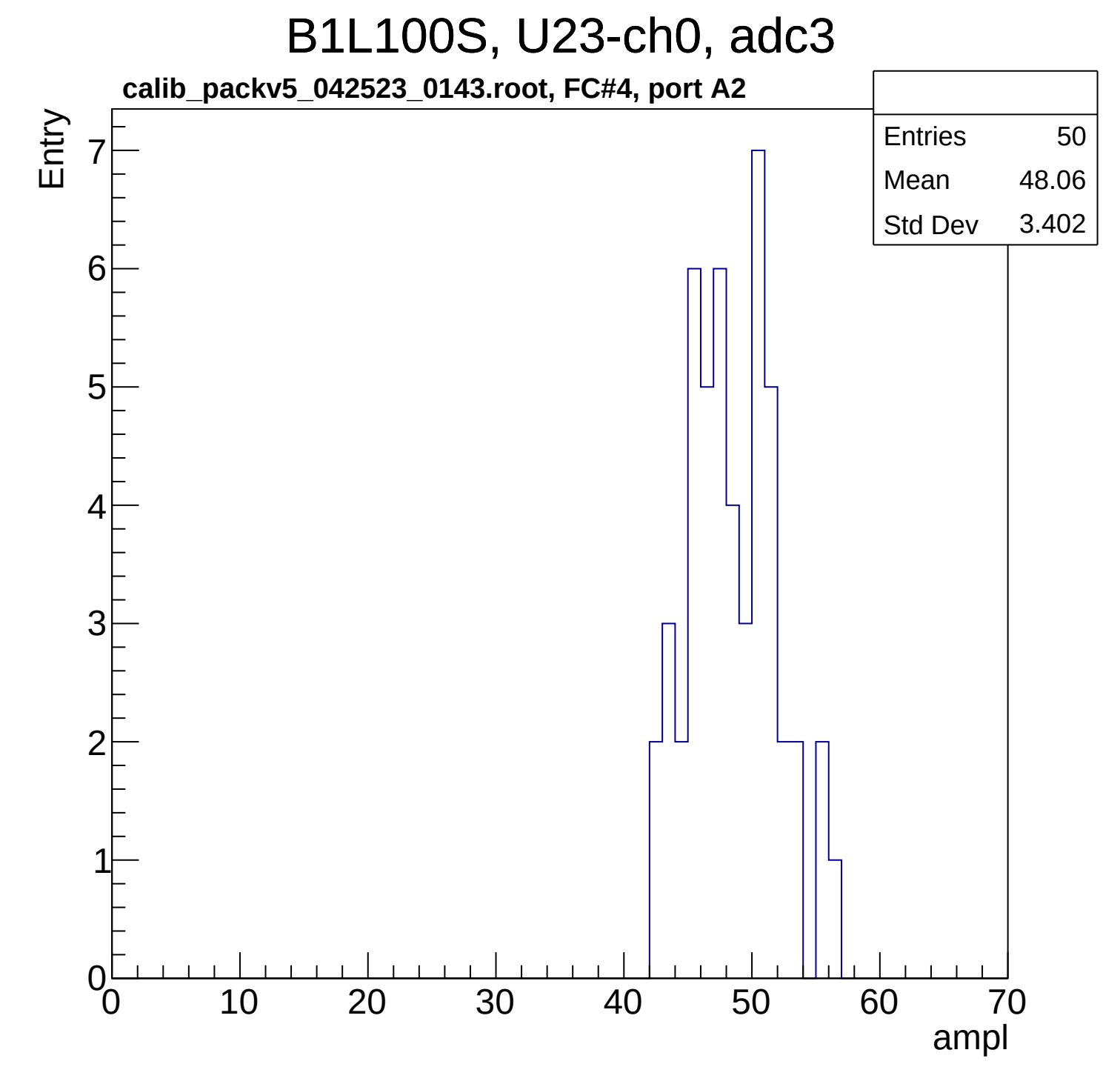
B1L100S, U23-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 48.06
Std Dev 3.402
Entry
ampl
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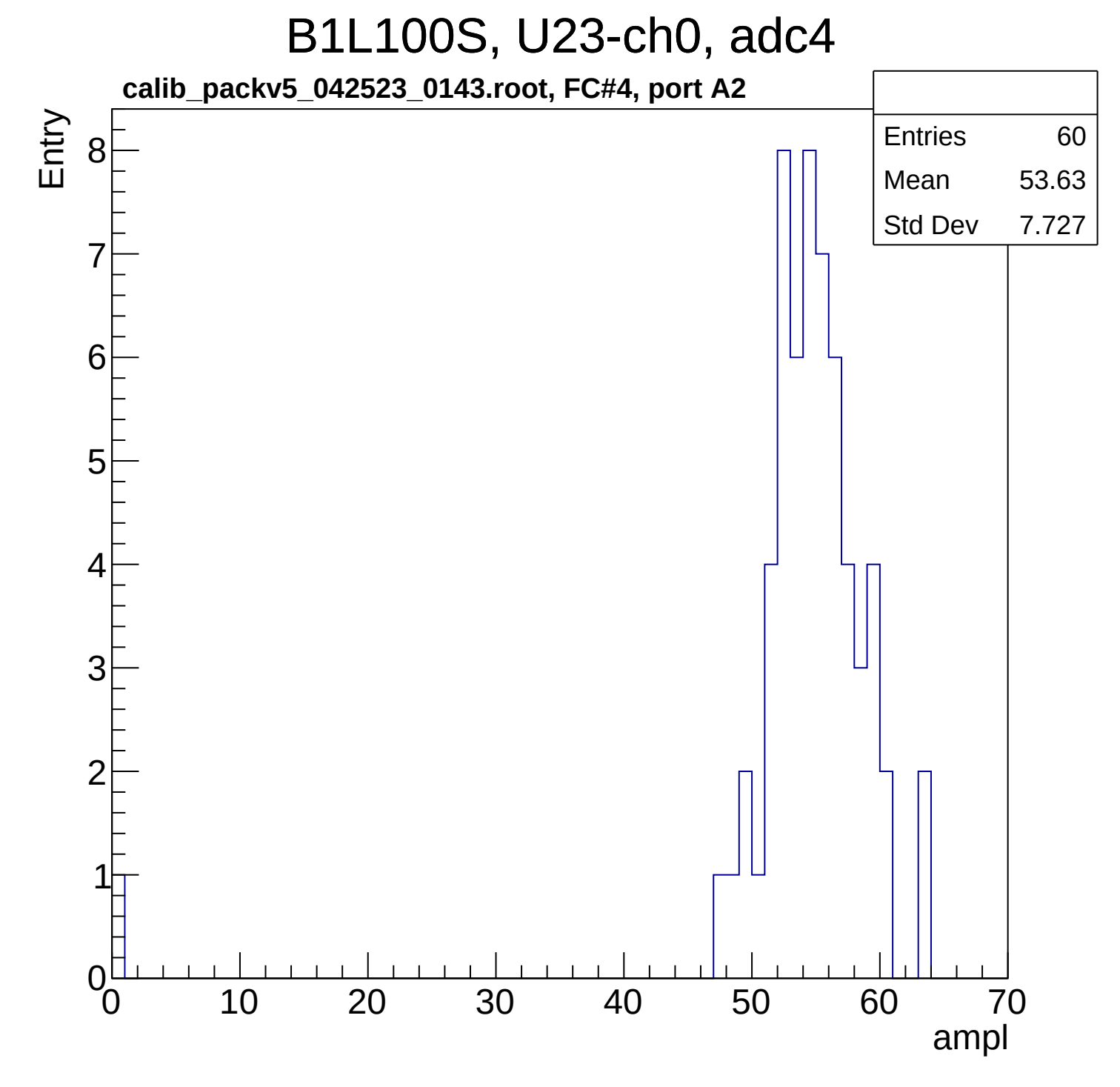
B1L100S, U23-ch0, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 53.63
Std Dev 7.727
Entry
ampl
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2
3
4
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0
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60
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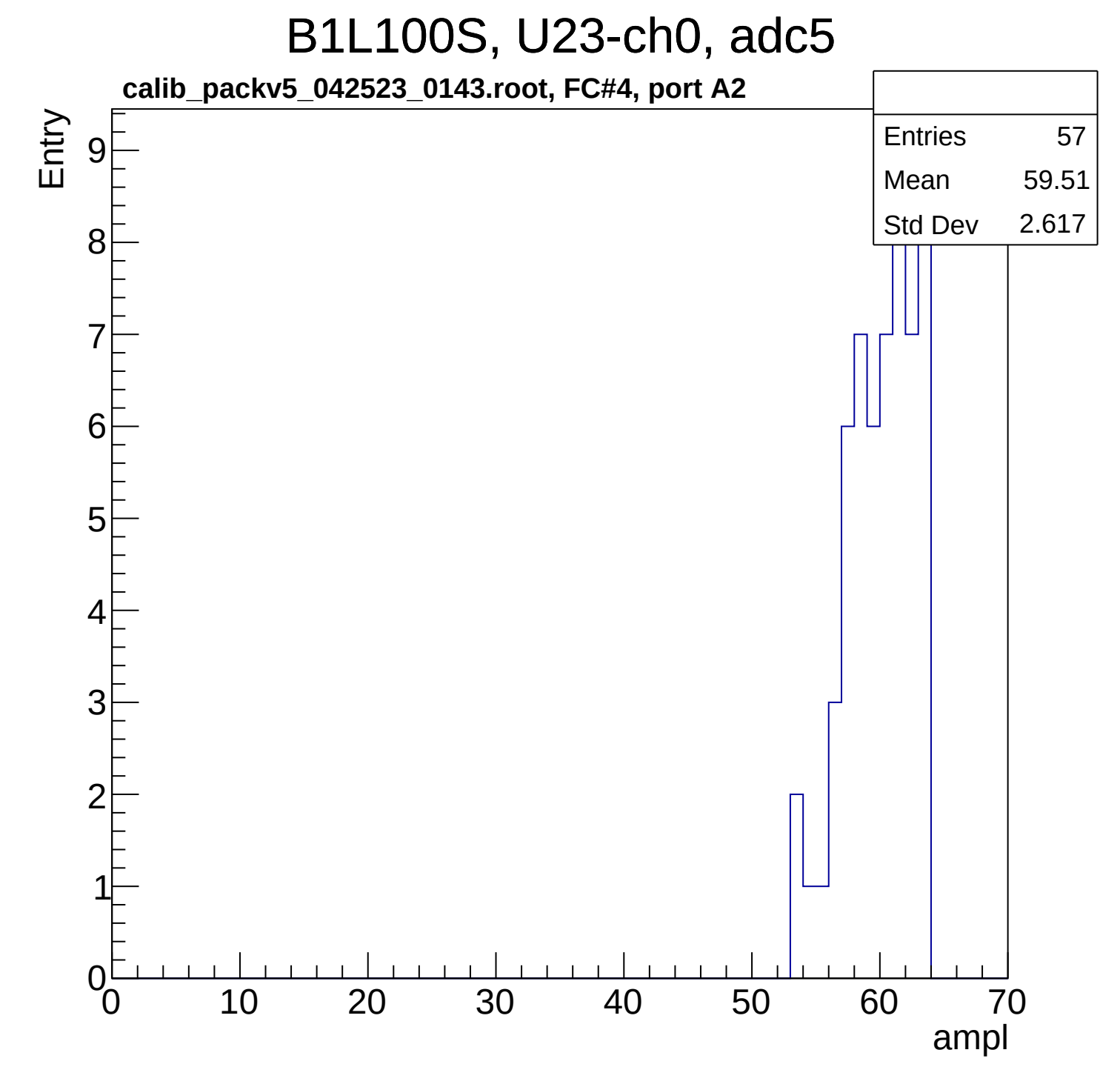
B1L100S, U23-ch0, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 59.51
Std Dev 2.617
Entry
ampl
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4
5
6
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0
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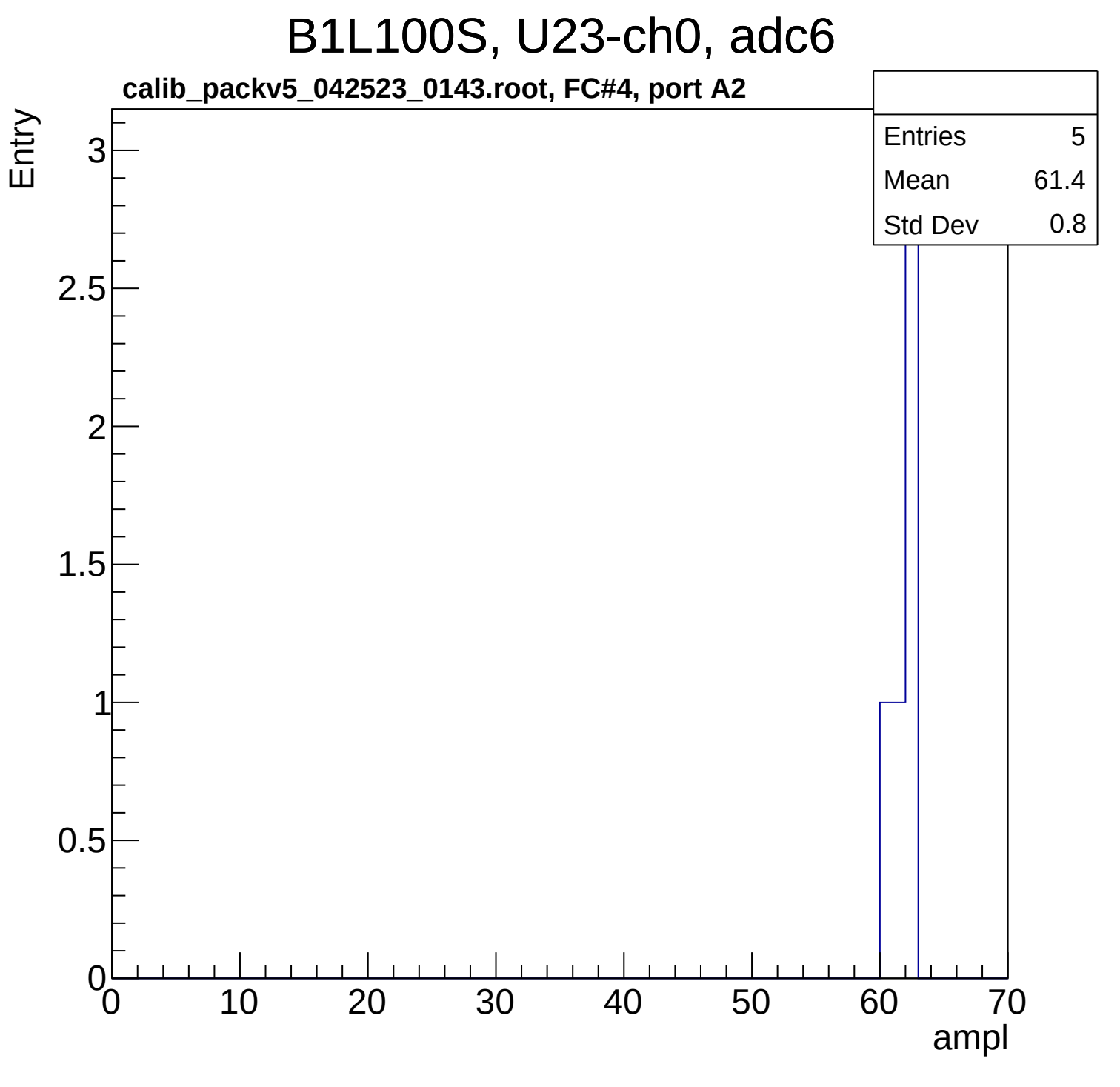
B1L100S, U23-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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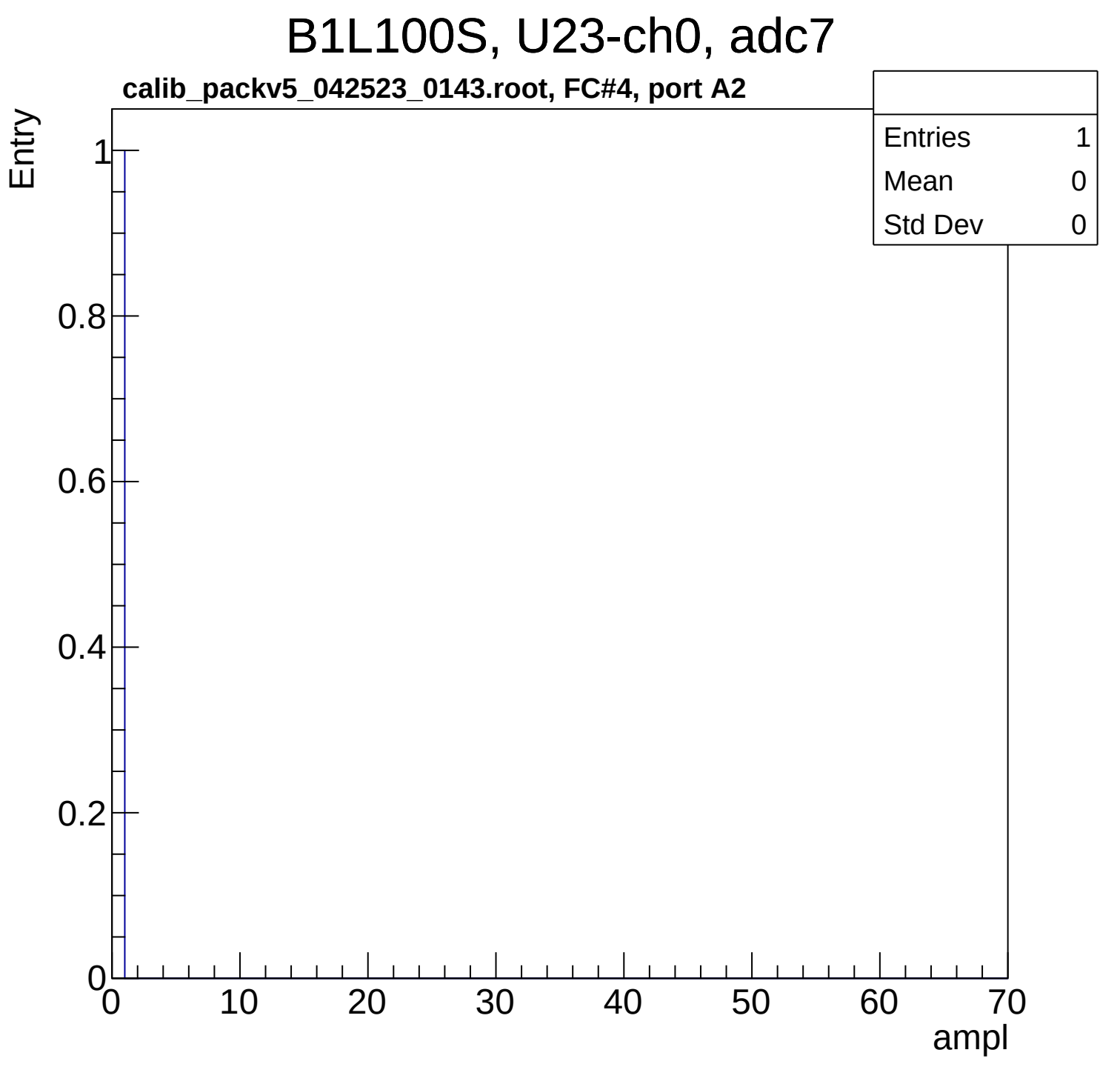
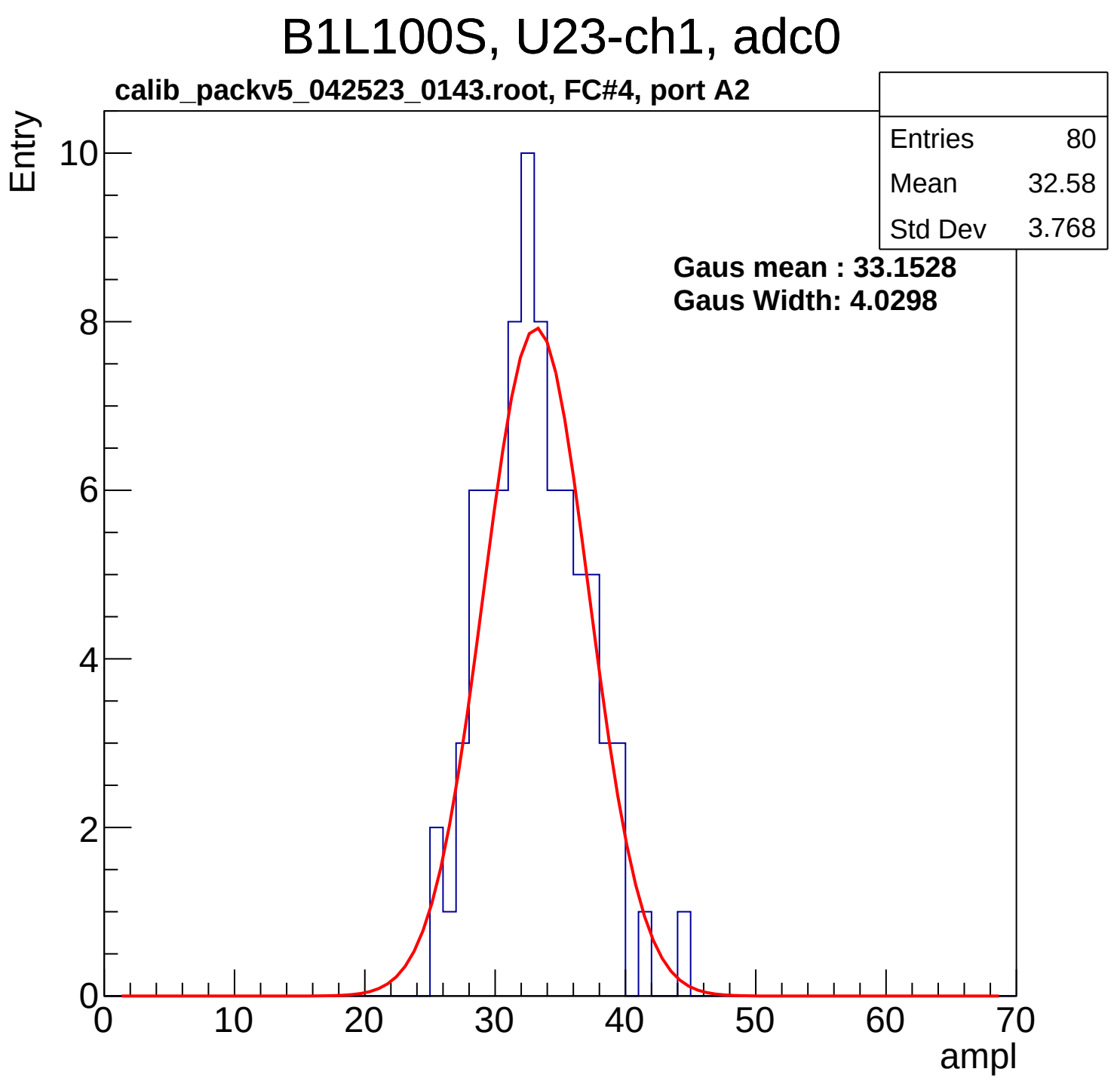
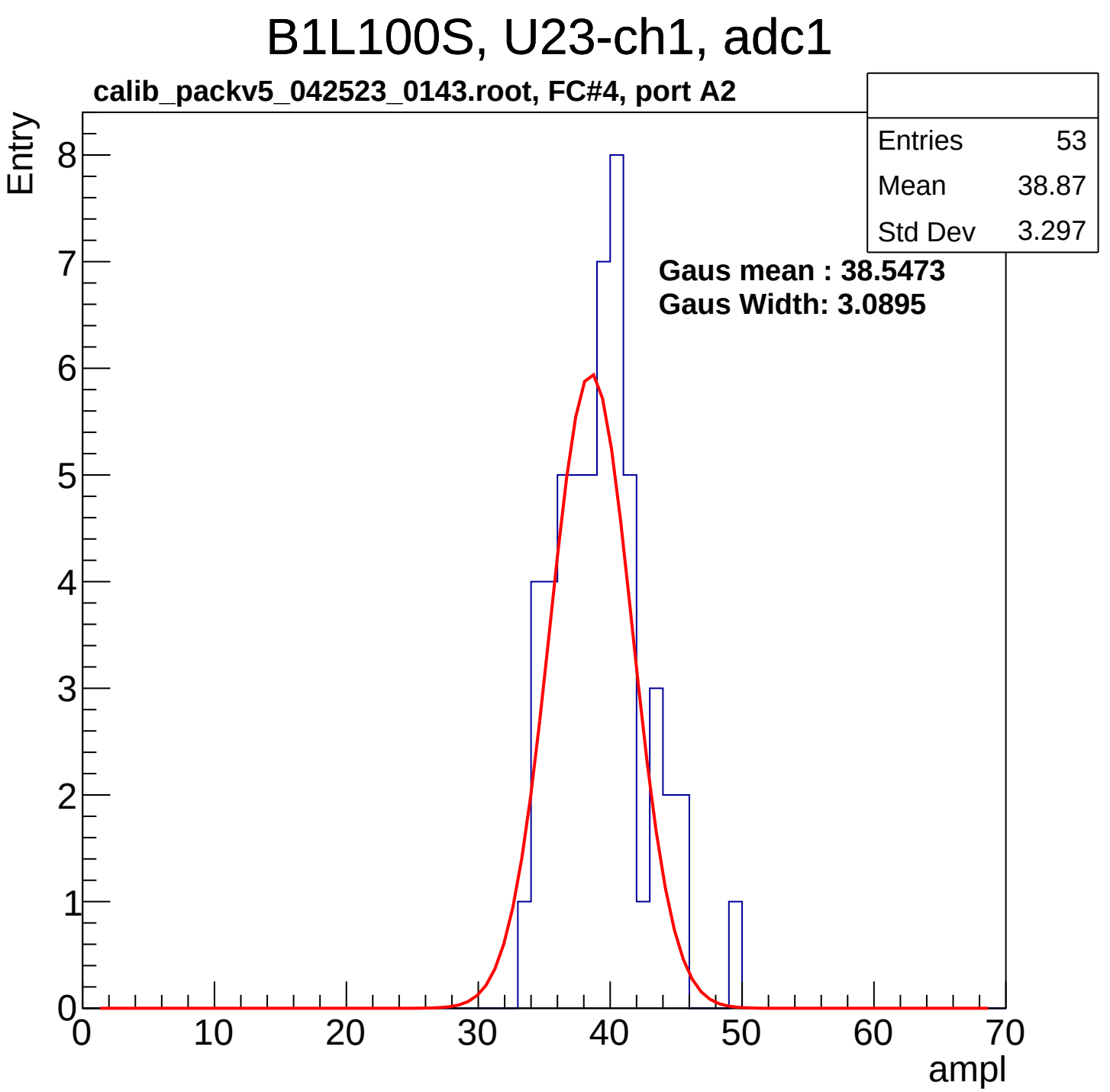

B1L100S, U23-ch0, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 32.58
Std Dev 3.768
Gaus mean : 33.1528
Gaus Width: 4.0298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 38.87
Std Dev 3.297
Gaus mean : 38.5473
Gaus Width: 3.0895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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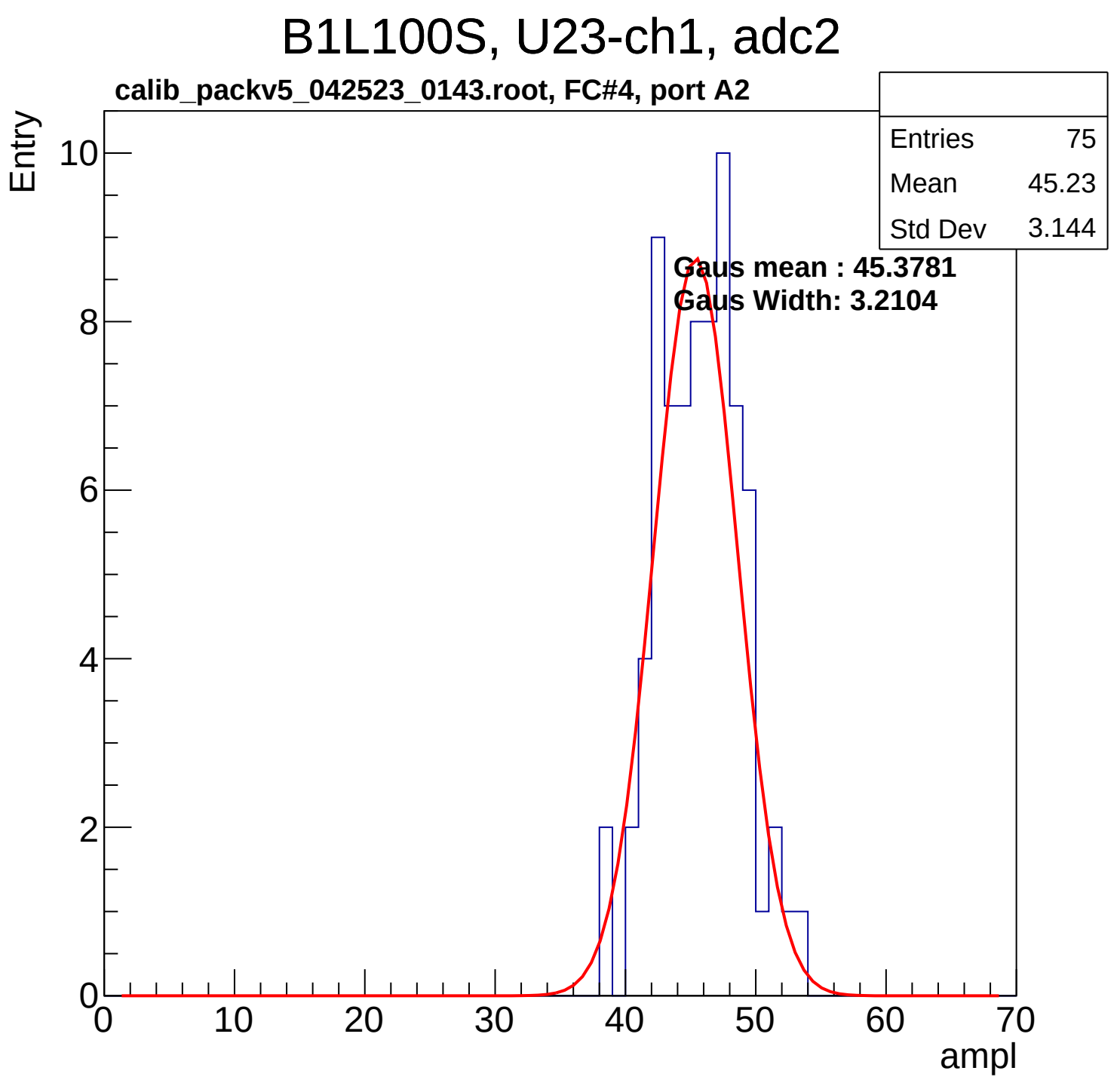
B1L100S, U23-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 45.23
Std Dev 3.144
Gaus mean : 45.3781
Gaus Width: 3.2104
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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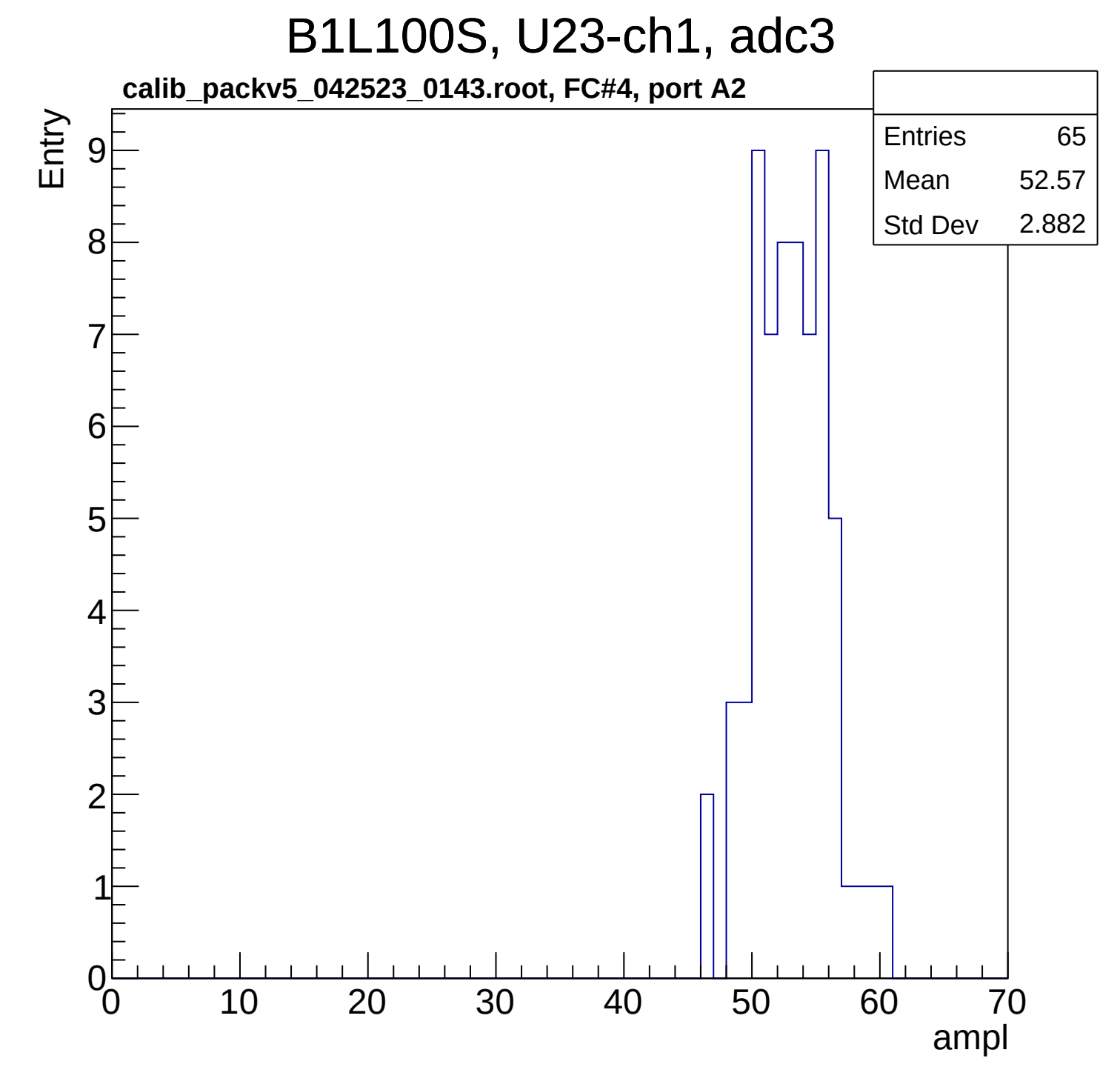
B1L100S, U23-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 52.57
Std Dev 2.882
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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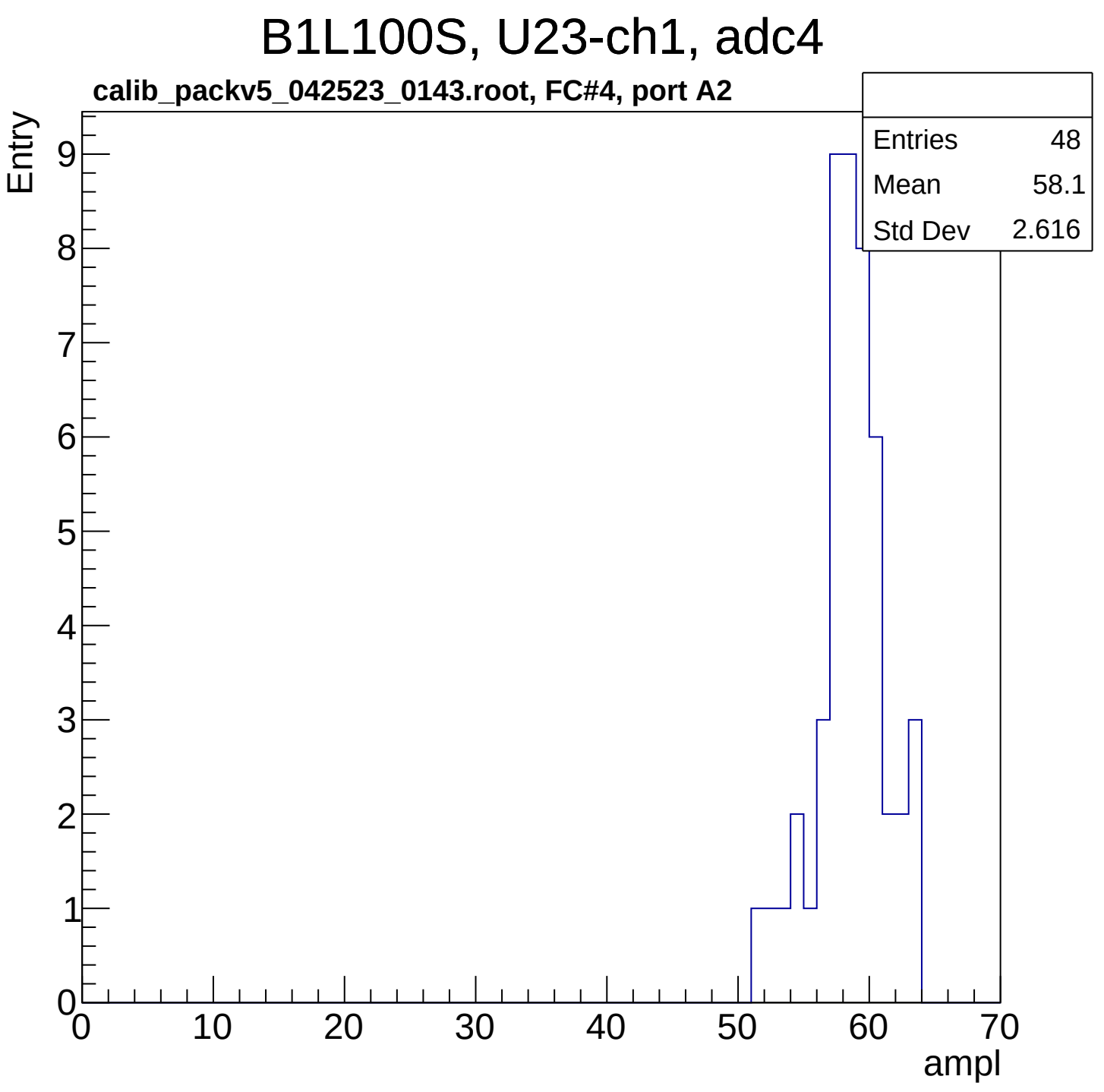
B1L100S, U23-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.1
Std Dev 2.616
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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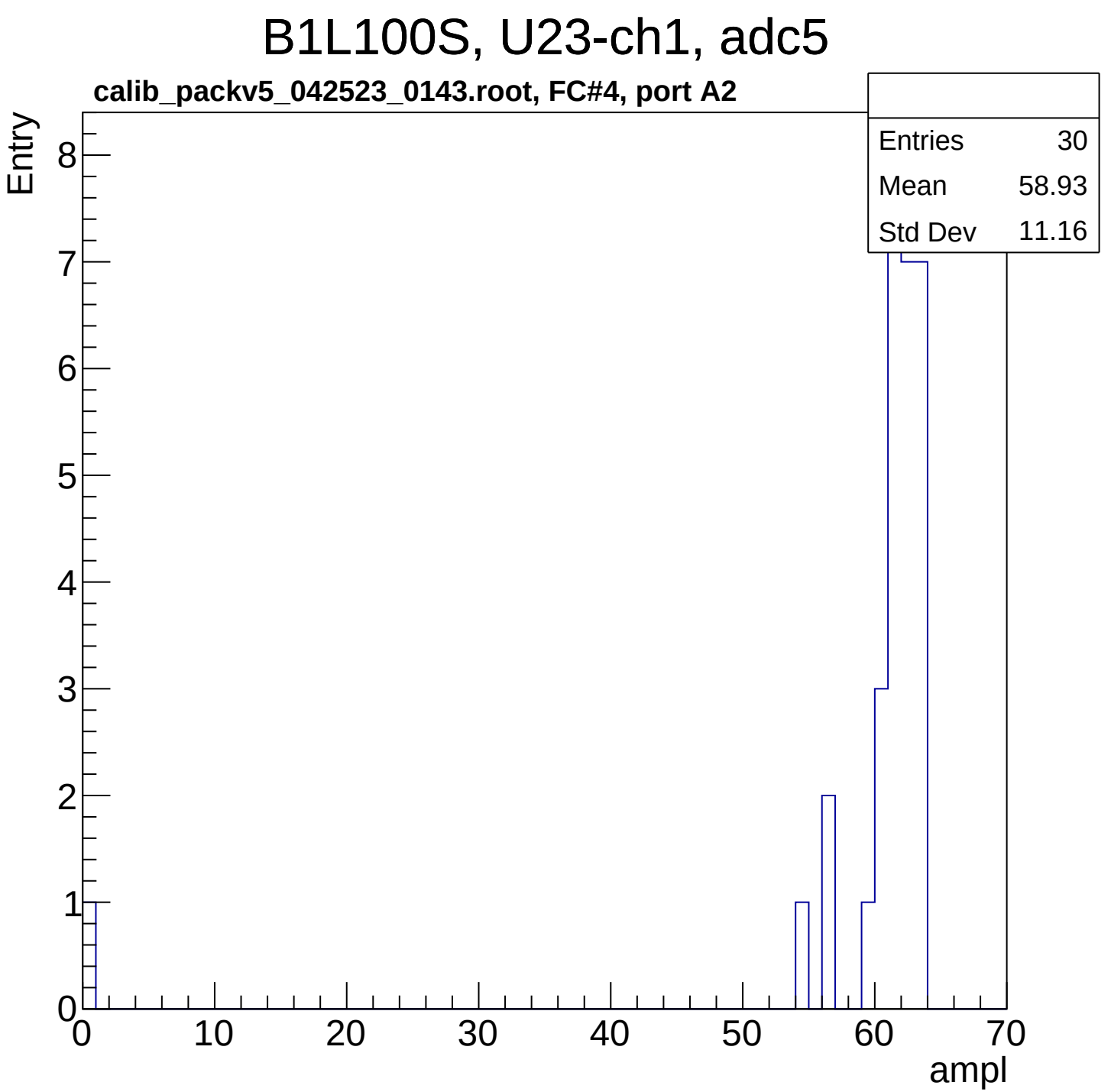
B1L100S, U23-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.93
Std Dev 11.16
Entry
ampl
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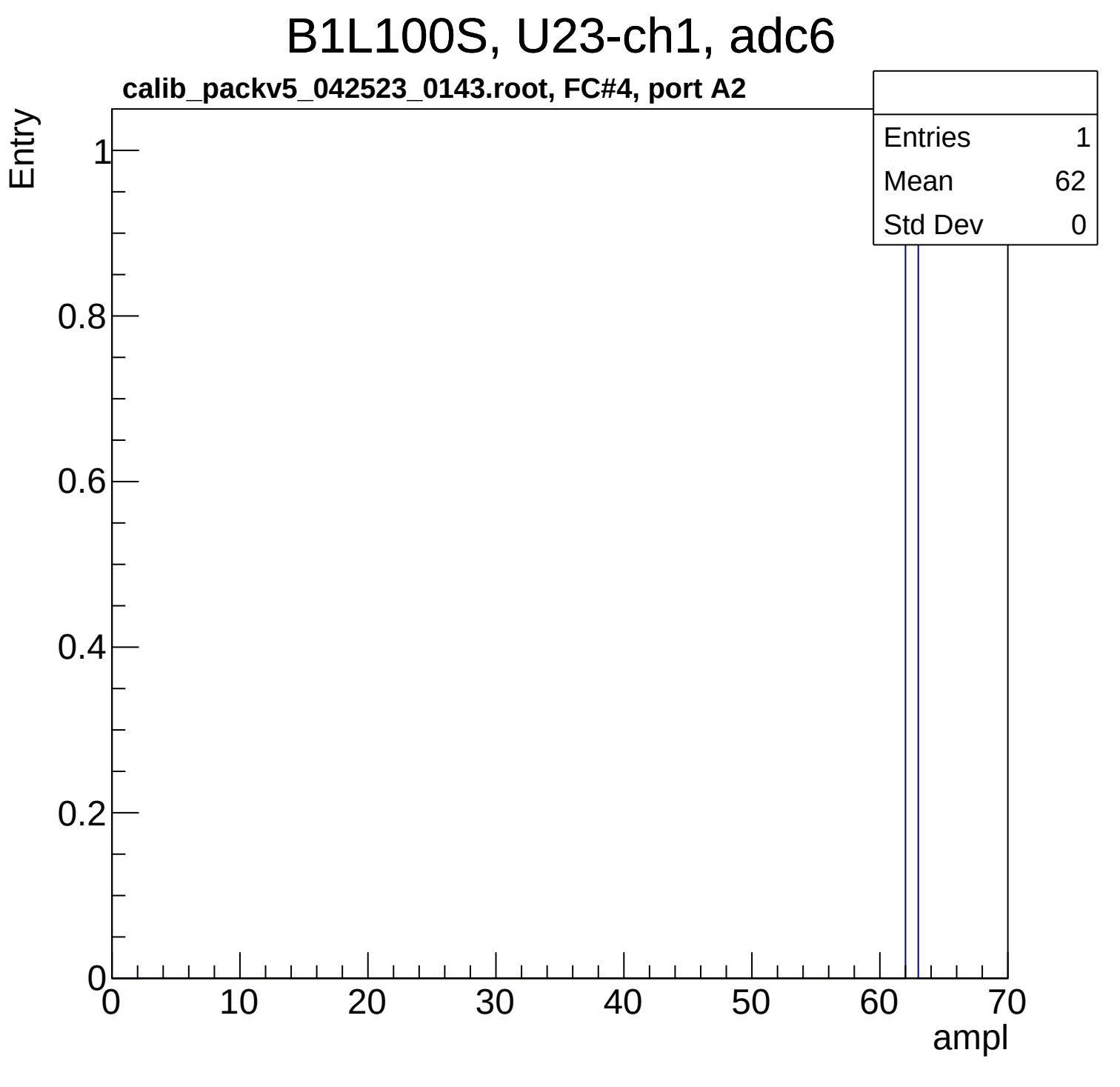
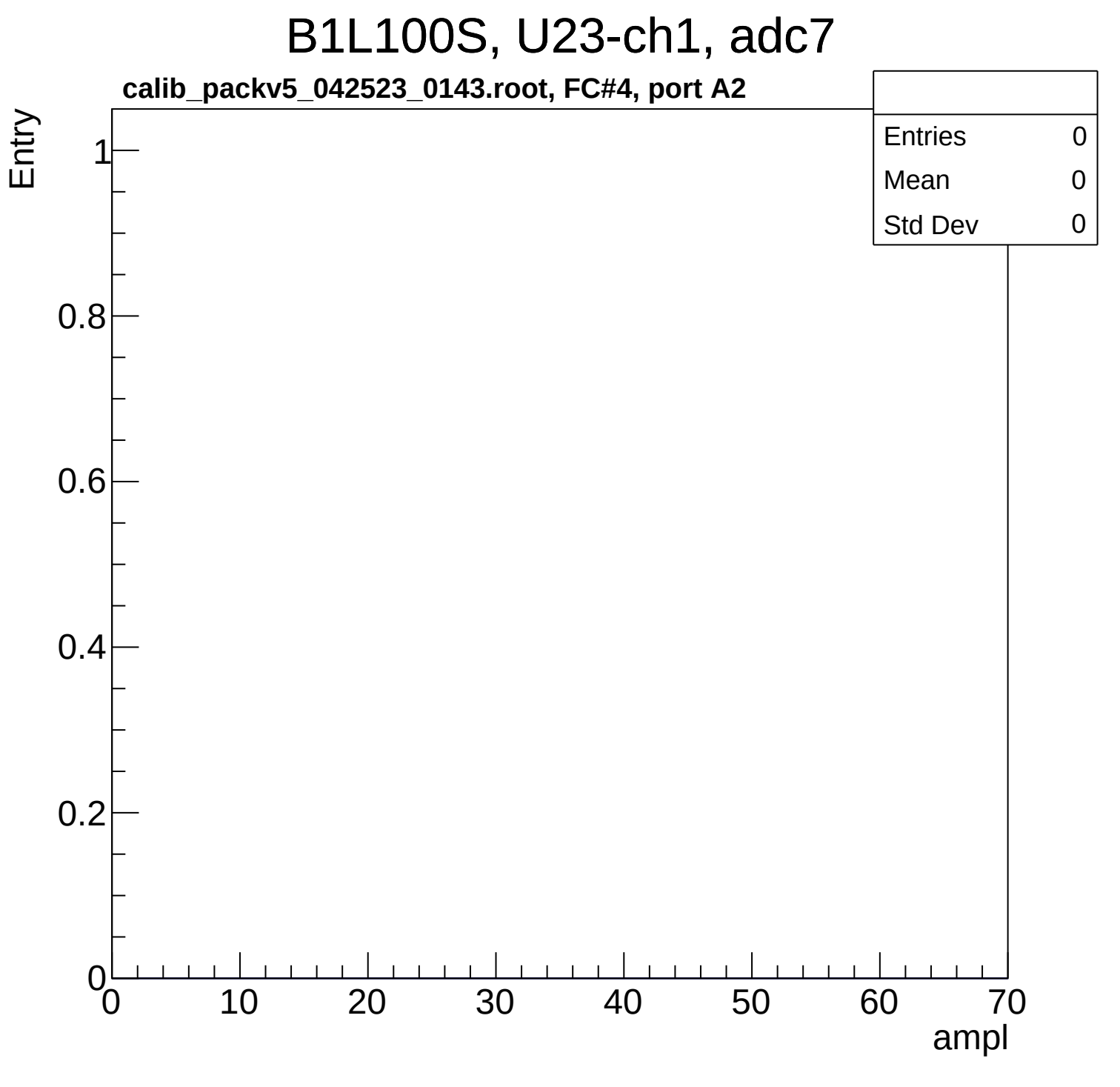
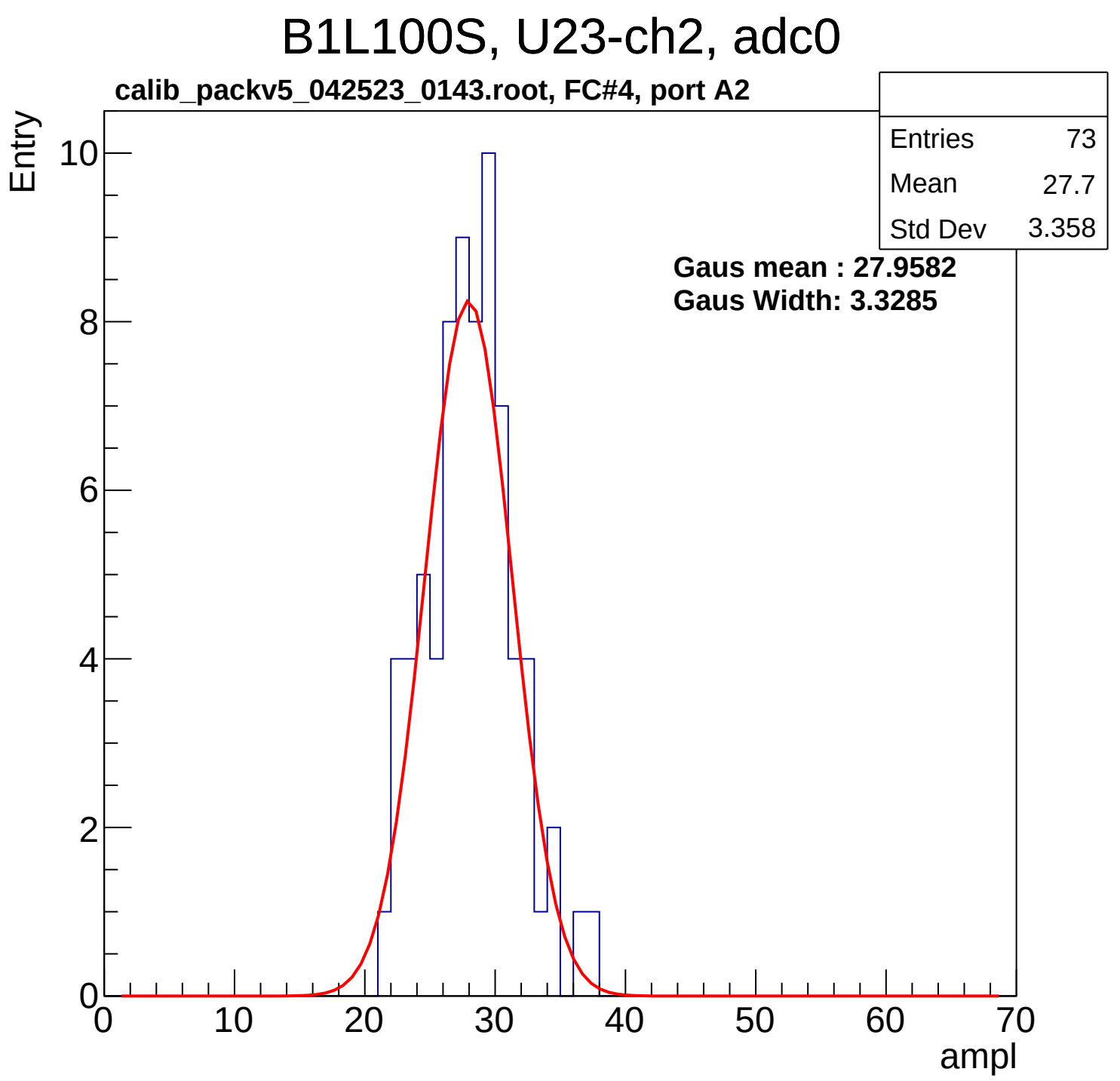
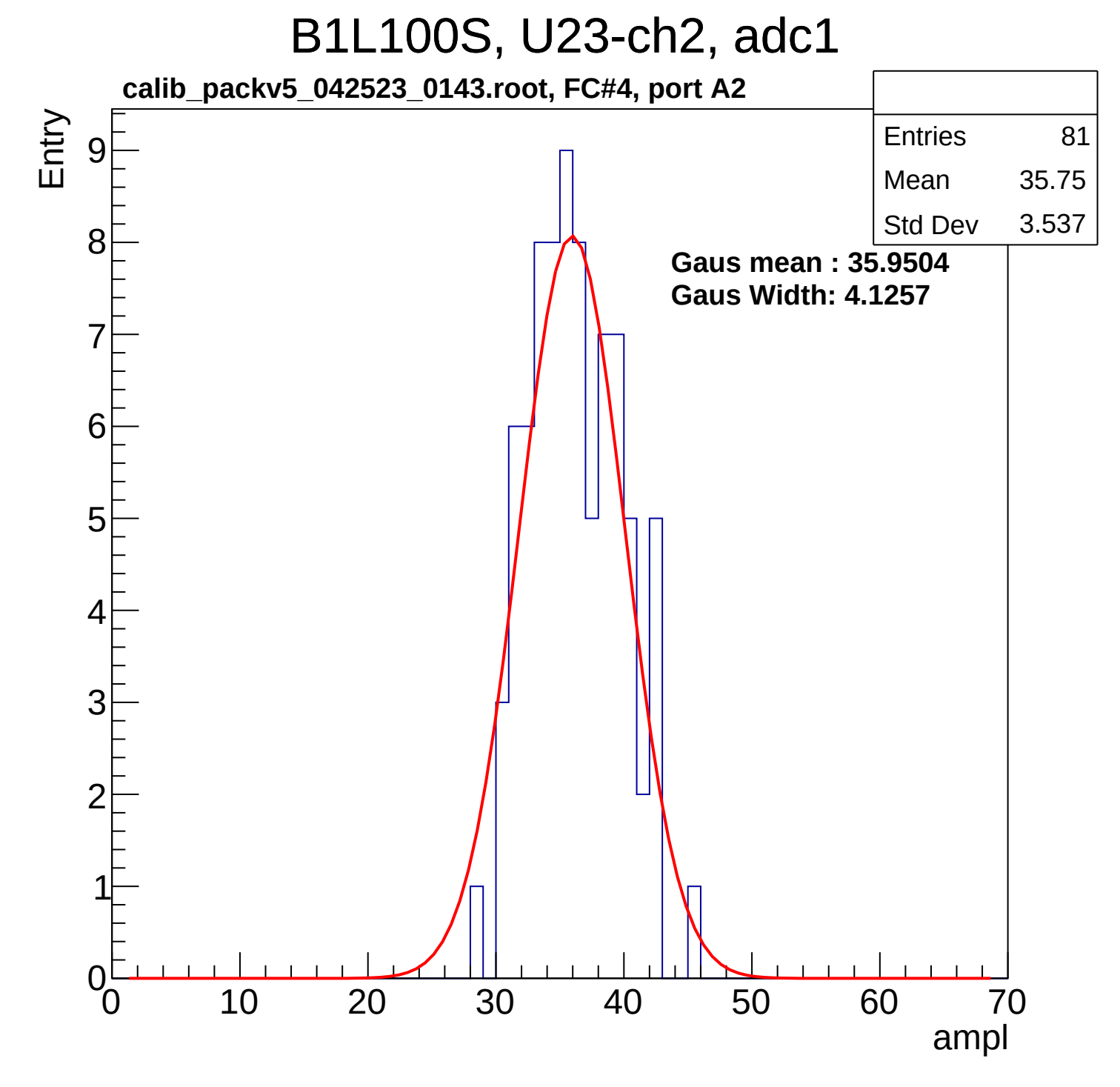
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch1, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 27.7
Std Dev 3.358
Gaus mean : 27.9582
Gaus Width: 3.3285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.75
Std Dev 3.537
Gaus mean : 35.9504
Gaus Width: 4.1257
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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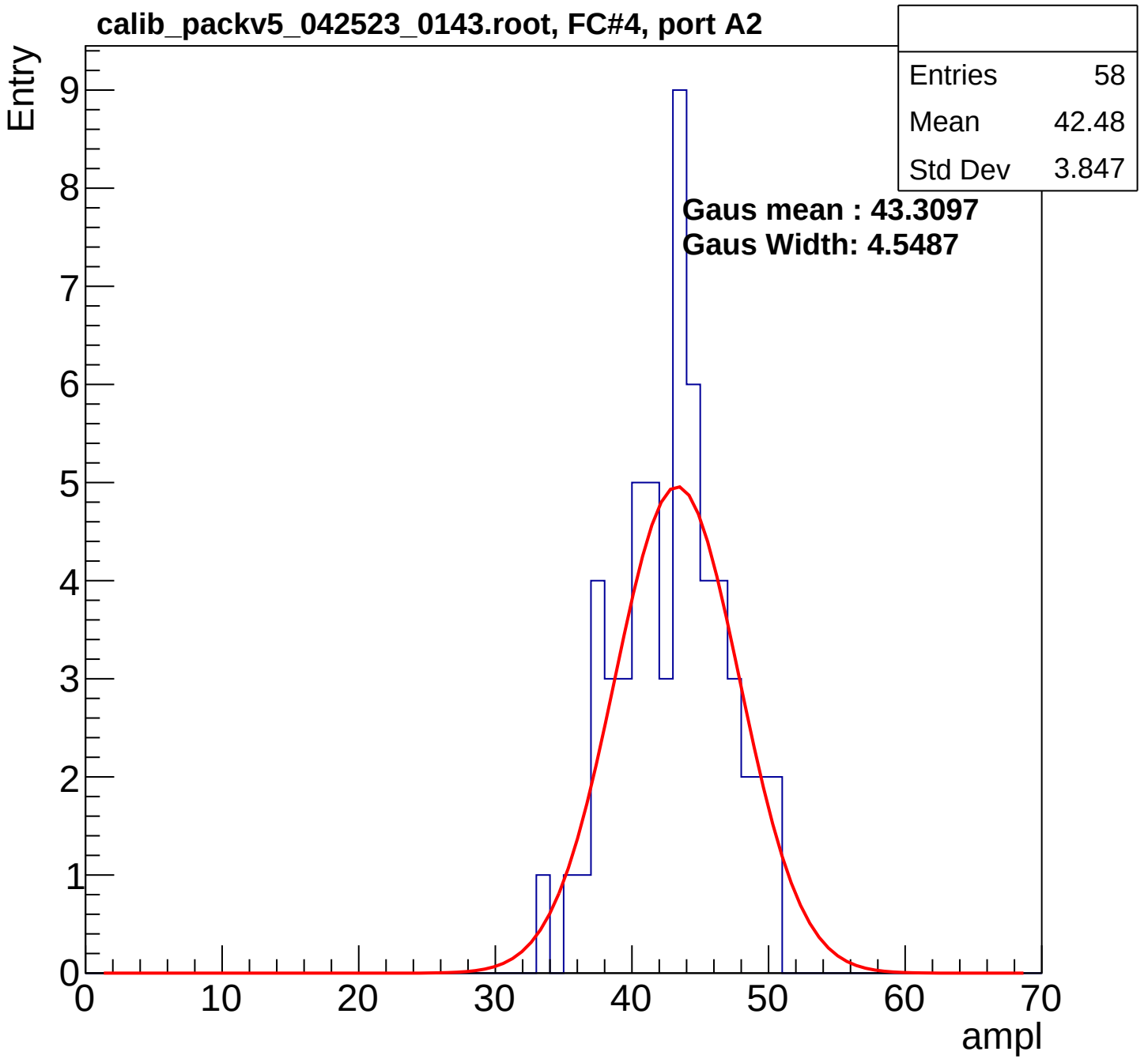
B1L100S, U23-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 42.48
Std Dev 3.847
Gaus mean : 43.3097
Gaus Width: 4.5487
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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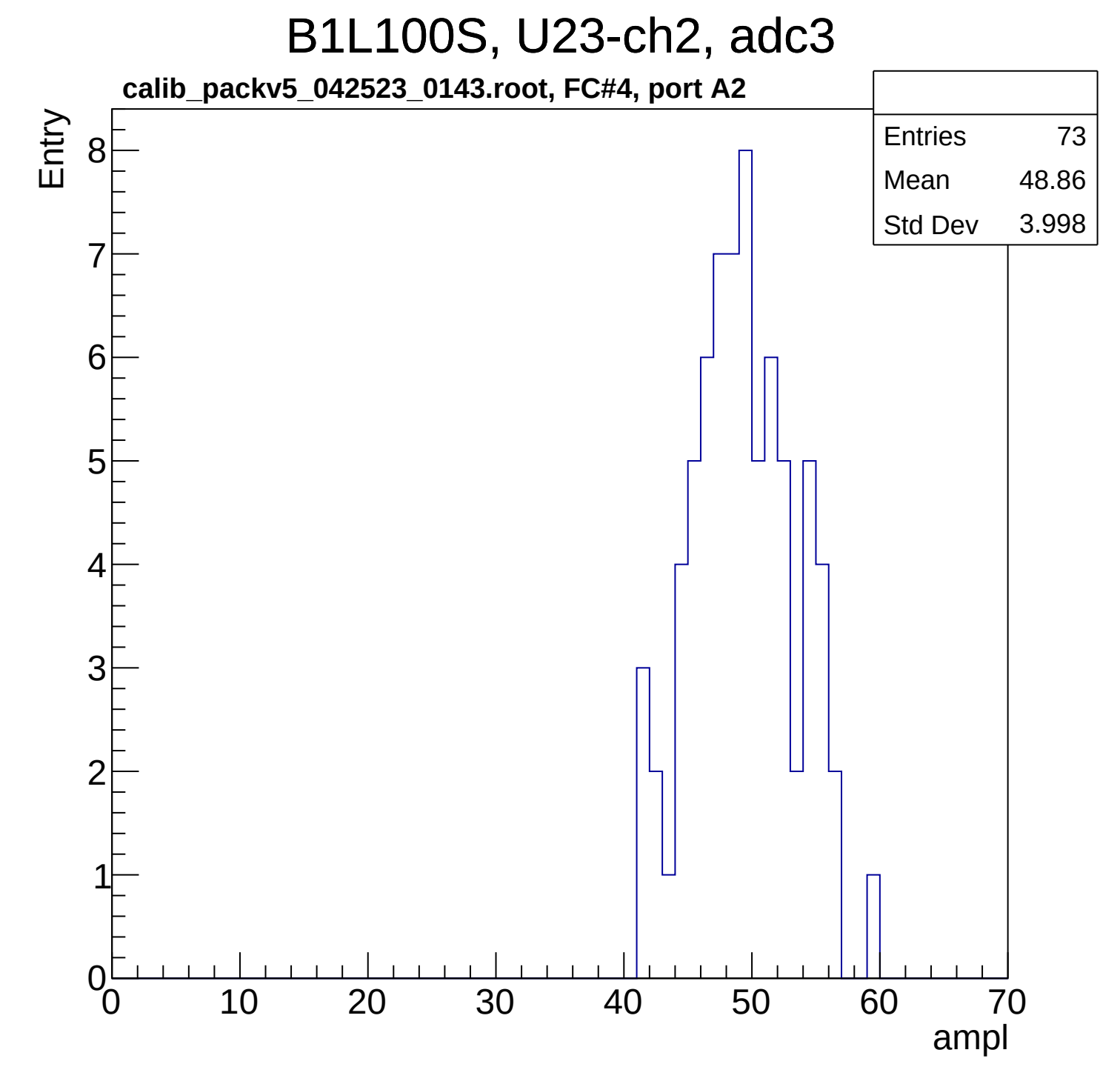
B1L100S, U23-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 48.86
Std Dev 3.998
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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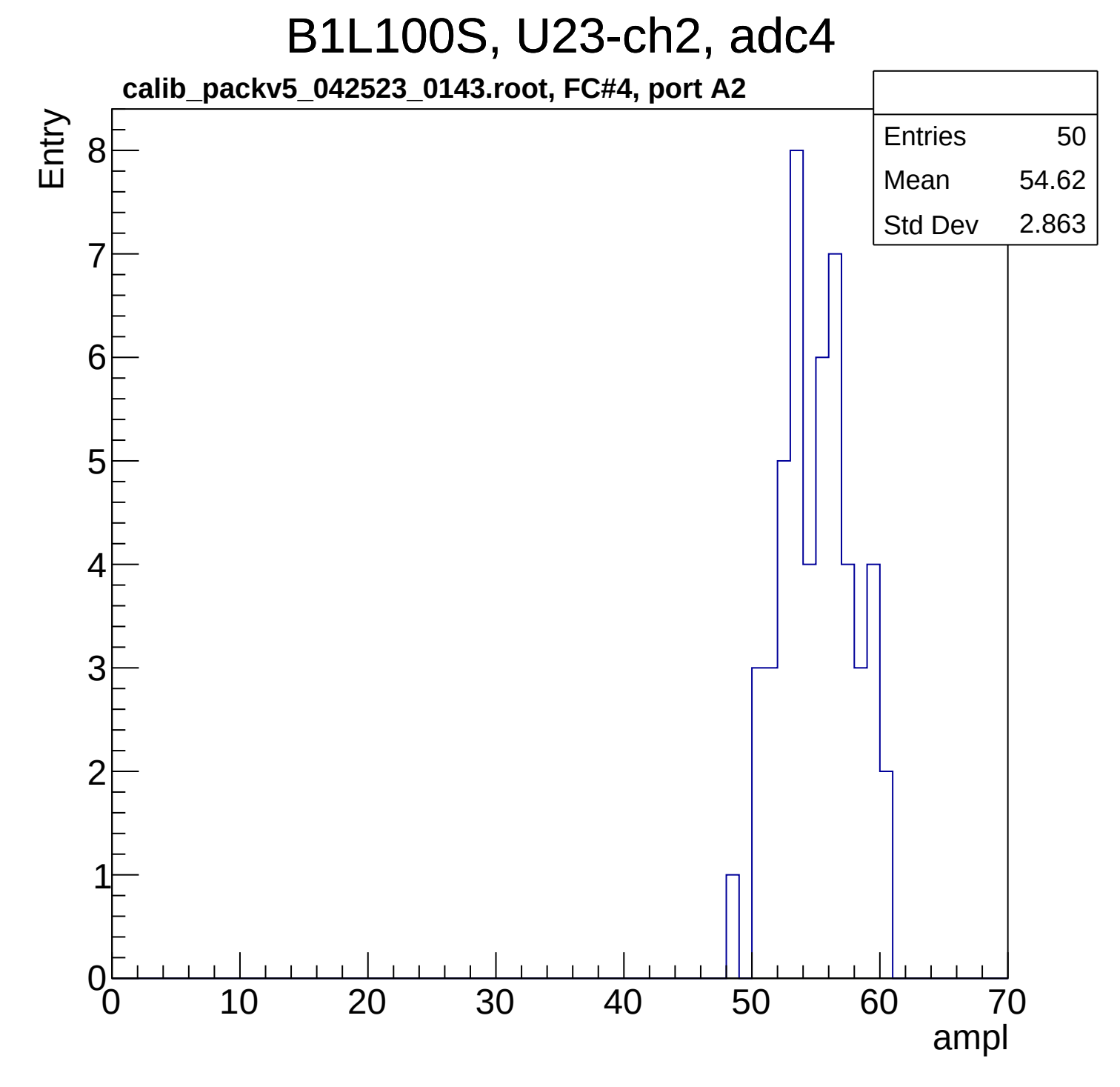
B1L100S, U23-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.62
Std Dev 2.863
Entry
ampl
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2
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0
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60
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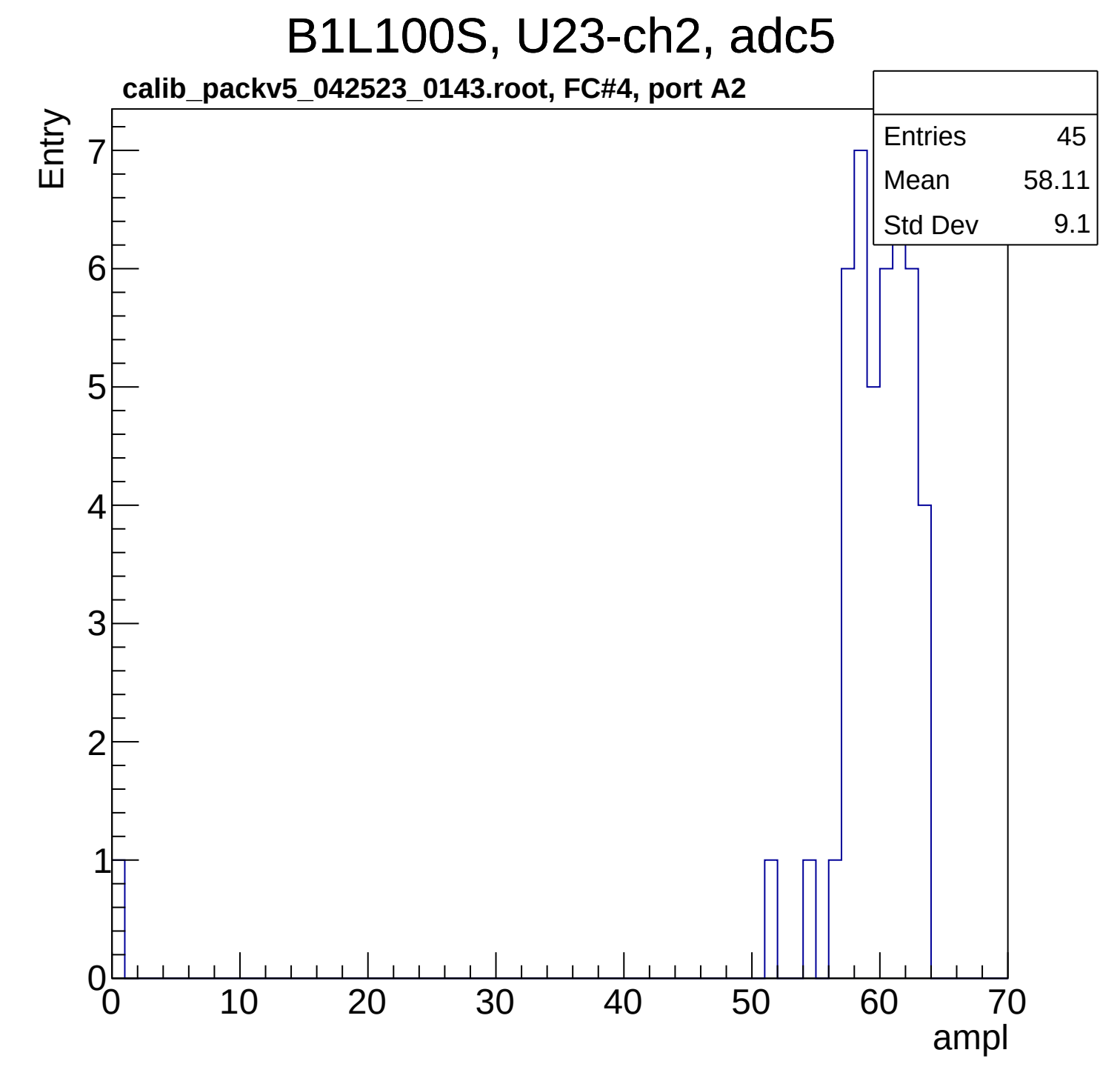
B1L100S, U23-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.11
Std Dev 9.1
Entry
ampl
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1
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0
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20
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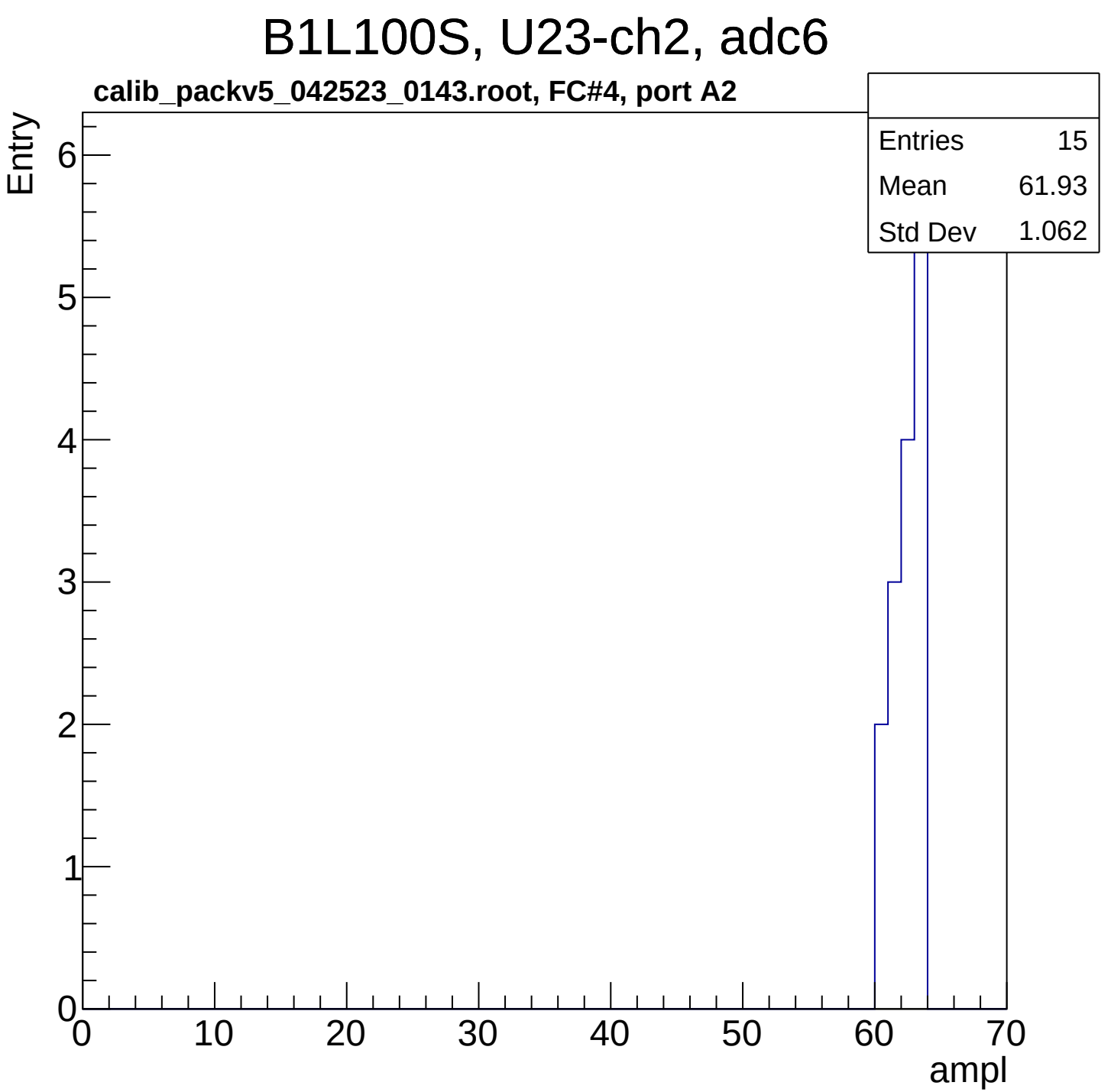
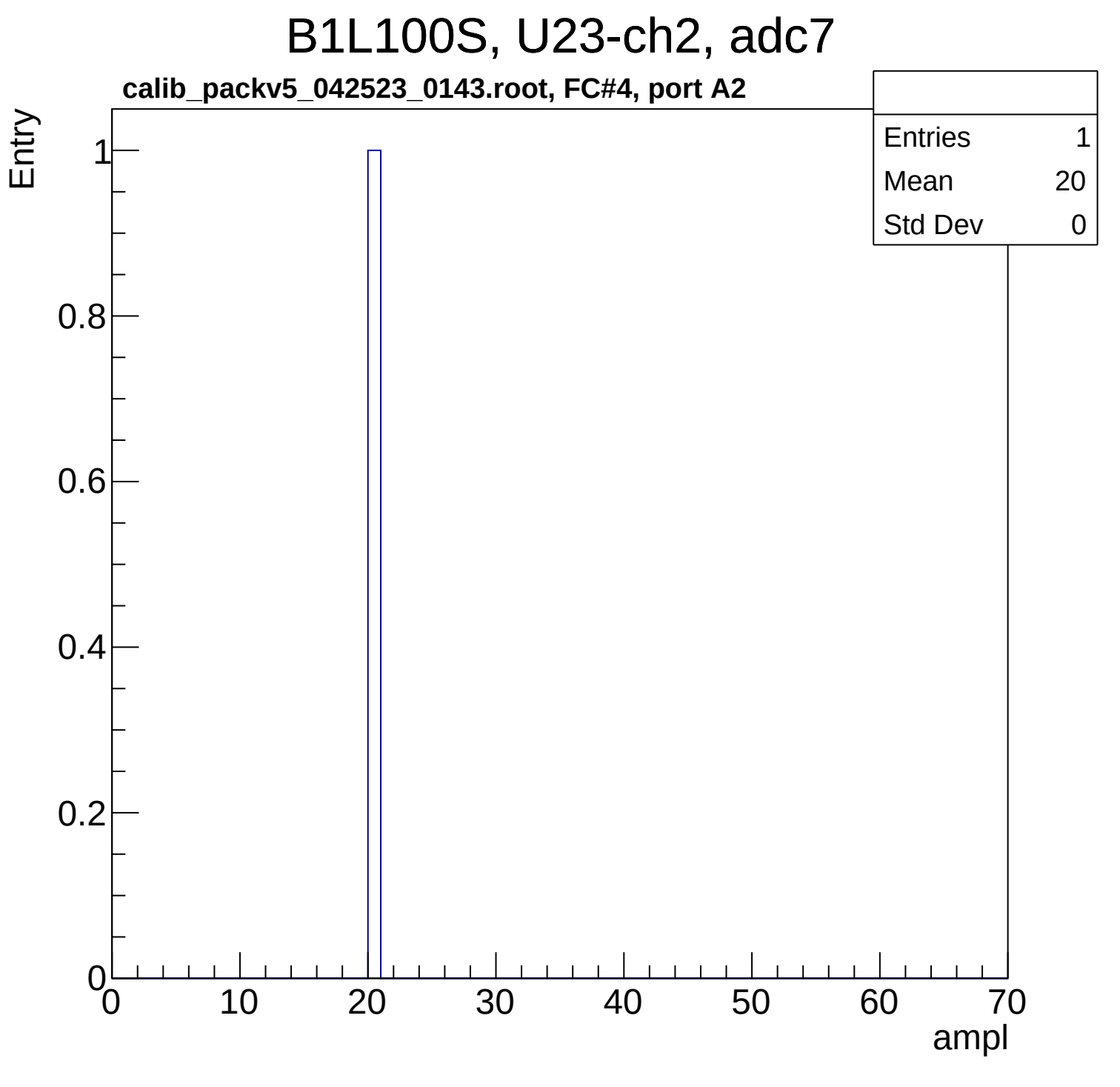

B1L100S, U23-ch2, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 15
Mean 61.93
Std Dev 1.062
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
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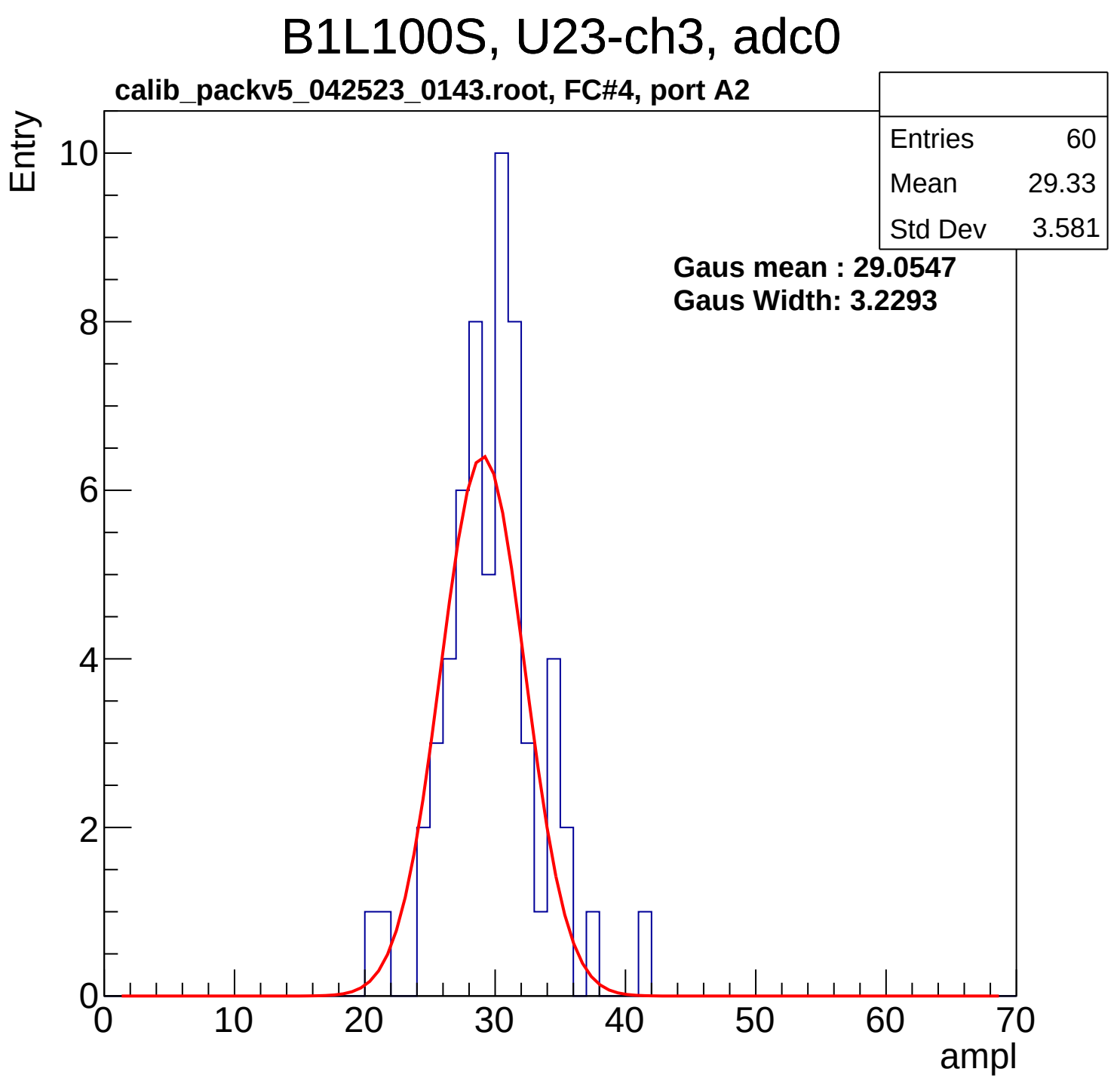
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.33
Std Dev 3.581
Gaus mean : 29.0547
Gaus Width: 3.2293
Entry
ampl
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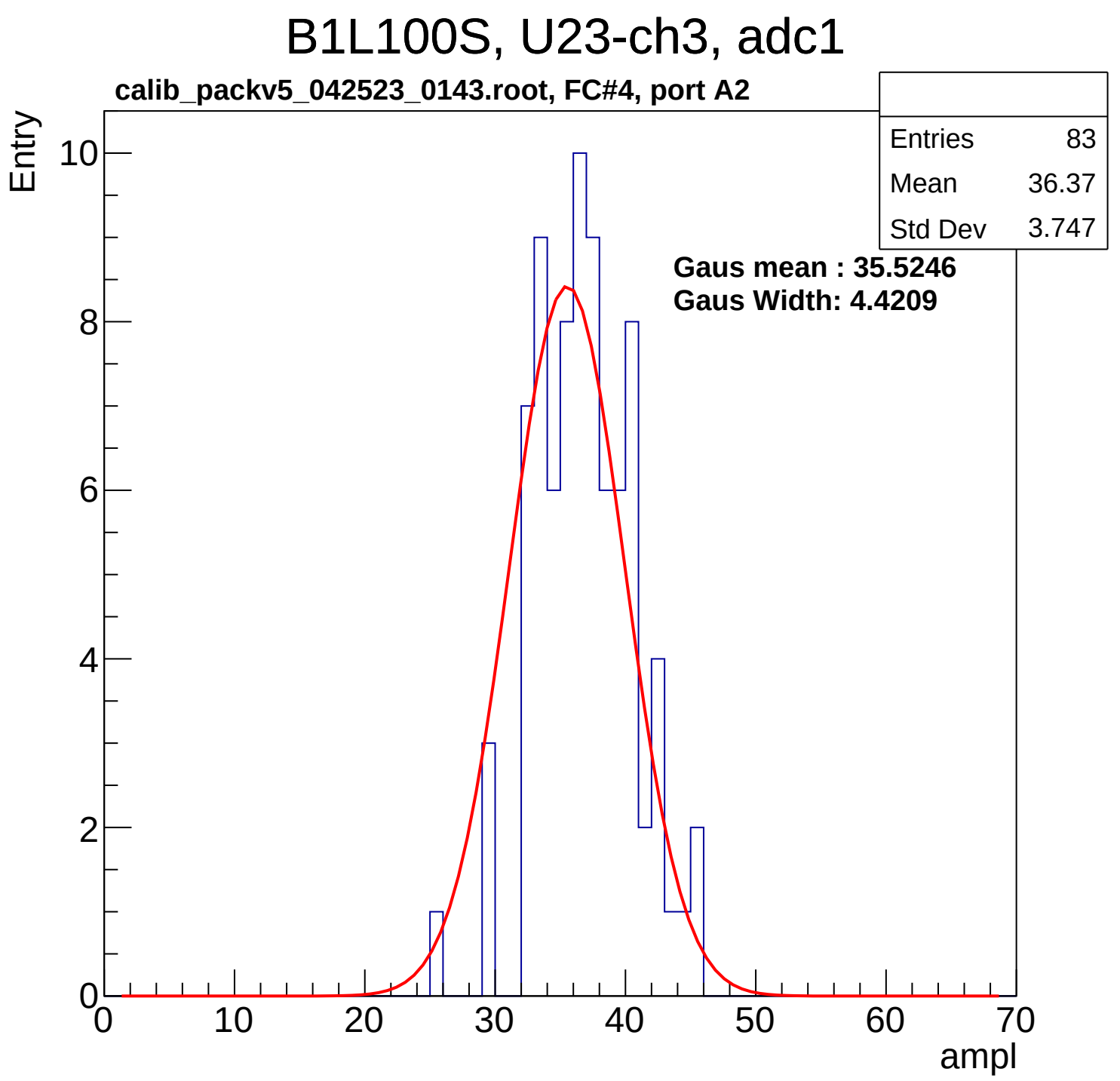
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.37
Std Dev 3.747
Gaus mean : 35.5246
Gaus Width: 4.4209
Entry
ampl
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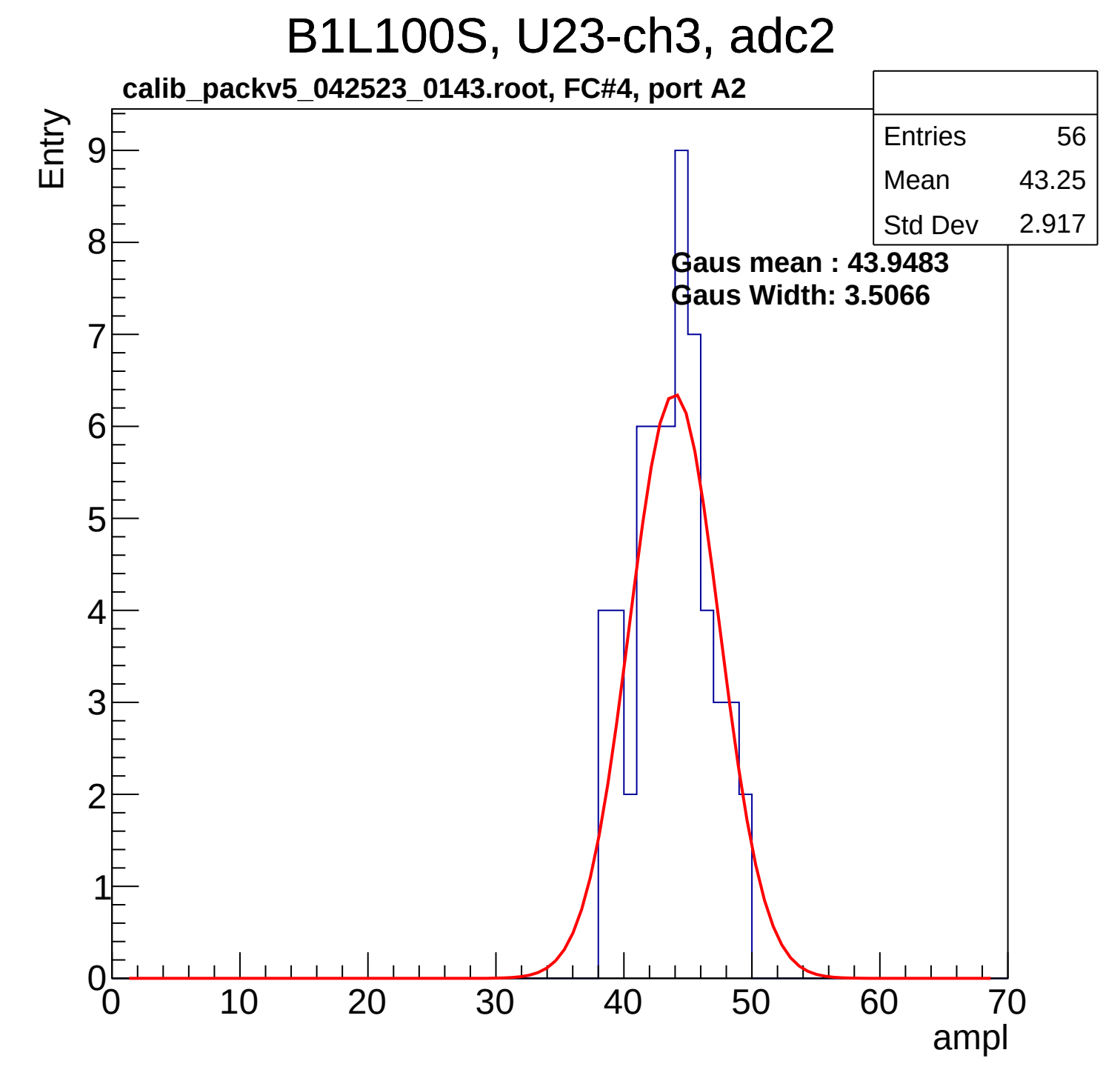
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.25
Std Dev 2.917
Gaus mean : 43.9483
Gaus Width: 3.5066
Entry
ampl
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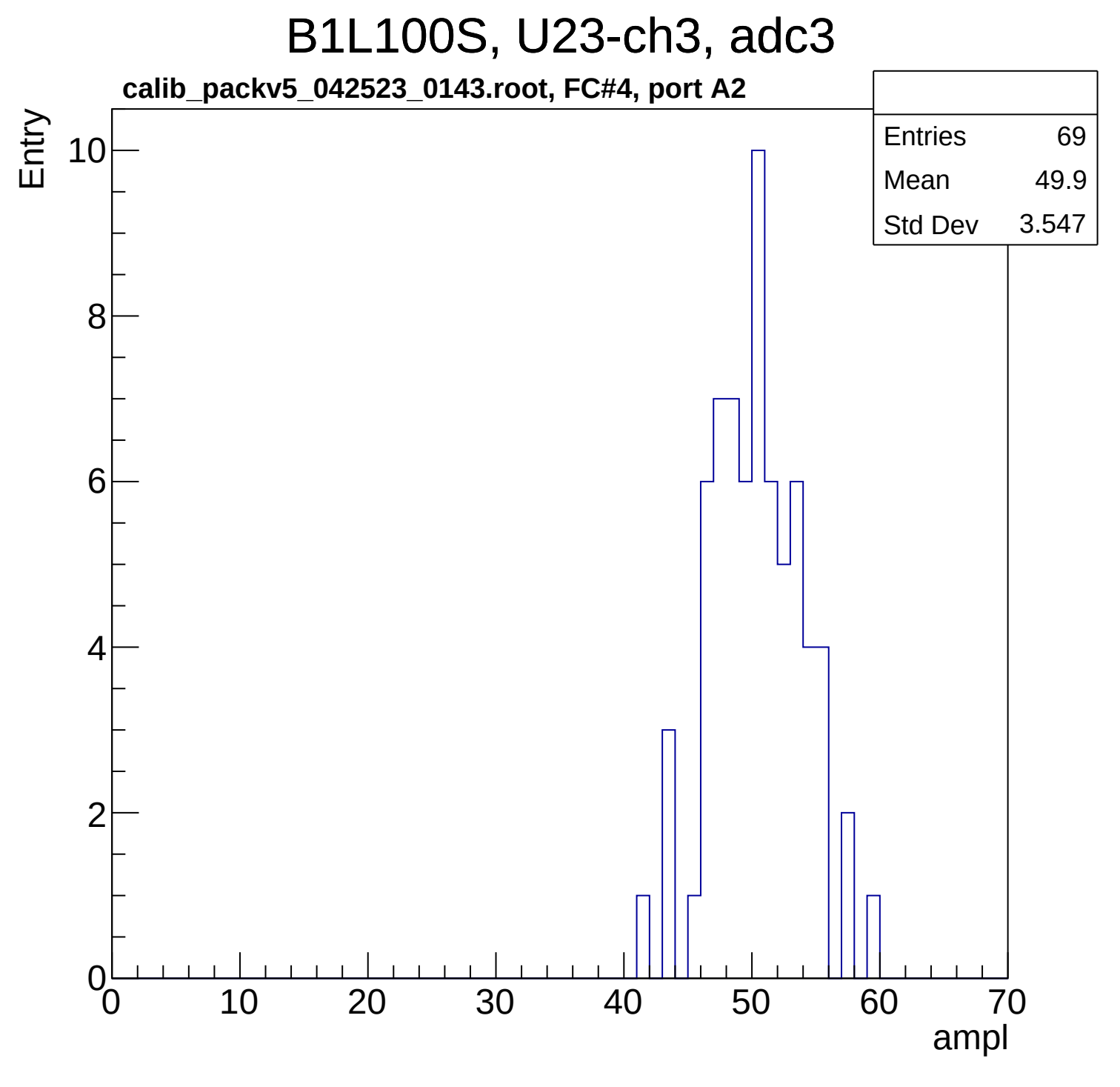
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.9
Std Dev 3.547
Entry
ampl
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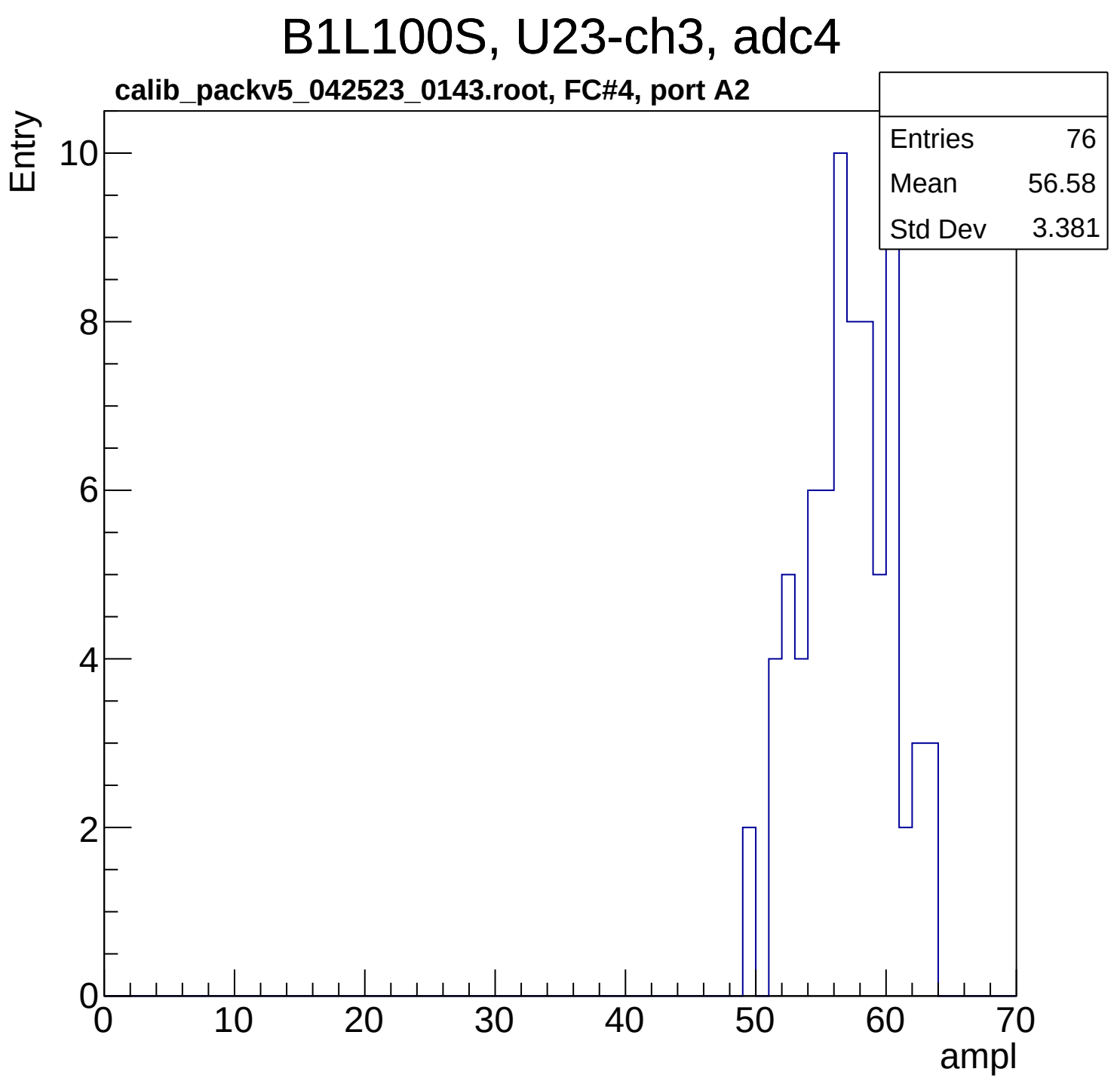
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 56.58
Std Dev 3.381
Entry
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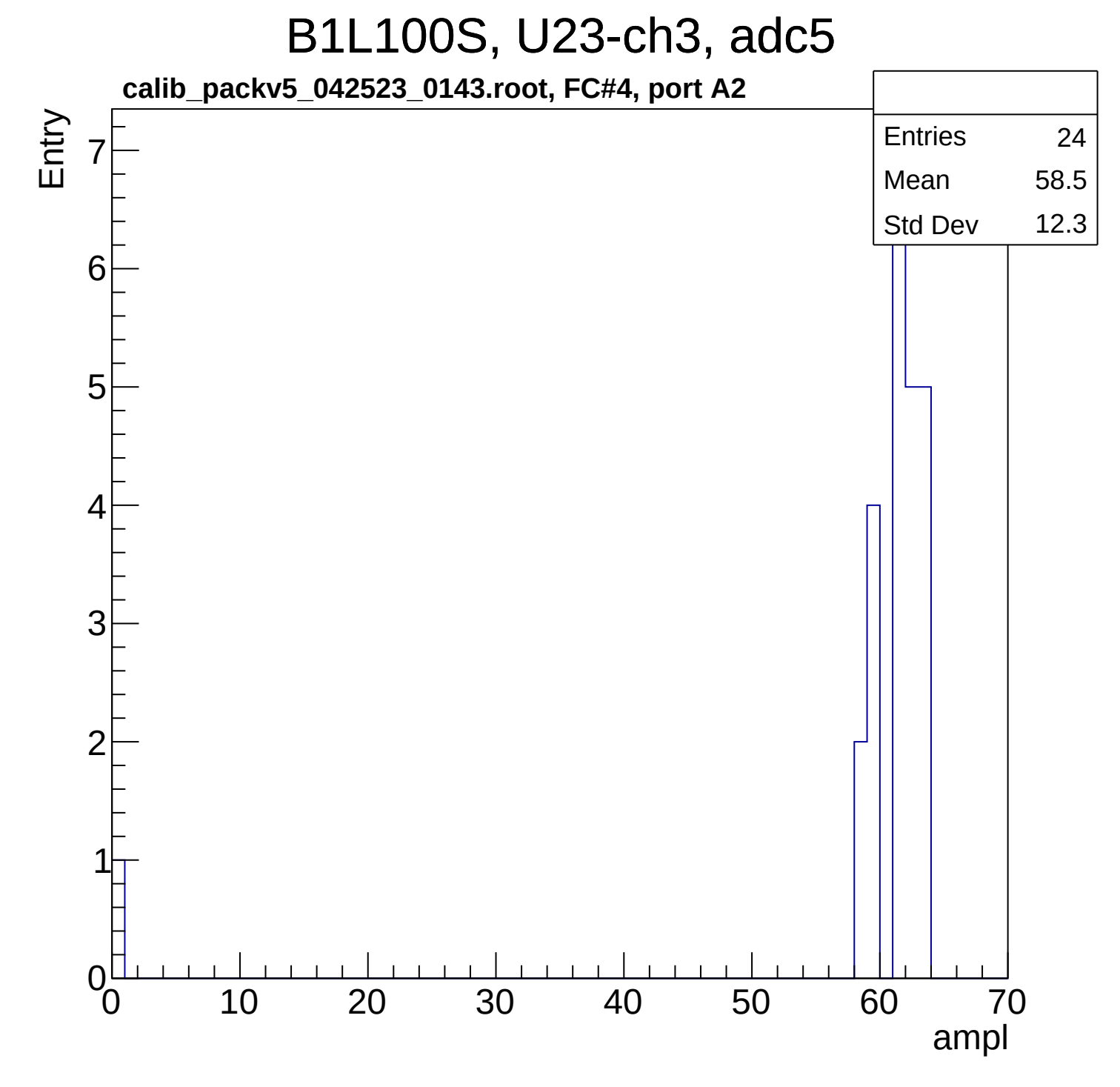
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch3, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.5
Std Dev 12.3
Entry
ampl
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4
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6
7
0
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20
30
40
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60
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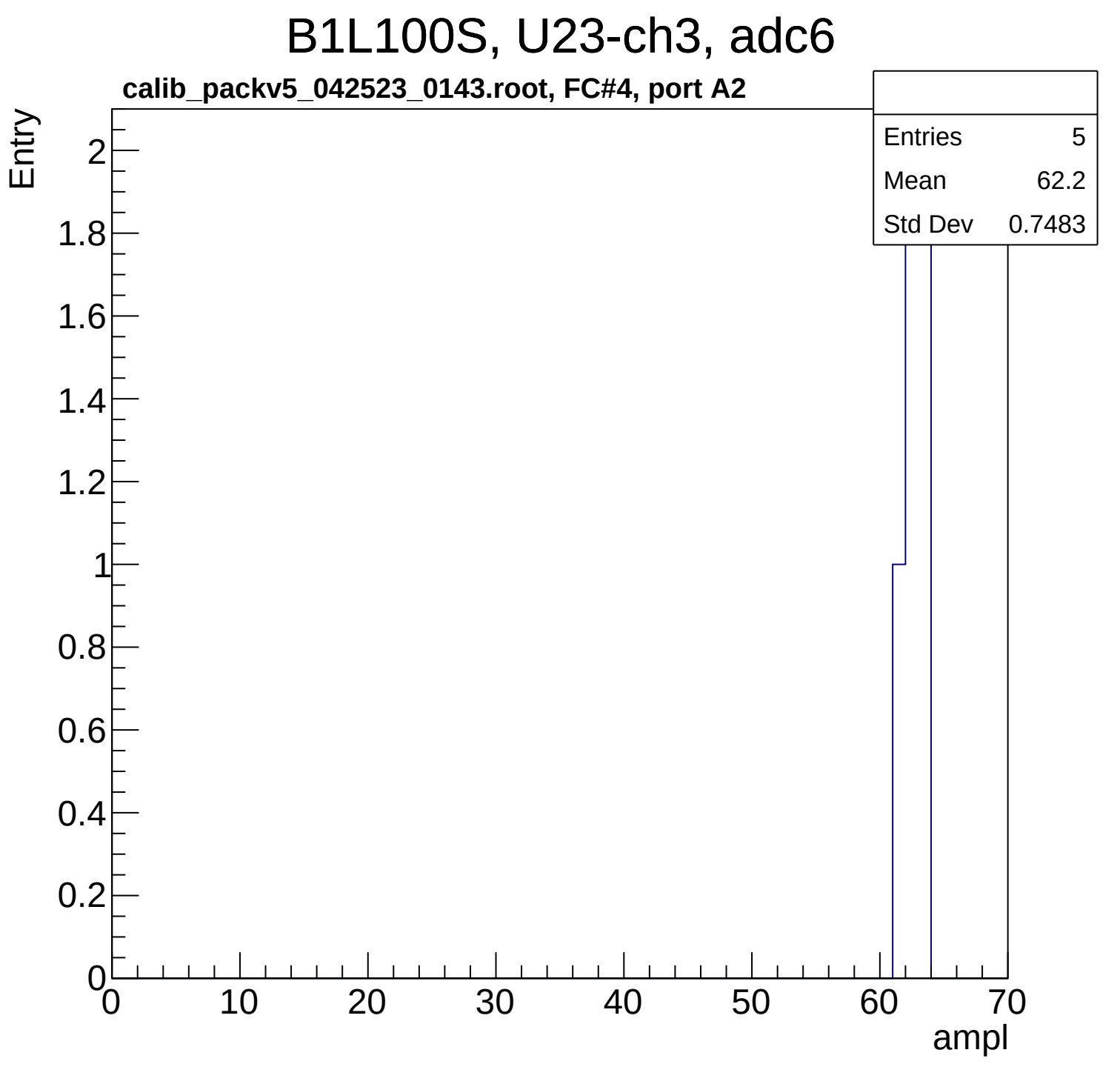
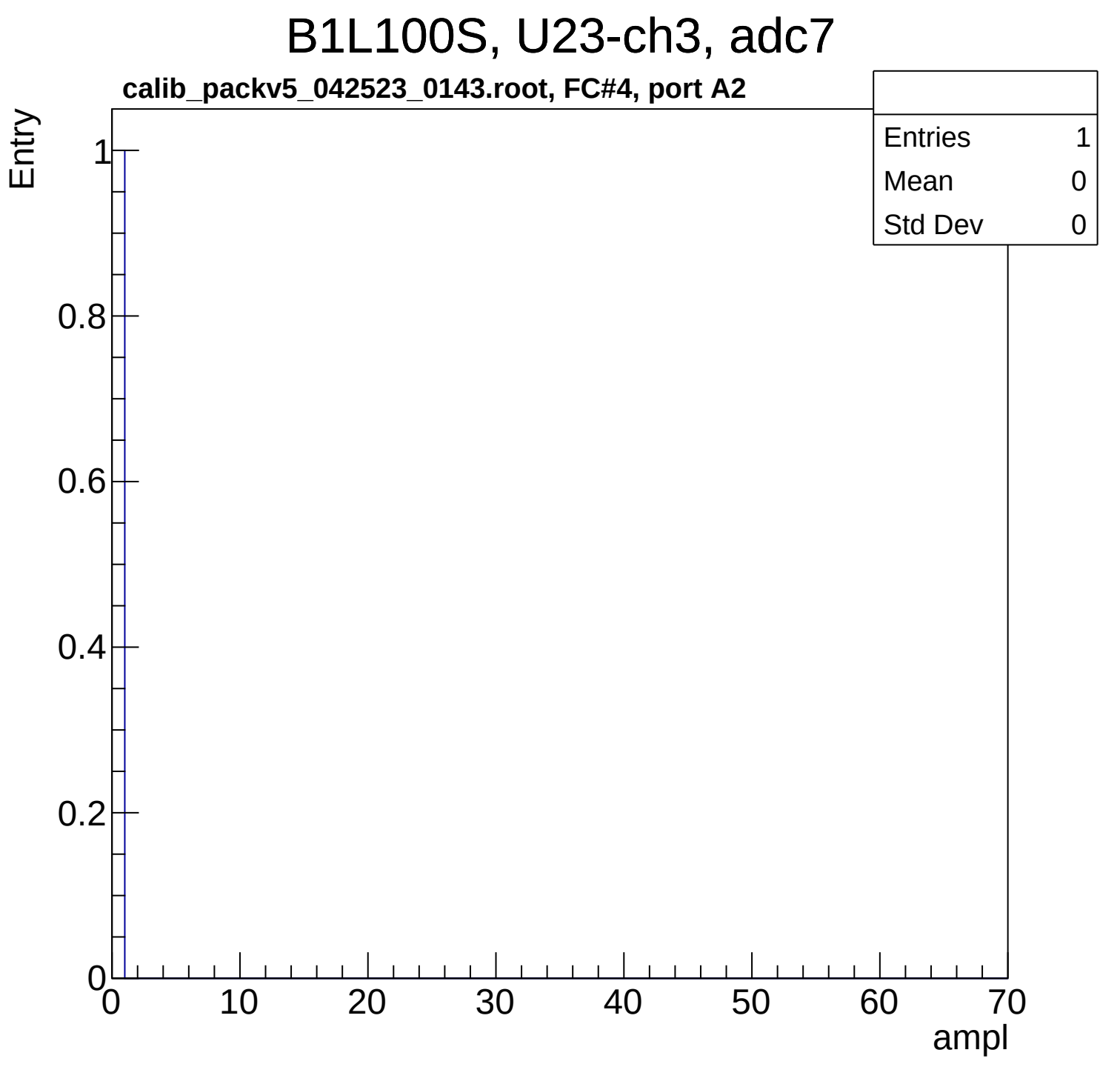

B1L100S, U23-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch3, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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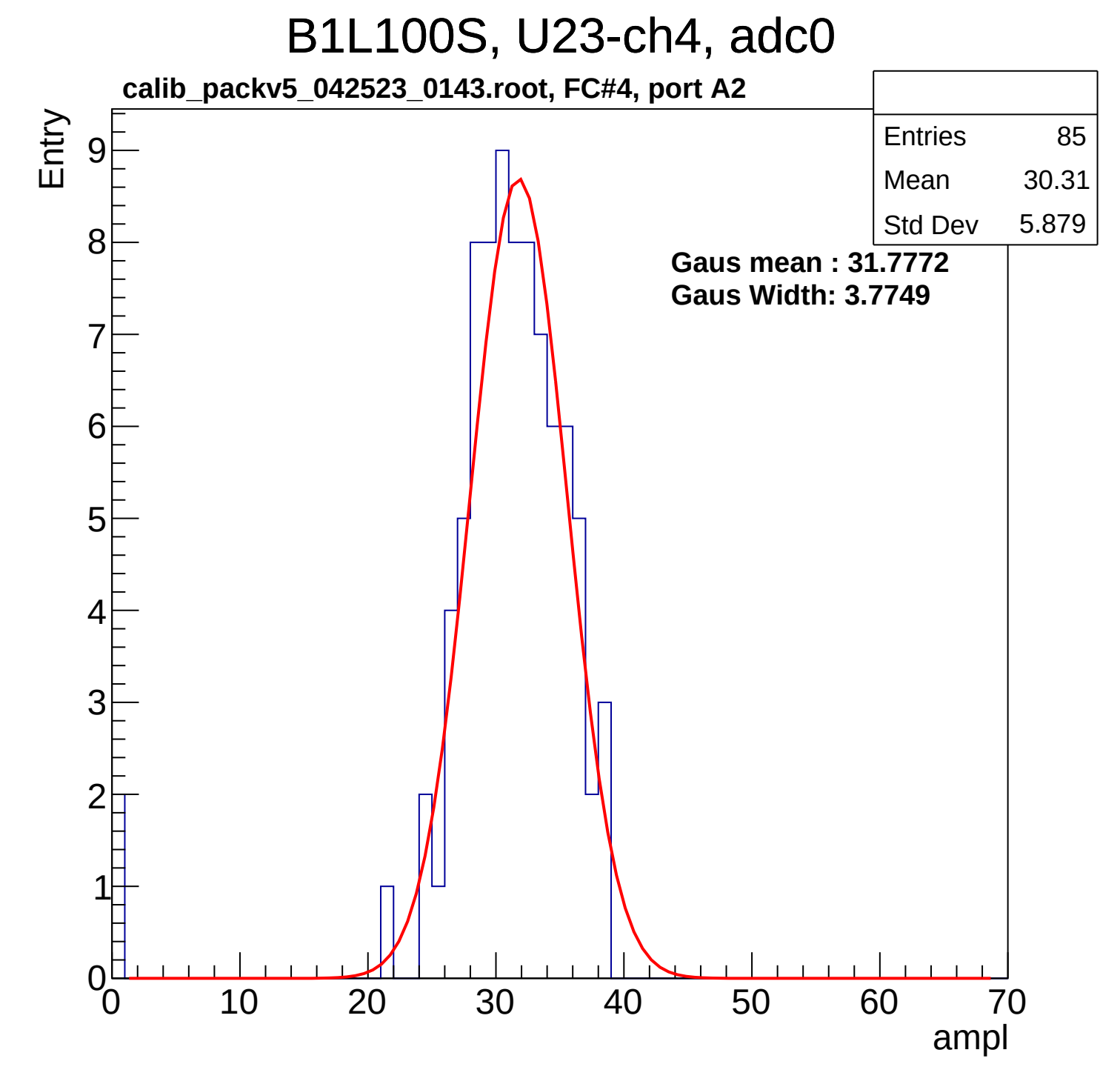
60
70
ampl


B1L100S, U23-ch4, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 30.31
Std Dev 5.879
Gaus mean : 31.7772
Gaus Width: 3.7749
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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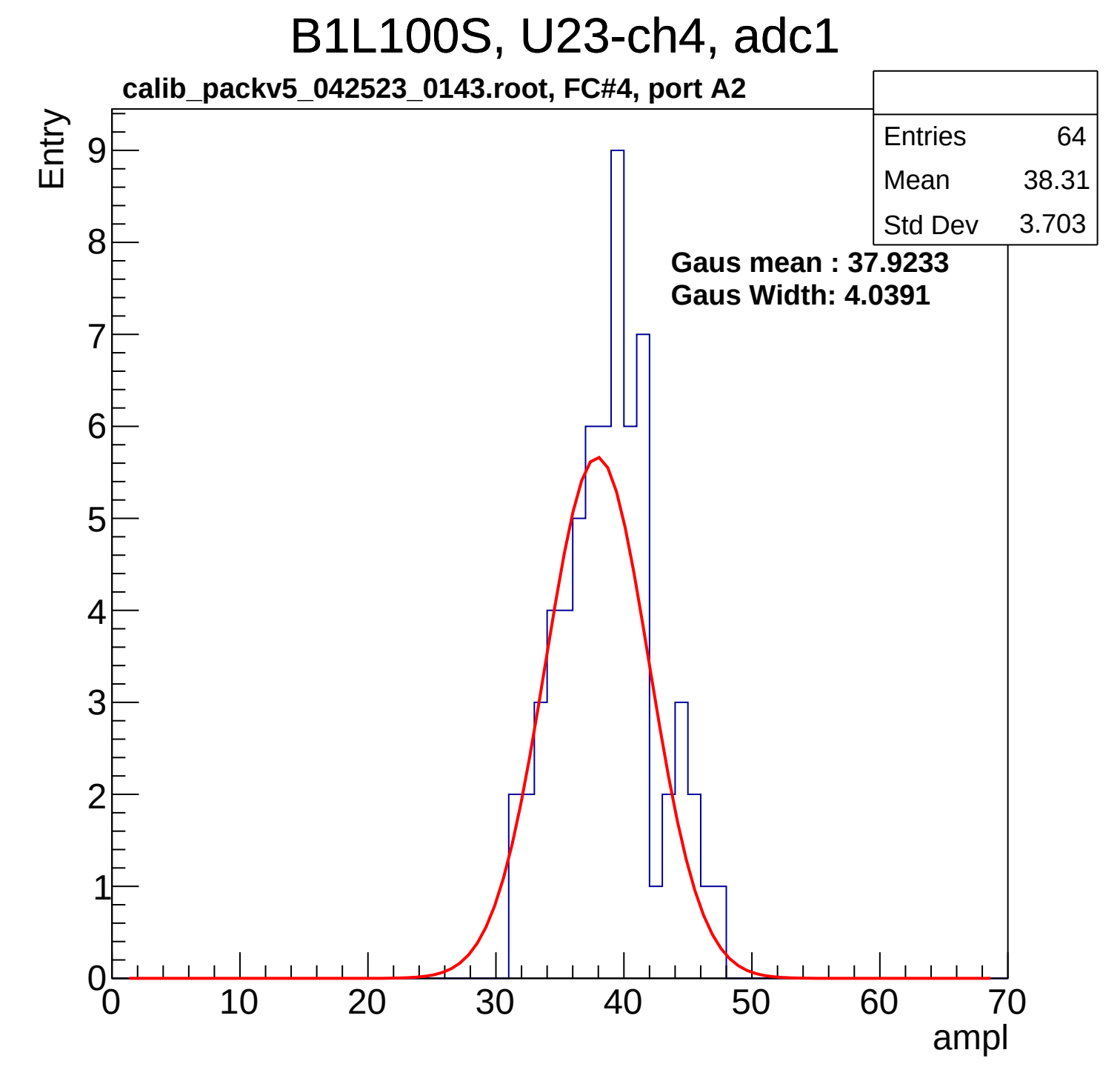
B1L100S, U23-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 38.31
Std Dev 3.703
Gaus mean : 37.9233
Gaus Width: 4.0391
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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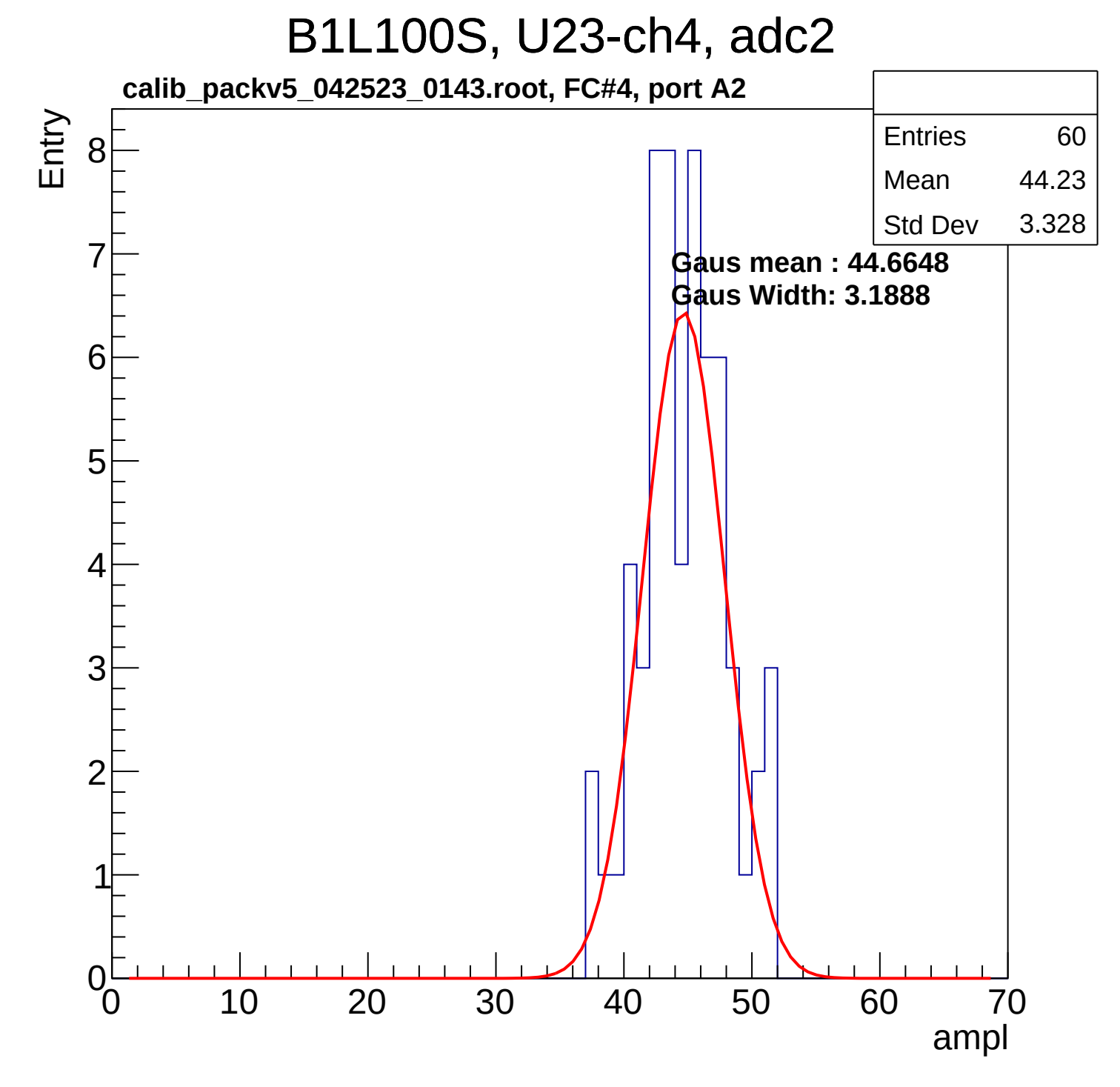
B1L100S, U23-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.23
Std Dev 3.328
Gaus mean : 44.6648
Gaus Width: 3.1888
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
50
60
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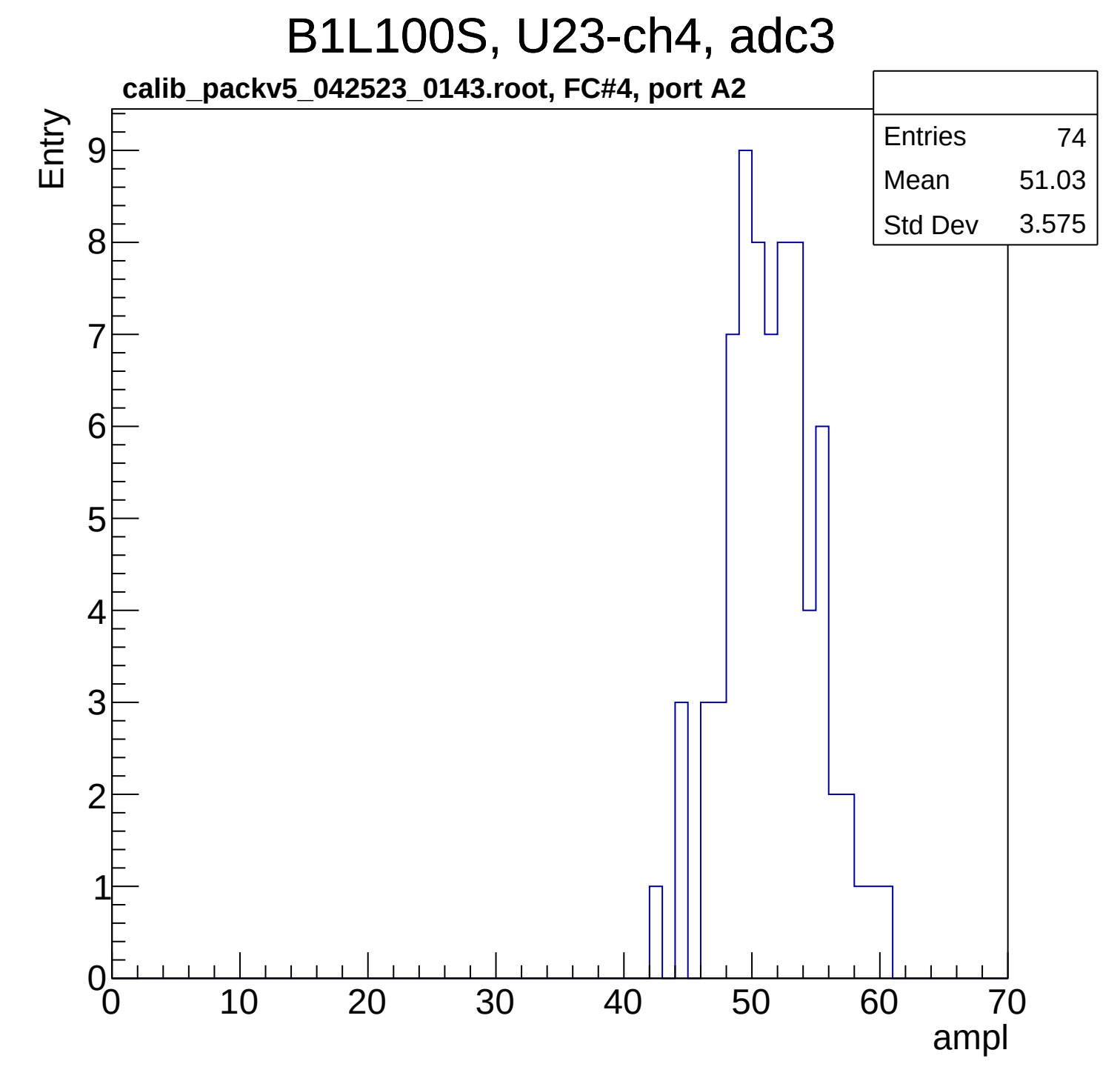
B1L100S, U23-ch4, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.03
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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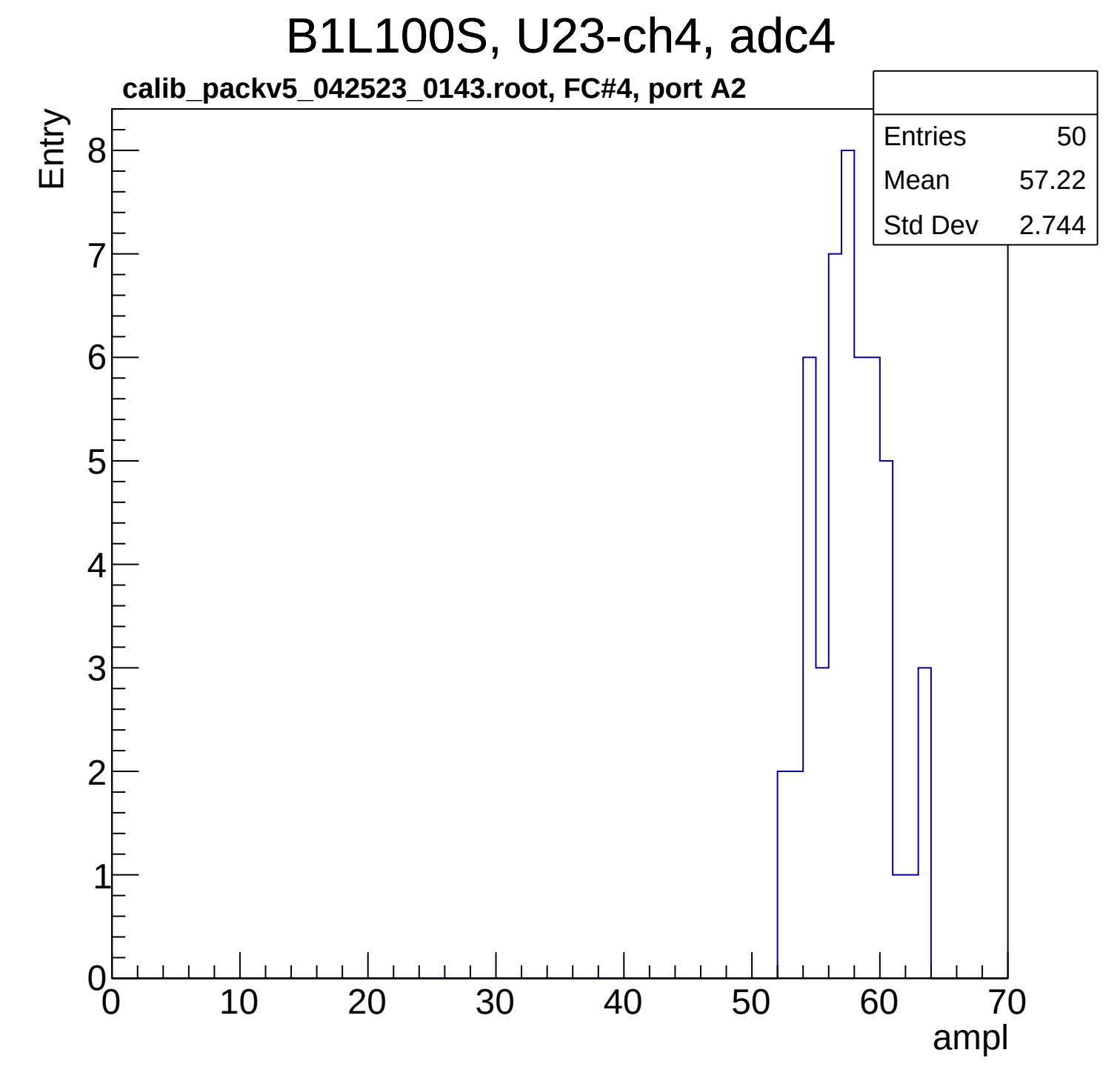
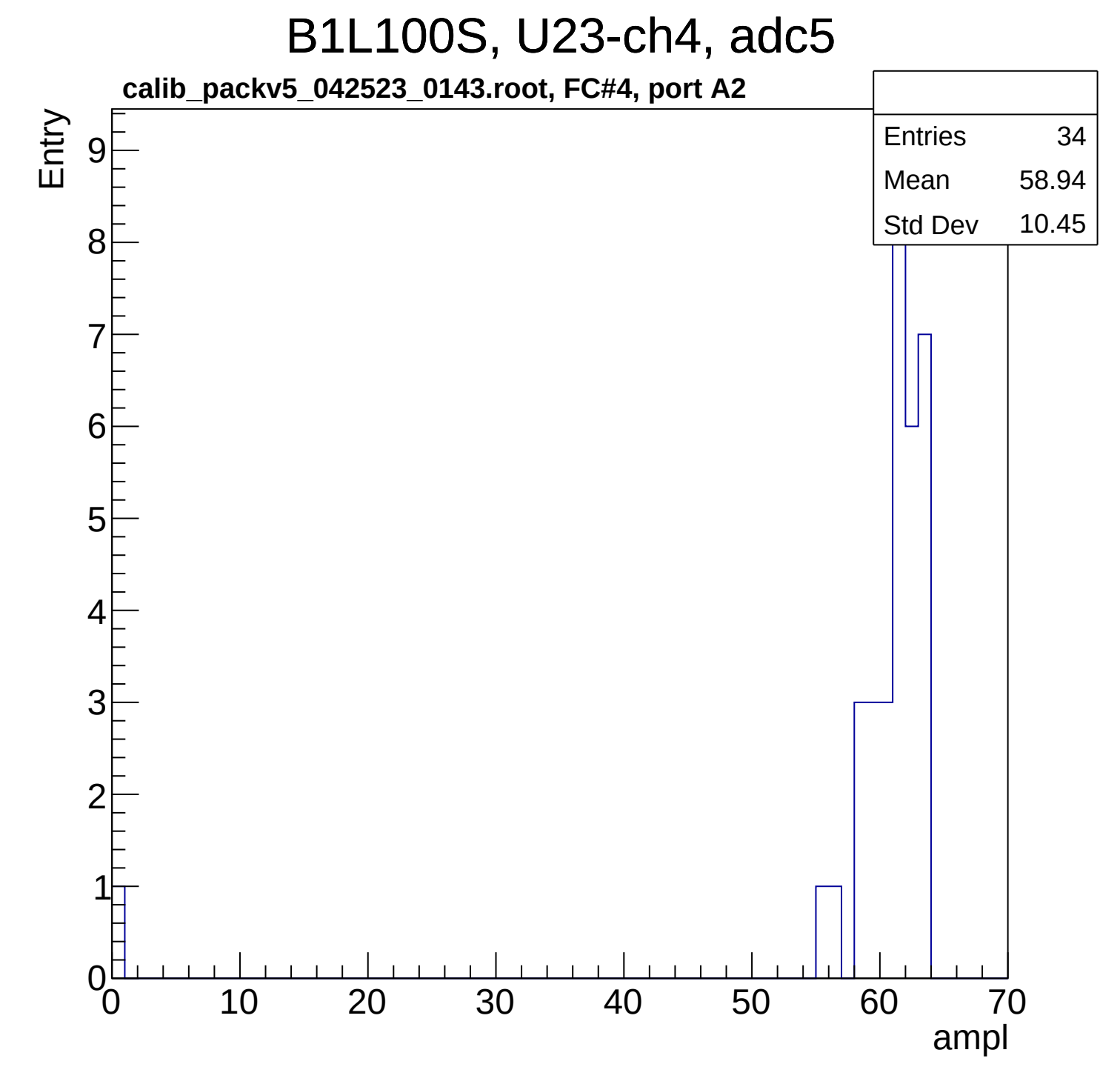

B1L100S, U23-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.22
Std Dev 2.744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.94
Std Dev 10.45
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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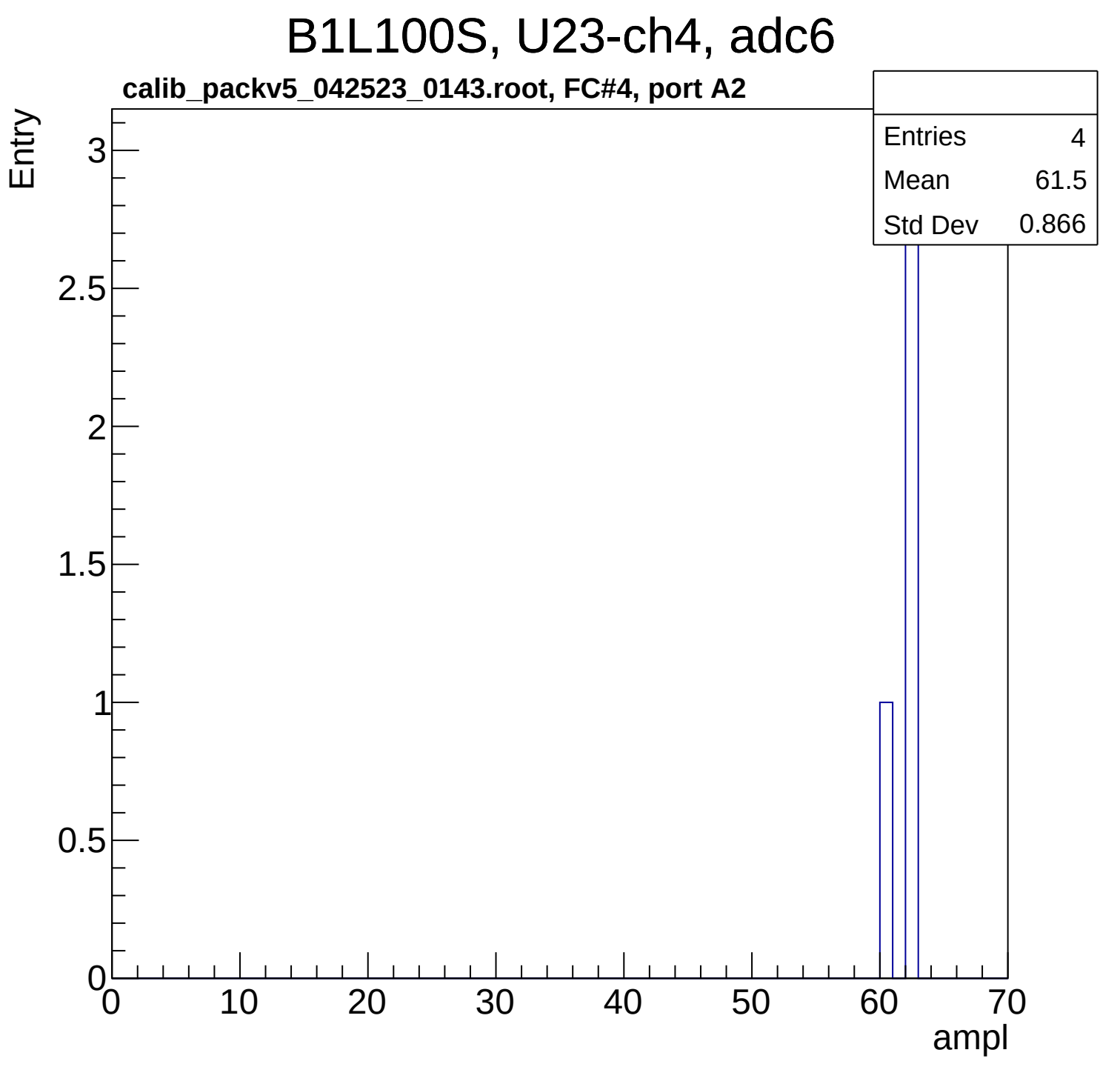
B1L100S, U23-ch4, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.5
Std Dev 0.866
Entry
ampl
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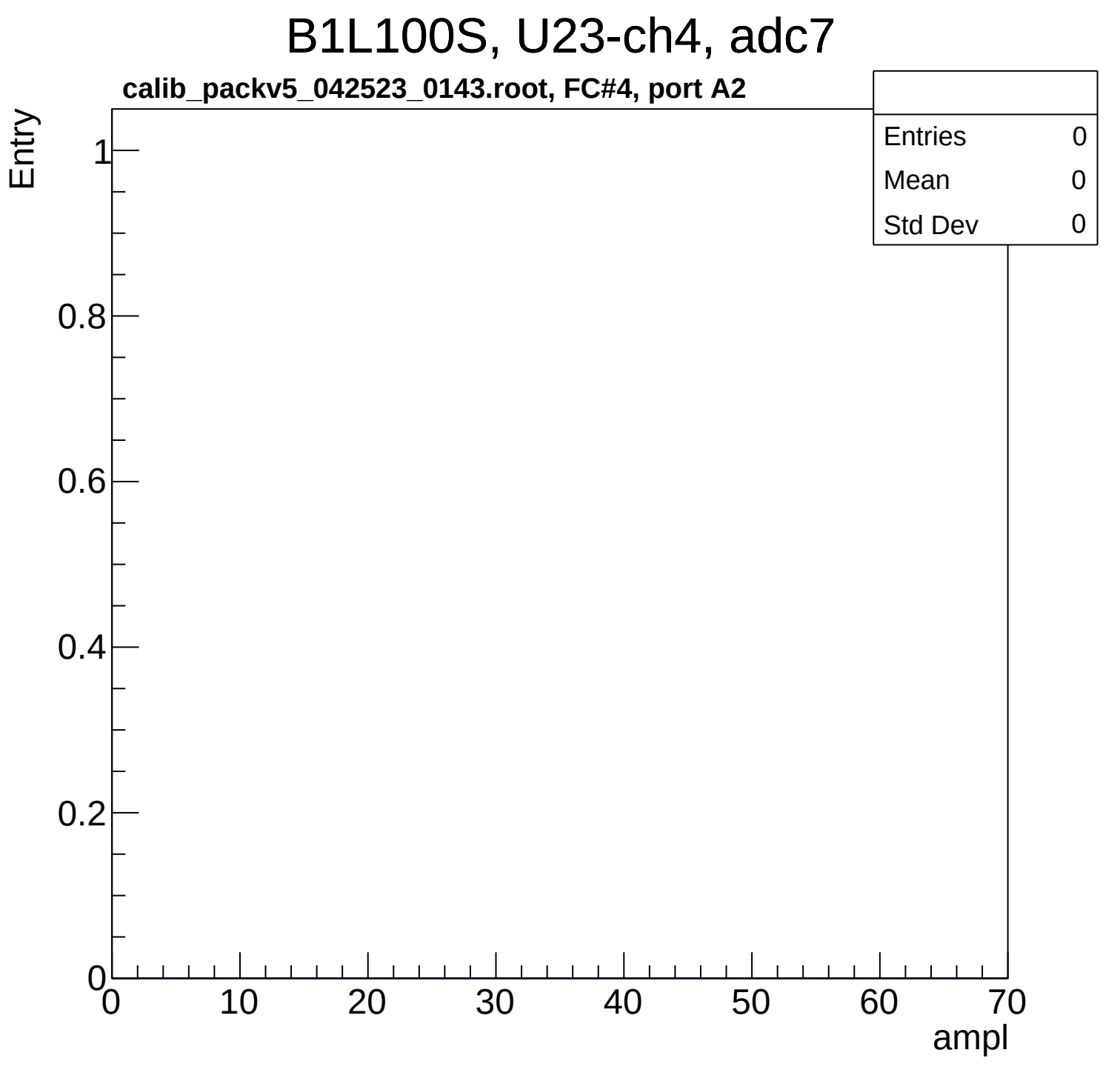
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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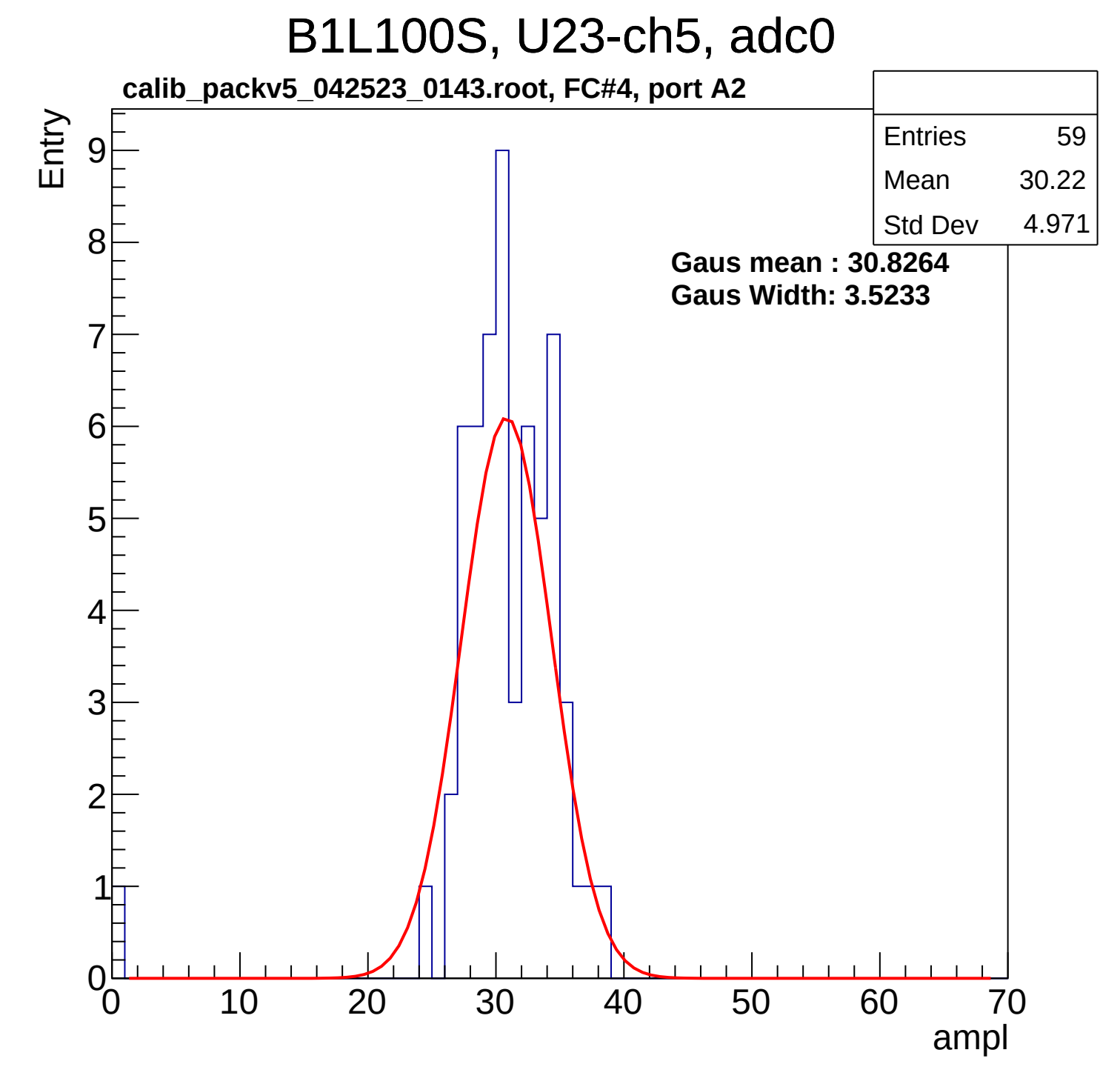
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 30.22
Std Dev 4.971
Gaus mean : 30.8264
Gaus Width: 3.5233
Entry
ampl
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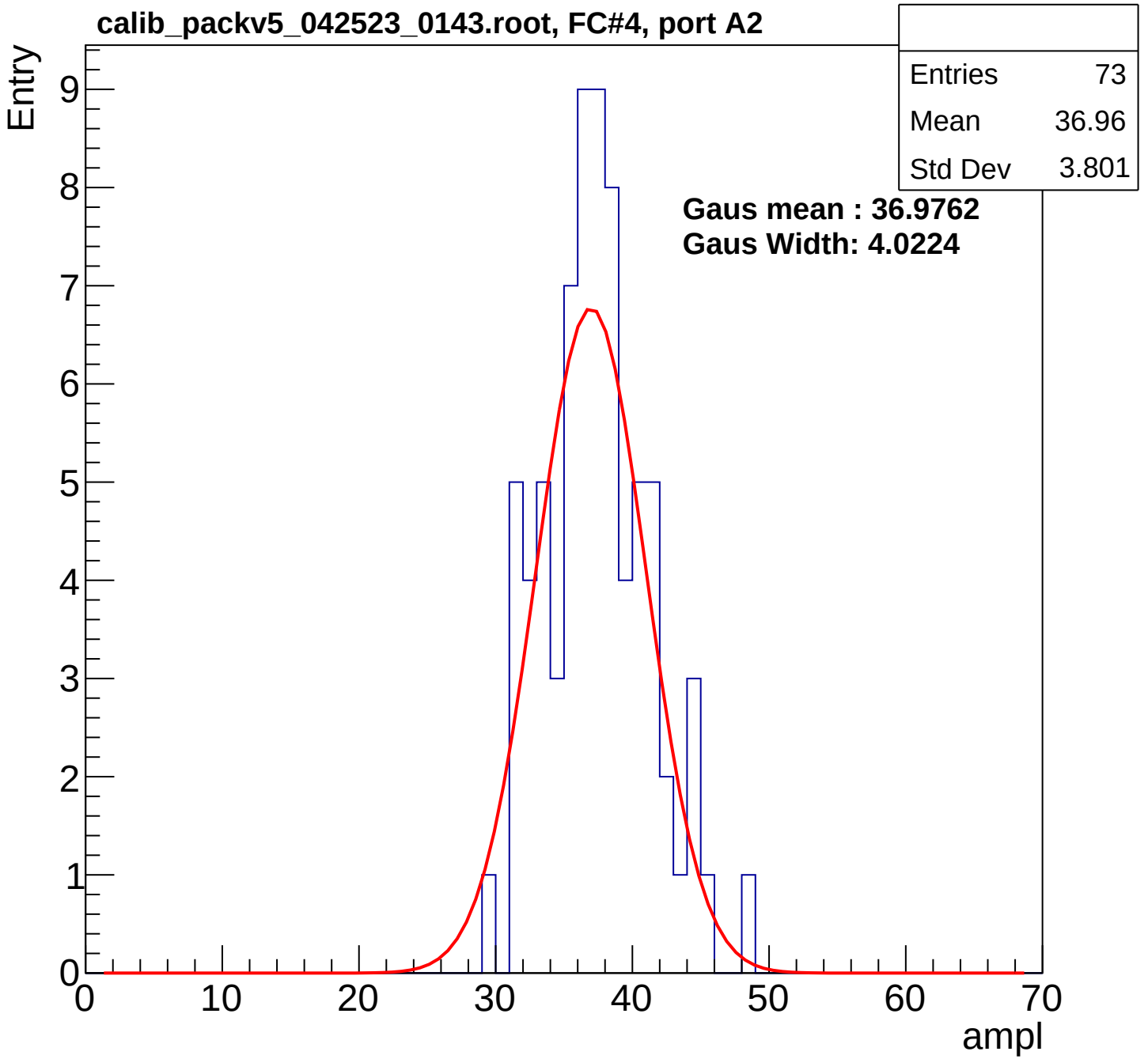
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.96
Std Dev 3.801
Gaus mean : 36.9762
Gaus Width: 4.0224
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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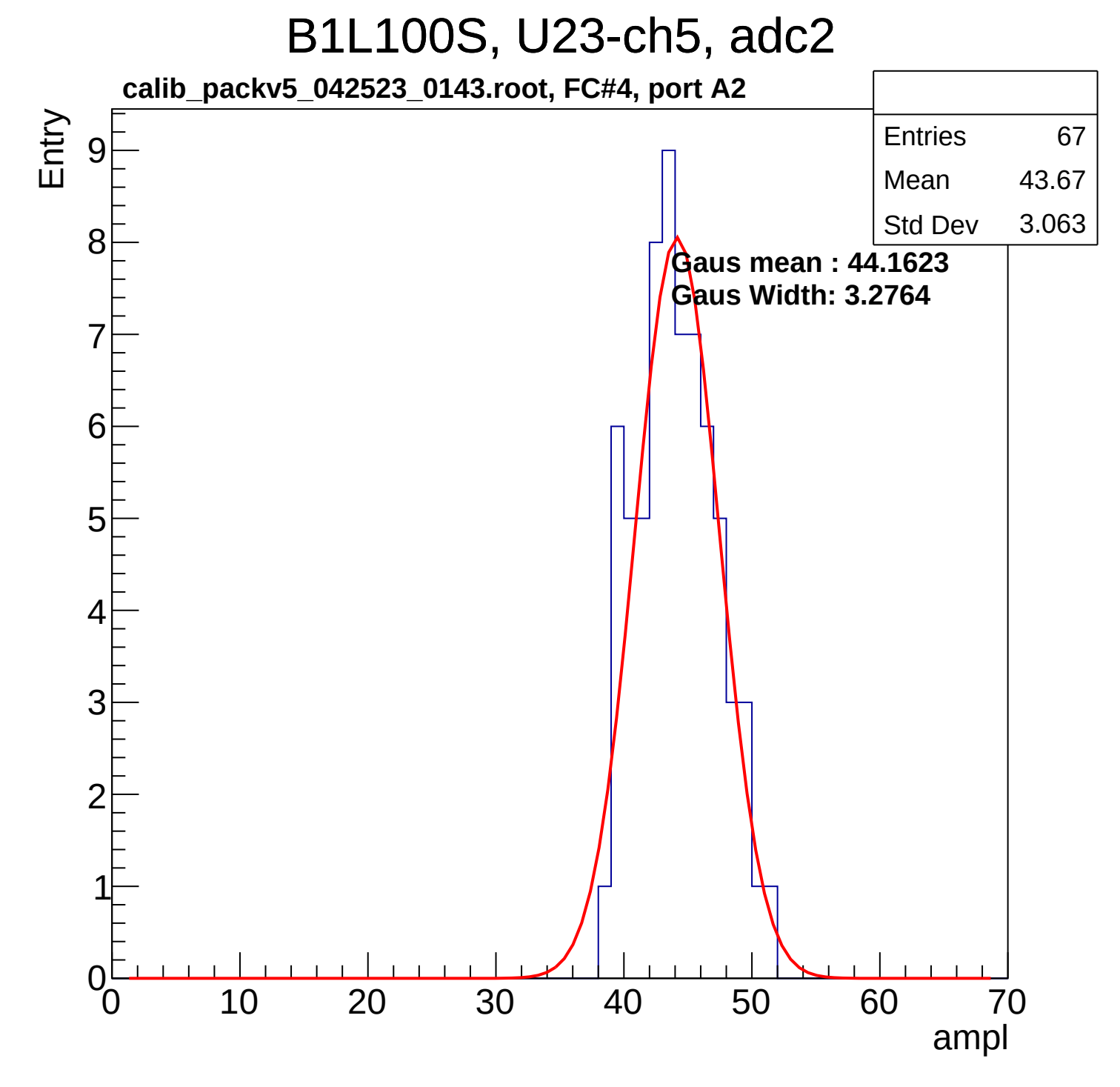
B1L100S, U23-ch5, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43.67
Std Dev 3.063
Gaus mean : 44.1623
Gaus Width: 3.2764
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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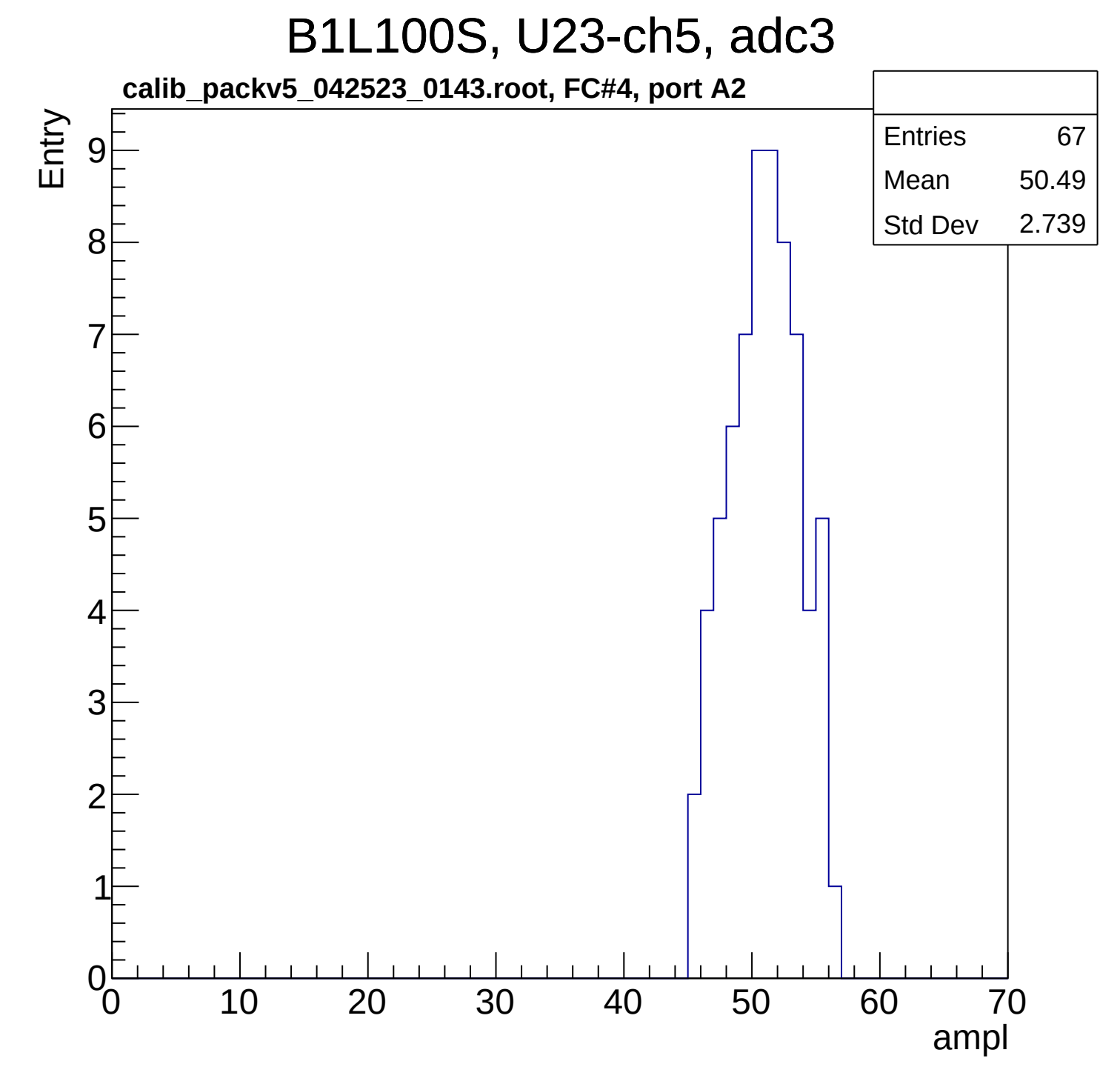
B1L100S, U23-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.49
Std Dev 2.739
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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40
50
60
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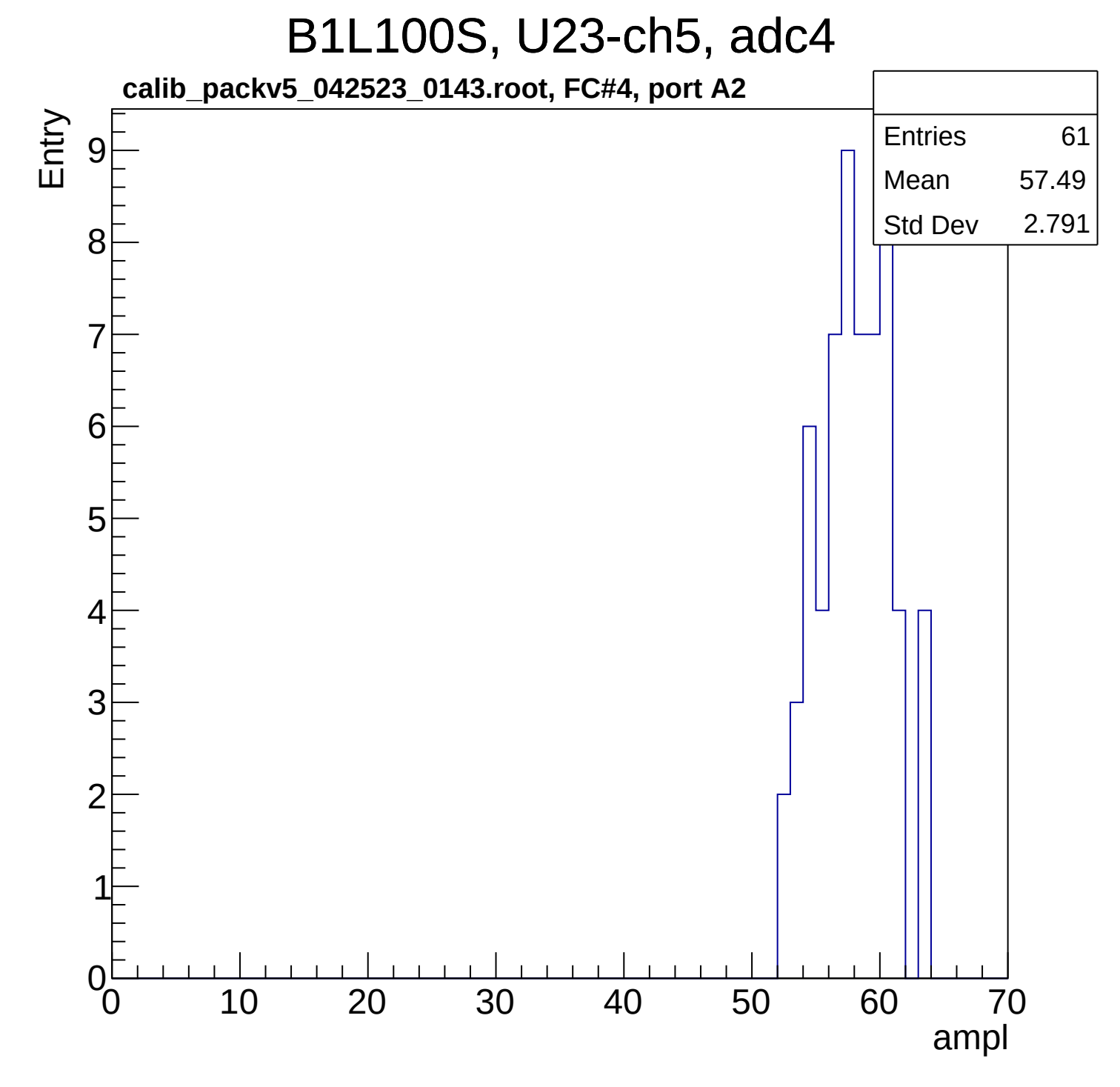
B1L100S, U23-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.49
Std Dev 2.791
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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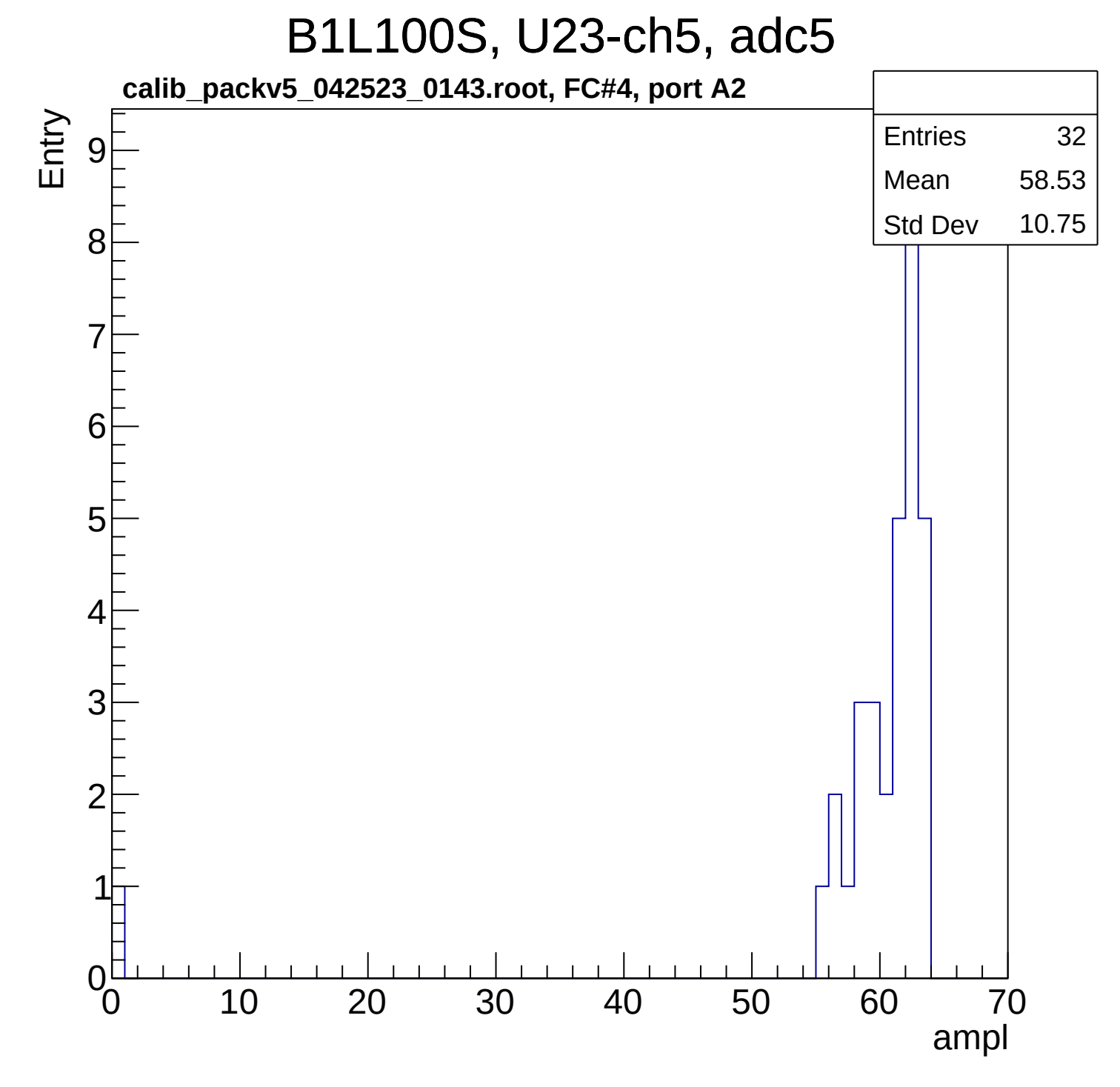
B1L100S, U23-ch5, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.53
Std Dev 10.75
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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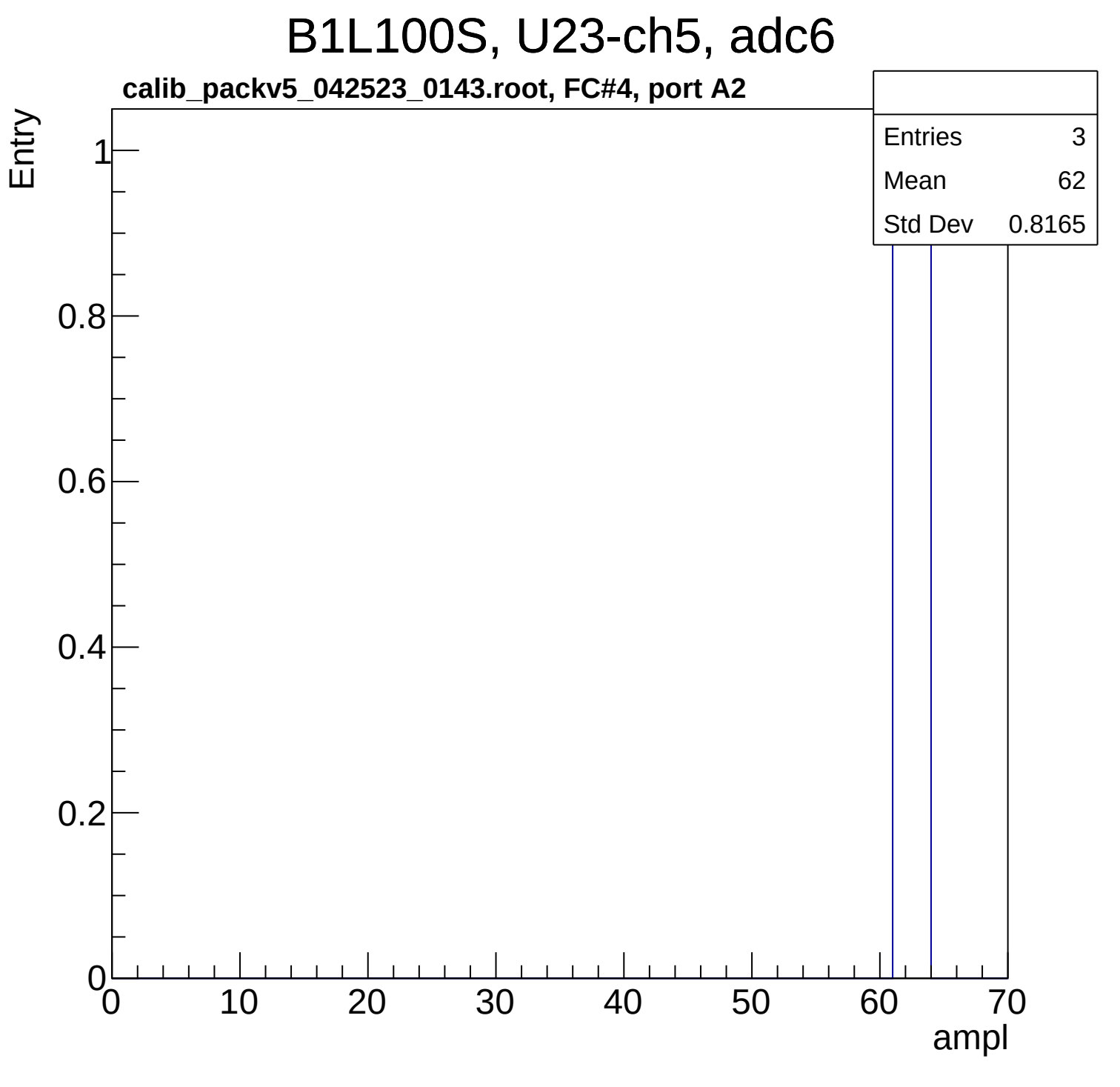
B1L100S, U23-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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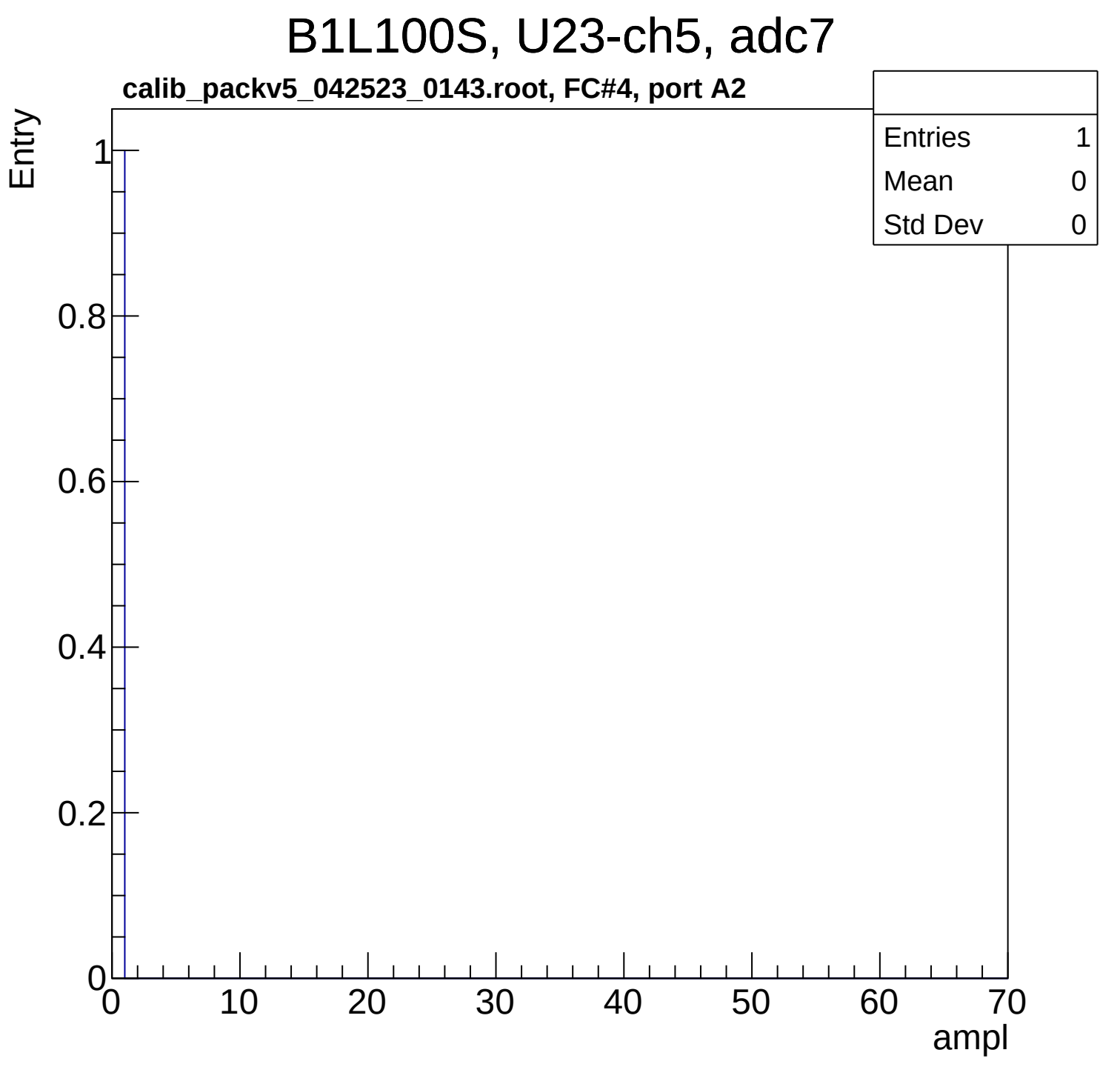
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch5, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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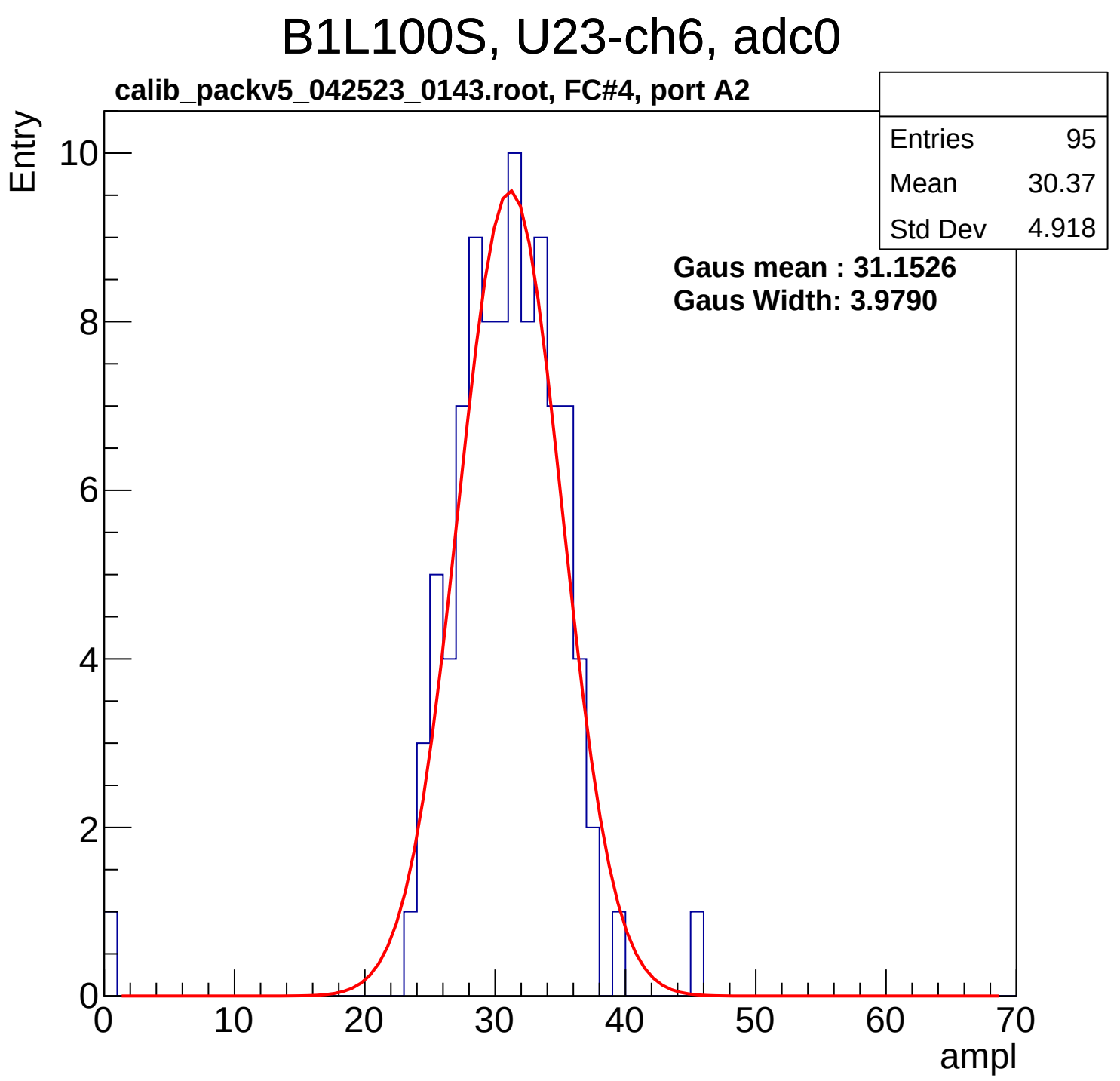
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 30.37
Std Dev 4.918
Gaus mean : 31.1526
Gaus Width: 3.9790
Entry
ampl
0
2
4
6
8
10
0
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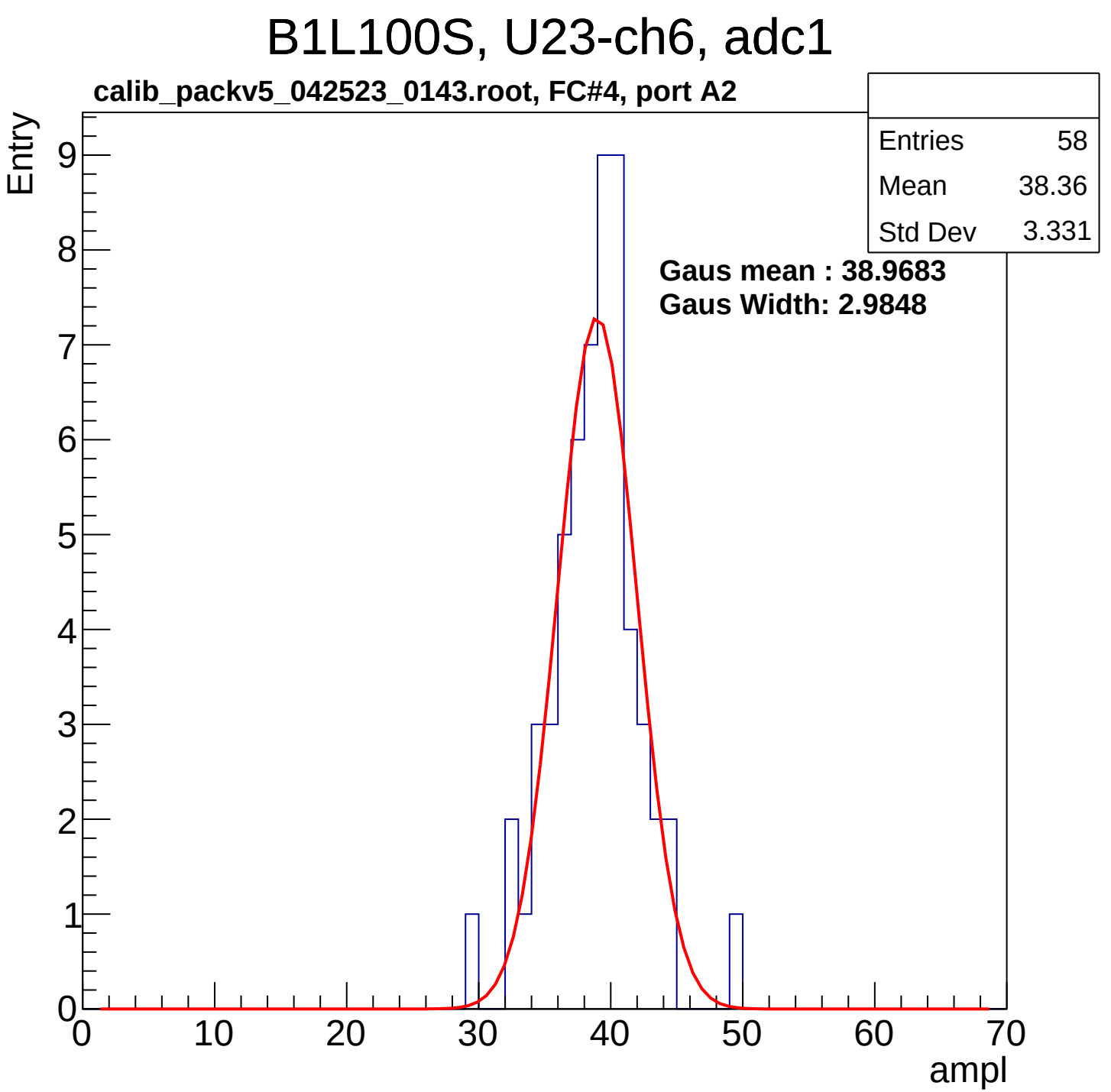
20
30
40
50
60
70


B1L100S, U23-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 38.36
Std Dev 3.331
Gaus mean : 38.9683
Gaus Width: 2.9848
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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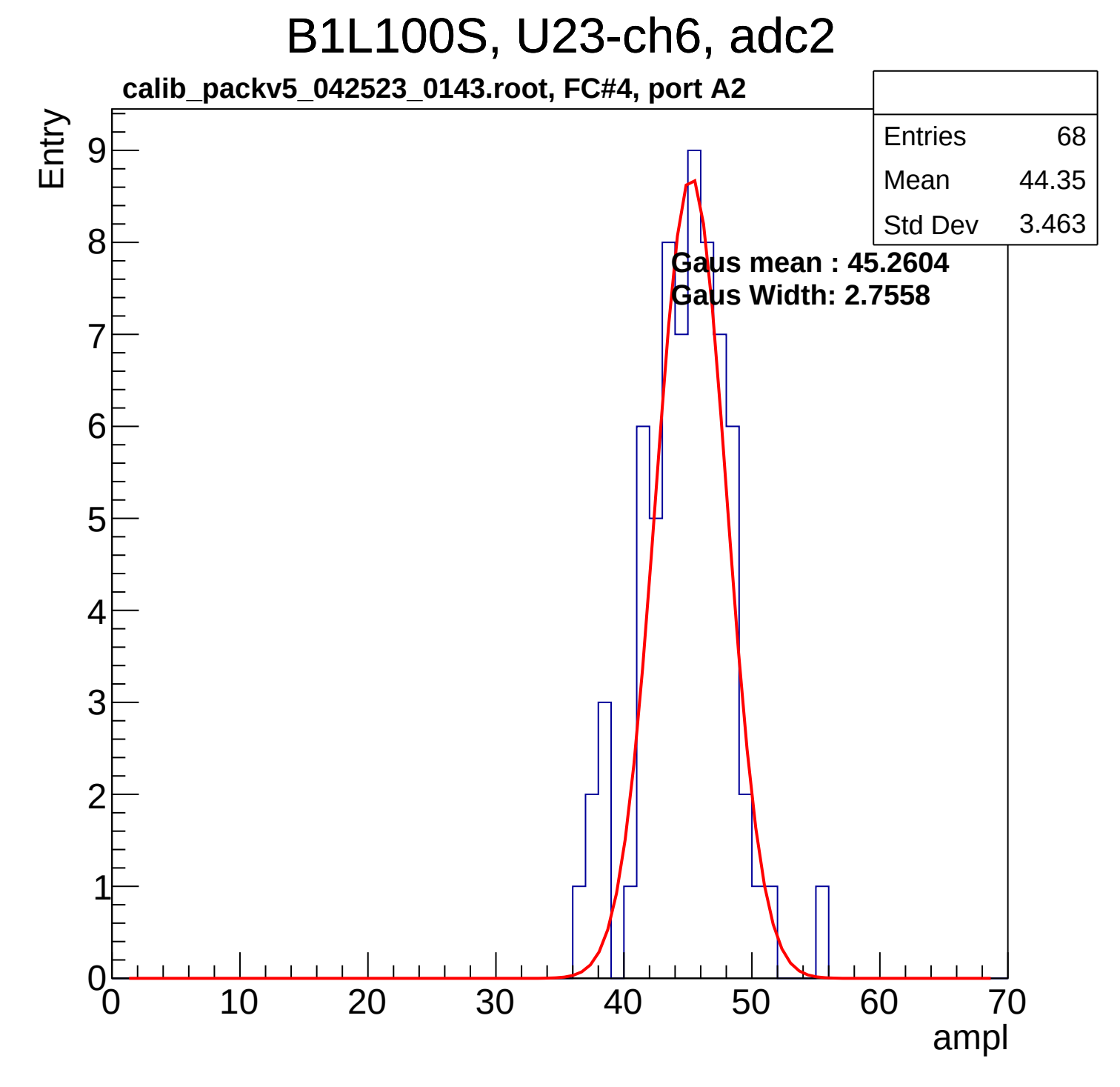
60
70


B1L100S, U23-ch6, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 44.35
Std Dev 3.463
Gaus mean : 45.2604
Gaus Width: 2.7558
Entry
ampl
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2
3
4
5
6
7
8
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0
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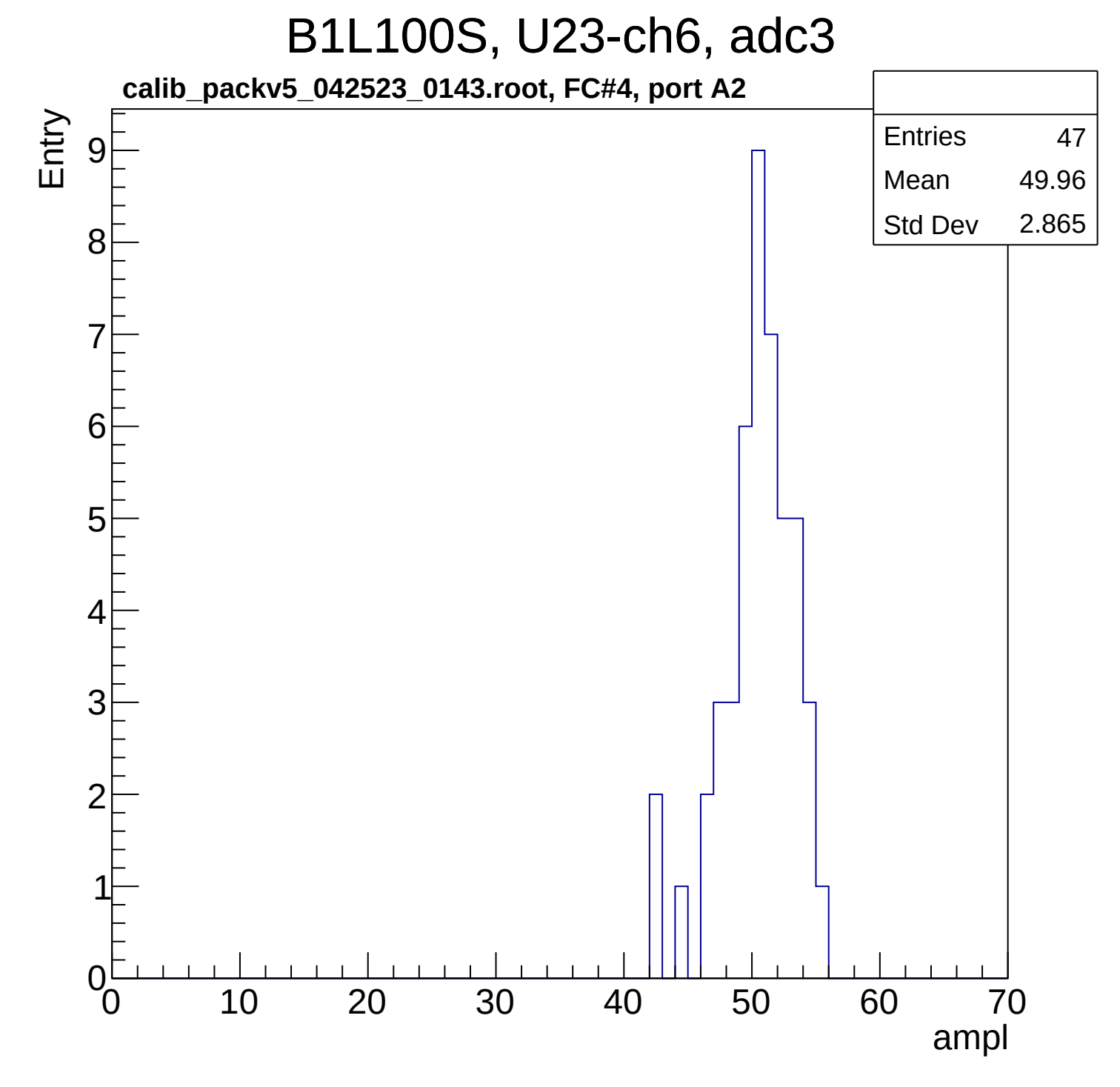
B1L100S, U23-ch6, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 49.96
Std Dev 2.865
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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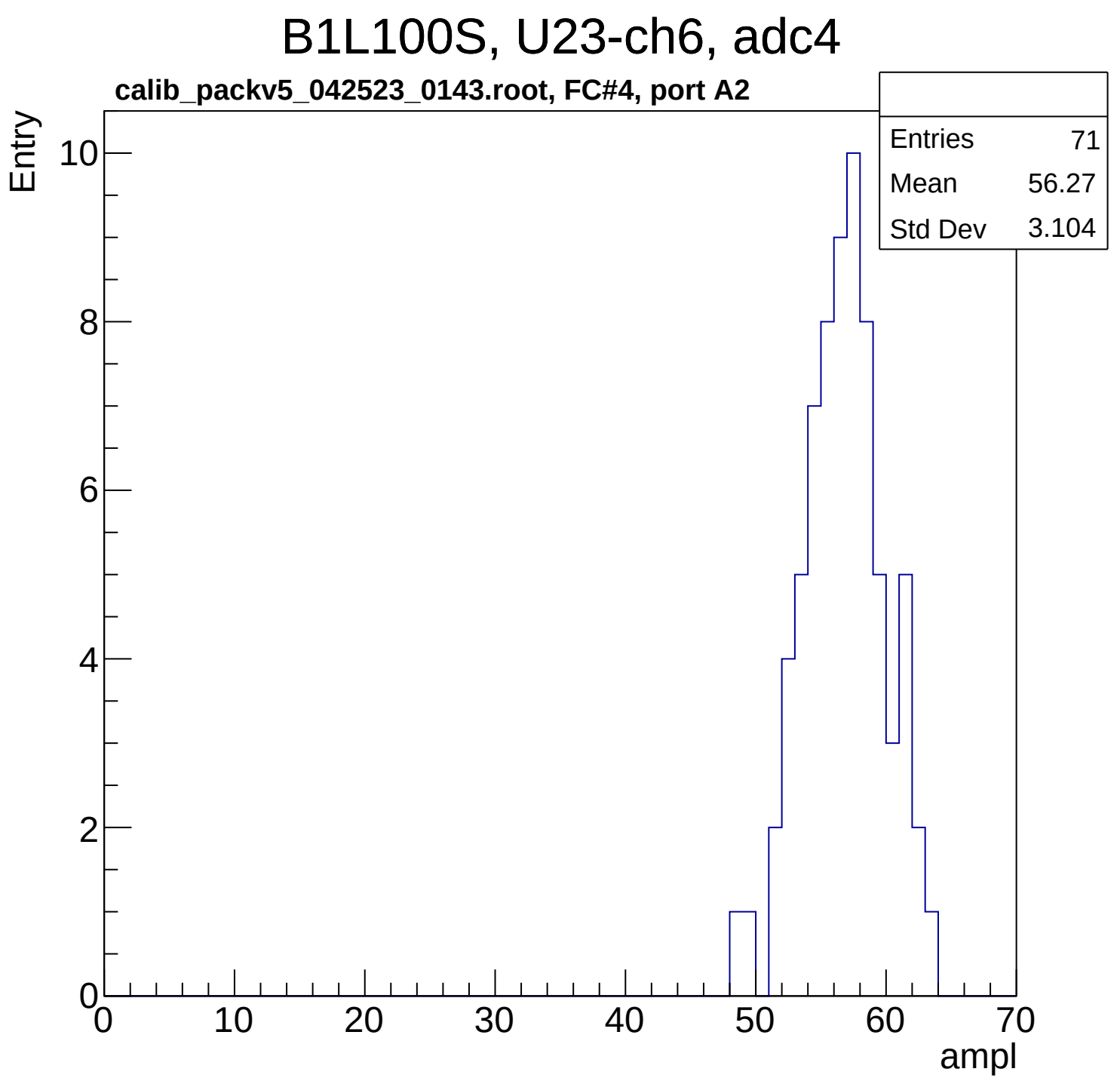
40
50
60
70


B1L100S, U23-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 56.27
Std Dev 3.104
Entry
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4
6
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10
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10
20
30
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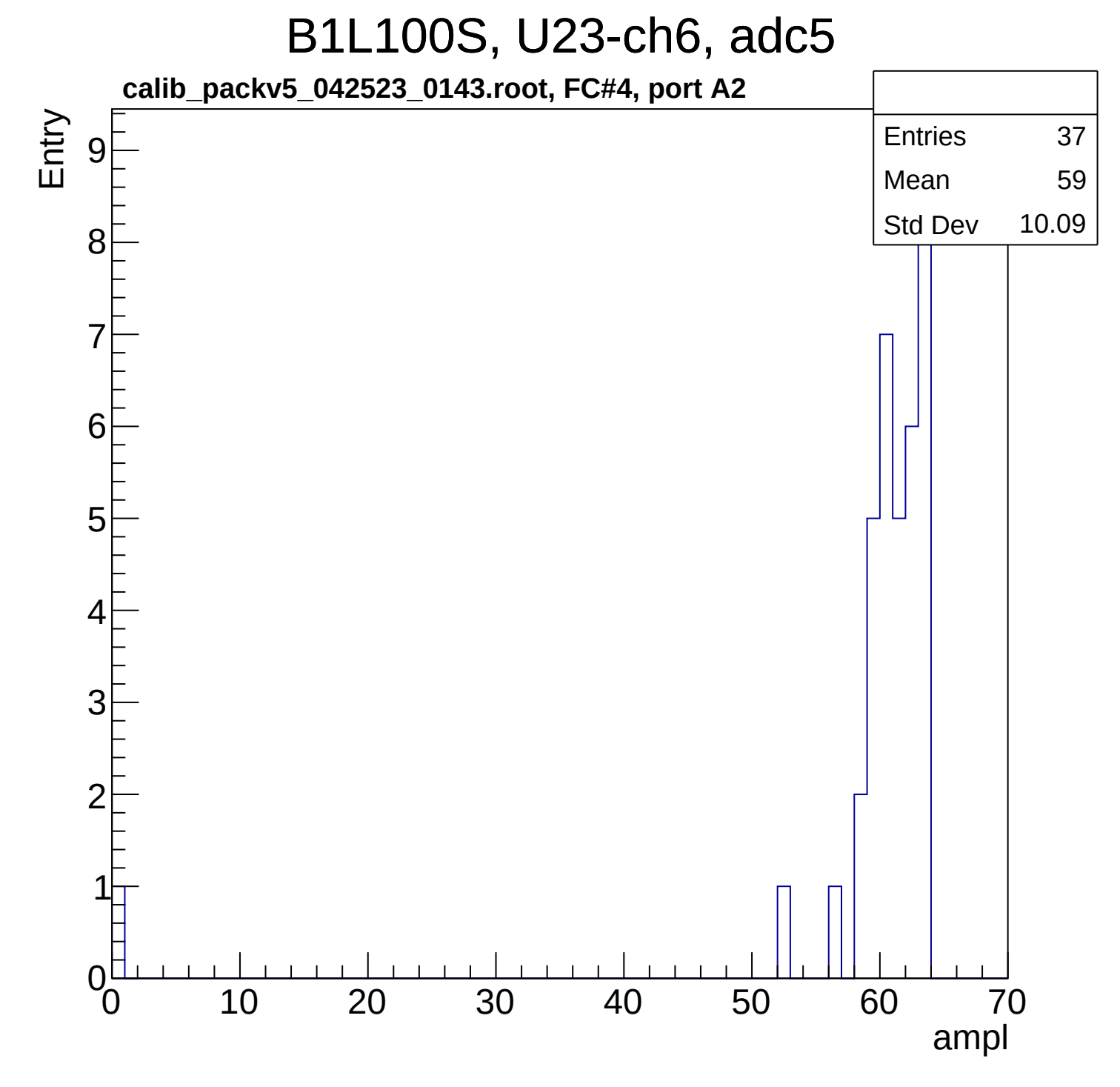
50
60
70
ampl


B1L100S, U23-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59
Std Dev 10.09
Entry
ampl
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2
3
4
5
6
7
8
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0
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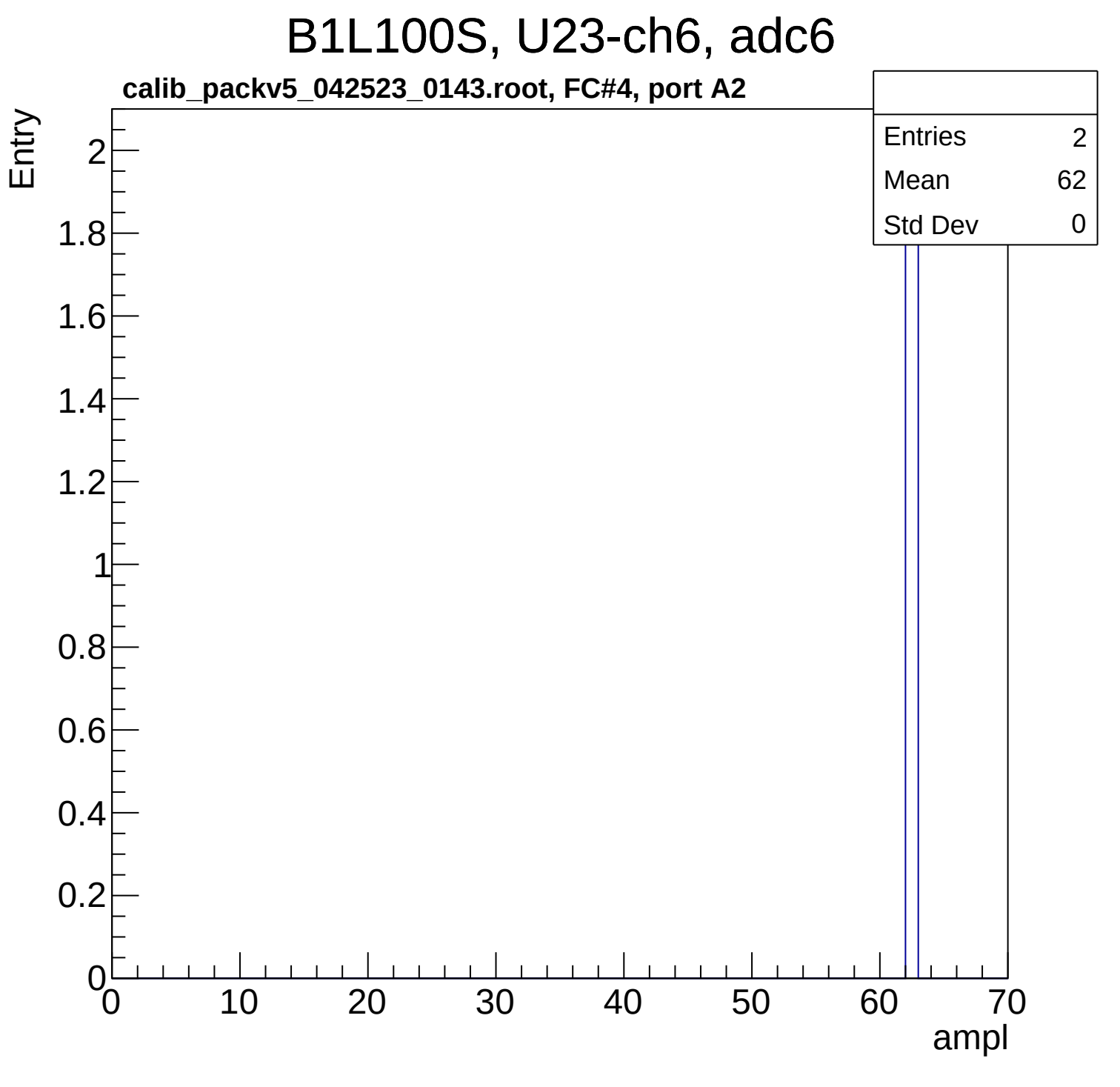
B1L100S, U23-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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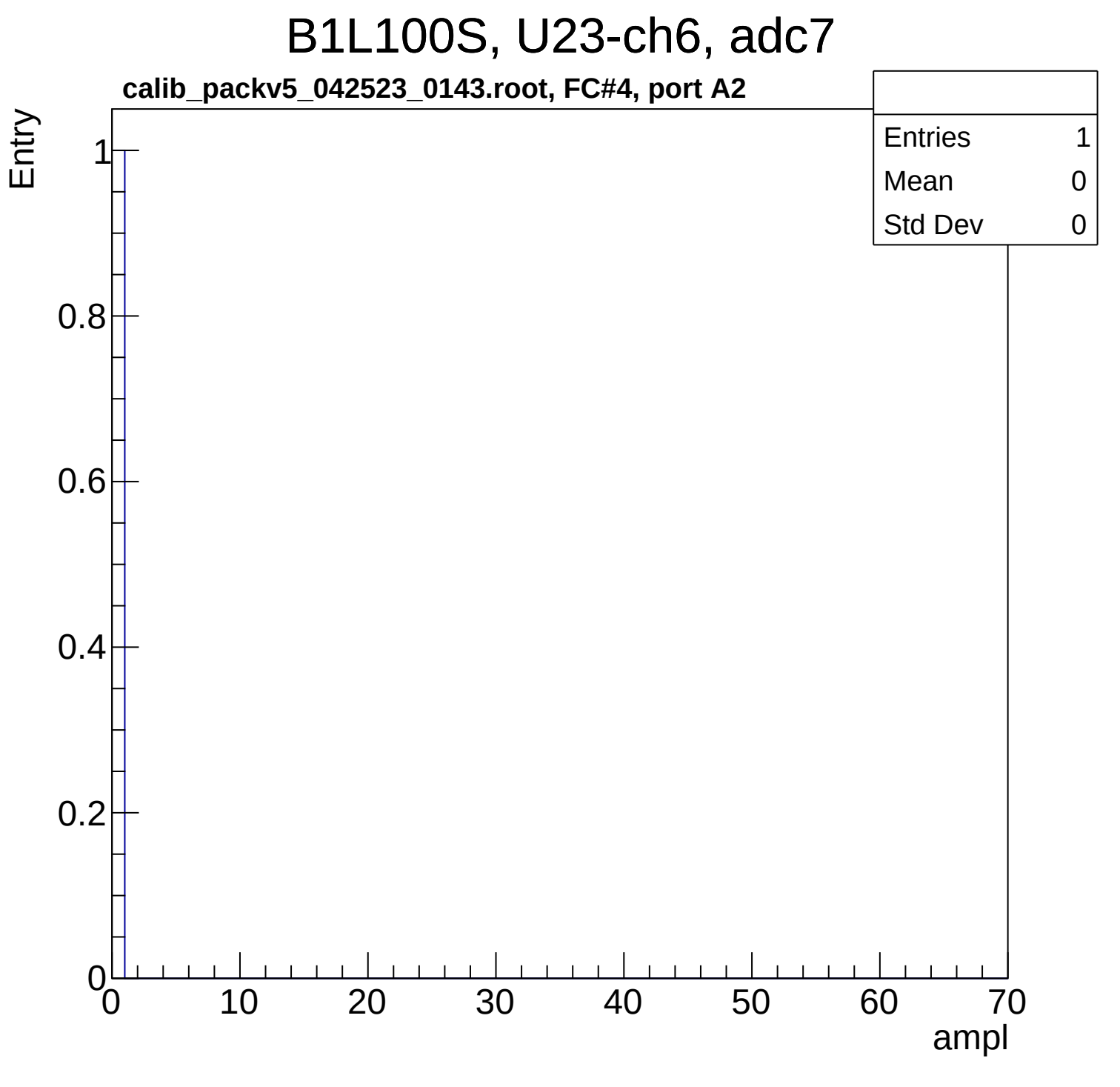
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch6, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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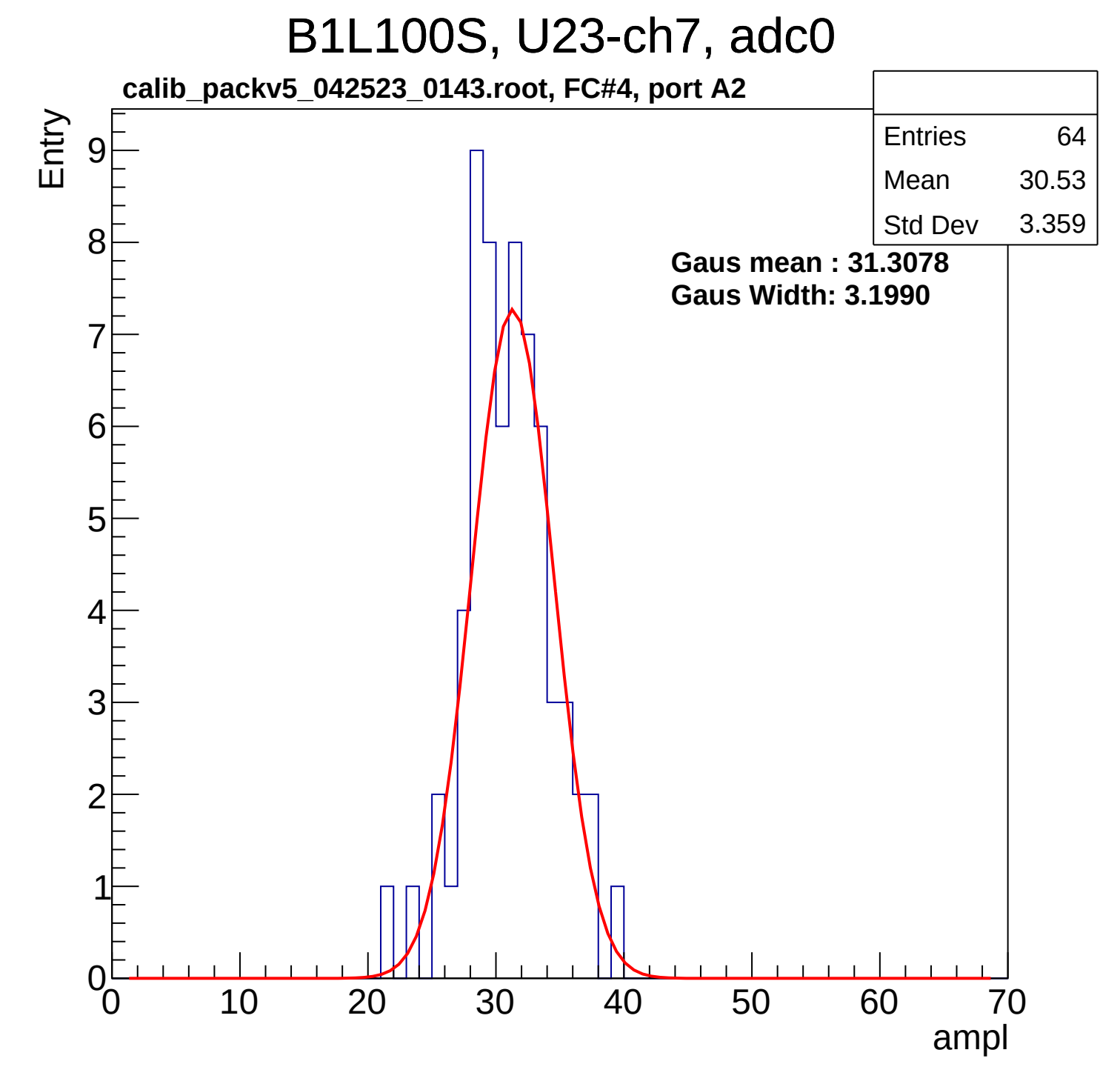
40
50
60
70
ampl


B1L100S, U23-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.53
Std Dev 3.359
Gaus mean : 31.3078
Gaus Width: 3.1990
Entry
ampl
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2
3
4
5
6
7
8
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0
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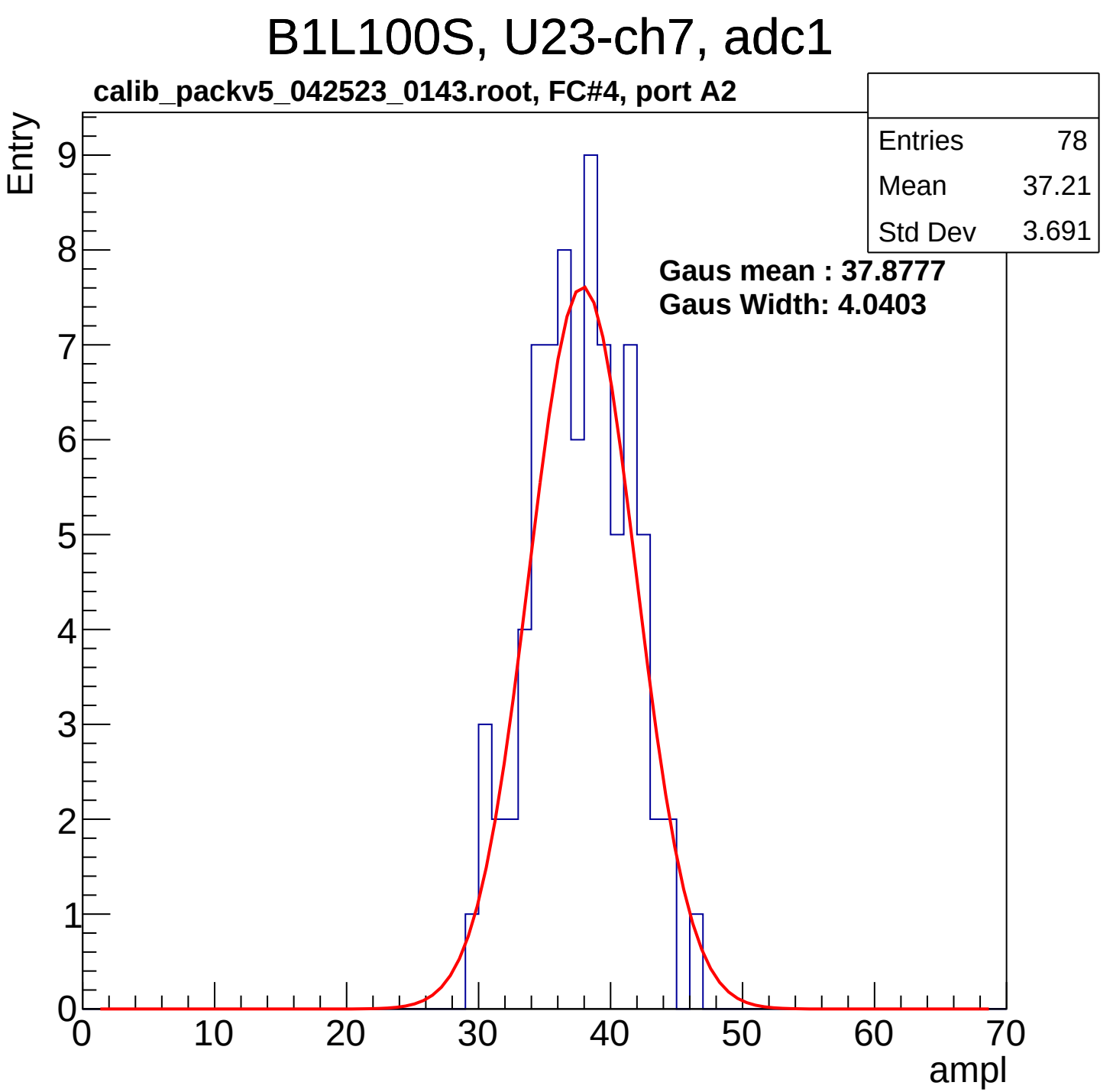
B1L100S, U23-ch7, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.21
Std Dev 3.691
Gaus mean : 37.8777
Gaus Width: 4.0403
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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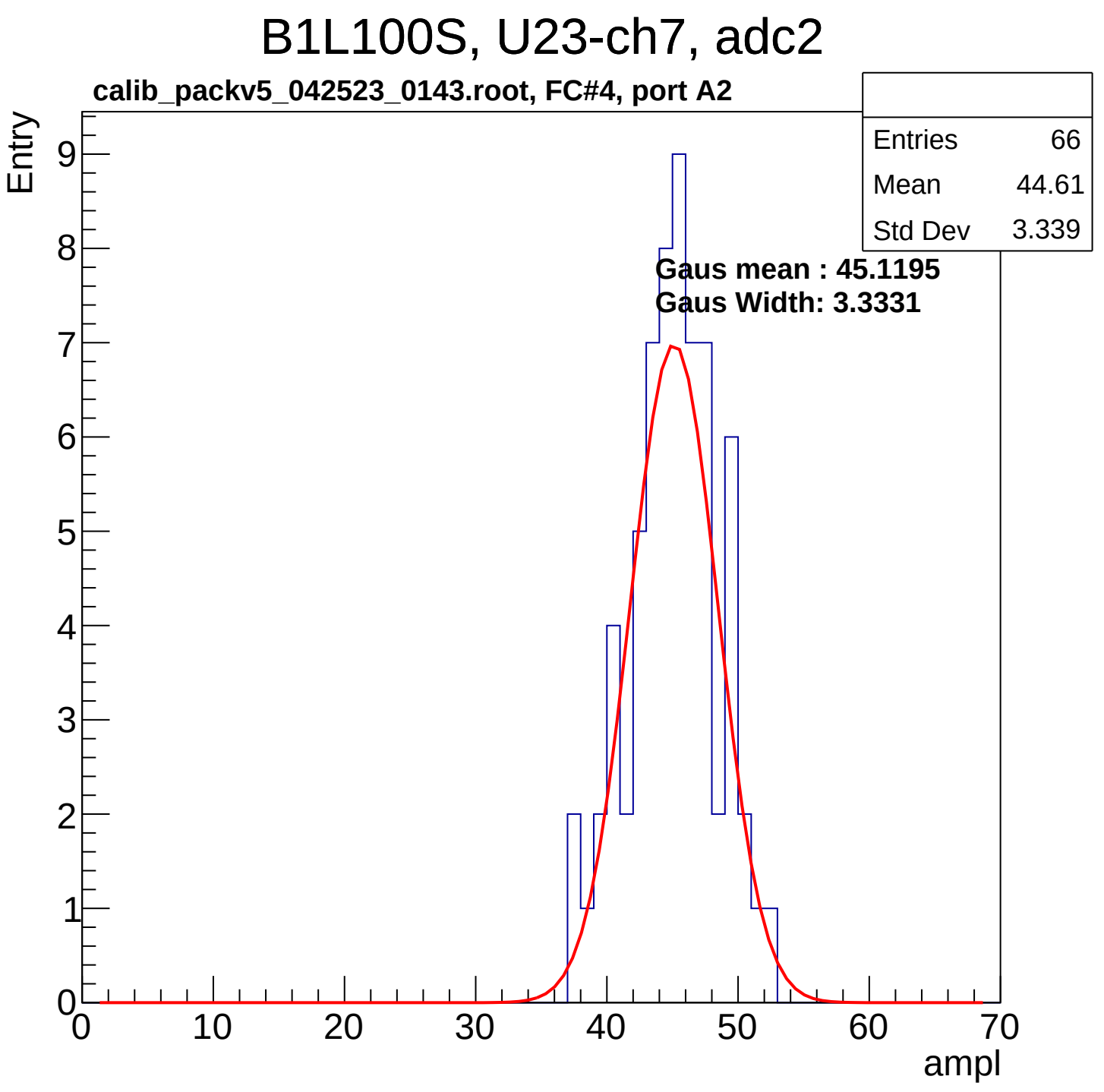
B1L100S, U23-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 44.61
Std Dev 3.339
Gaus mean : 45.1195
Gaus Width: 3.3331
Entry
ampl
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2
3
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5
6
7
8
9
0
10
20
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40
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60
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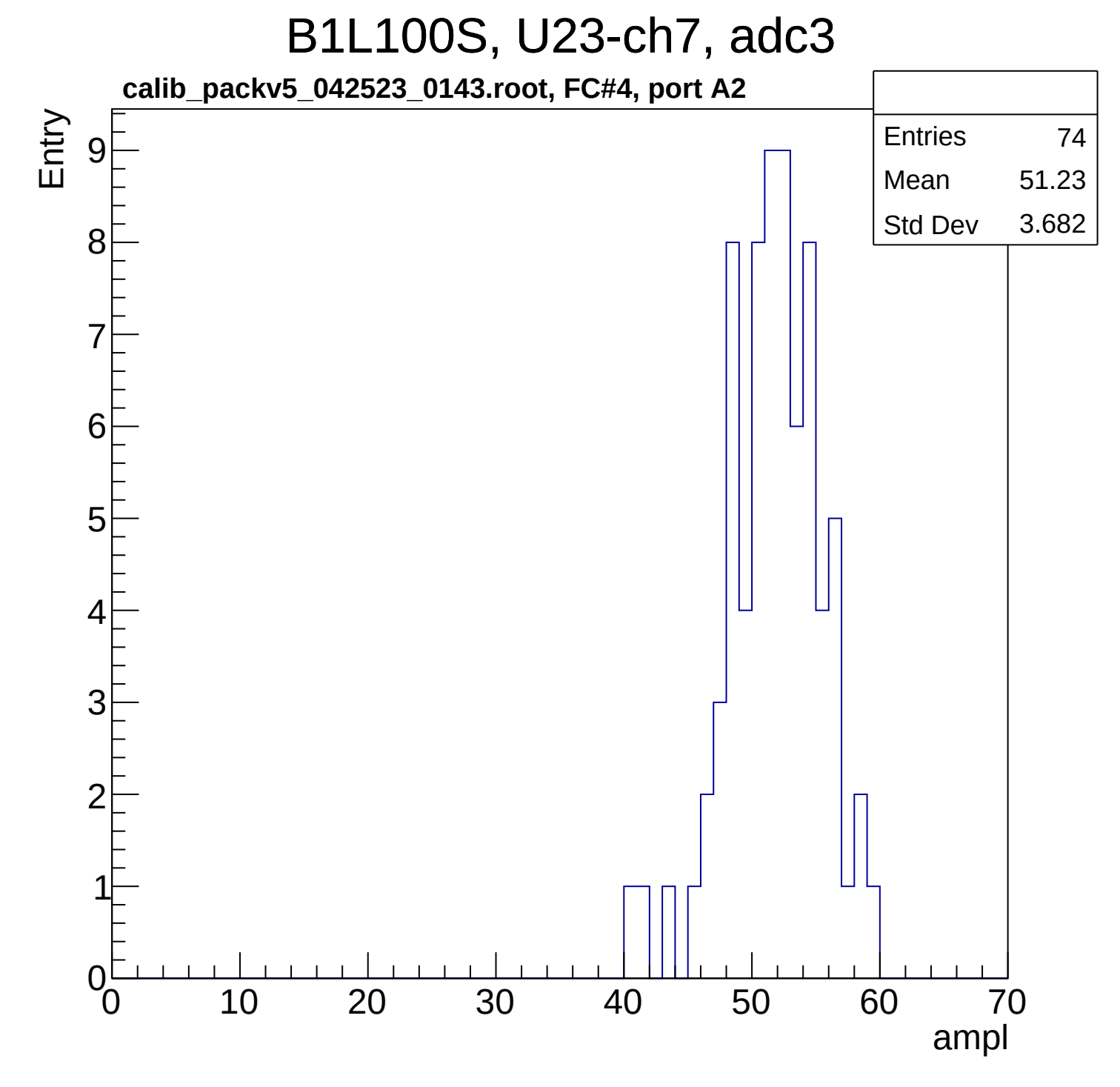
B1L100S, U23-ch7, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.23
Std Dev 3.682
Entry
ampl
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2
3
4
5
6
7
8
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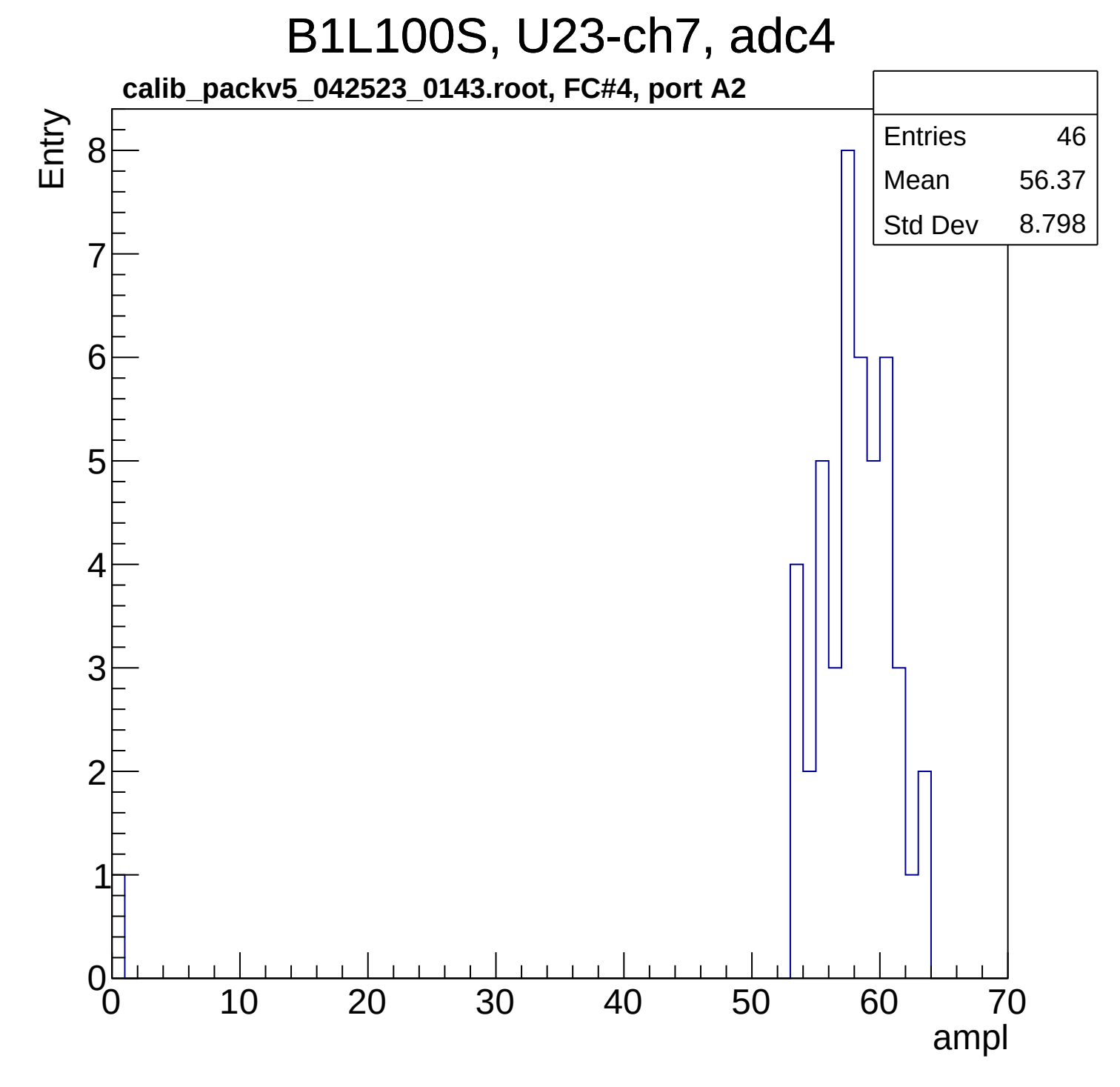
B1L100S, U23-ch7, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 56.37
Std Dev 8.798
Entry
ampl
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40
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60
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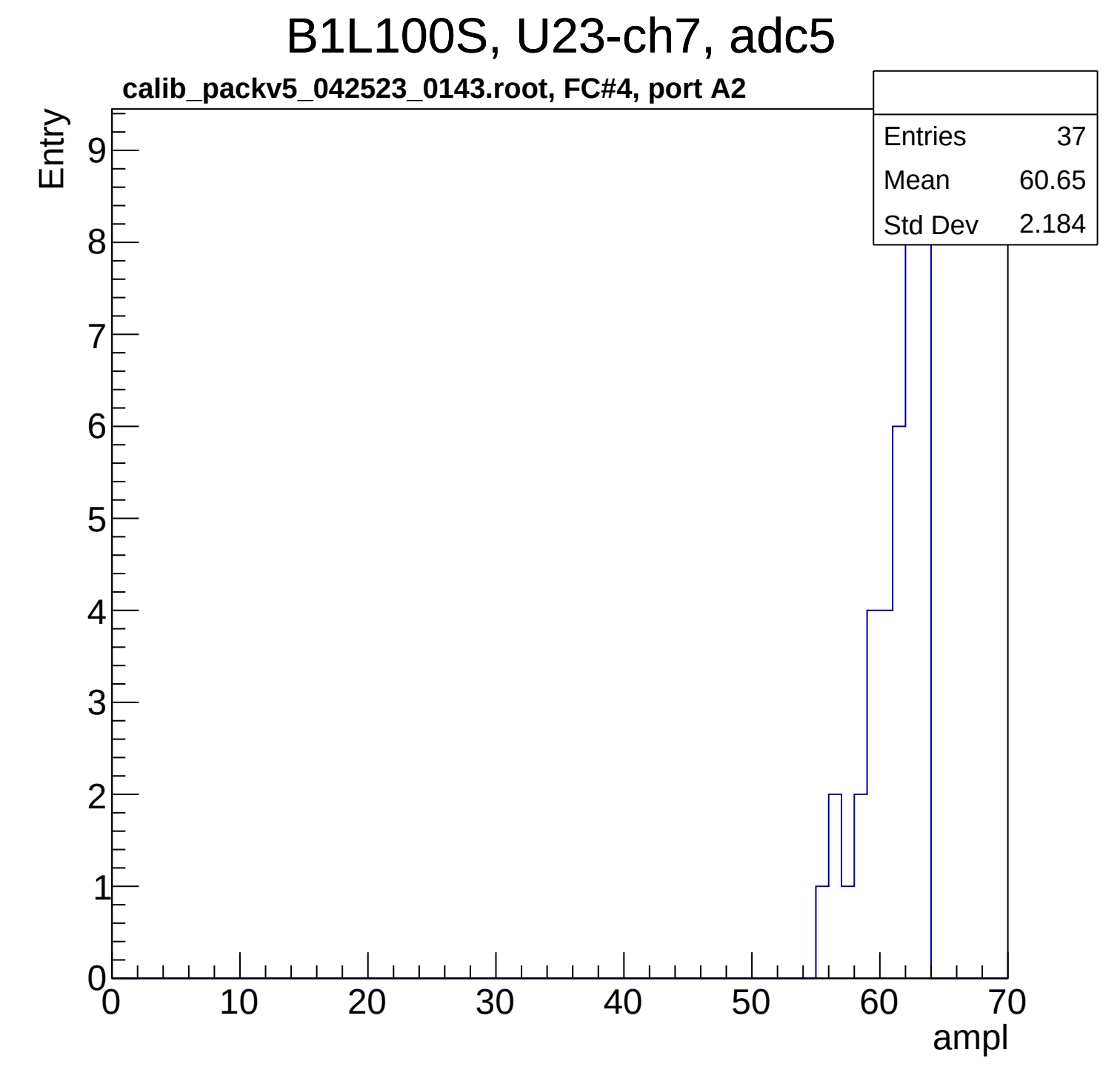
B1L100S, U23-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.65
Std Dev 2.184
Entry
ampl
0
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2
3
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5
6
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8
9
0
10
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60
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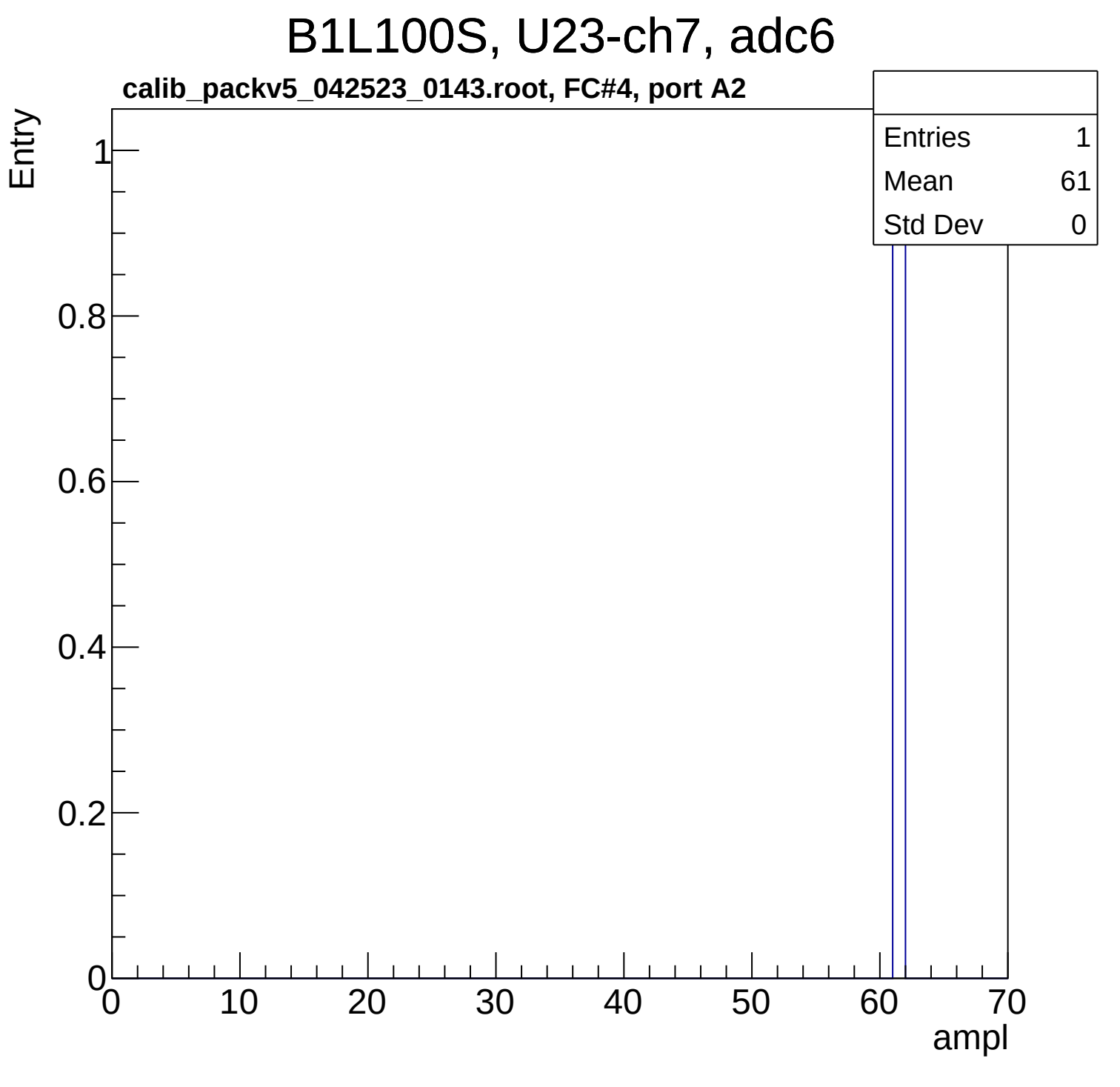
B1L100S, U23-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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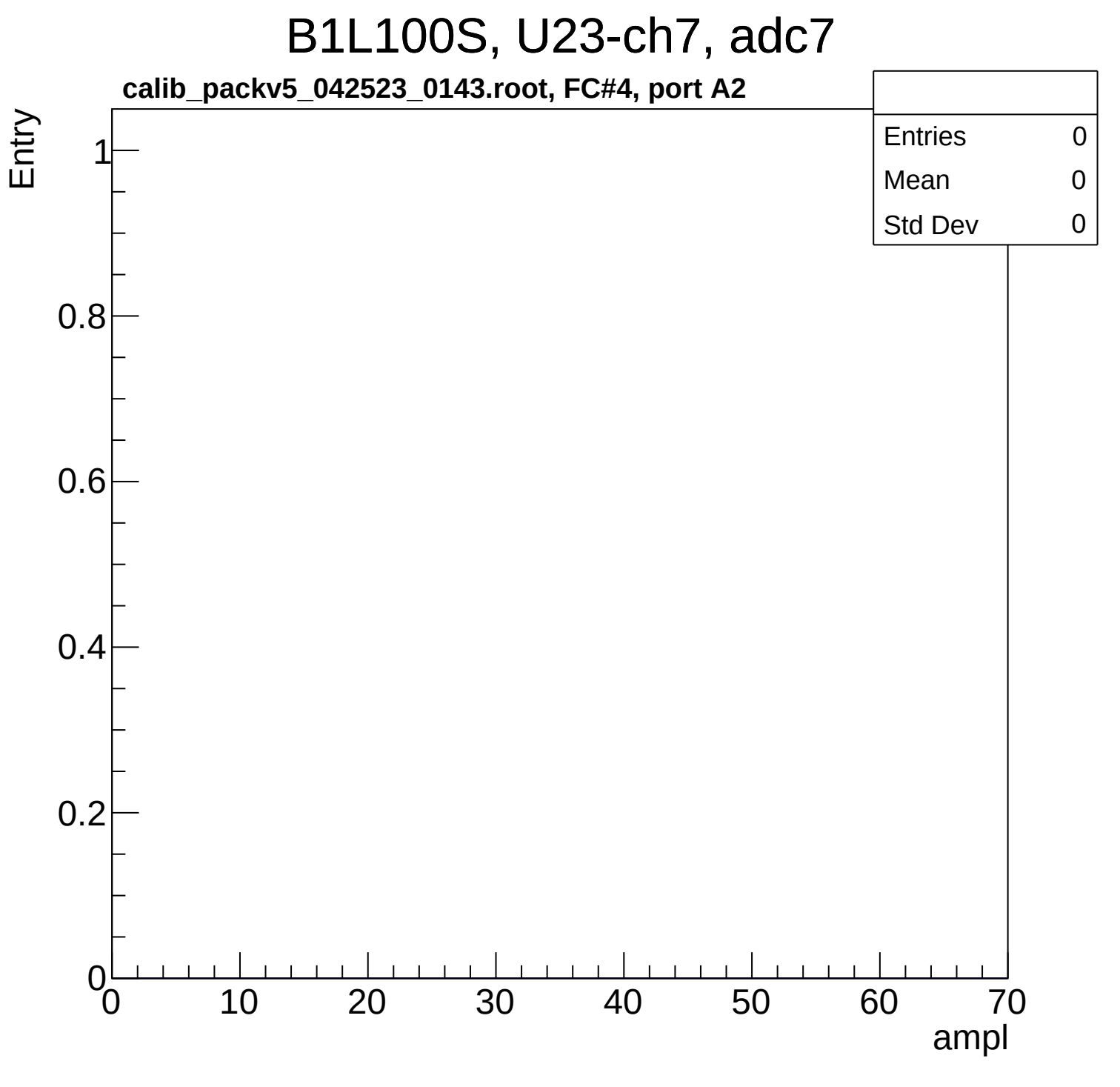
ampl


B1L100S, U23-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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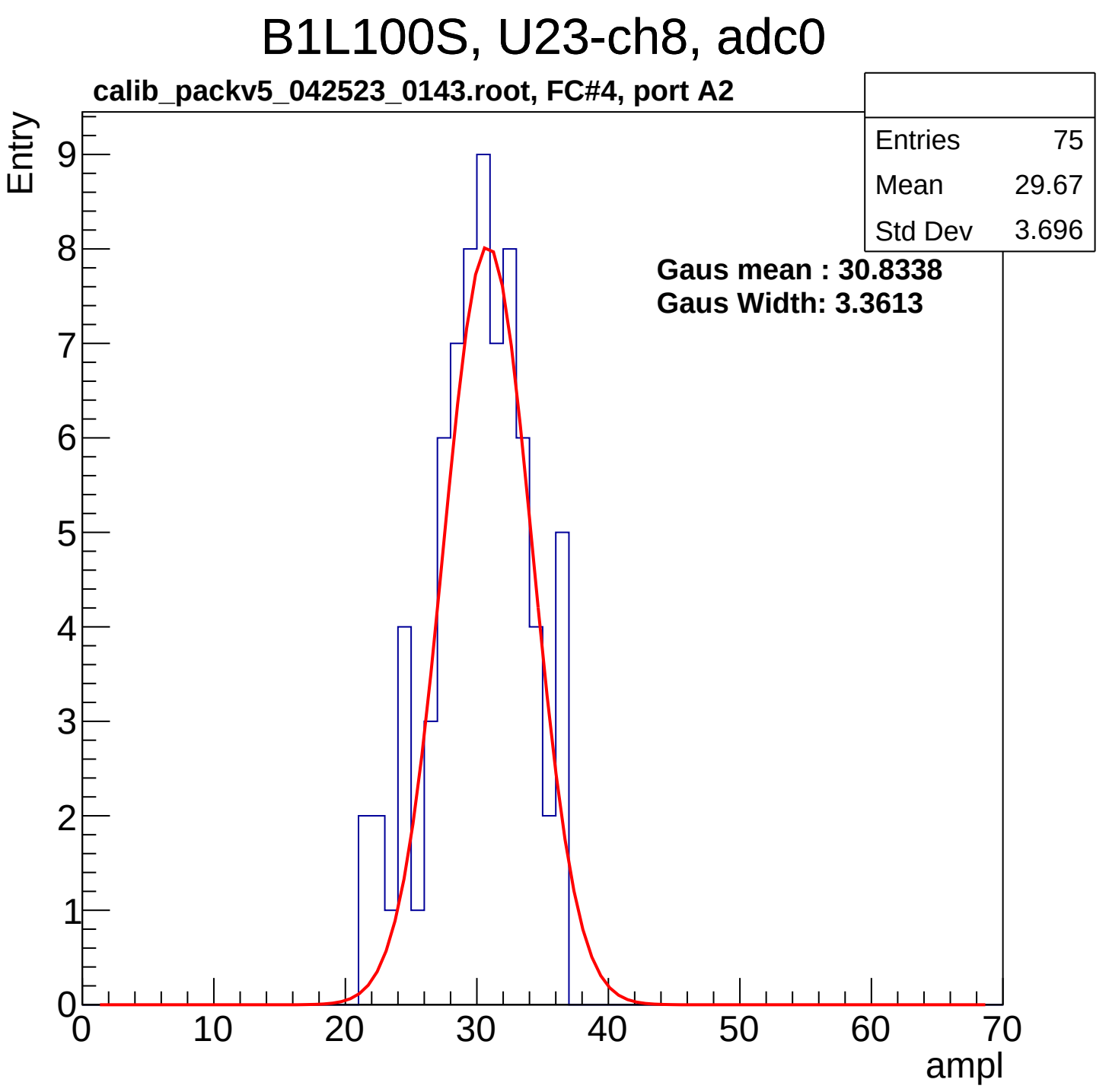
ampl


B1L100S, U23-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.67
Std Dev 3.696
Gaus mean : 30.8338
Gaus Width: 3.3613
Entry
ampl
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3
4
5
6
7
8
9
0
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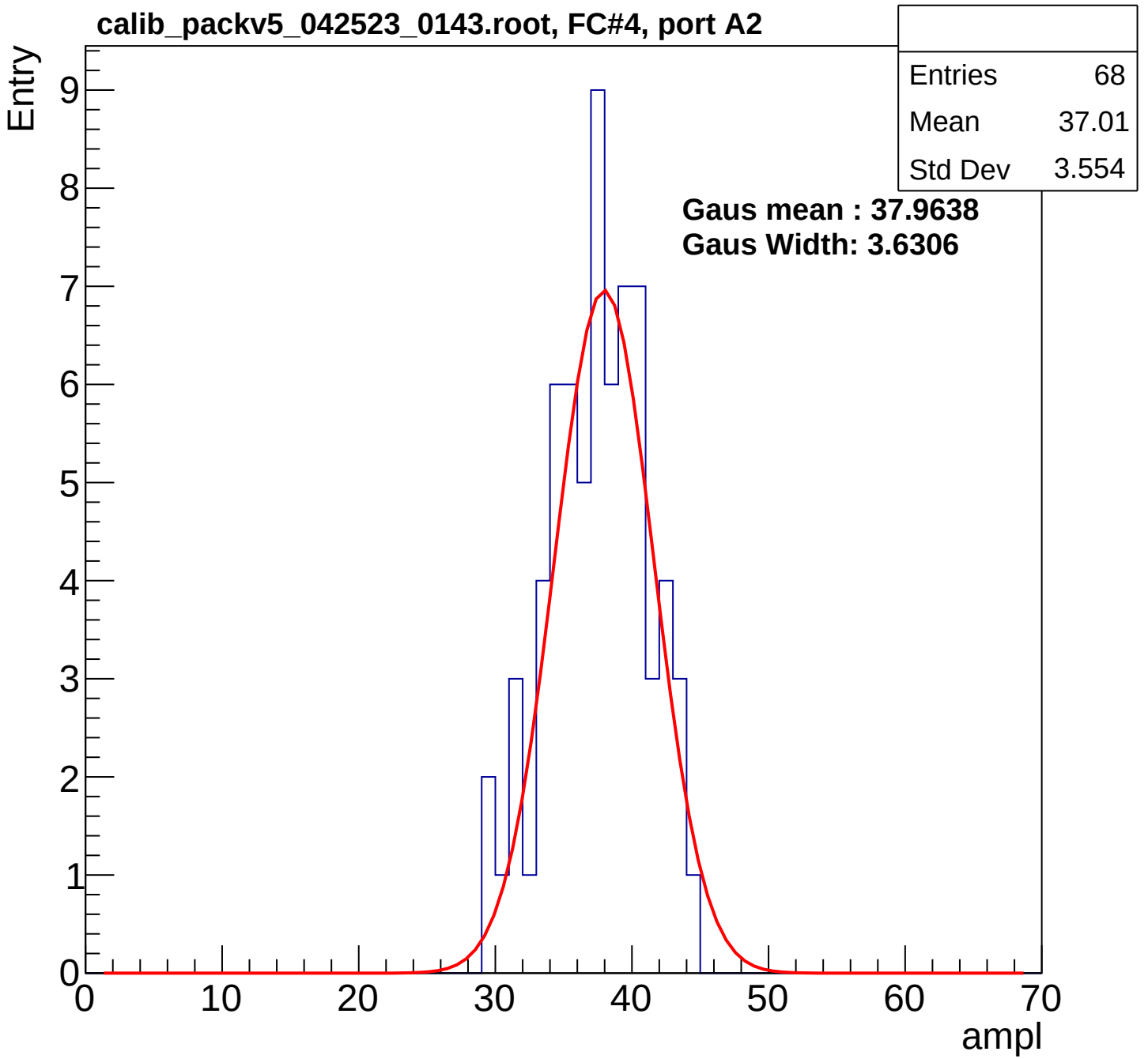
B1L100S, U23-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 37.01
Std Dev 3.554
Gaus mean : 37.9638
Gaus Width: 3.6306
Entry
ampl
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1
2
3
4
5
6
7
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0
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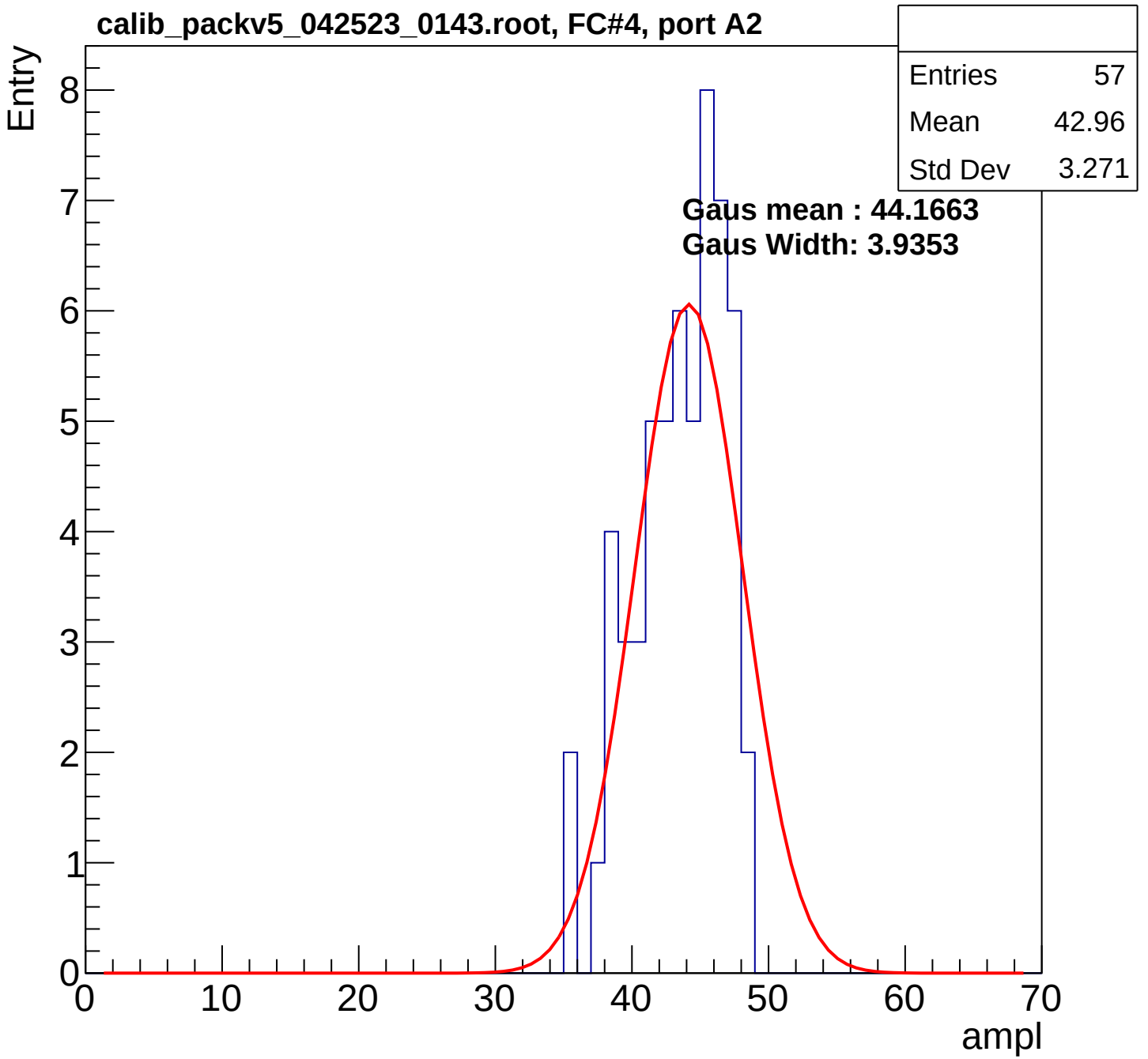
B1L100S, U23-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.96
Std Dev 3.271
Gaus mean : 44.1663
Gaus Width: 3.9353
Entry
ampl
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1
2
3
4
5
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7
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0
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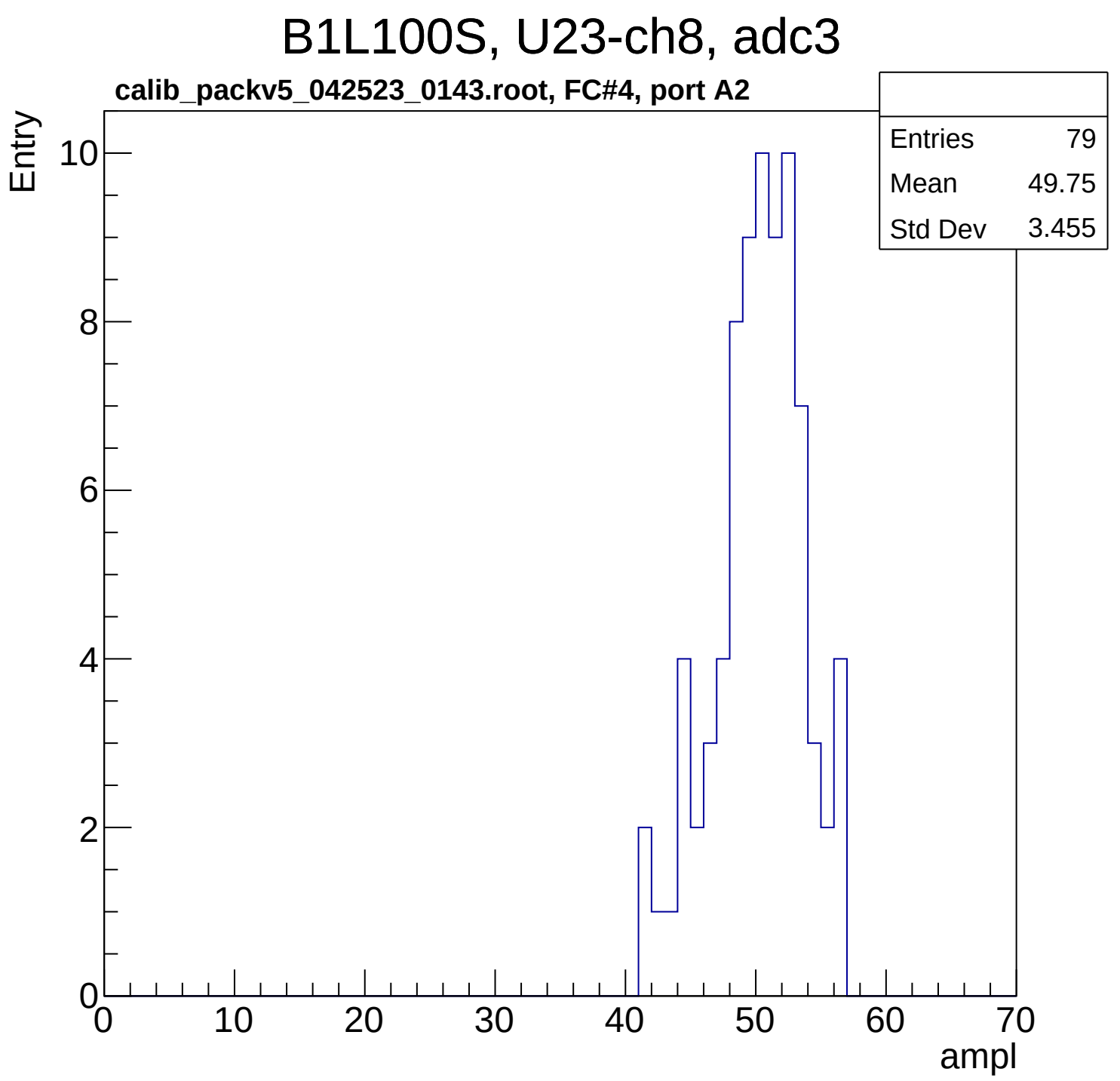
B1L100S, U23-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 49.75
Std Dev 3.455
Entry
0
2
4
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10
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10
20
30
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50
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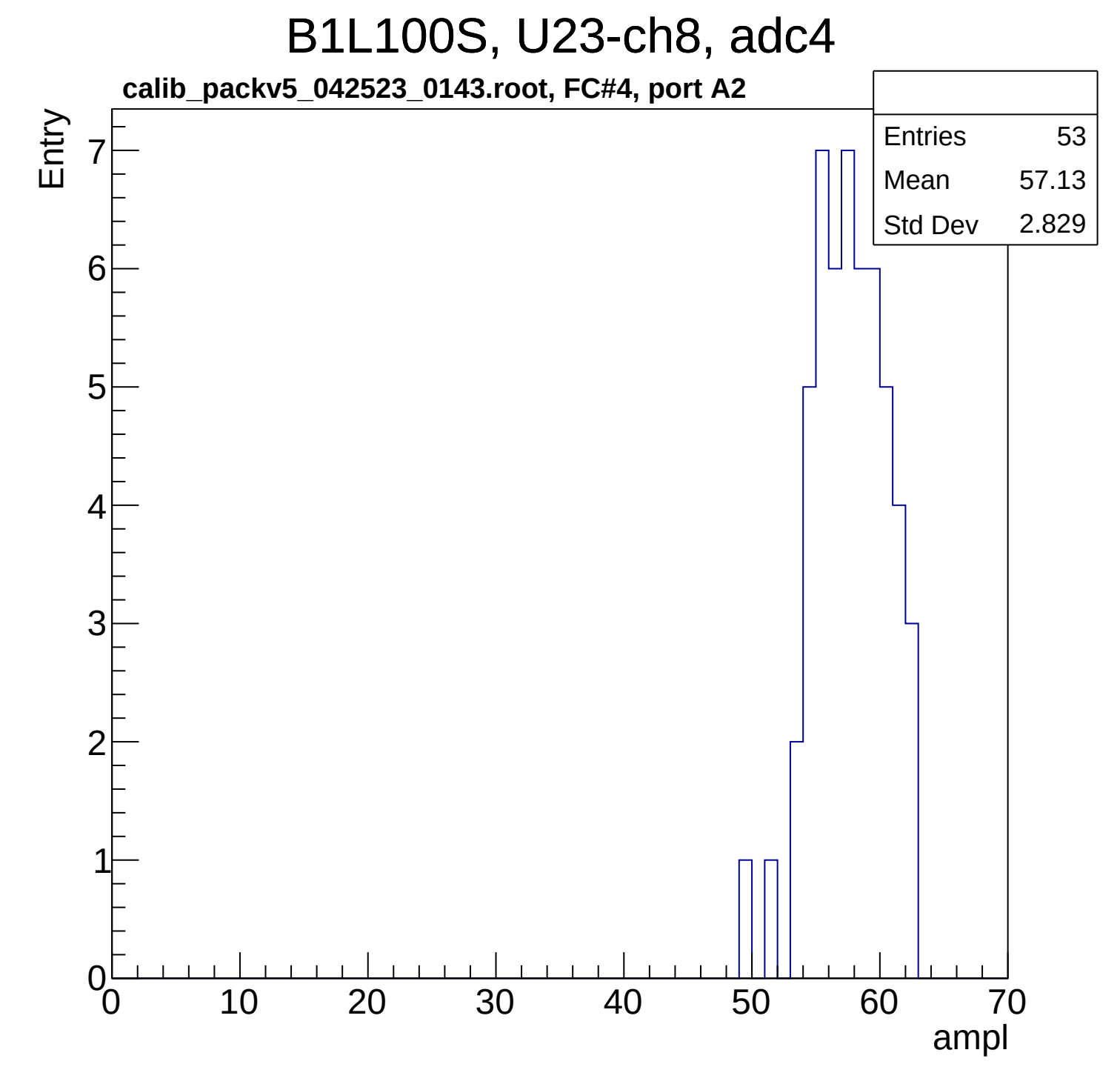
70
ampl


B1L100S, U23-ch8, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 57.13
Std Dev 2.829
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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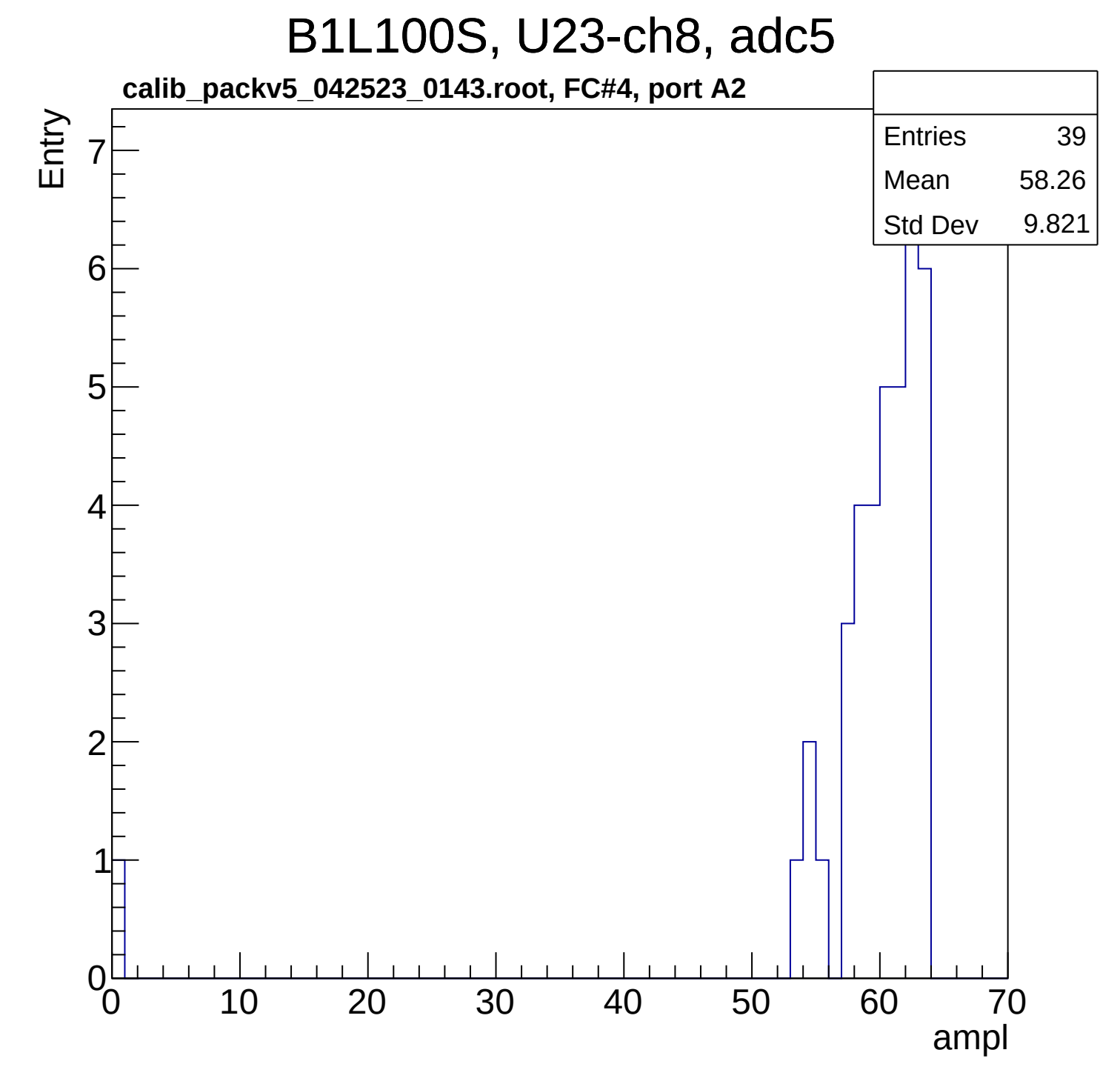
B1L100S, U23-ch8, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.26
Std Dev 9.821
Entry
ampl
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2
3
4
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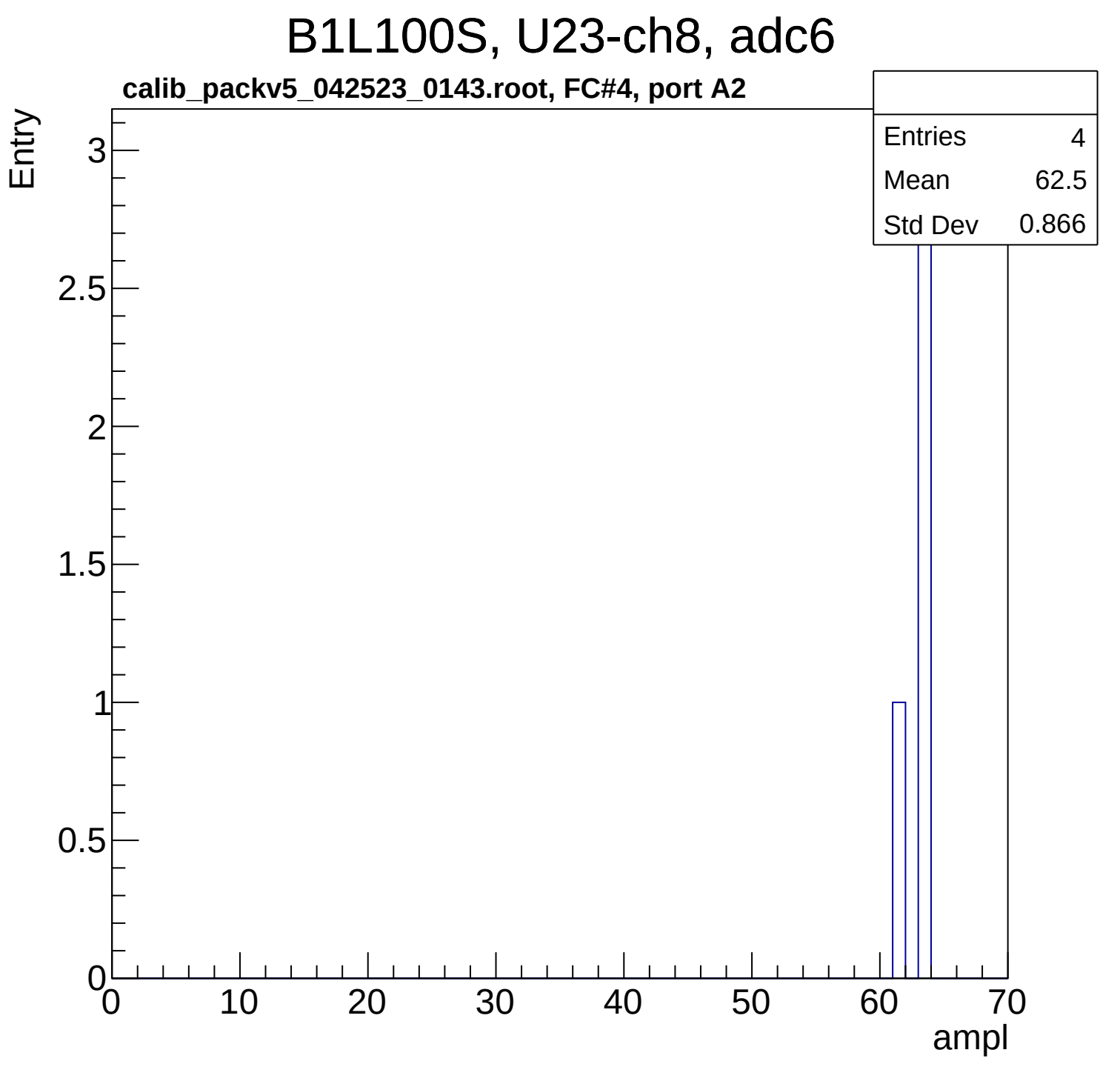
B1L100S, U23-ch8, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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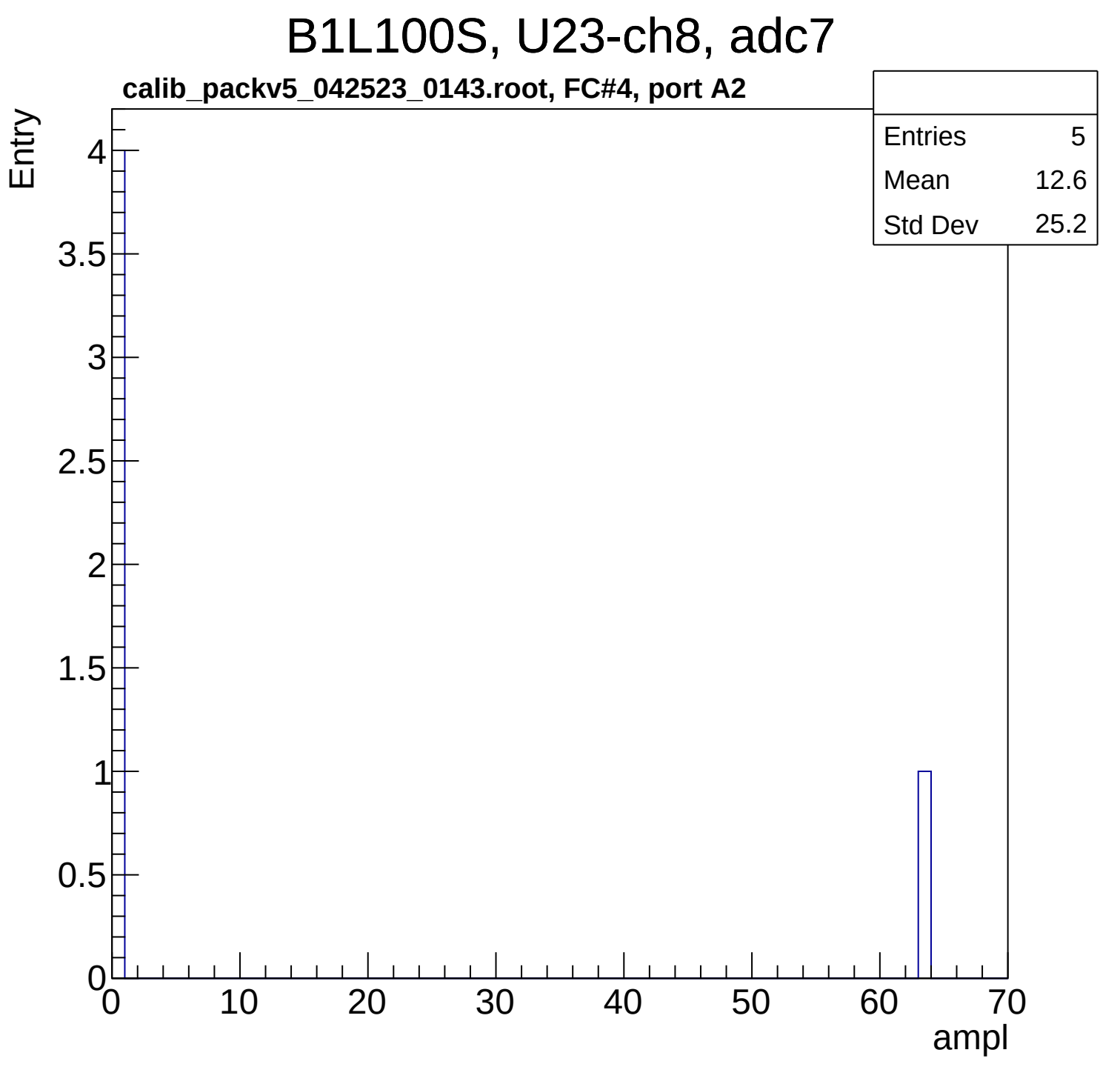
B1L100S, U23-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 12.6
Std Dev 25.2
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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40
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60
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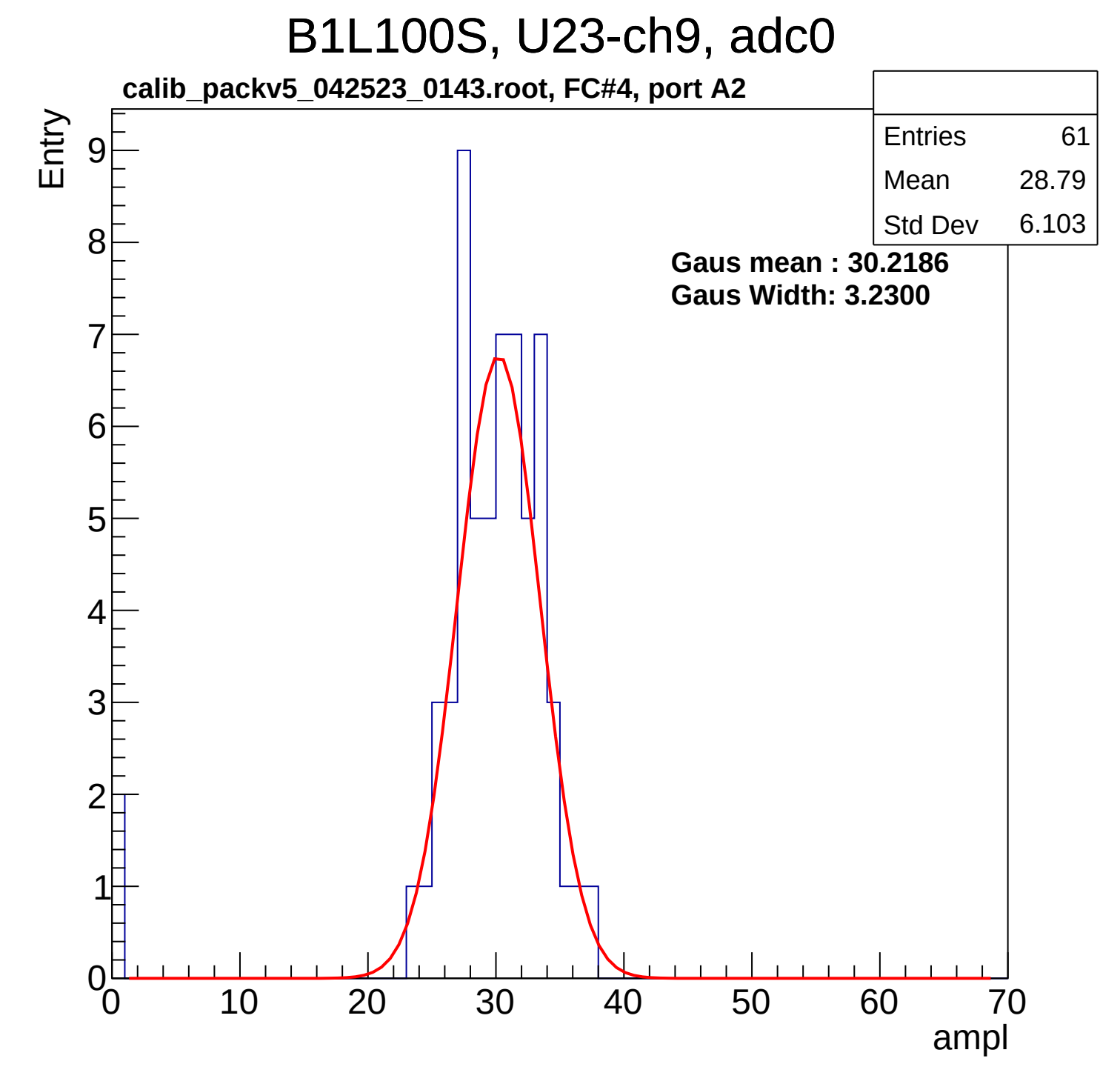
B1L100S, U23-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.79
Std Dev 6.103
Gaus mean : 30.2186
Gaus Width: 3.2300
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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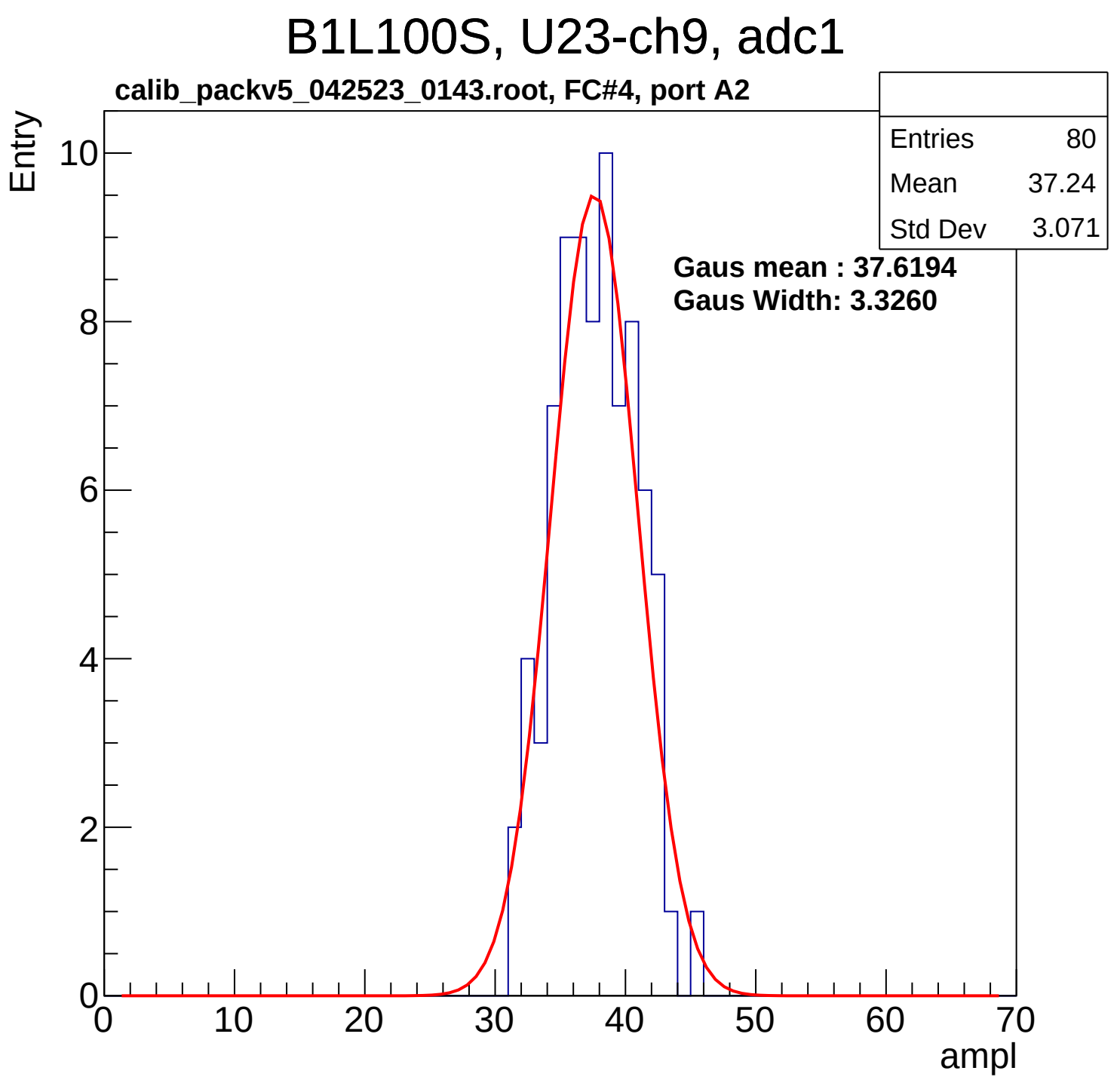
B1L100S, U23-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.24
Std Dev 3.071
Gaus mean : 37.6194
Gaus Width: 3.3260
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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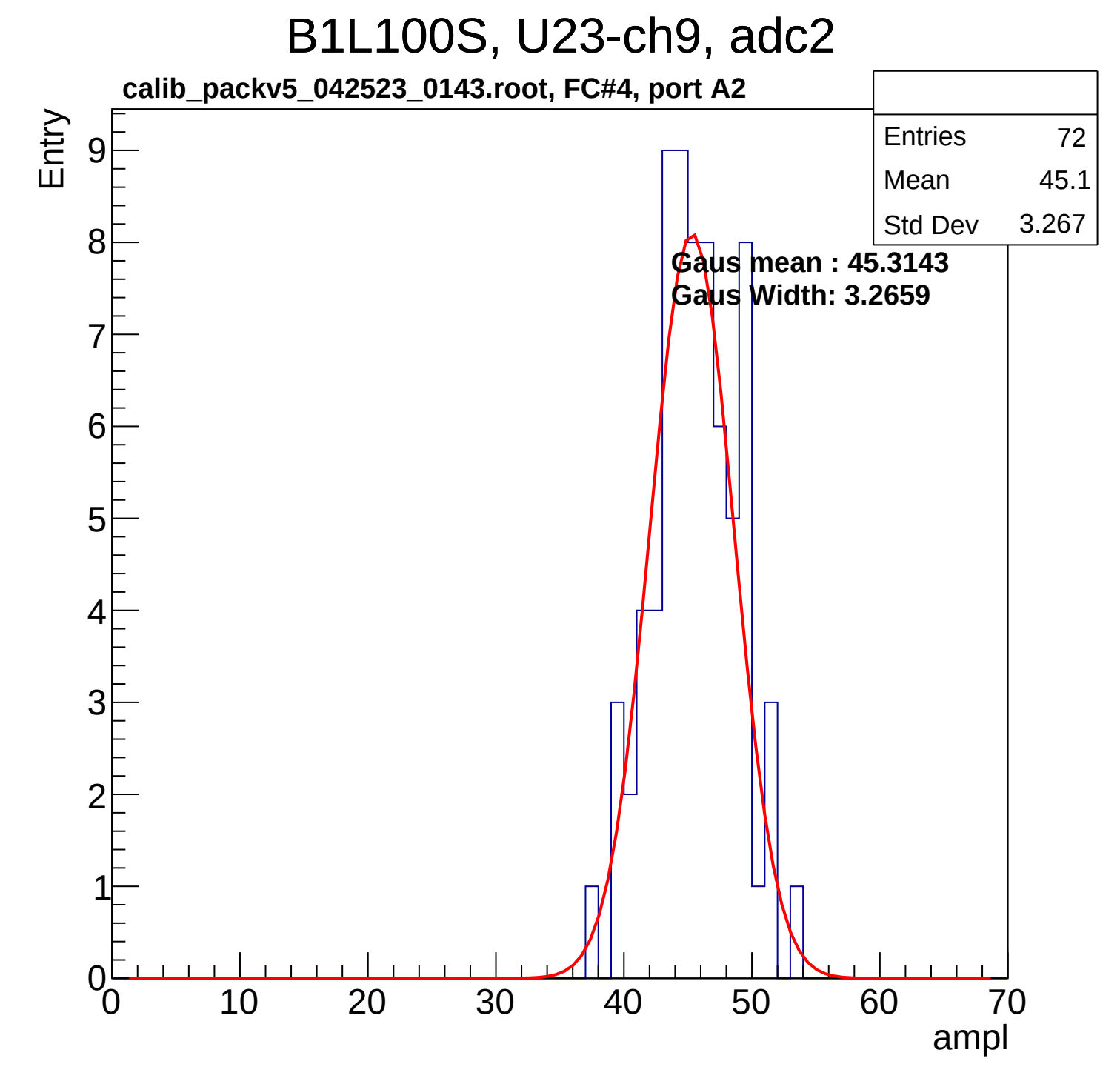
B1L100S, U23-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 45.1
Std Dev 3.267
Gaus mean : 45.3143
Gaus Width: 3.2659
Entry
ampl
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2
3
4
5
6
7
8
9
0
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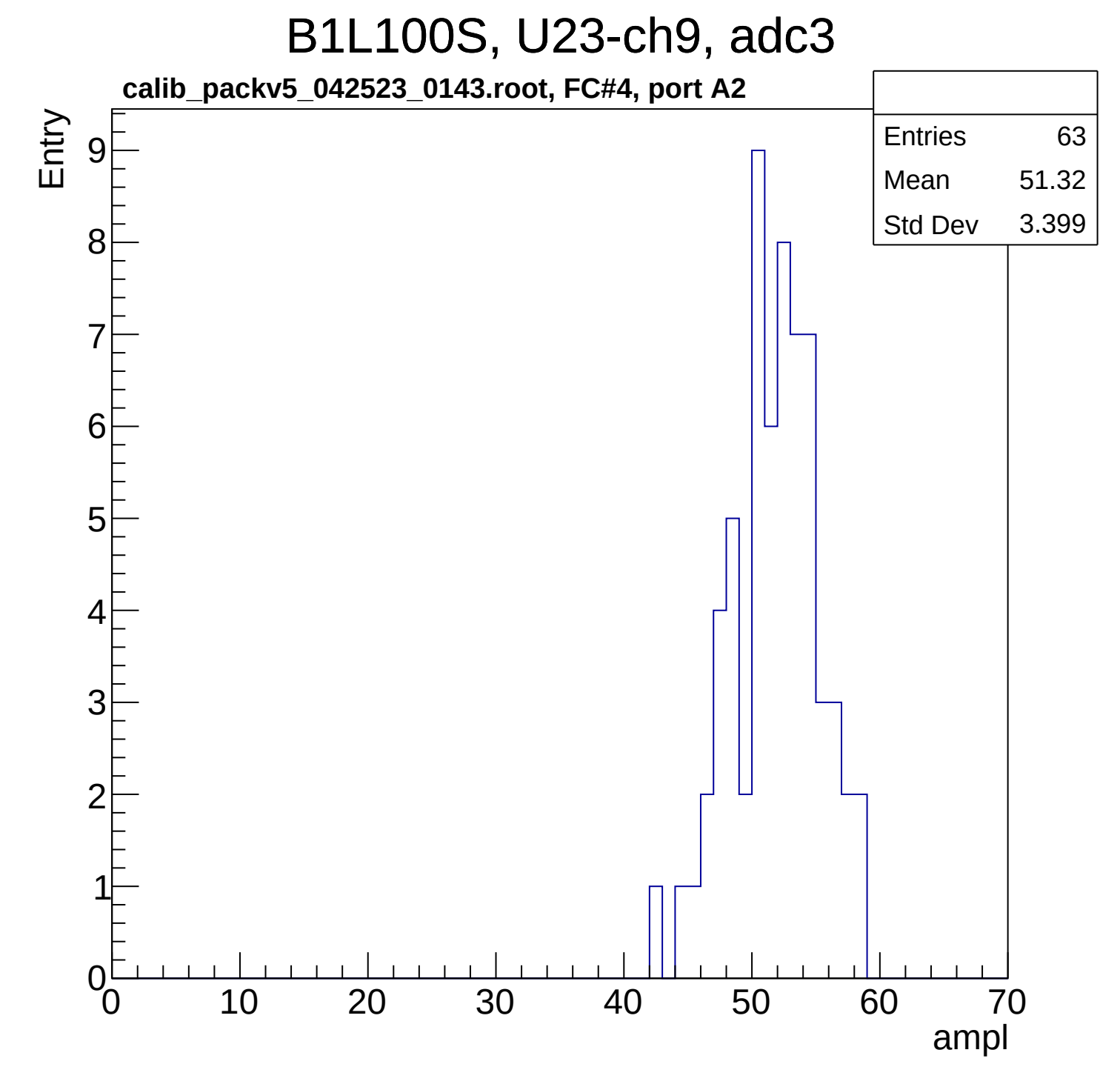
B1L100S, U23-ch9, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.32
Std Dev 3.399
Entry
ampl
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3
4
5
6
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8
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0
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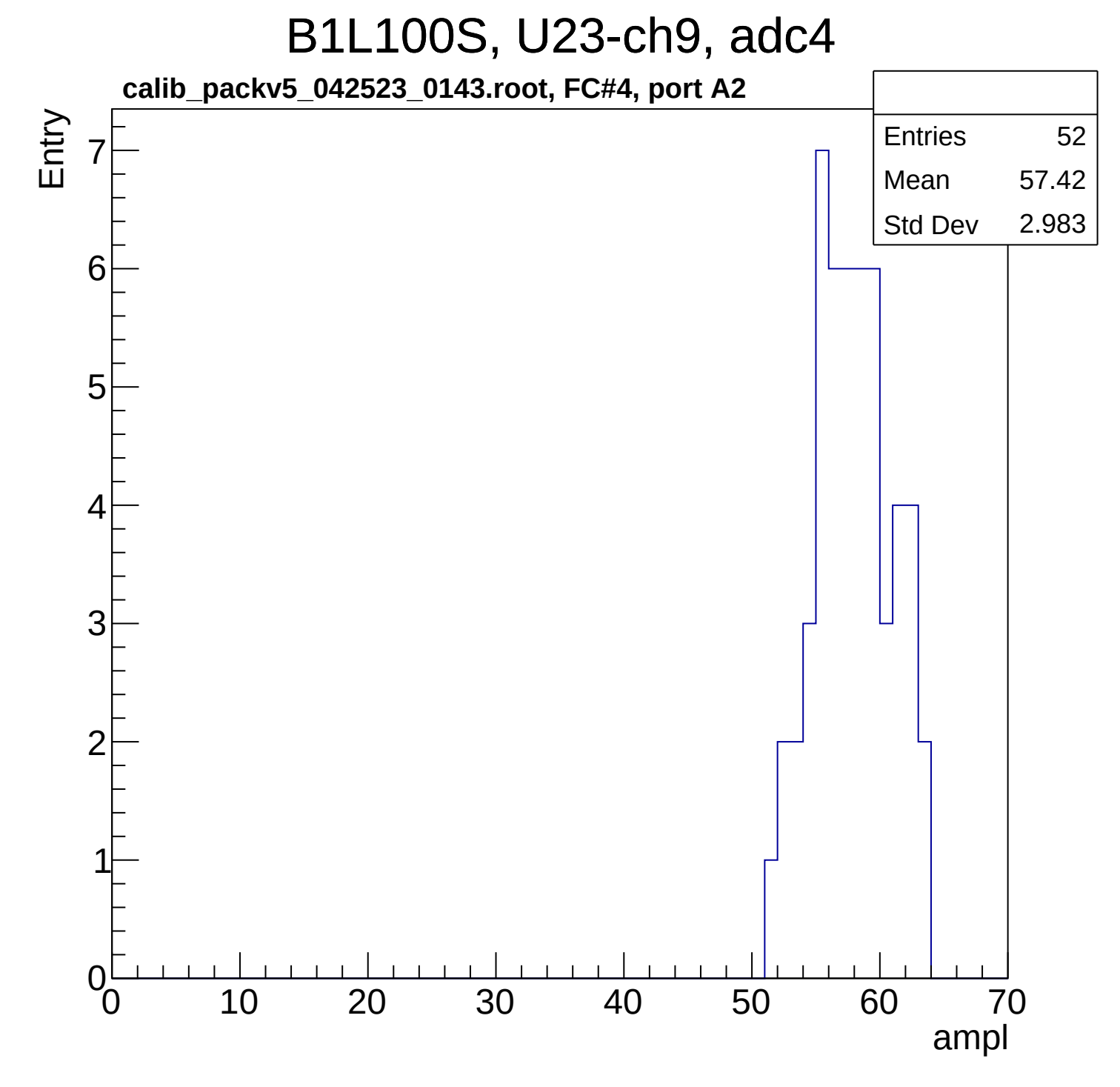
B1L100S, U23-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 57.42
Std Dev 2.983
Entry
ampl
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1
2
3
4
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6
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0
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40
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60
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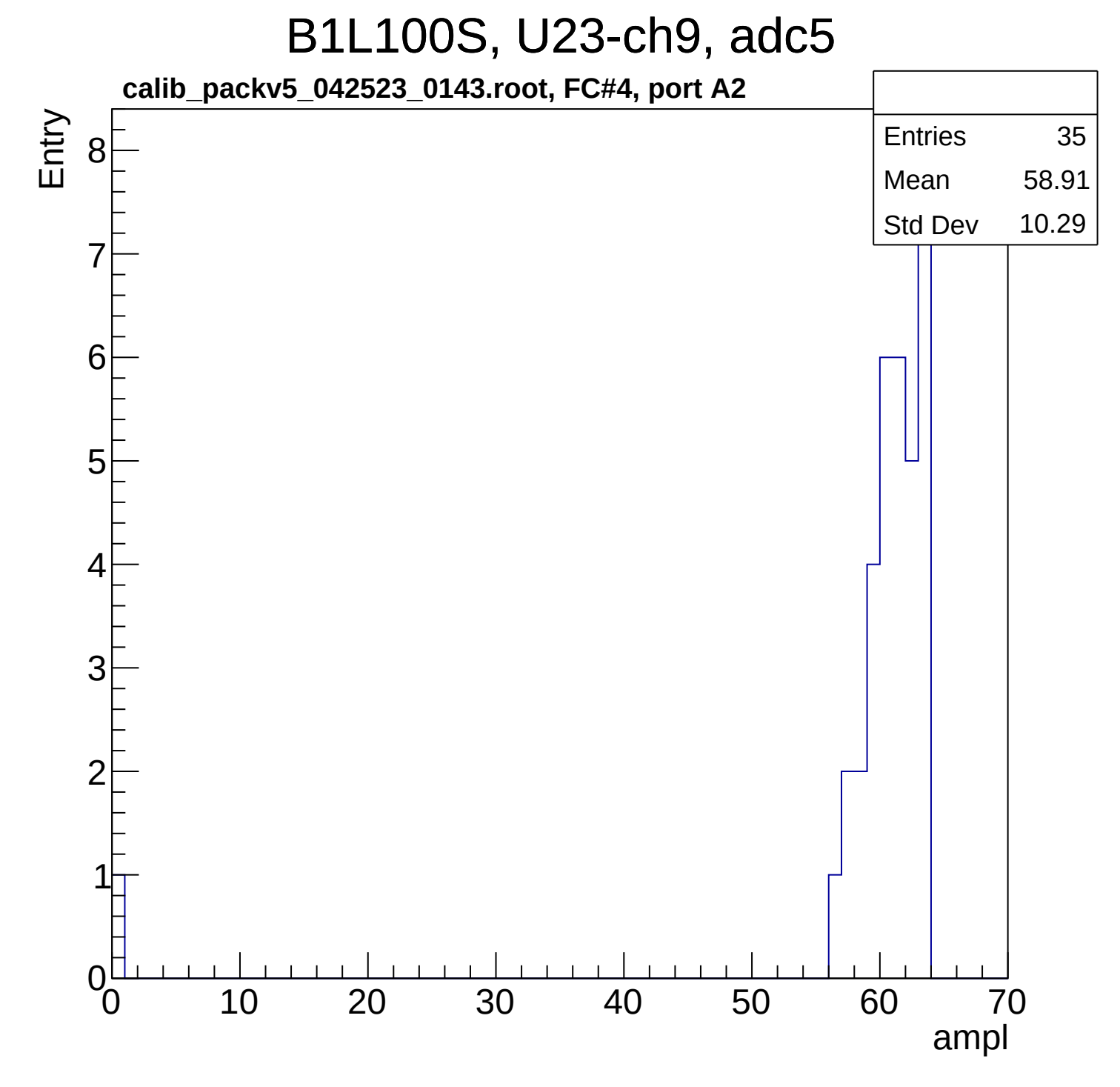
B1L100S, U23-ch9, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.91
Std Dev 10.29
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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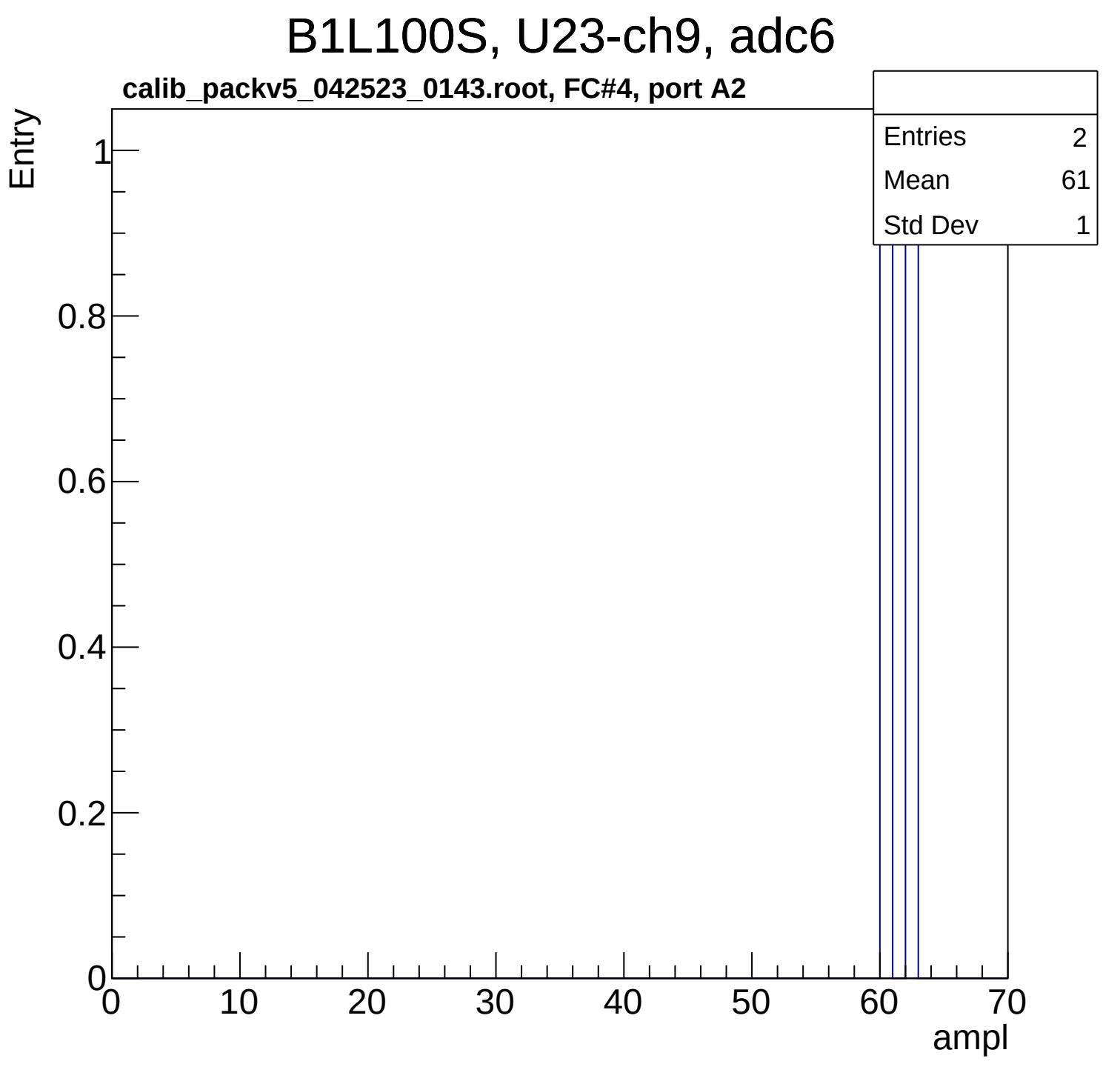
B1L100S, U23-ch9, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 1
Entry
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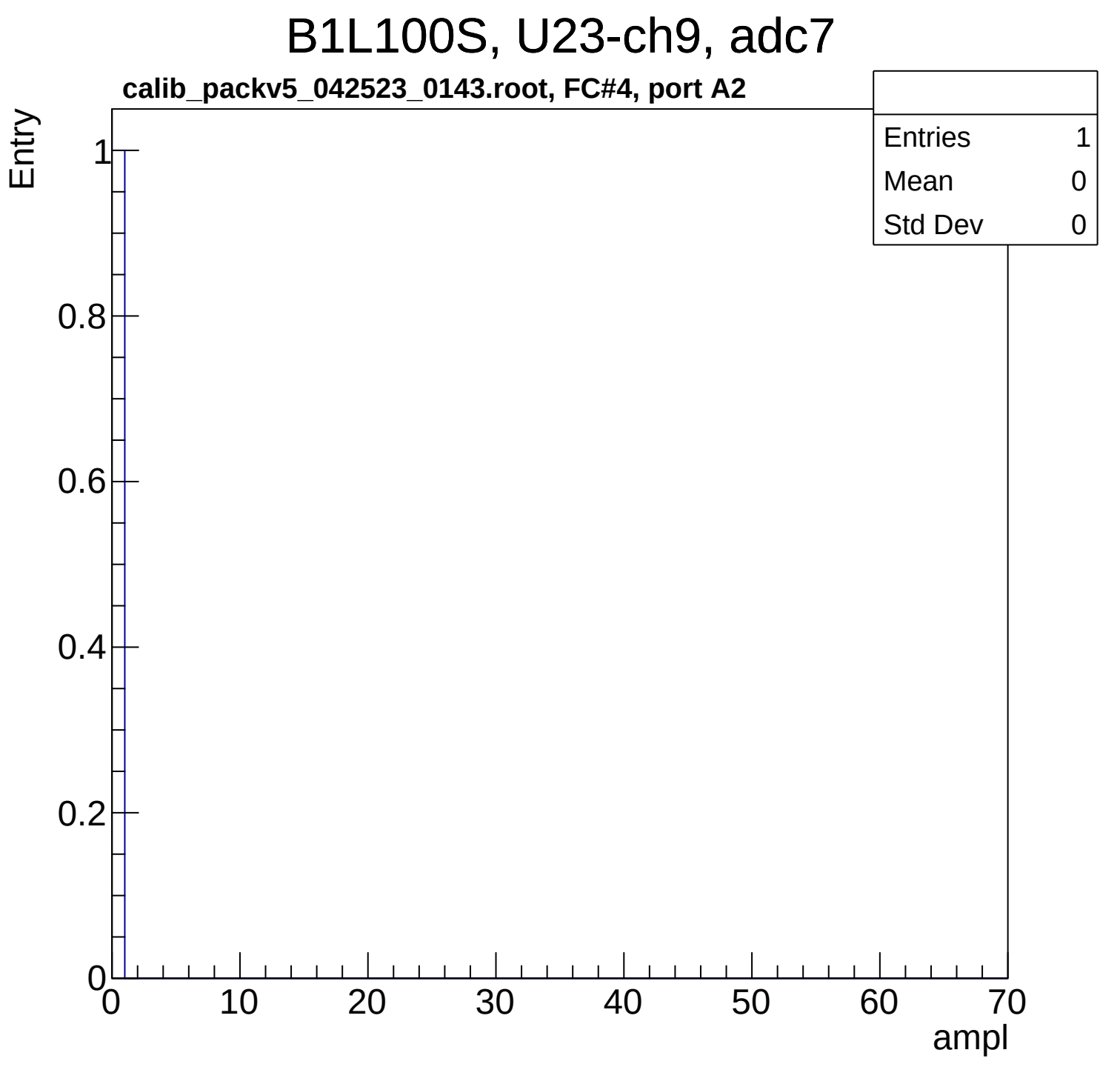
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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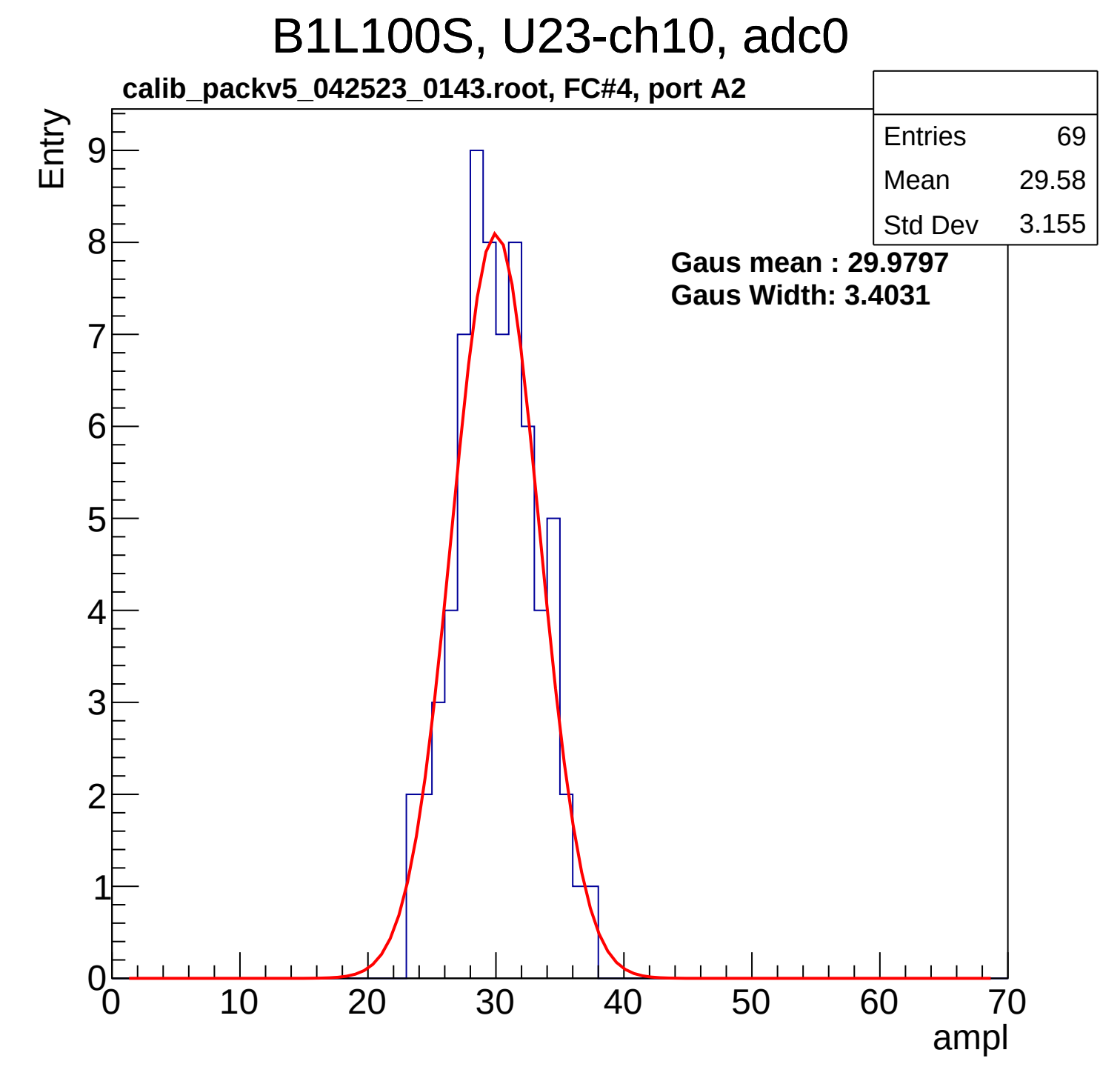
50
60
70
ampl


B1L100S, U23-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.58
Std Dev 3.155
Gaus mean : 29.9797
Gaus Width: 3.4031
Entry
ampl
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3
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5
6
7
8
9
0
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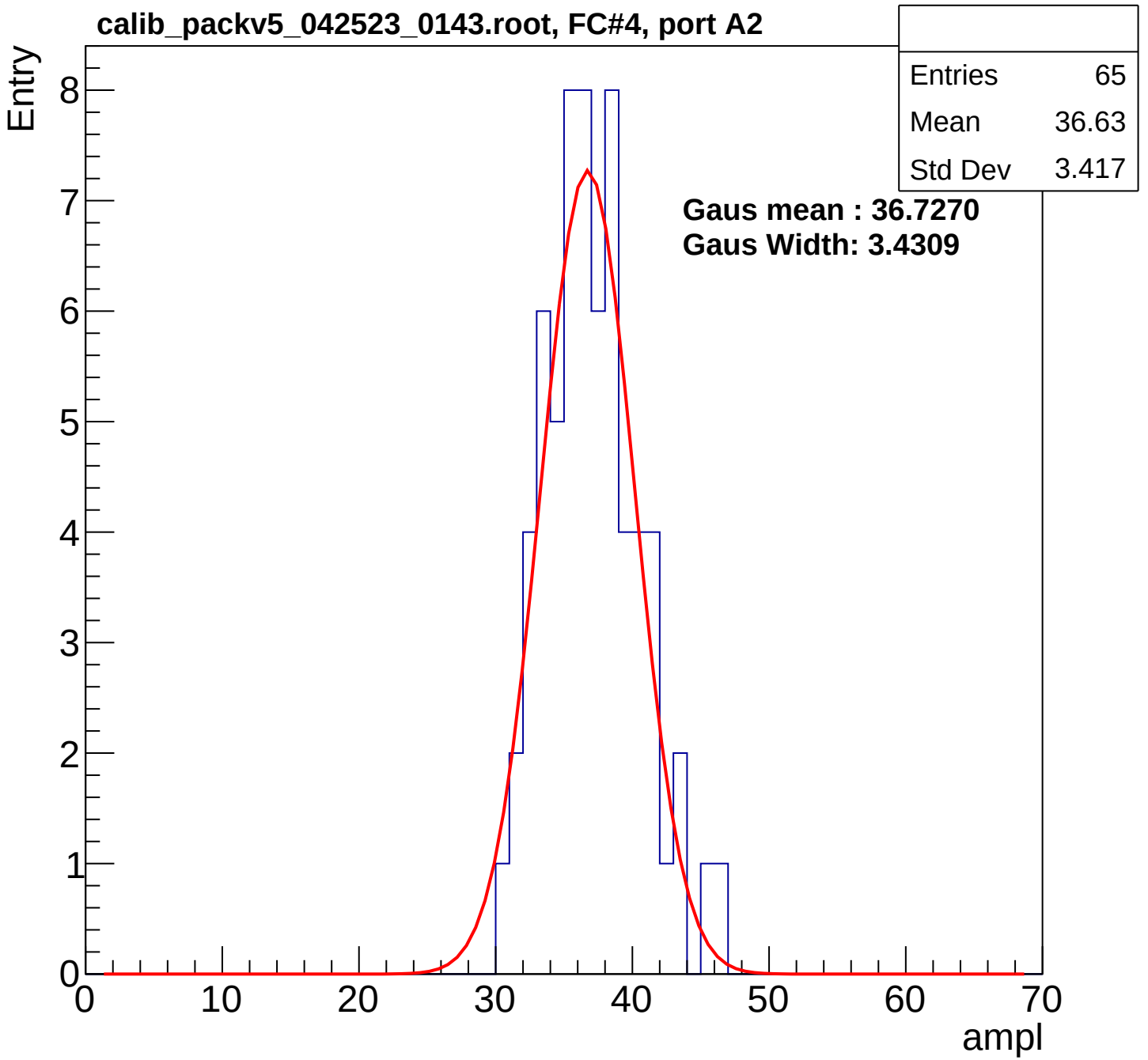
B1L100S, U23-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.63
Std Dev 3.417
Gaus mean : 36.7270
Gaus Width: 3.4309
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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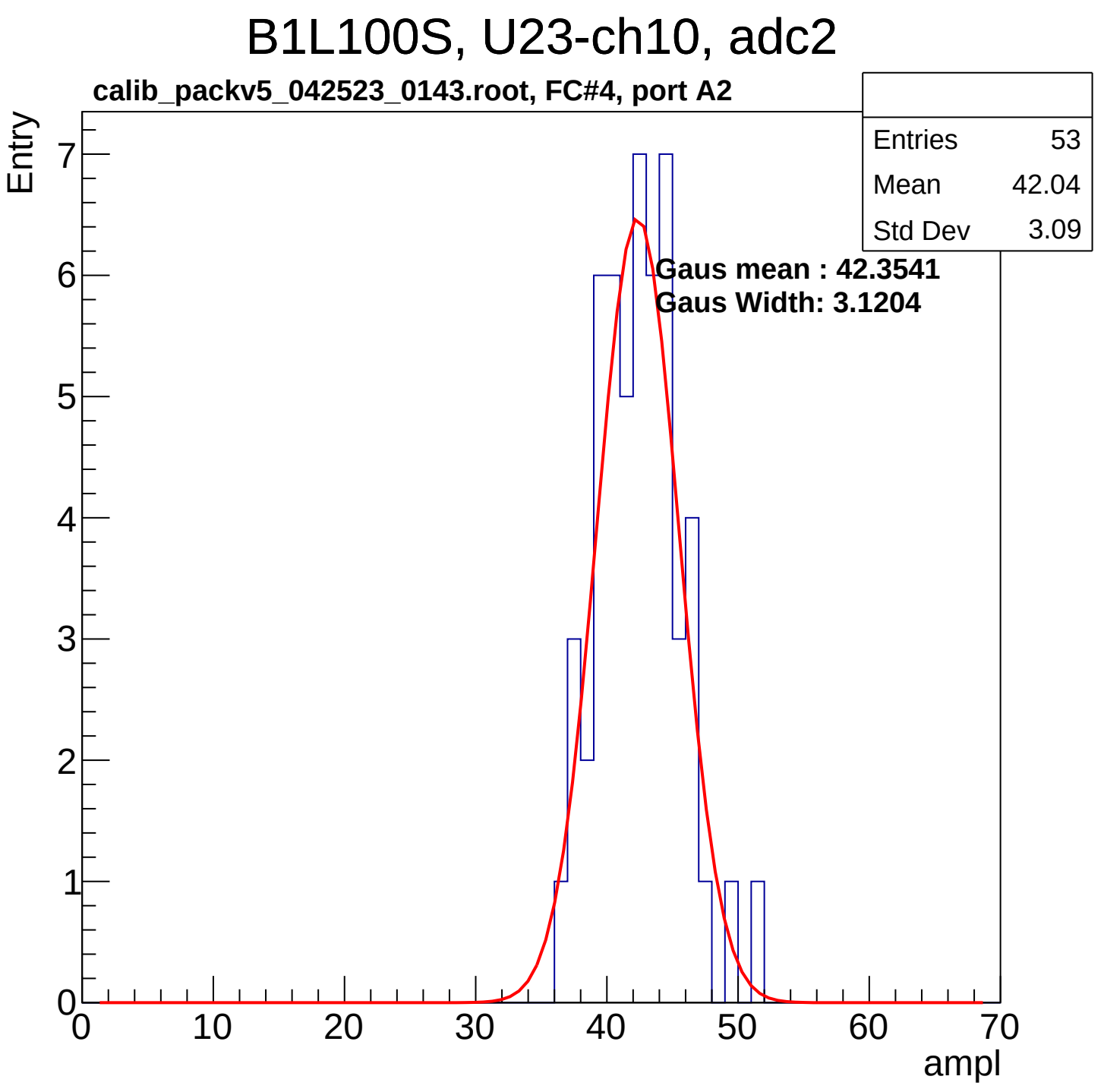
B1L100S, U23-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 42.04
Std Dev 3.09
Gaus mean : 42.3541
Gaus Width: 3.1204
Entry
ampl
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3
4
5
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0
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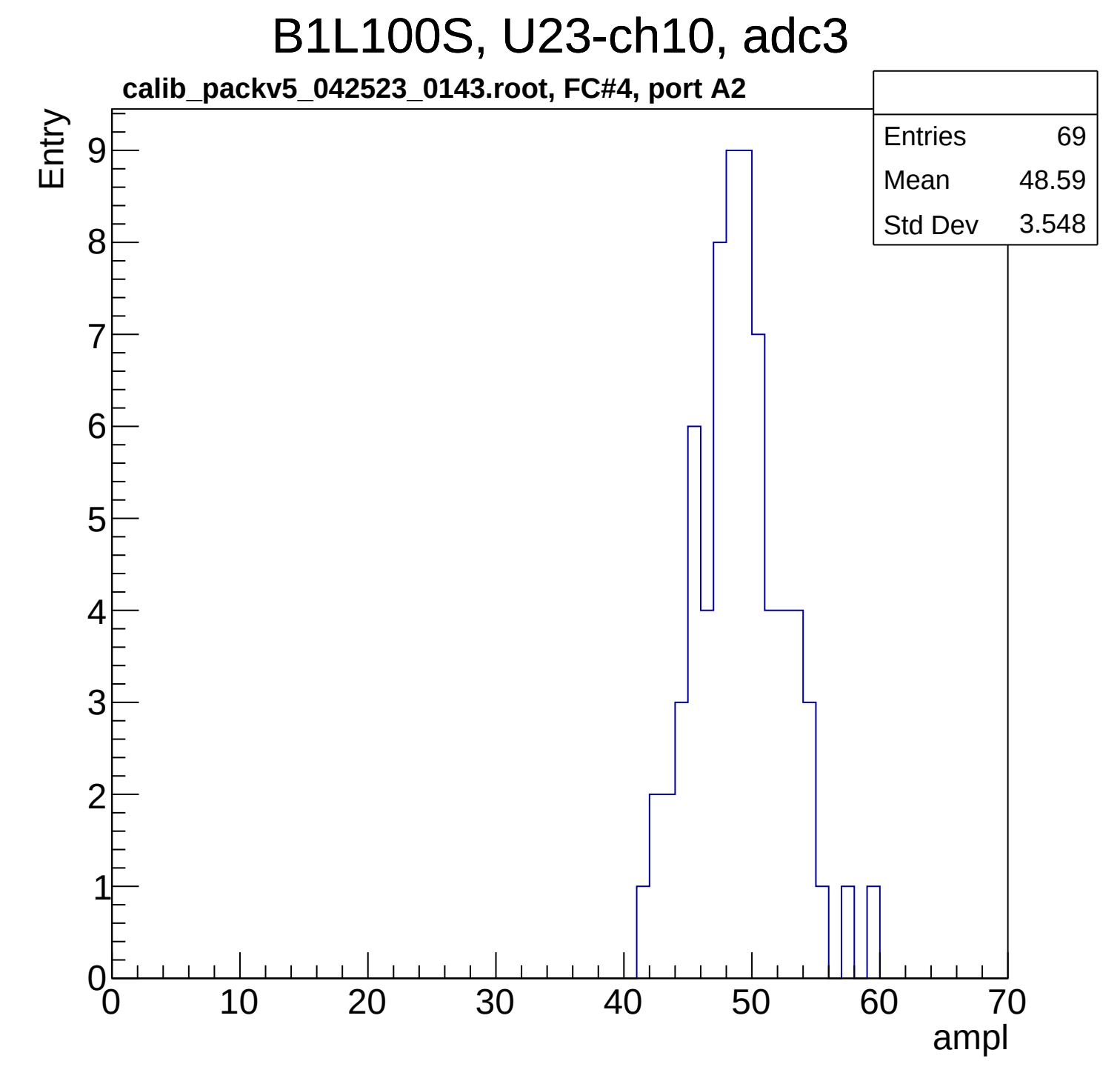
B1L100S, U23-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.59
Std Dev 3.548
Entry
ampl
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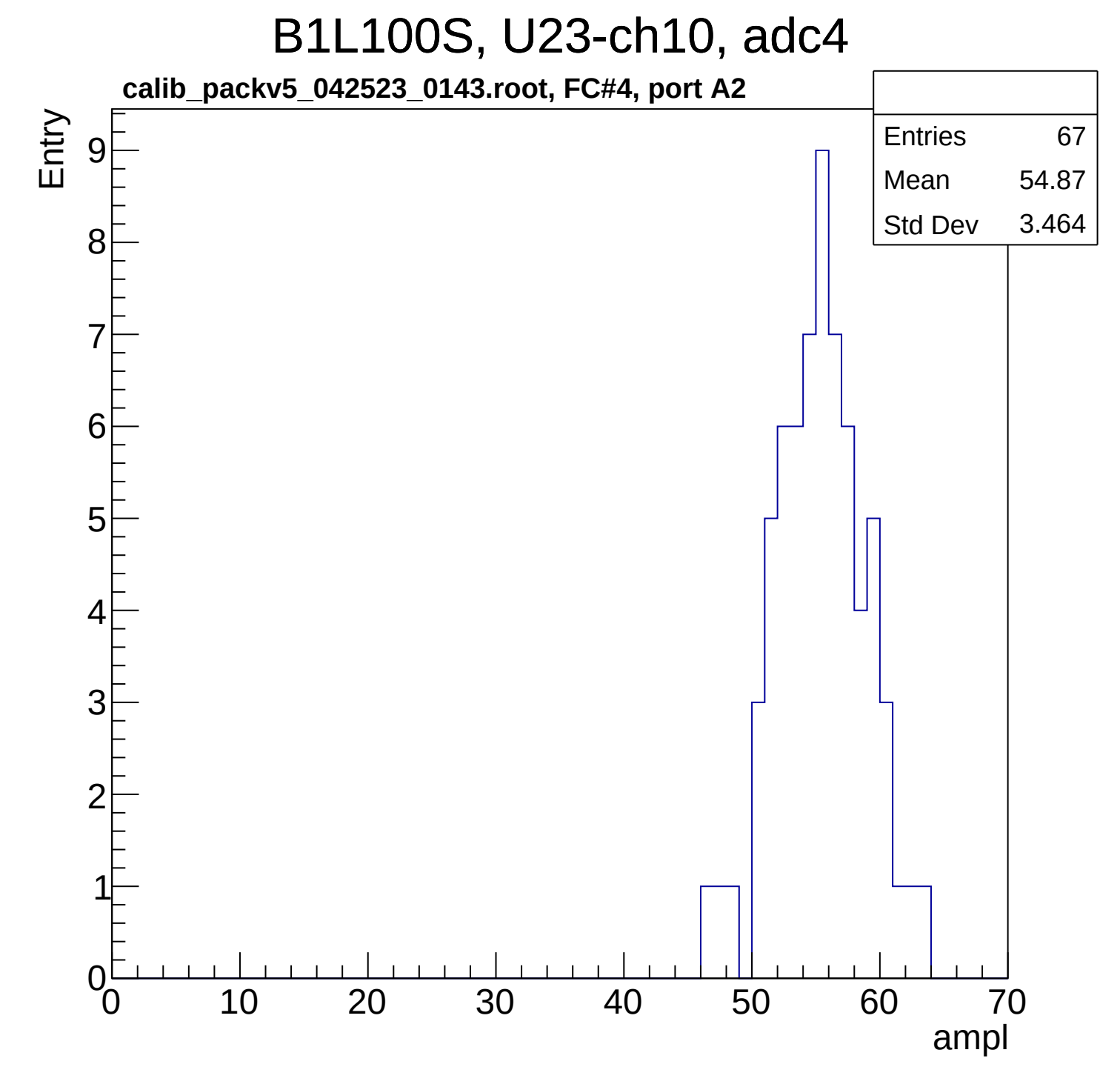
B1L100S, U23-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 54.87
Std Dev 3.464
Entry
ampl
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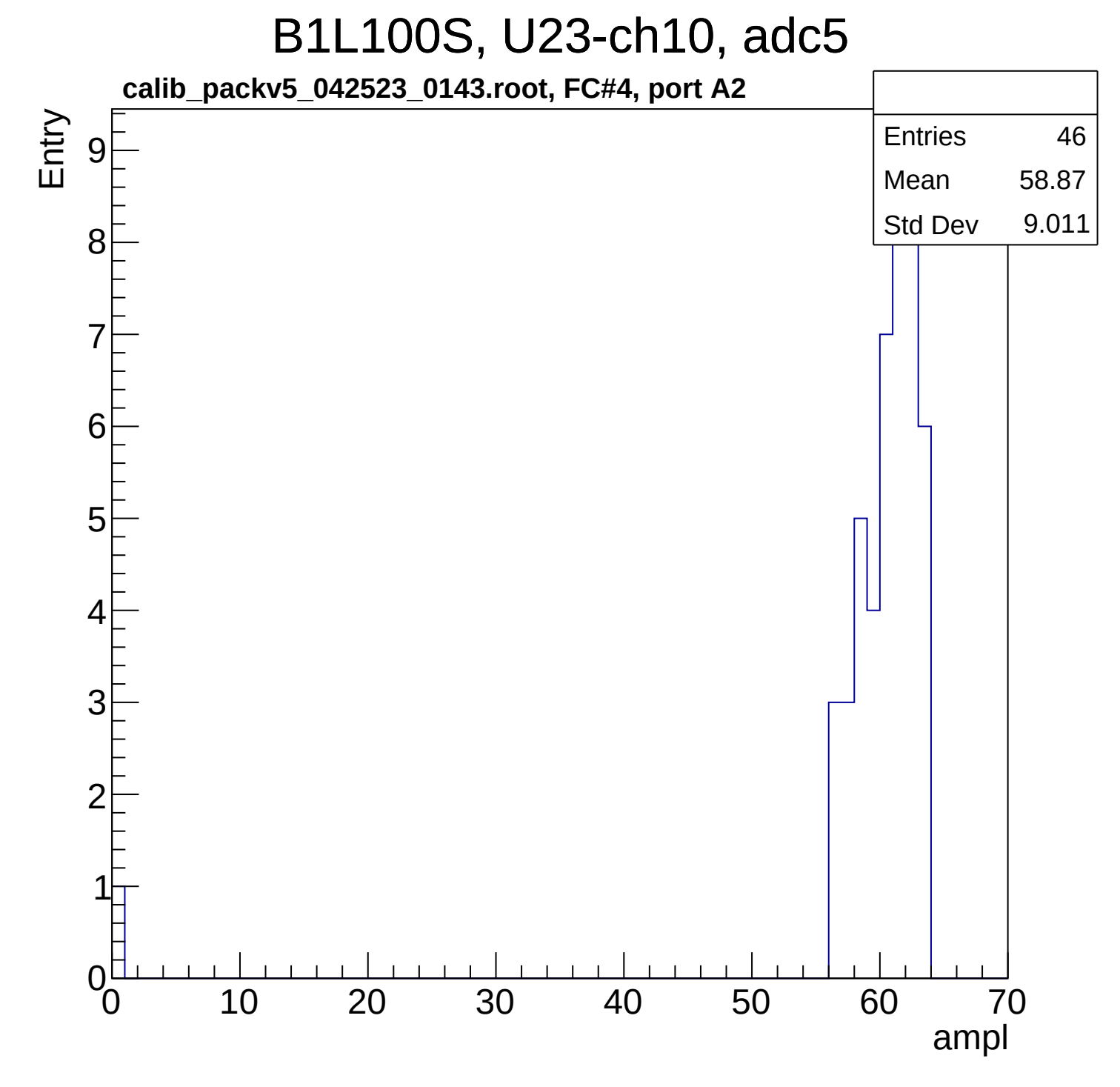
B1L100S, U23-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.87
Std Dev 9.011
Entry
ampl
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0
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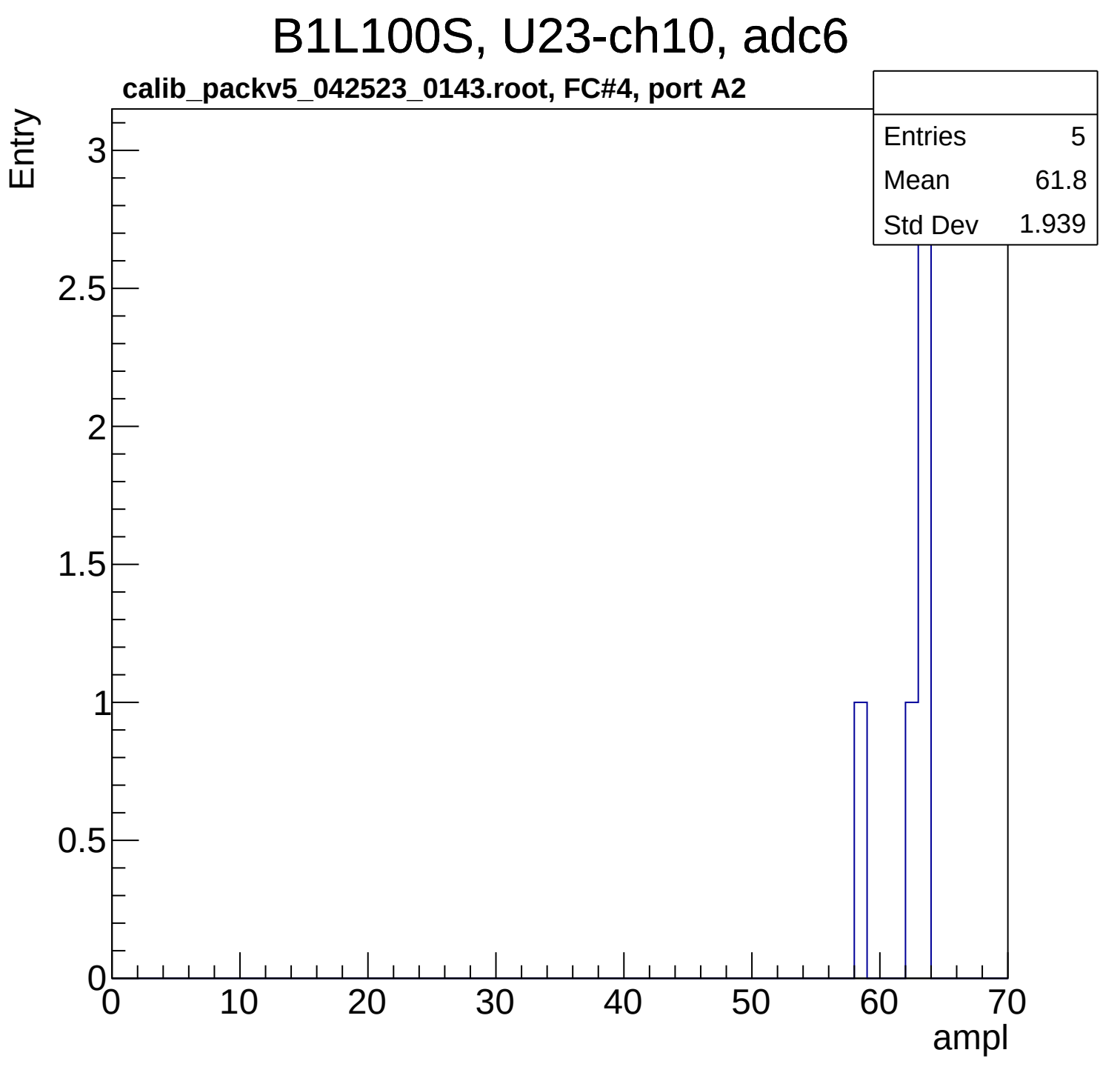
B1L100S, U23-ch10, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 1.939
Entry
ampl
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1
1.5
2
2.5
3
0
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20
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60
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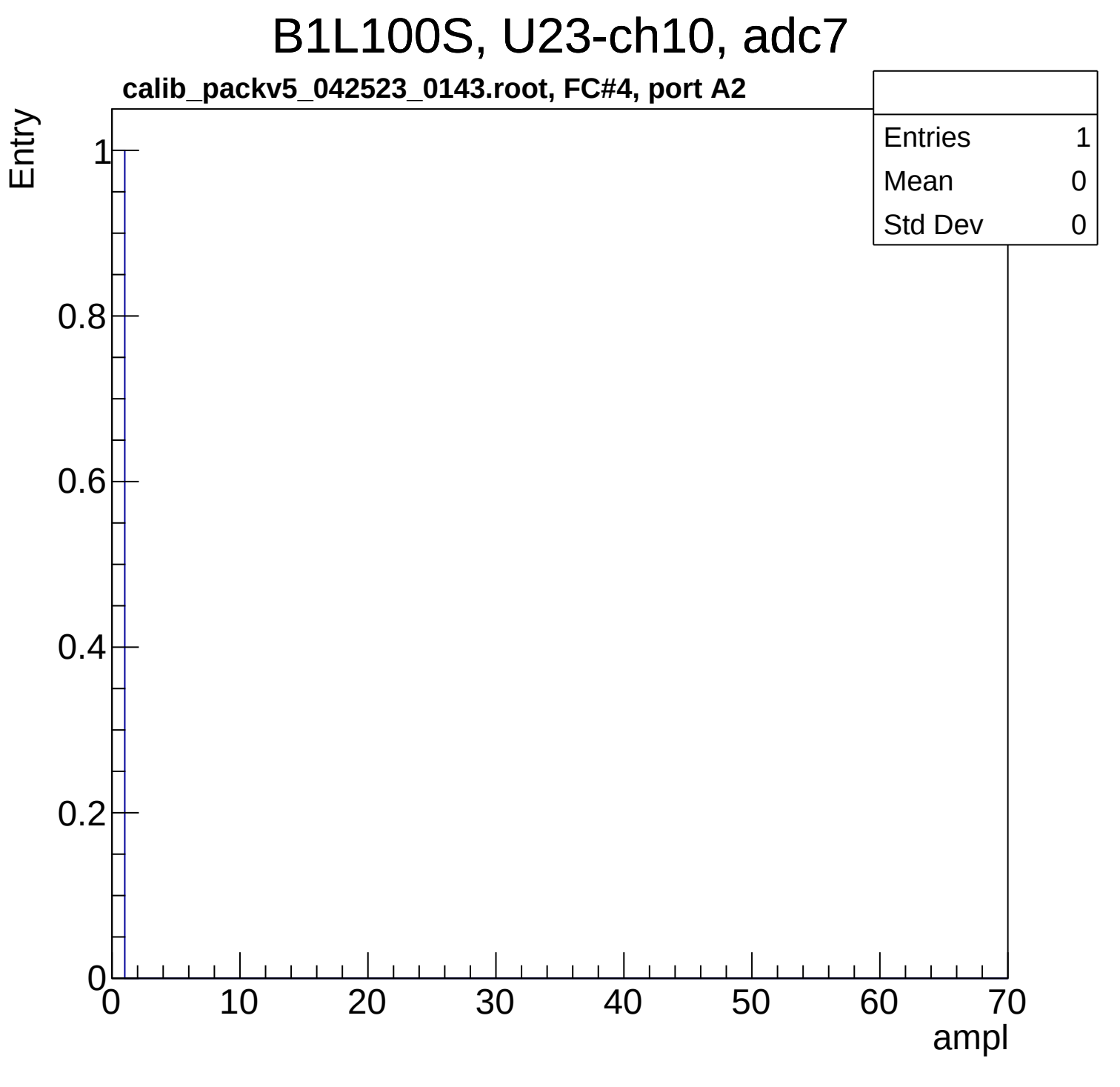
B1L100S, U23-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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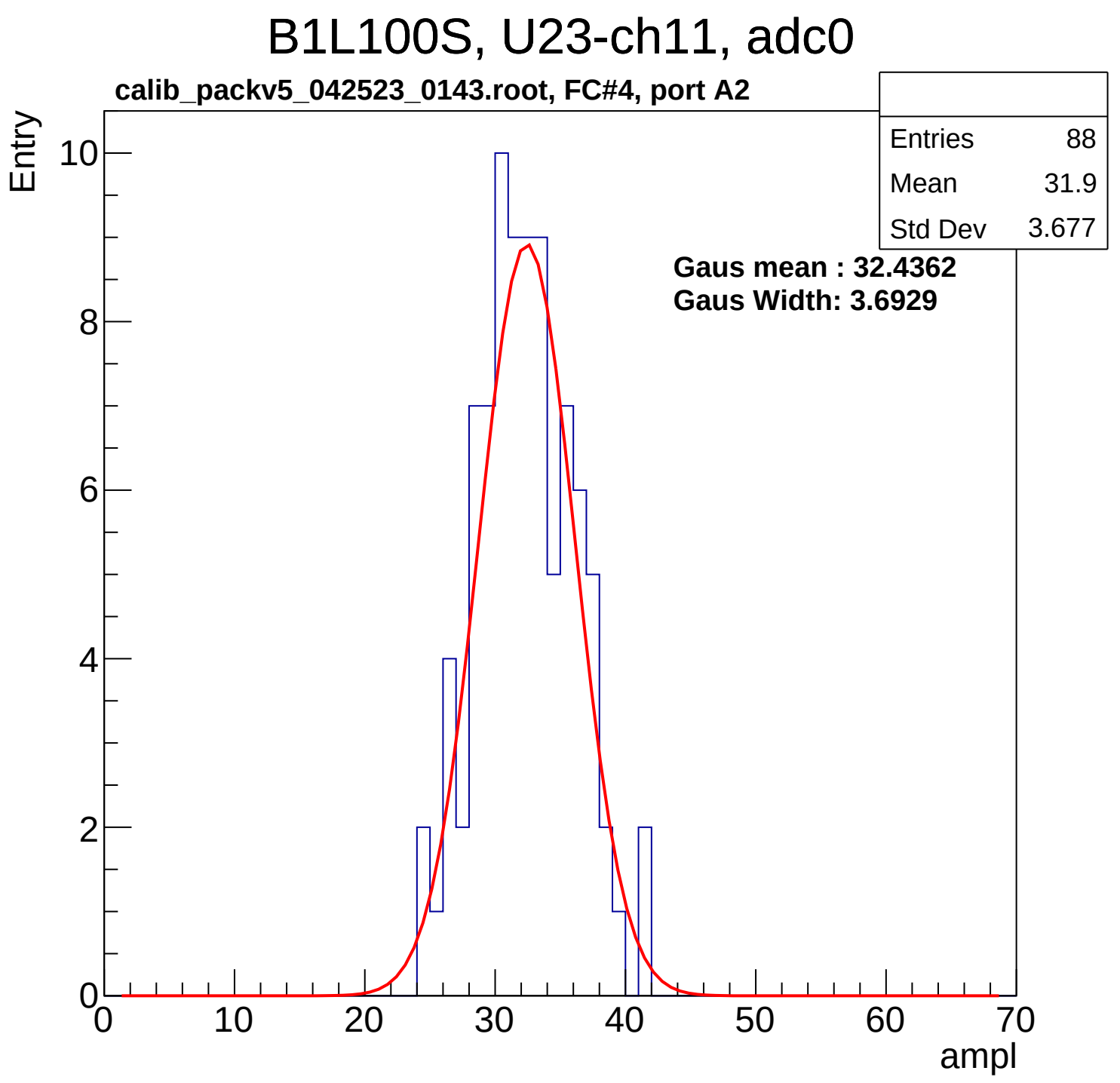
50
60
70
ampl


B1L100S, U23-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 31.9
Std Dev 3.677
Gaus mean : 32.4362
Gaus Width: 3.6929
Entry
ampl
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6
8
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0
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60
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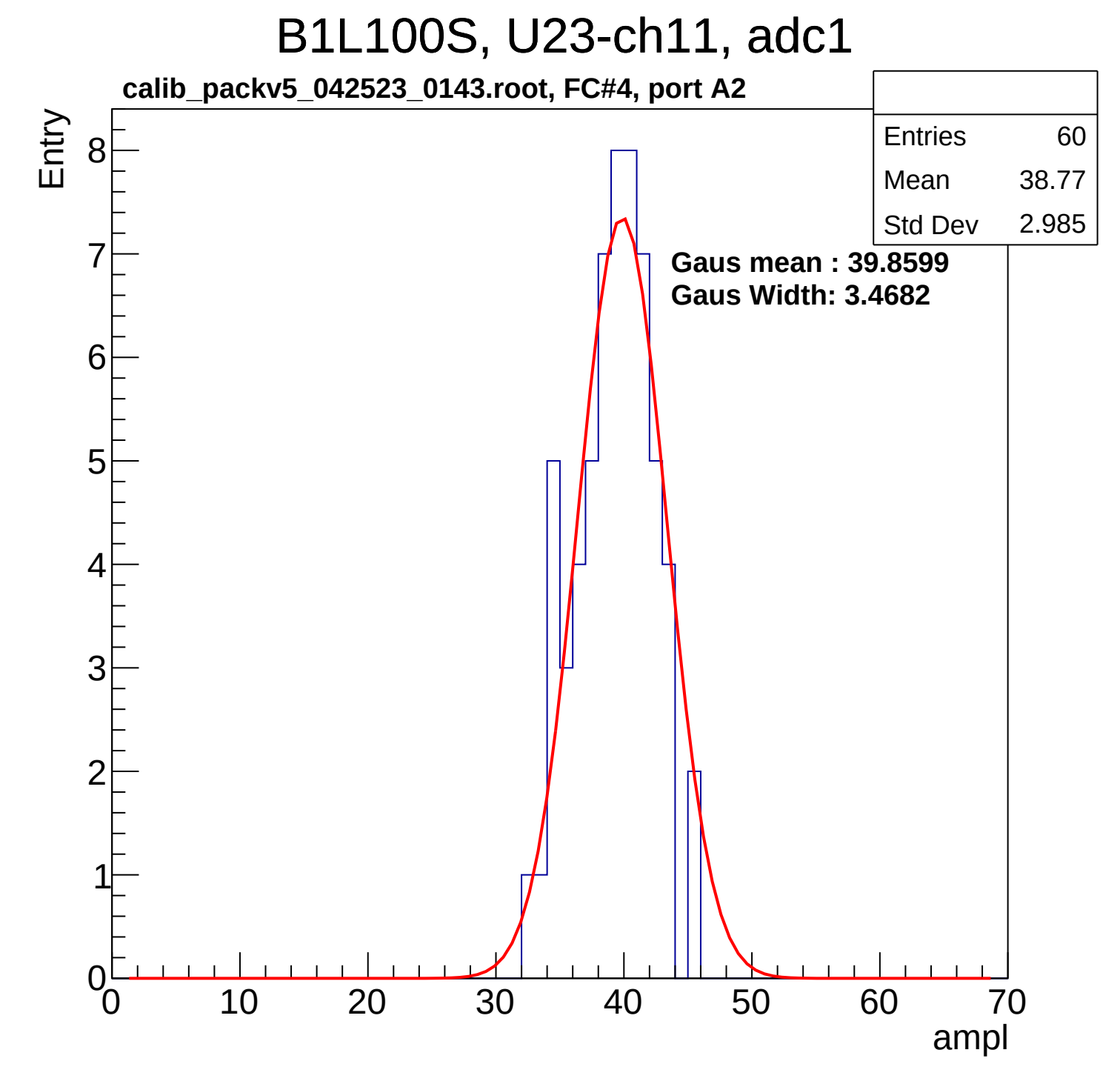
B1L100S, U23-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 38.77
Std Dev 2.985
Gaus mean : 39.8599
Gaus Width: 3.4682
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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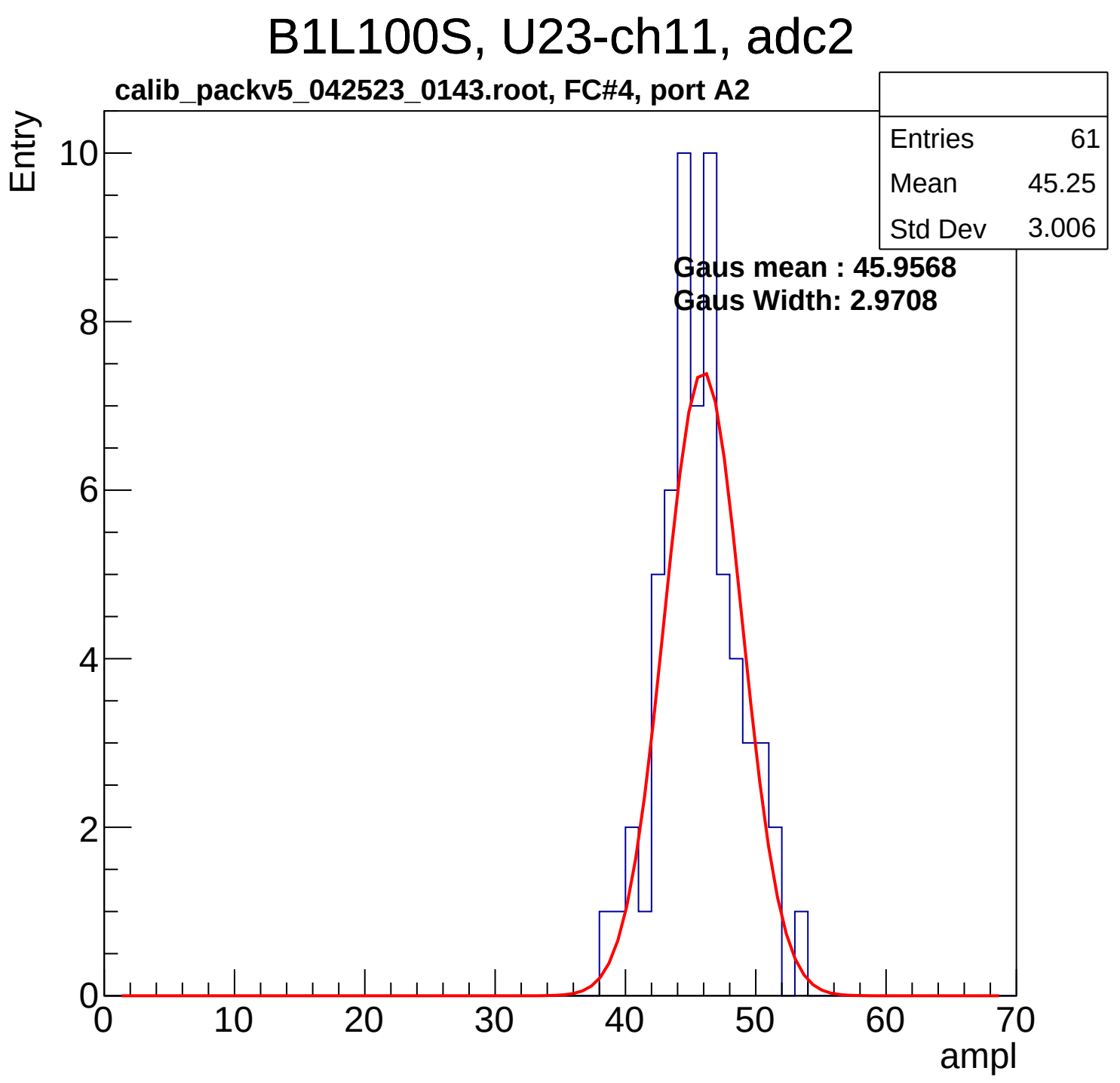
B1L100S, U23-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 45.25
Std Dev 3.006
Gaus mean : 45.9568
Gaus Width: 2.9708
Entry
ampl
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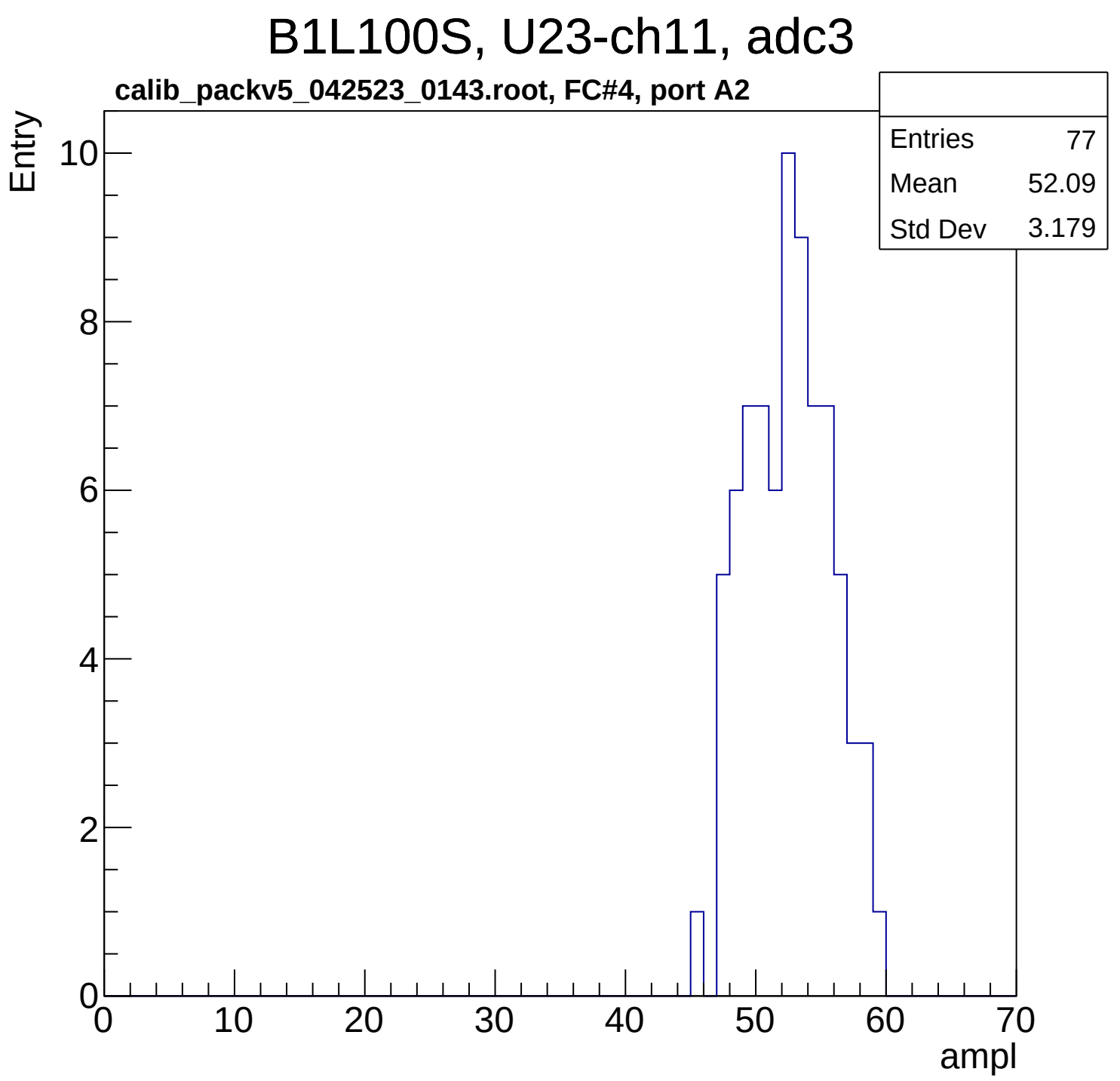
B1L100S, U23-ch11, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 52.09
Std Dev 3.179
Entry
ampl
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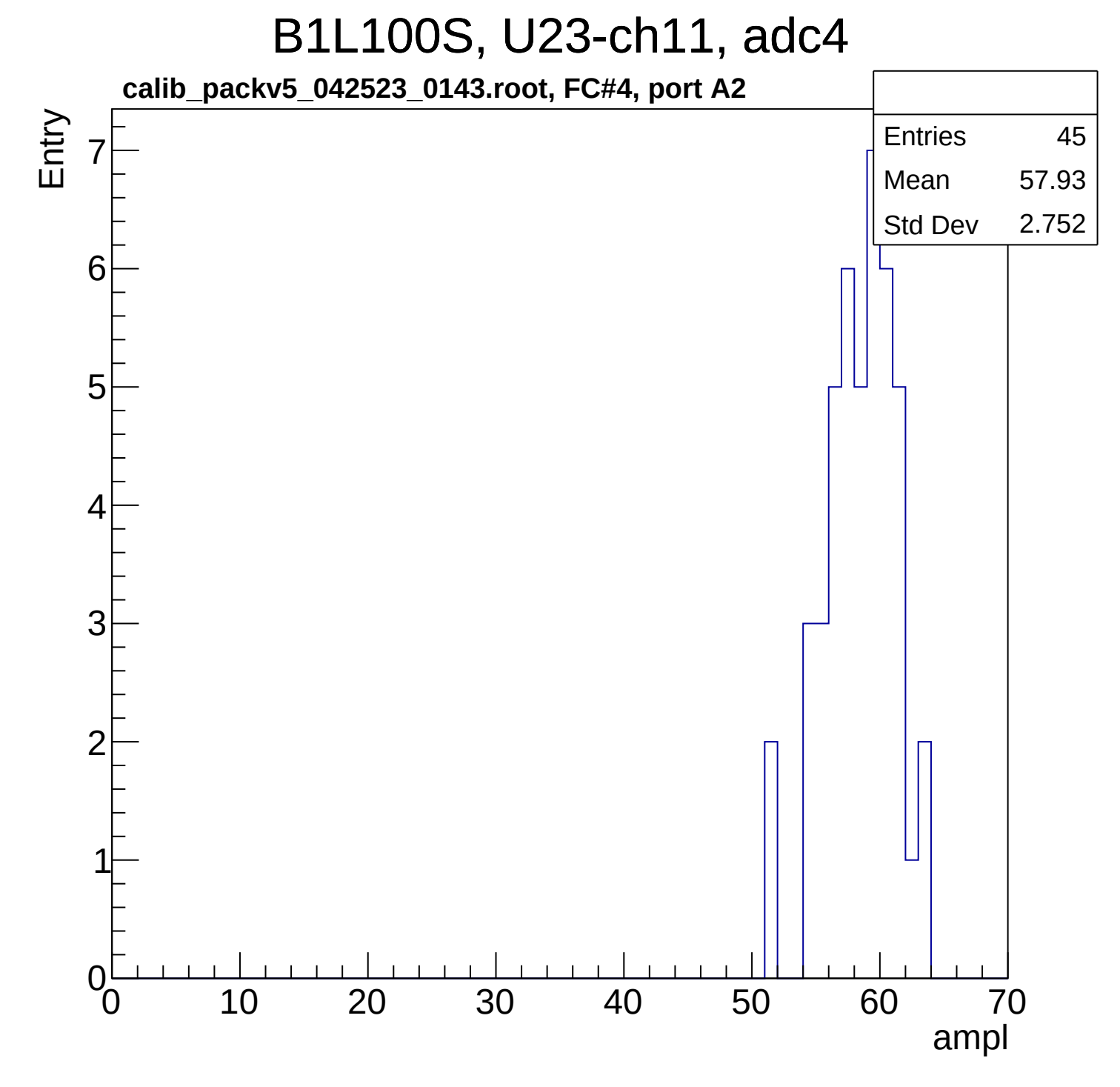
B1L100S, U23-ch11, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 57.93
Std Dev 2.752
Entry
ampl
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6
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0
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20
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60
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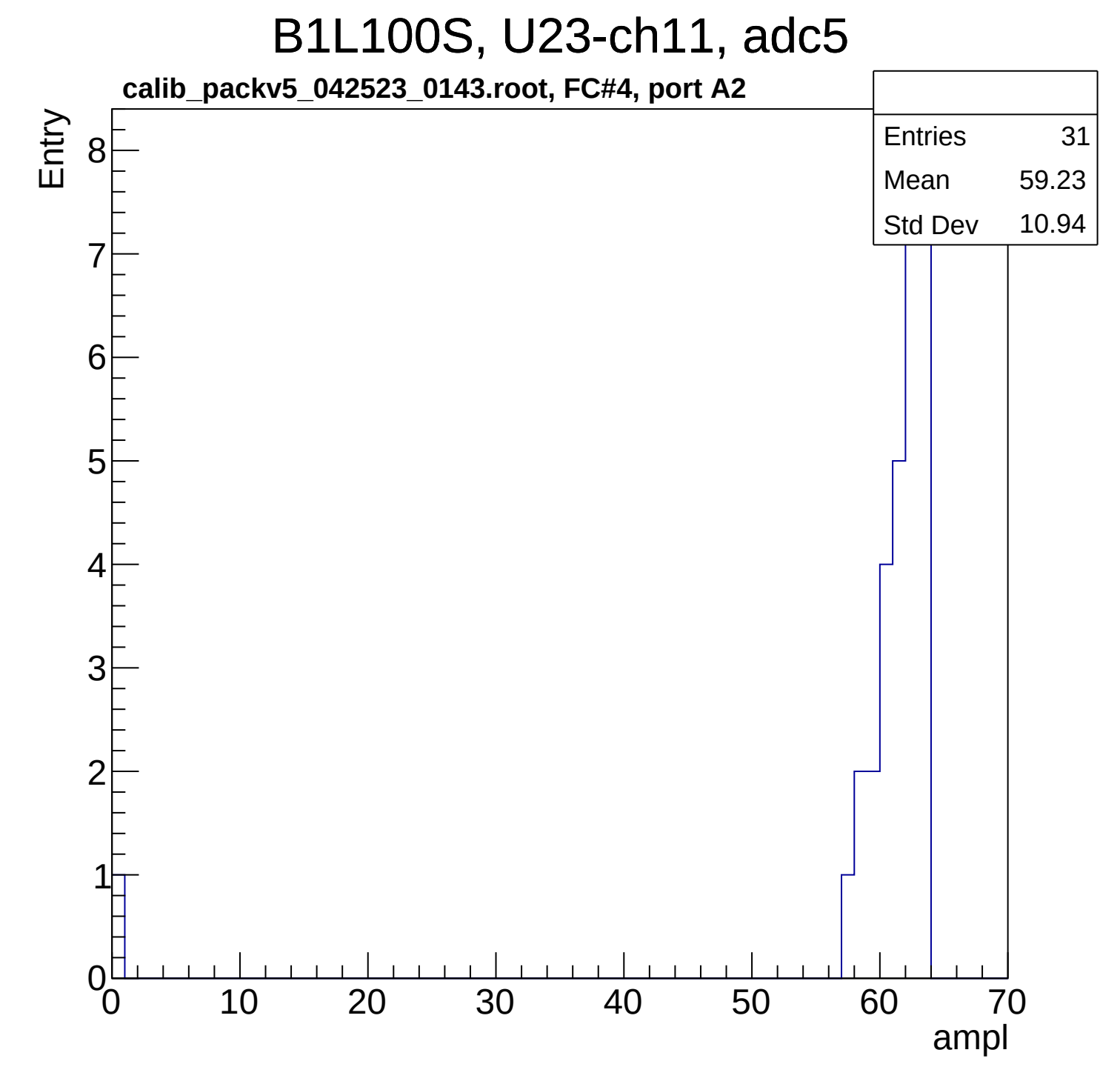
B1L100S, U23-ch11, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59.23
Std Dev 10.94
Entry
0
1
2
3
4
5
6
7
8
0
10
20
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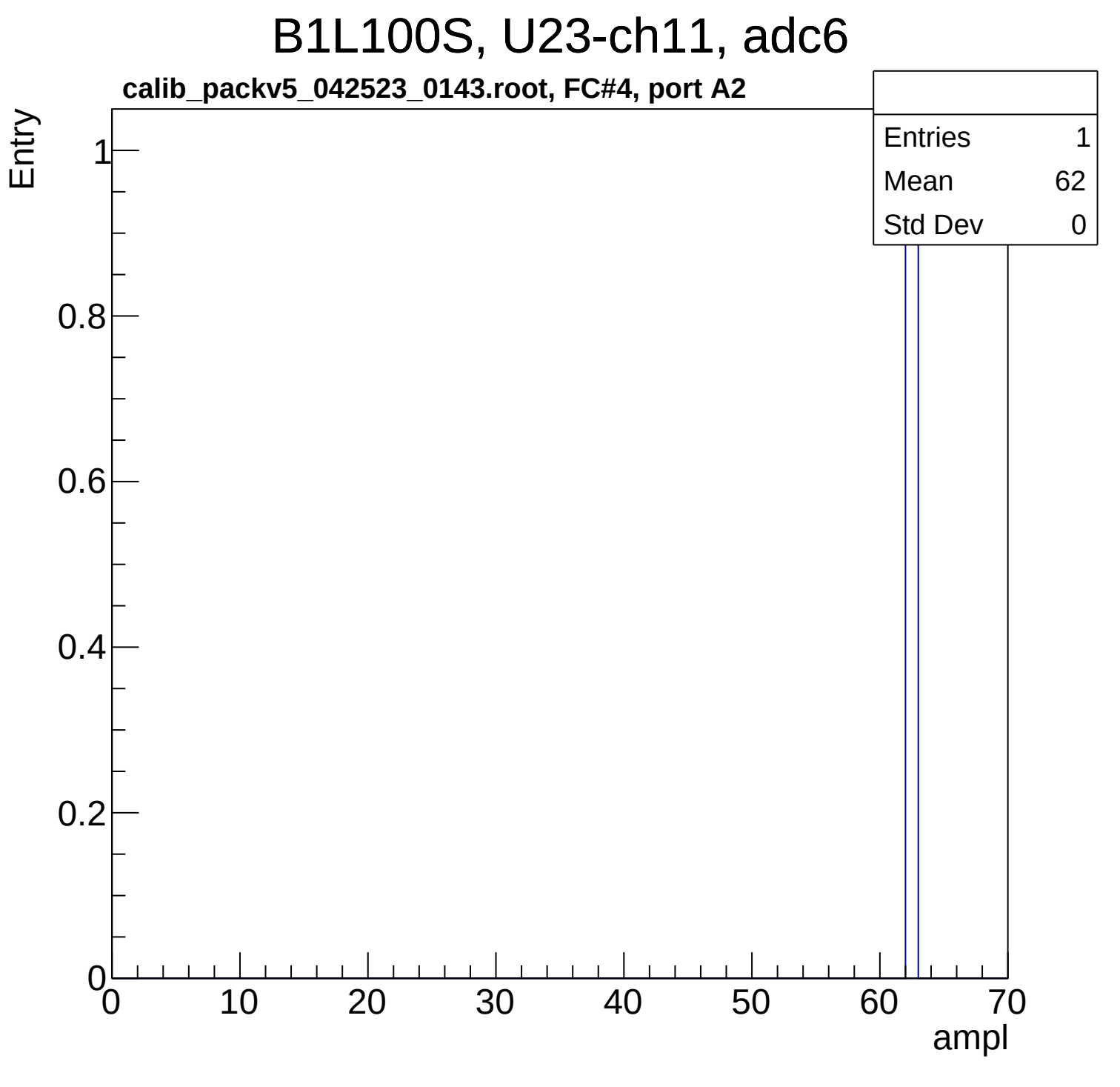
70
ampl


B1L100S, U23-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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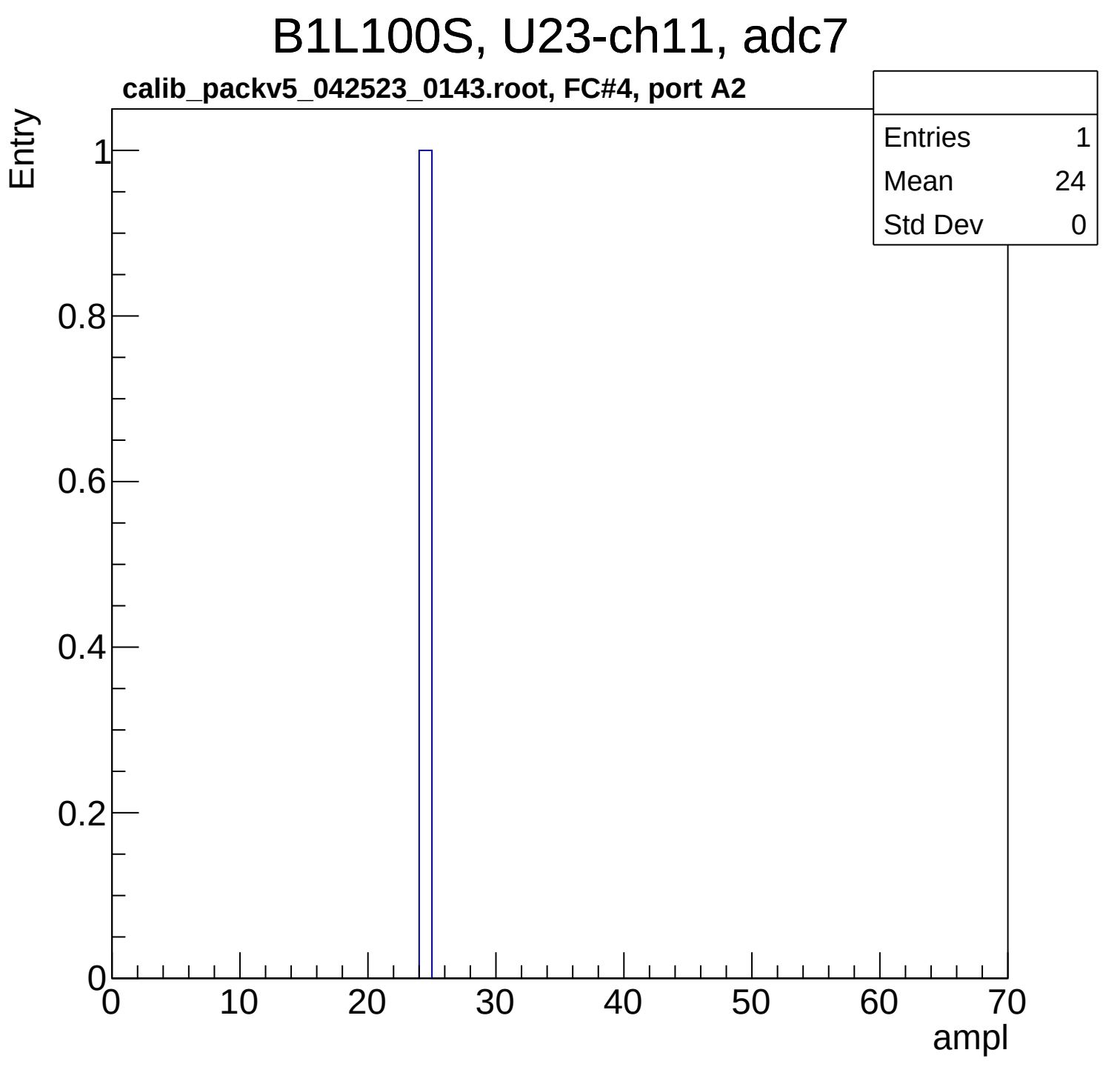
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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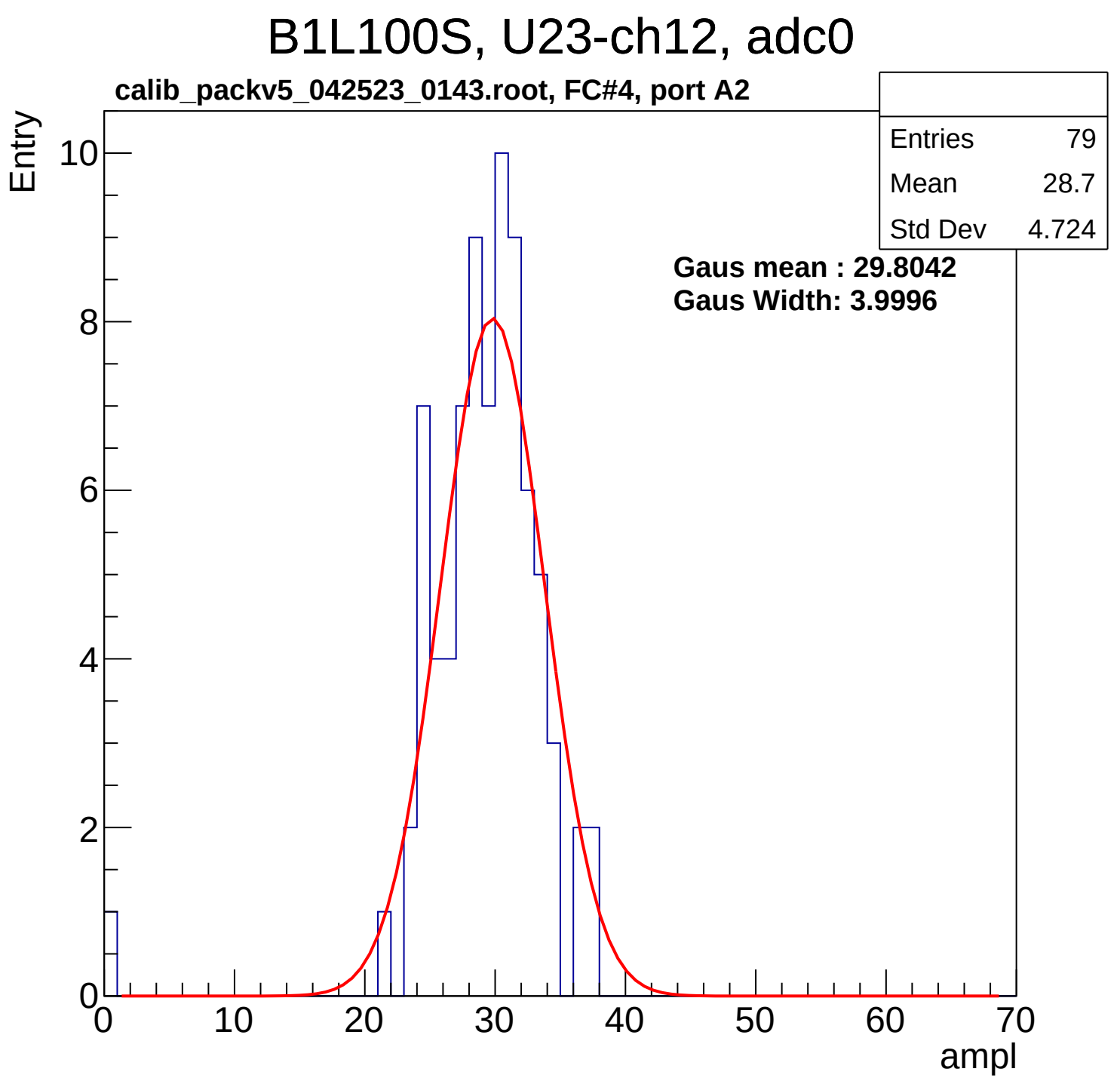
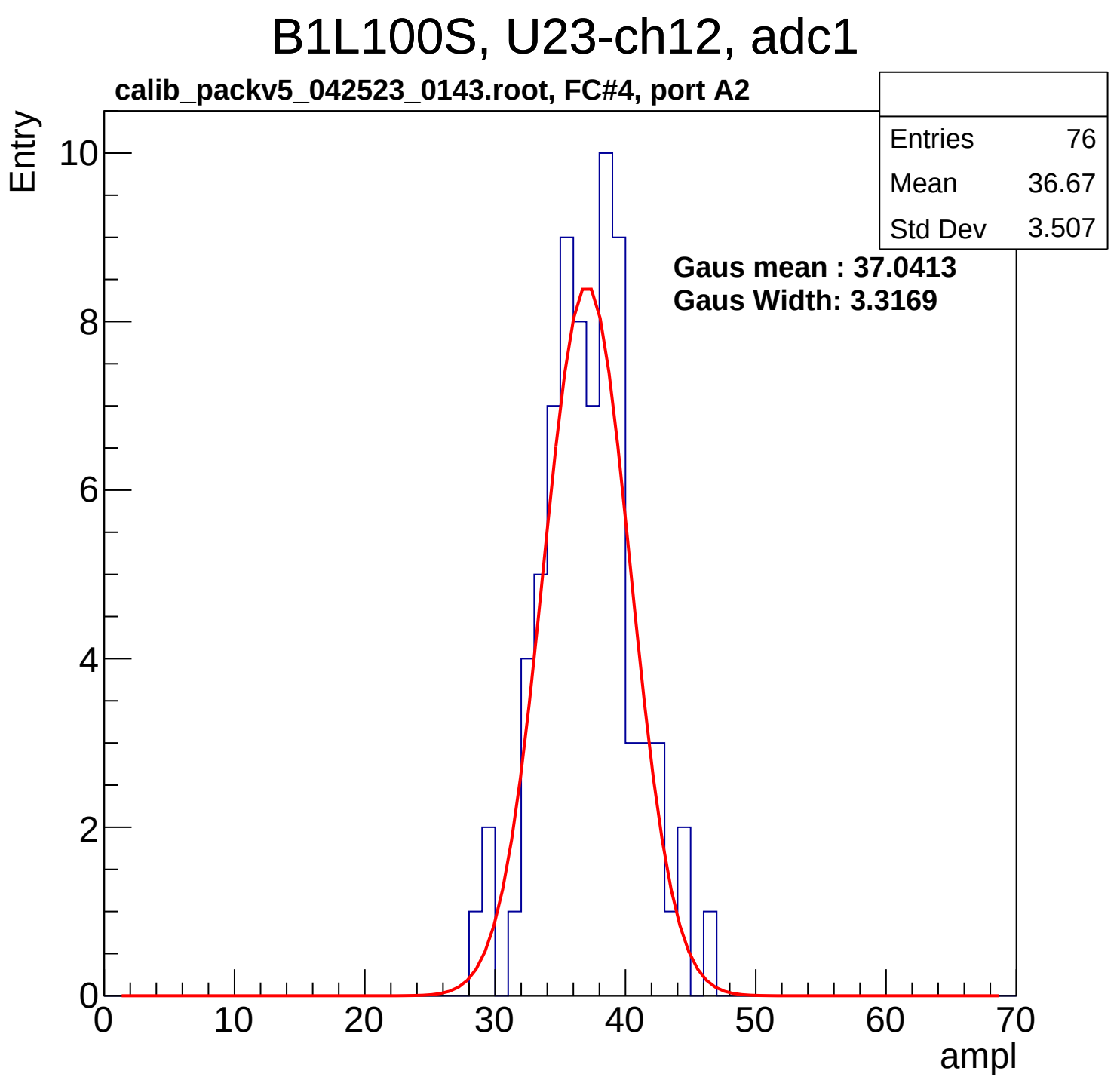
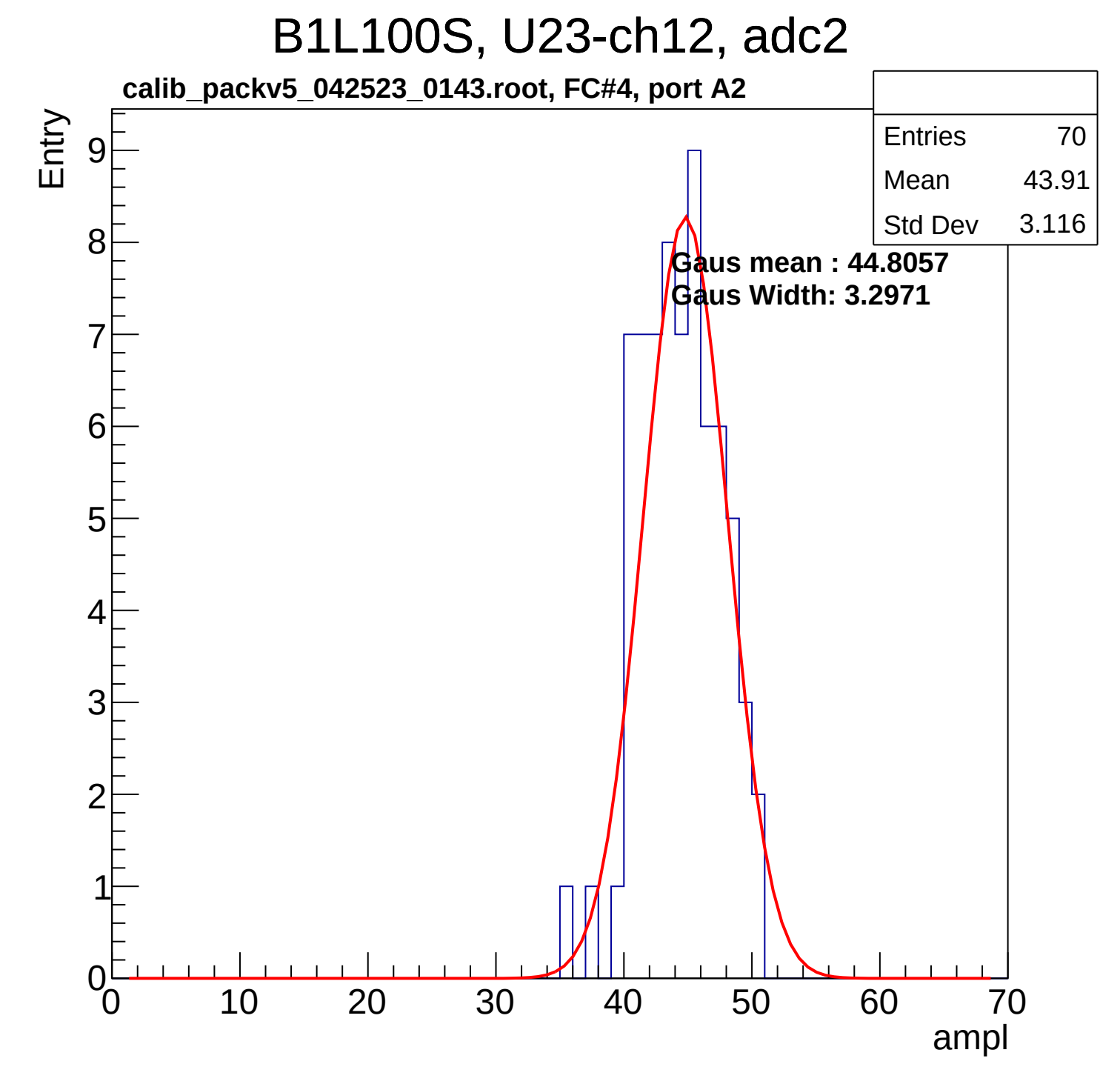
50
60
70
ampl


B1L100S, U23-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.7
Std Dev 4.724
Gaus mean : 29.8042
Gaus Width: 3.9996
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.67
Std Dev 3.507
Gaus mean : 37.0413
Gaus Width: 3.3169
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 43.91
Std Dev 3.116
Gaus mean : 44.8057
Gaus Width: 3.2971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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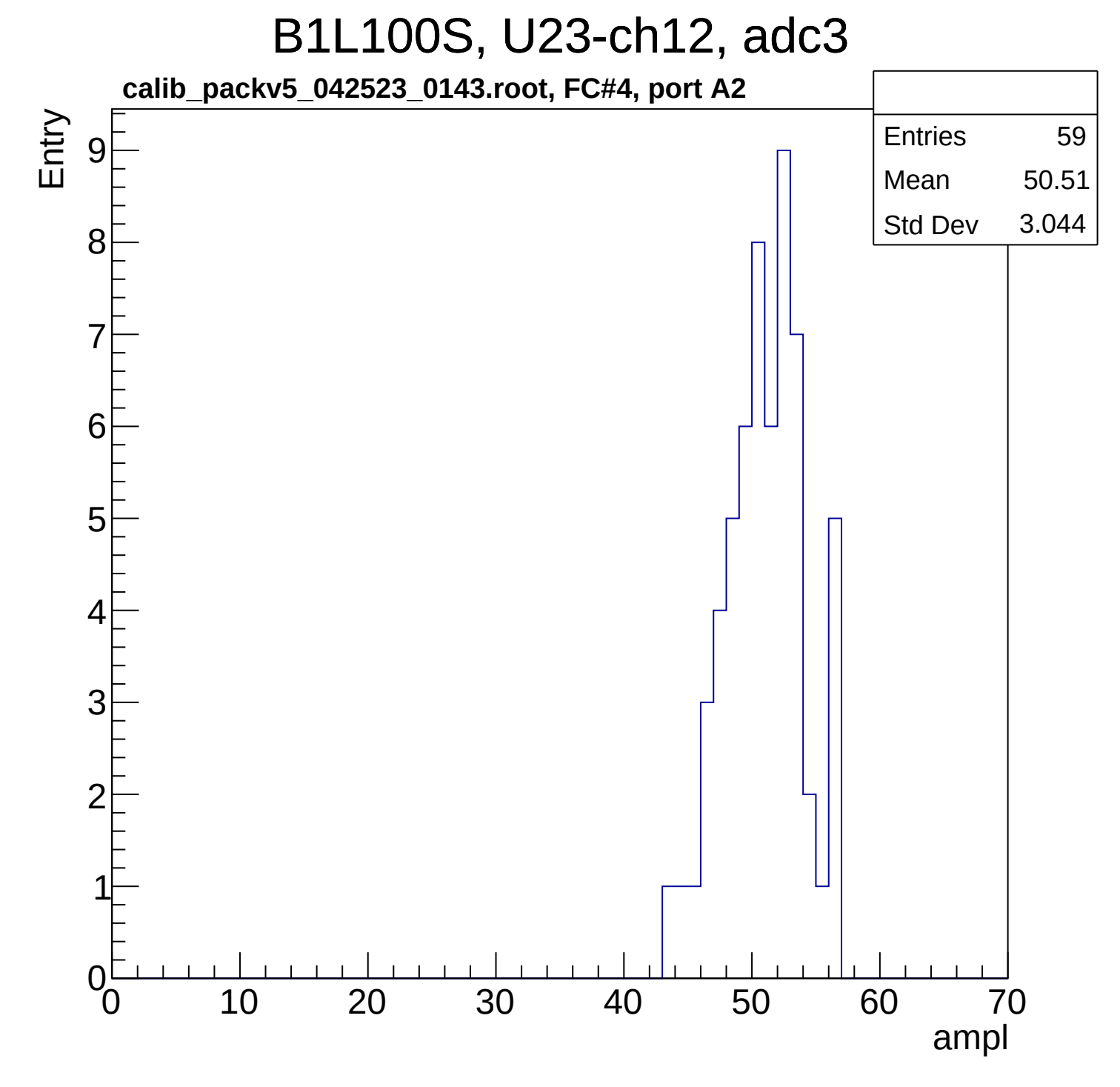
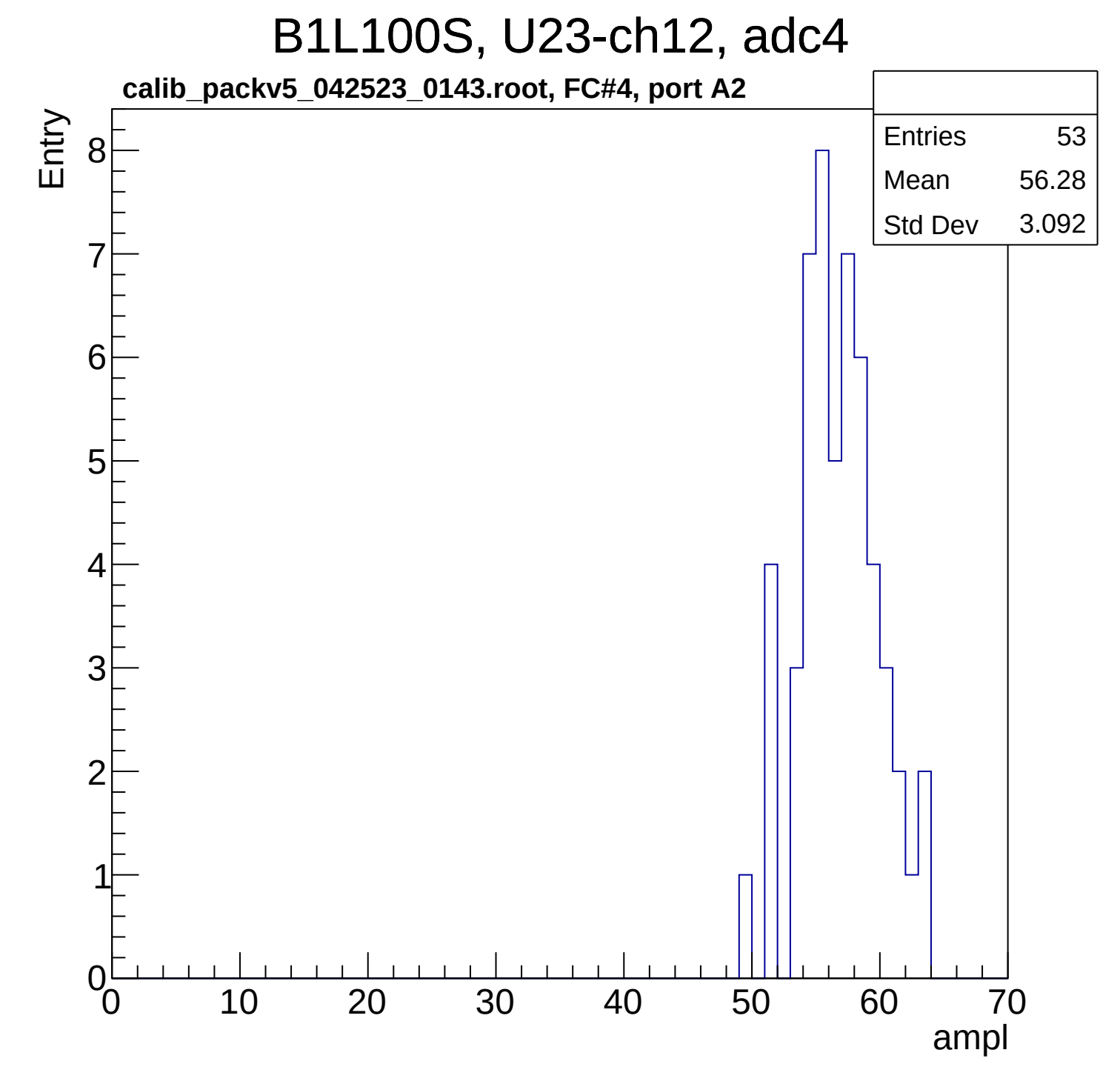

B1L100S, U23-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 50.51
Std Dev 3.044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.28
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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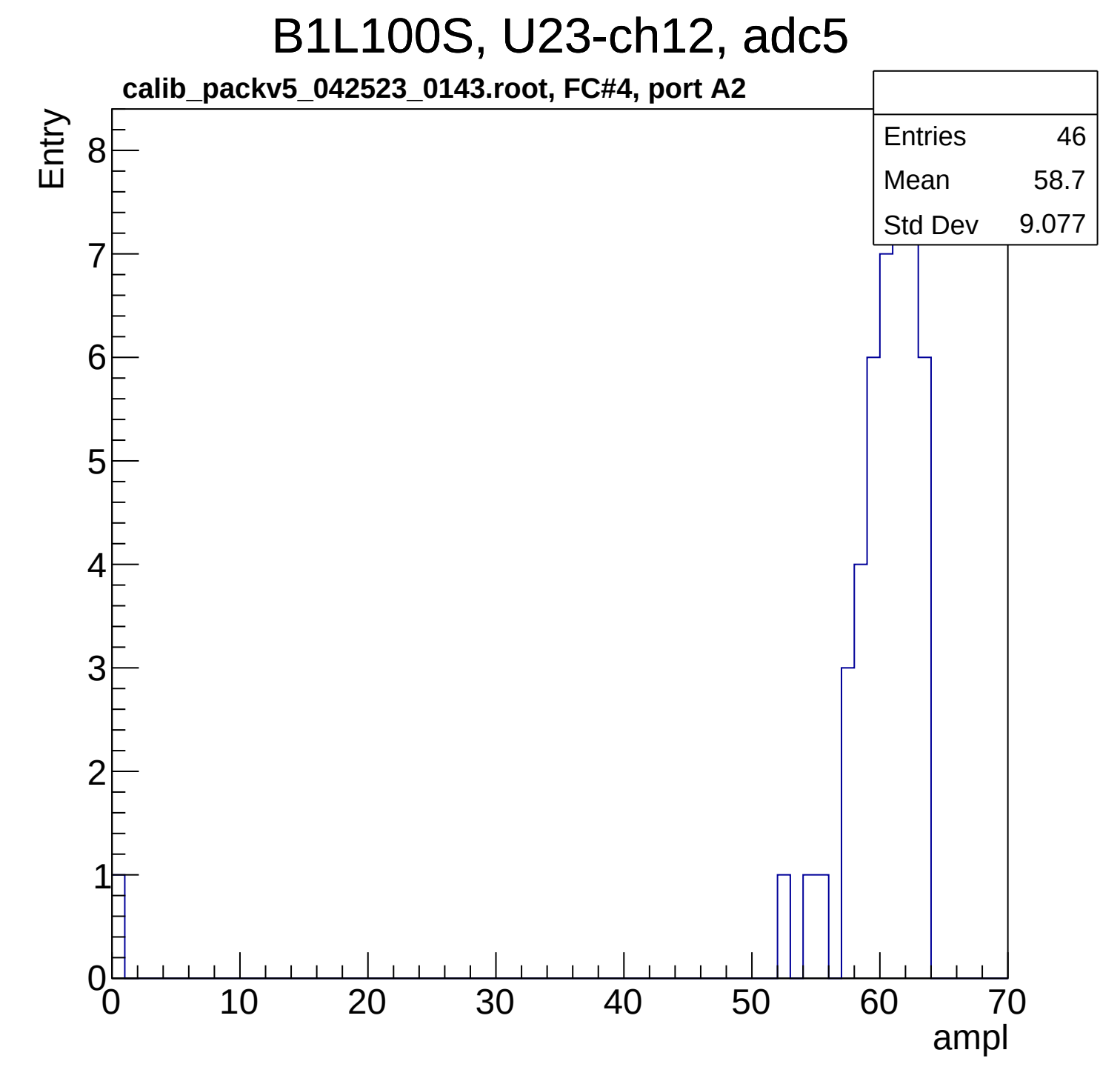
B1L100S, U23-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.7
Std Dev 9.077
Entry
ampl
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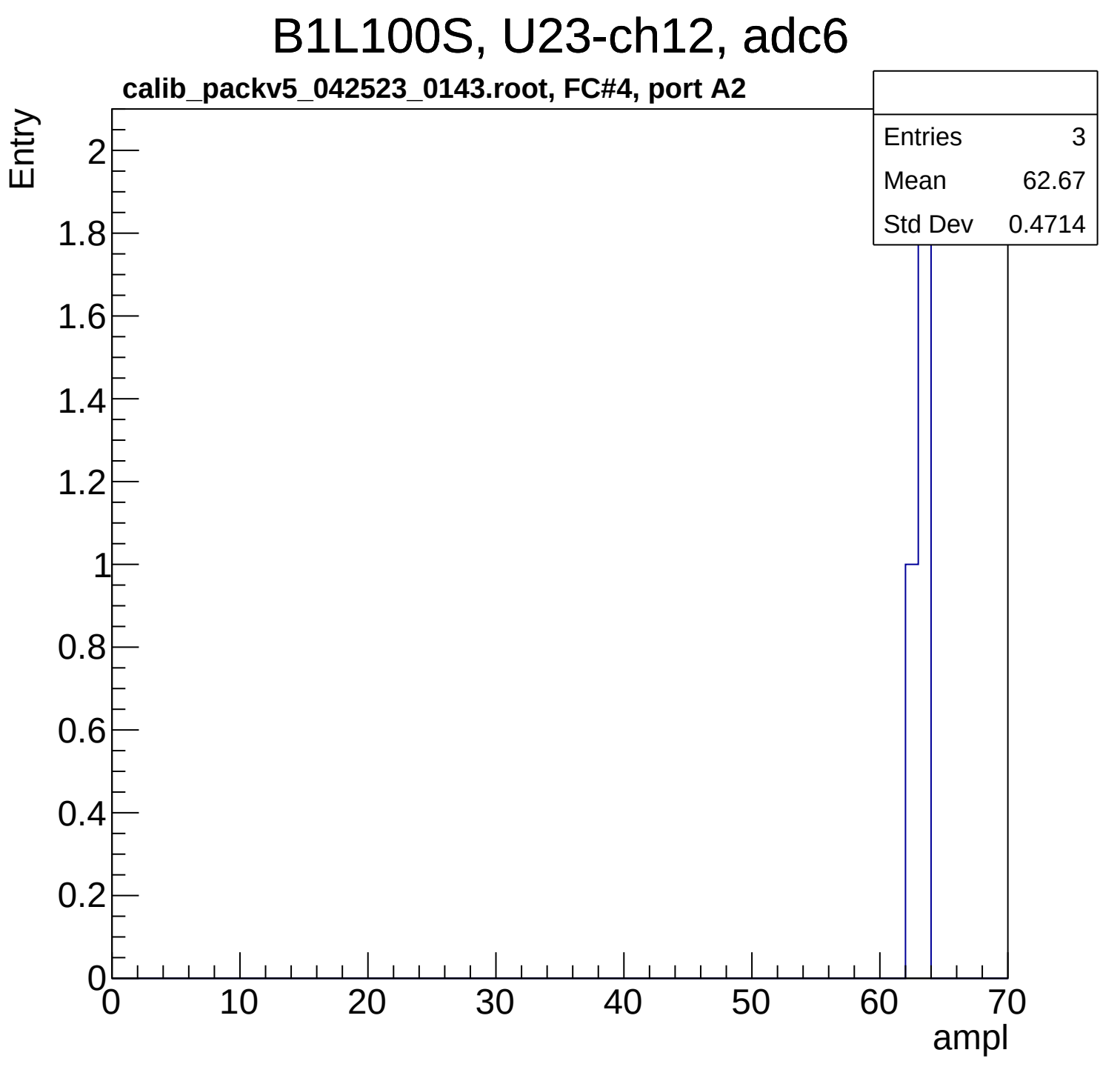
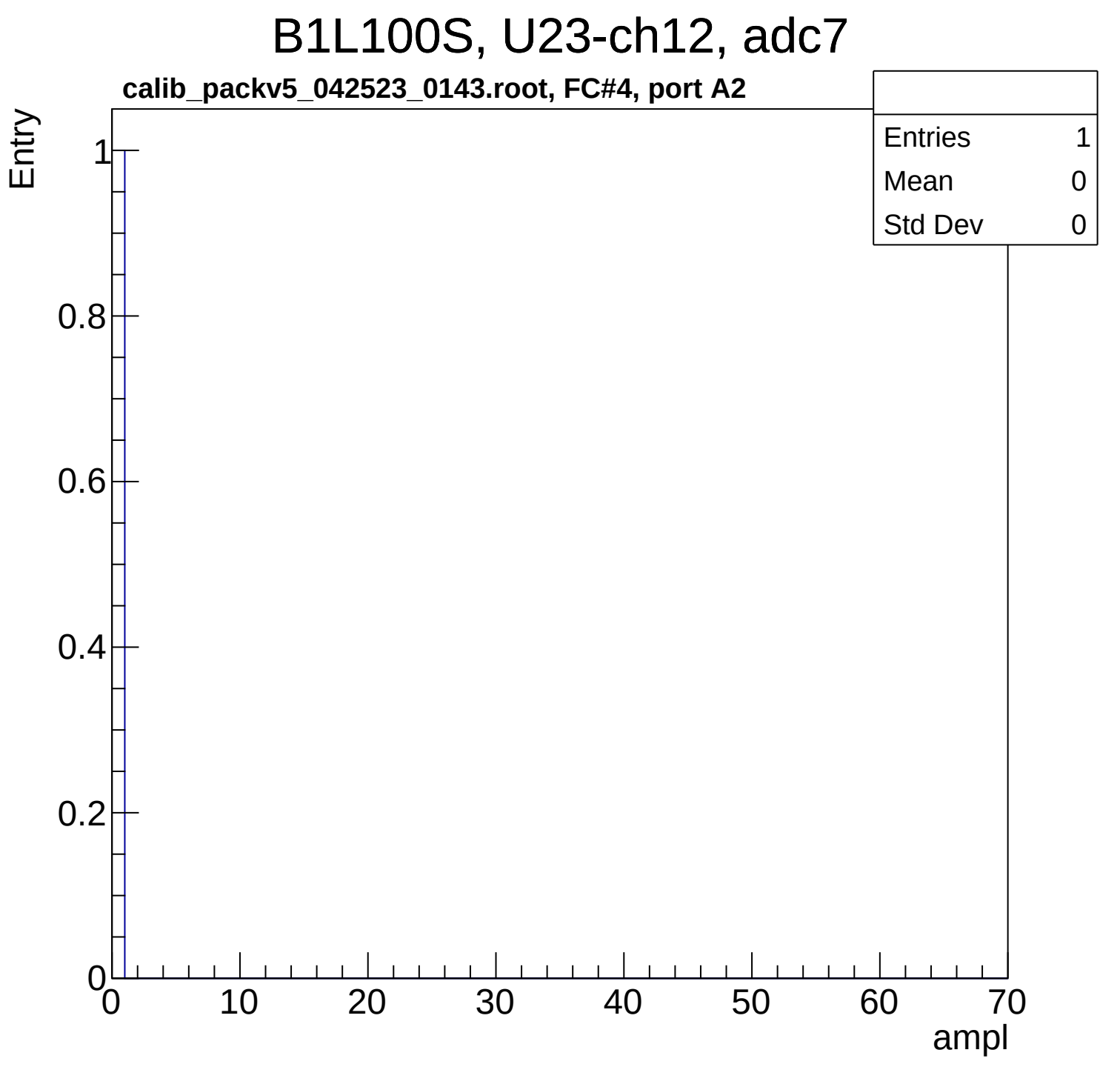
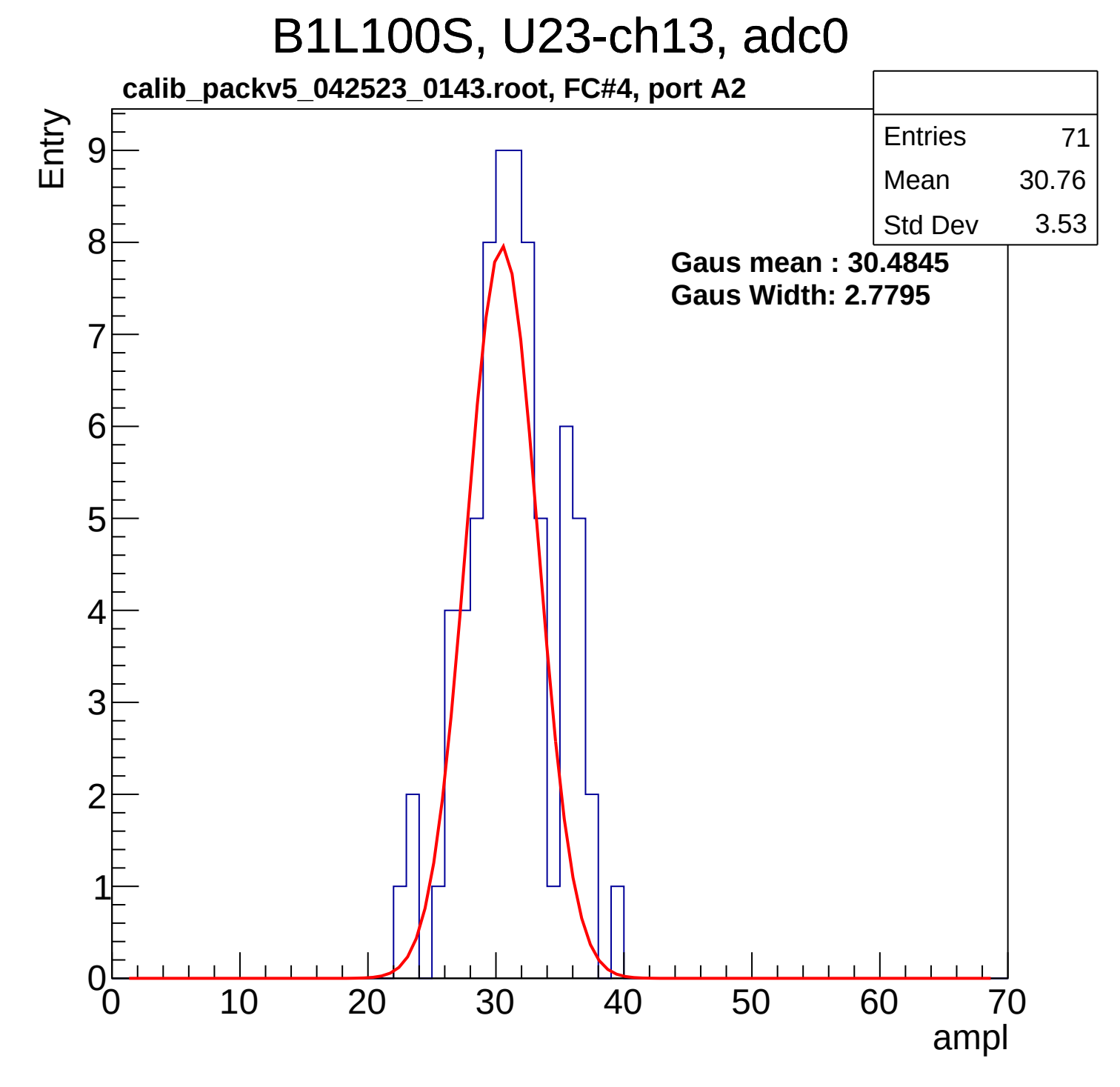
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch12, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.76
Std Dev 3.53
Gaus mean : 30.4845
Gaus Width: 2.7795
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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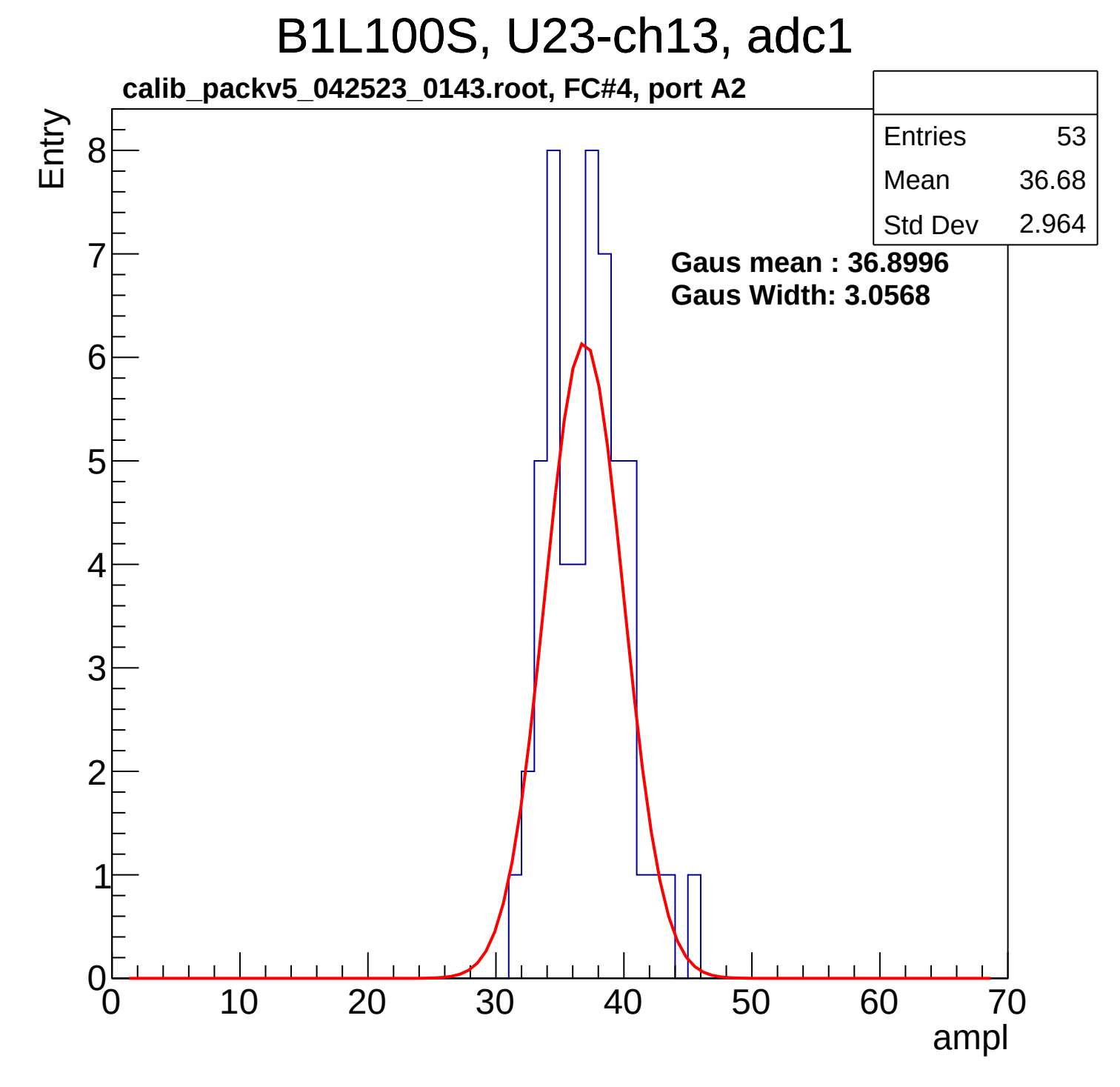
B1L100S, U23-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 36.68
Std Dev 2.964
Gaus mean : 36.8996
Gaus Width: 3.0568
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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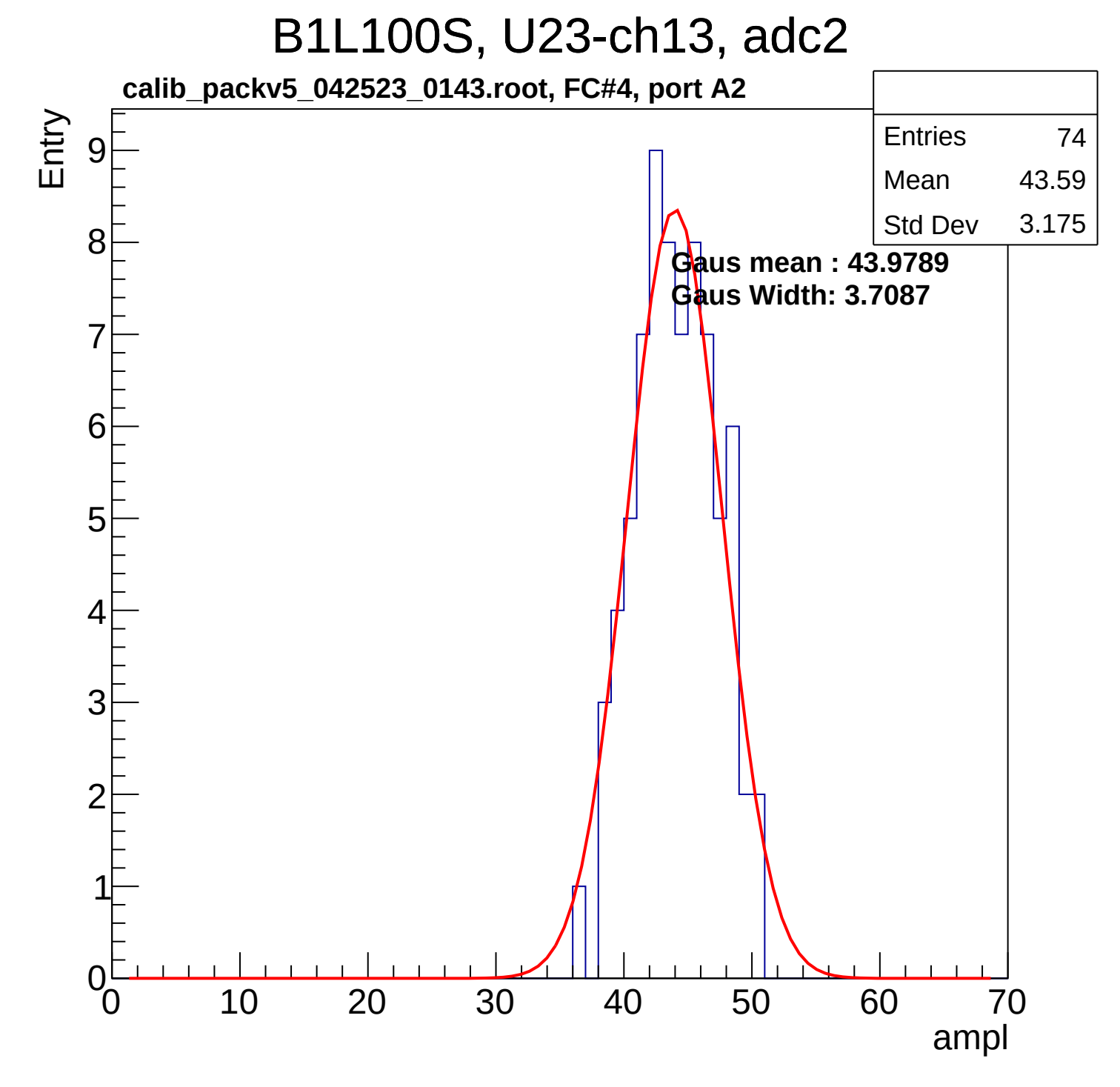
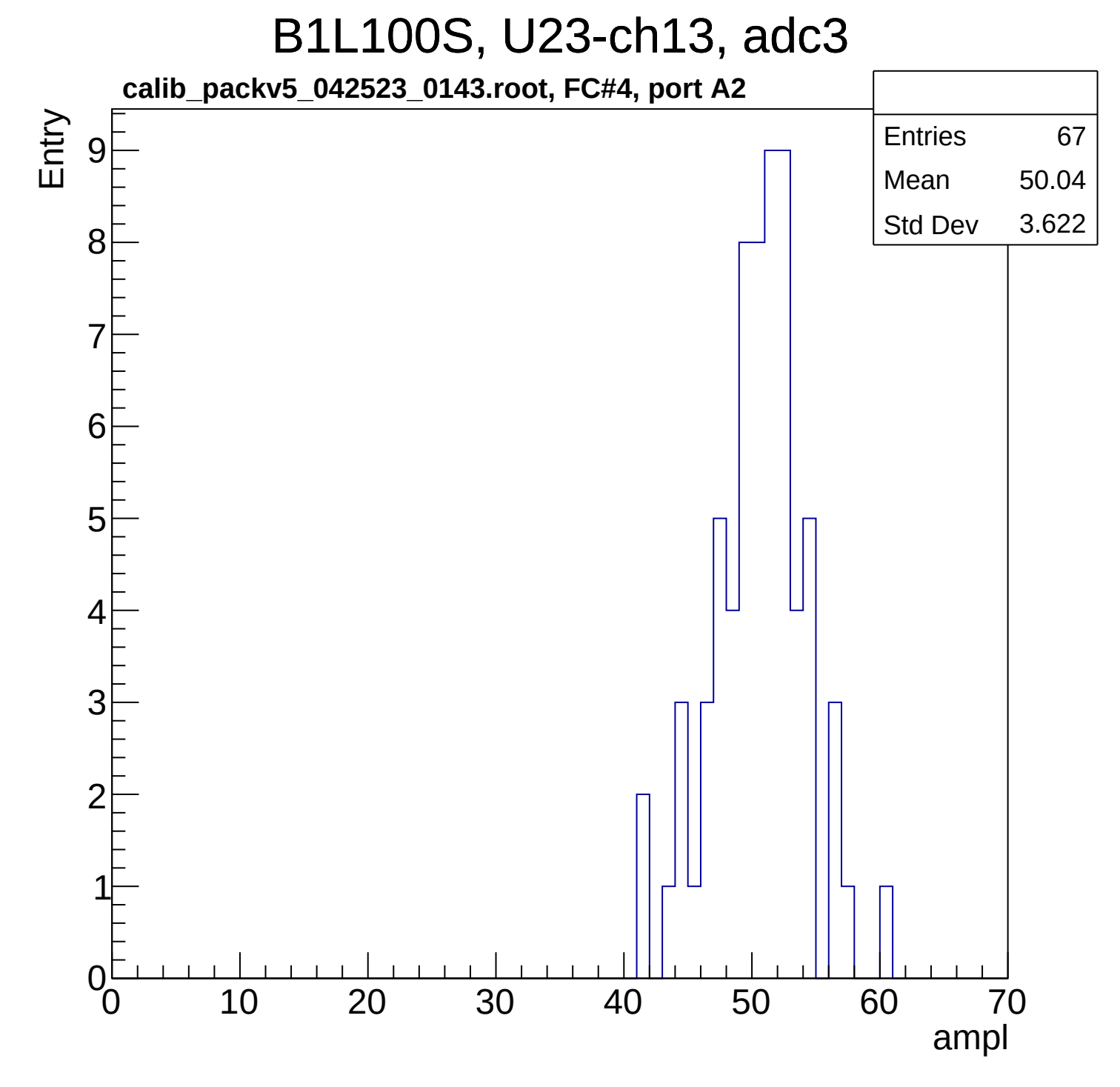

B1L100S, U23-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.59
Std Dev 3.175
Gaus mean : 43.9789
Gaus Width: 3.7087
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U23-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.04
Std Dev 3.622
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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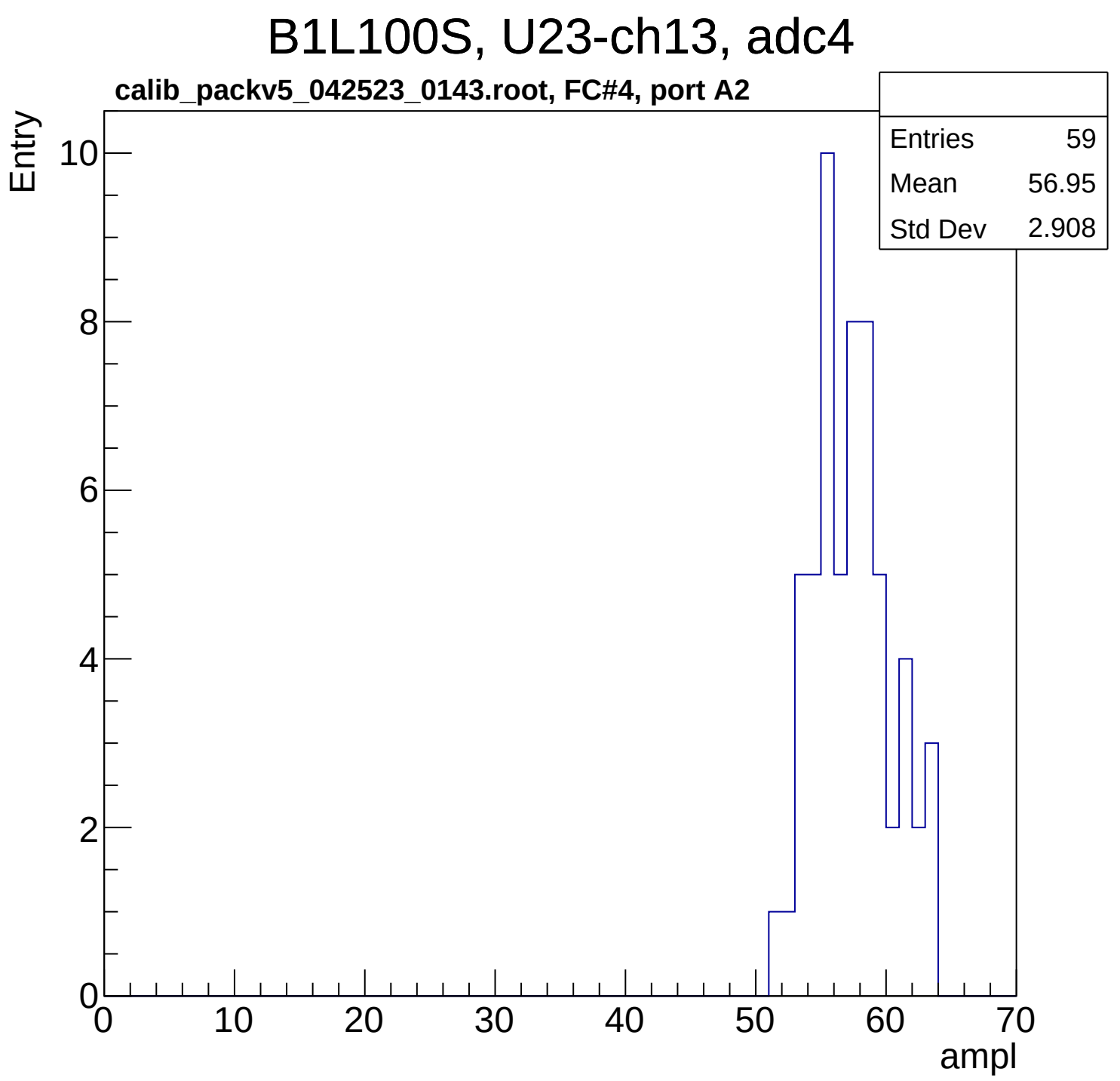
B1L100S, U23-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 56.95
Std Dev 2.908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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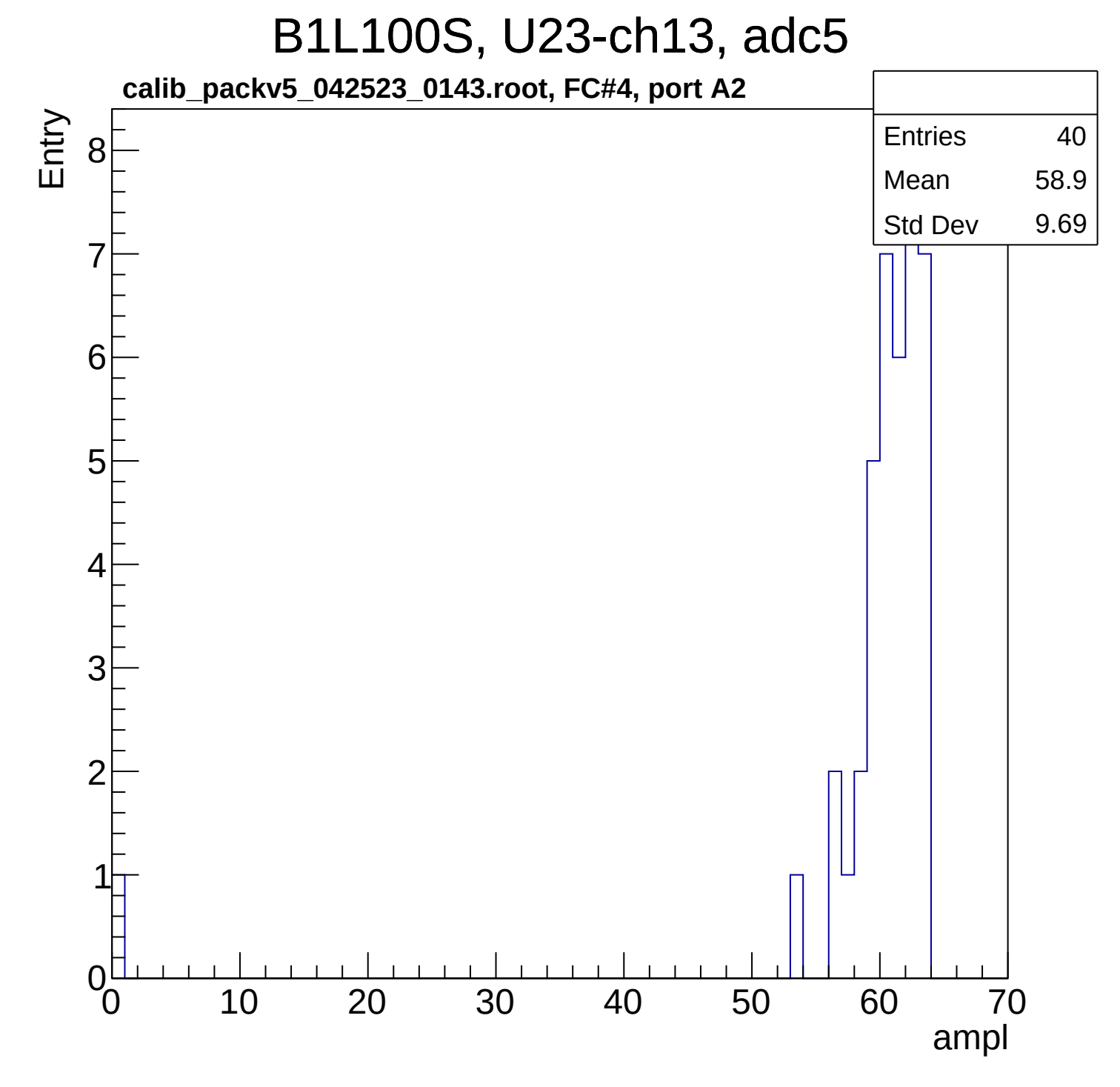
B1L100S, U23-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.9
Std Dev 9.69
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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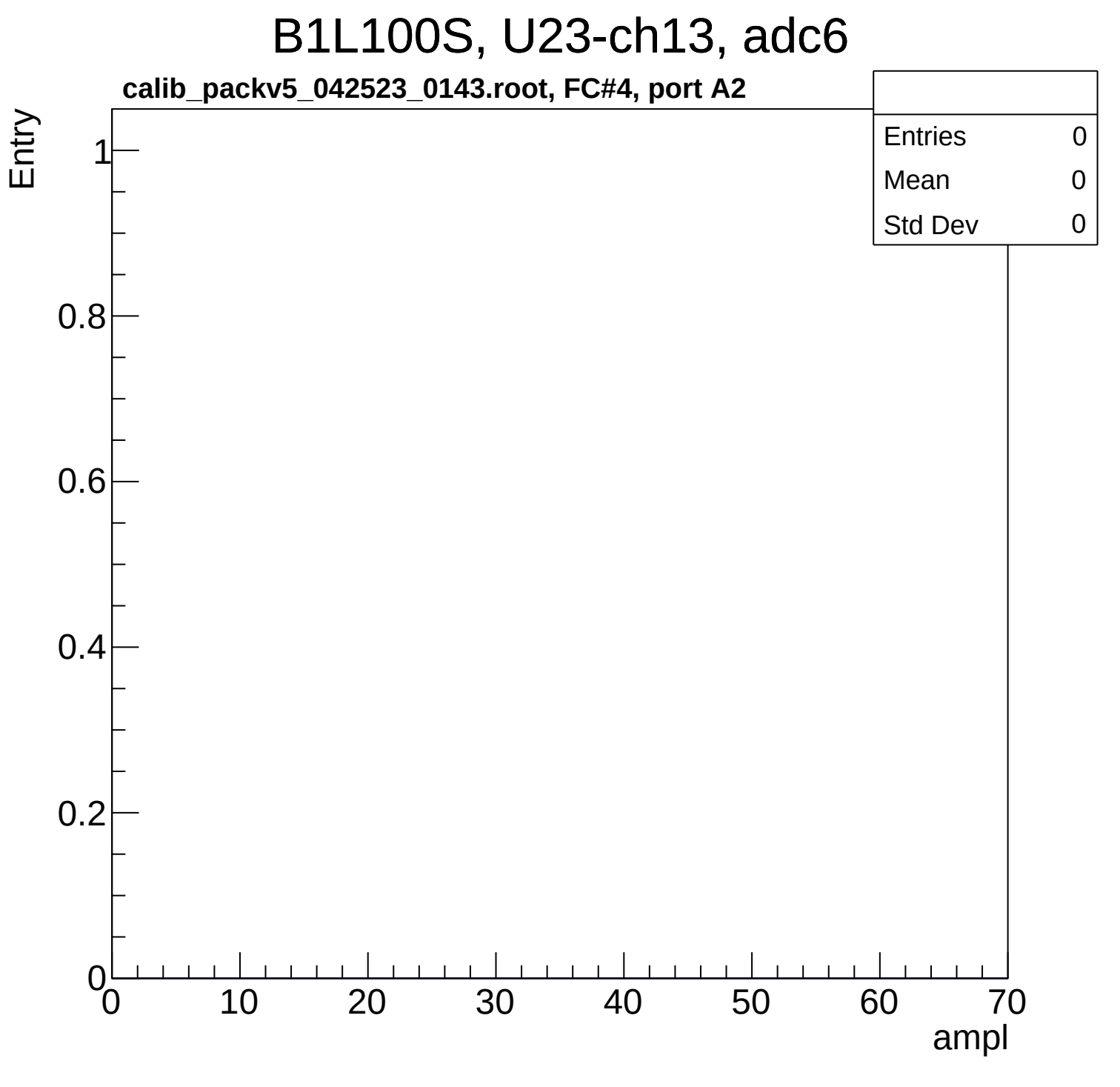
B1L100S, U23-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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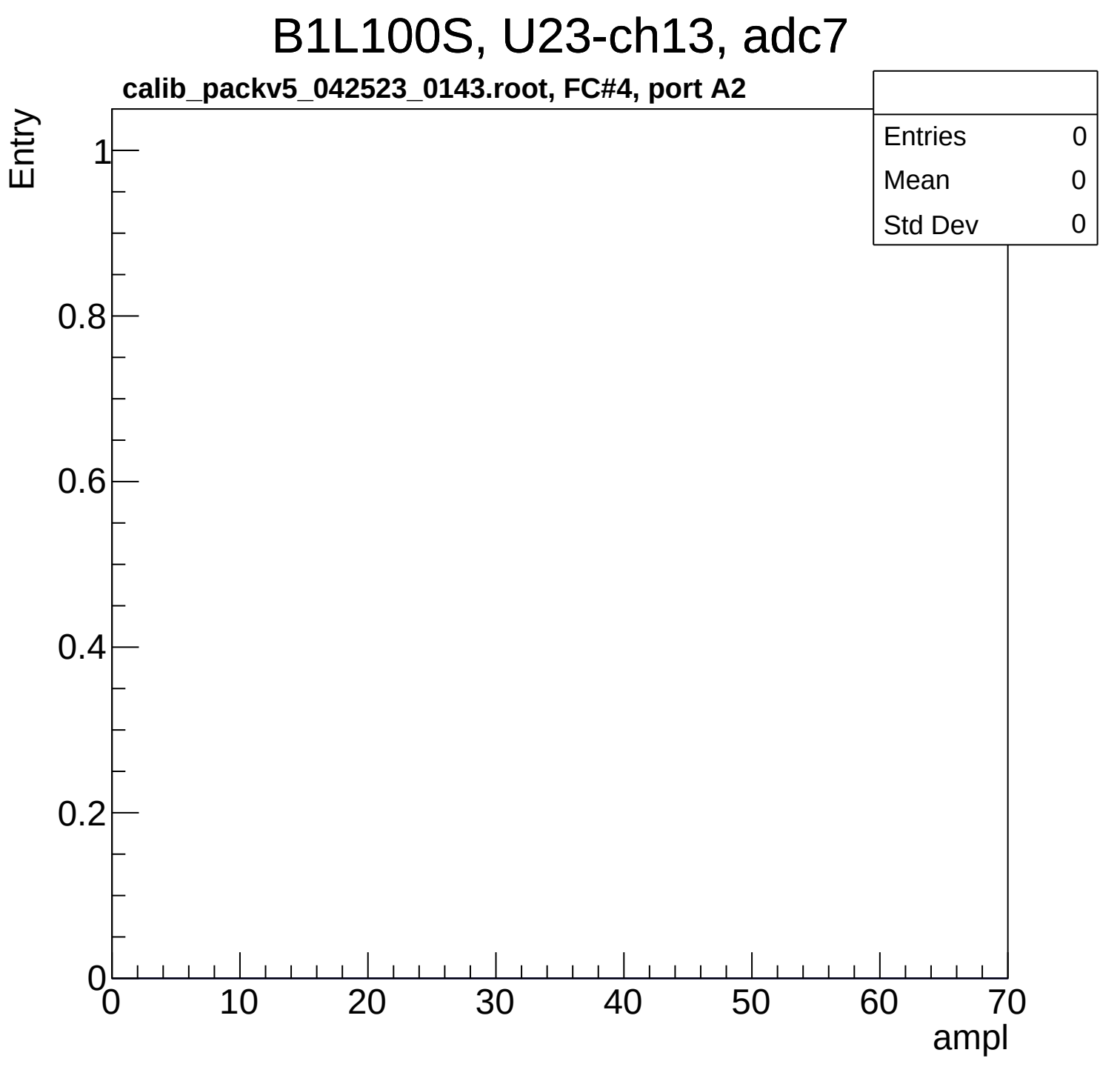
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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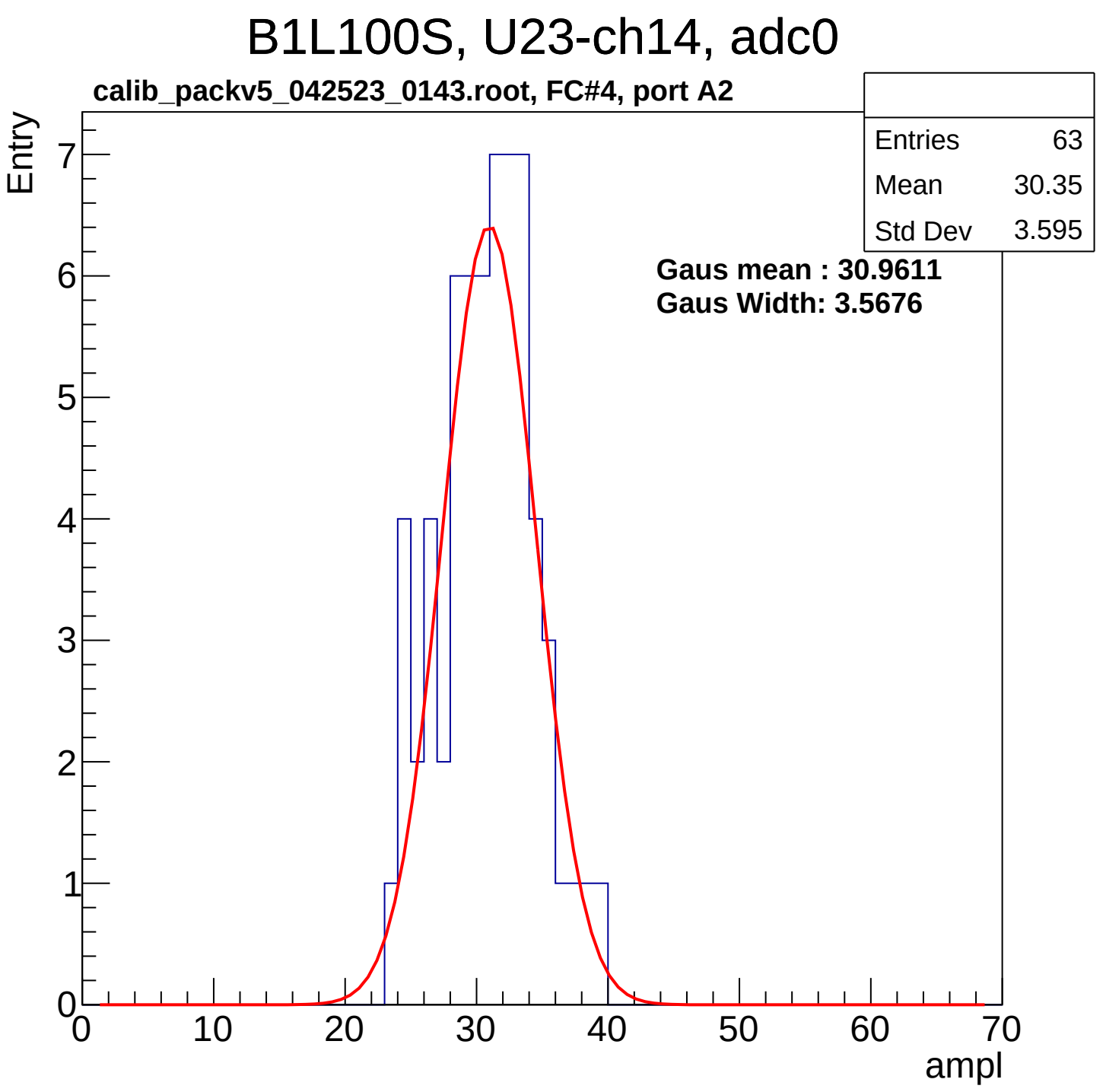
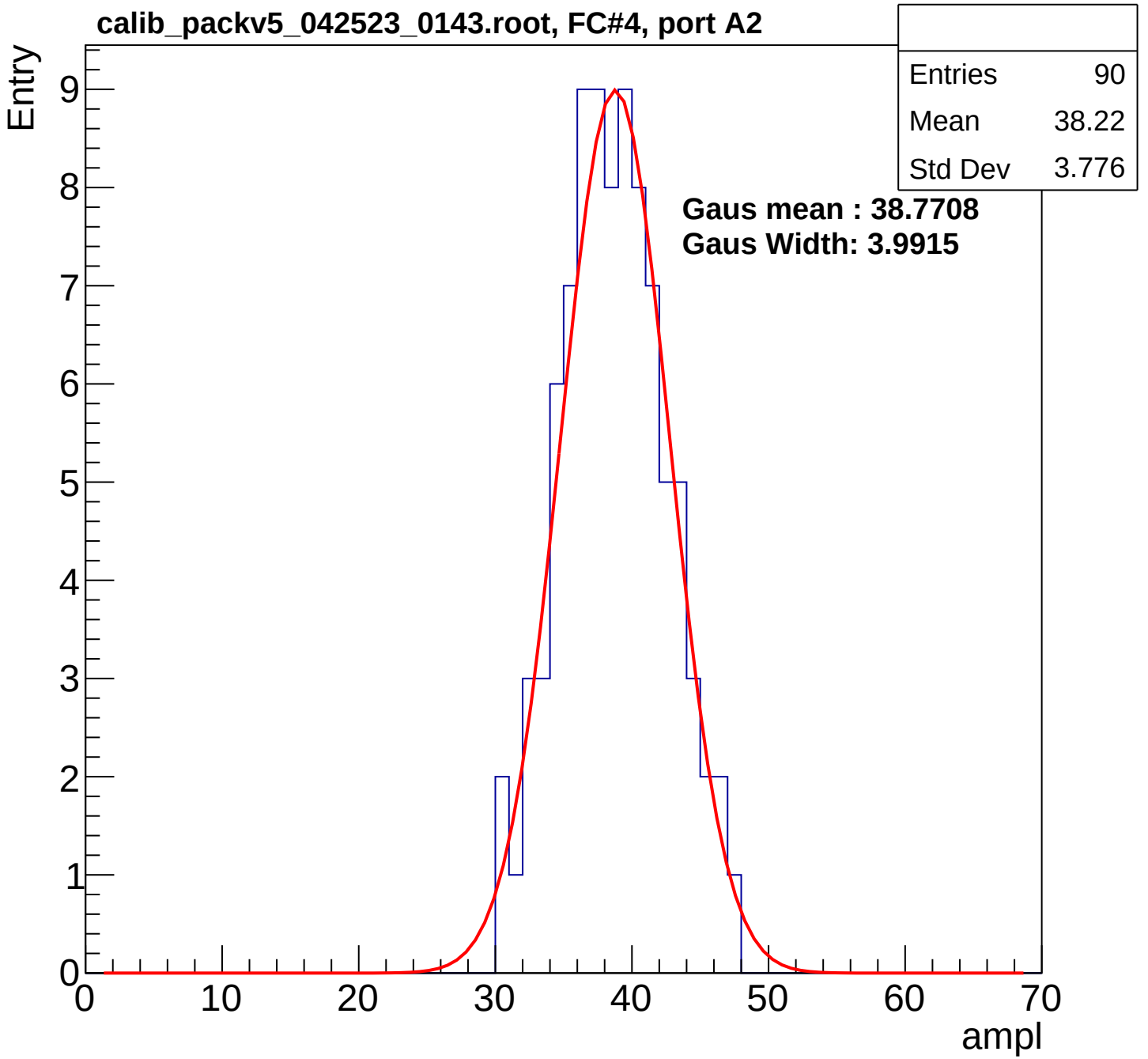
50
60
70
ampl


B1L100S, U23-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 30.35
Std Dev 3.595
Gaus mean : 30.9611
Gaus Width: 3.5676
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 38.22
Std Dev 3.776
Gaus mean : 38.7708
Gaus Width: 3.9915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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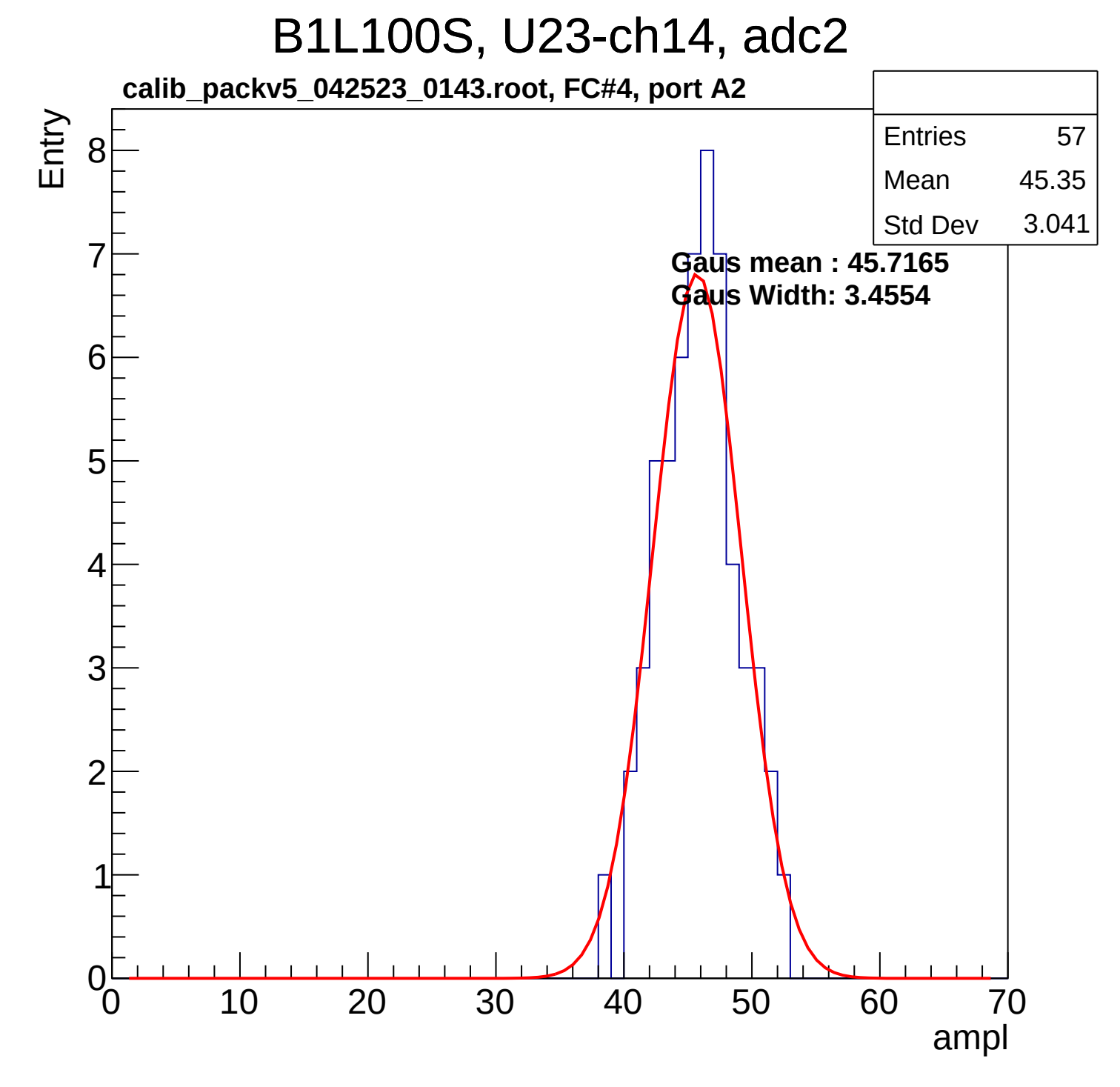
60
70


B1L100S, U23-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 45.35
Std Dev 3.041
Gaus mean : 45.7165
Gaus Width: 3.4554
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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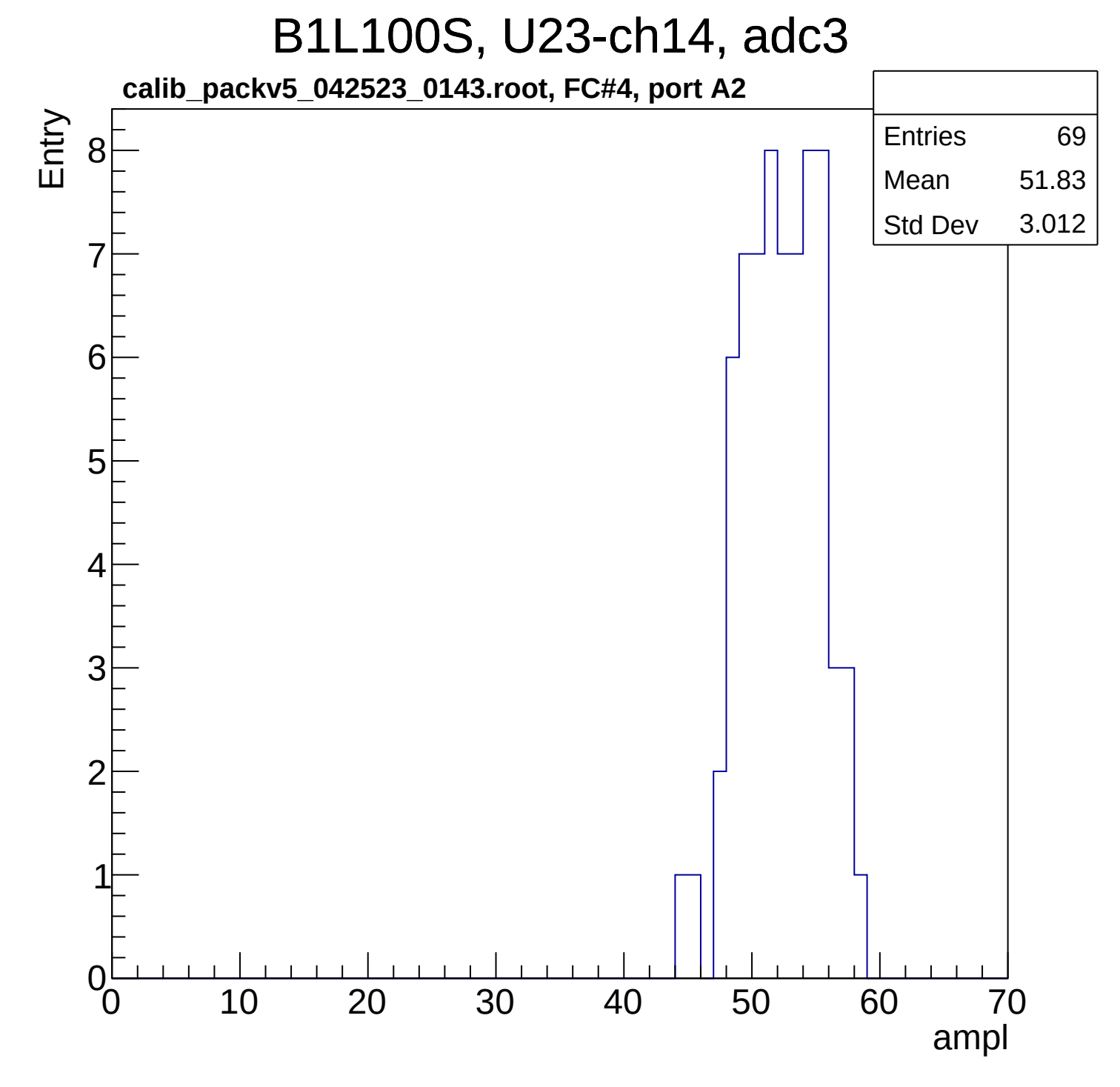
40
50
60
70


B1L100S, U23-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.83
Std Dev 3.012
Entry
ampl
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3
4
5
6
7
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0
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20
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40
50
60
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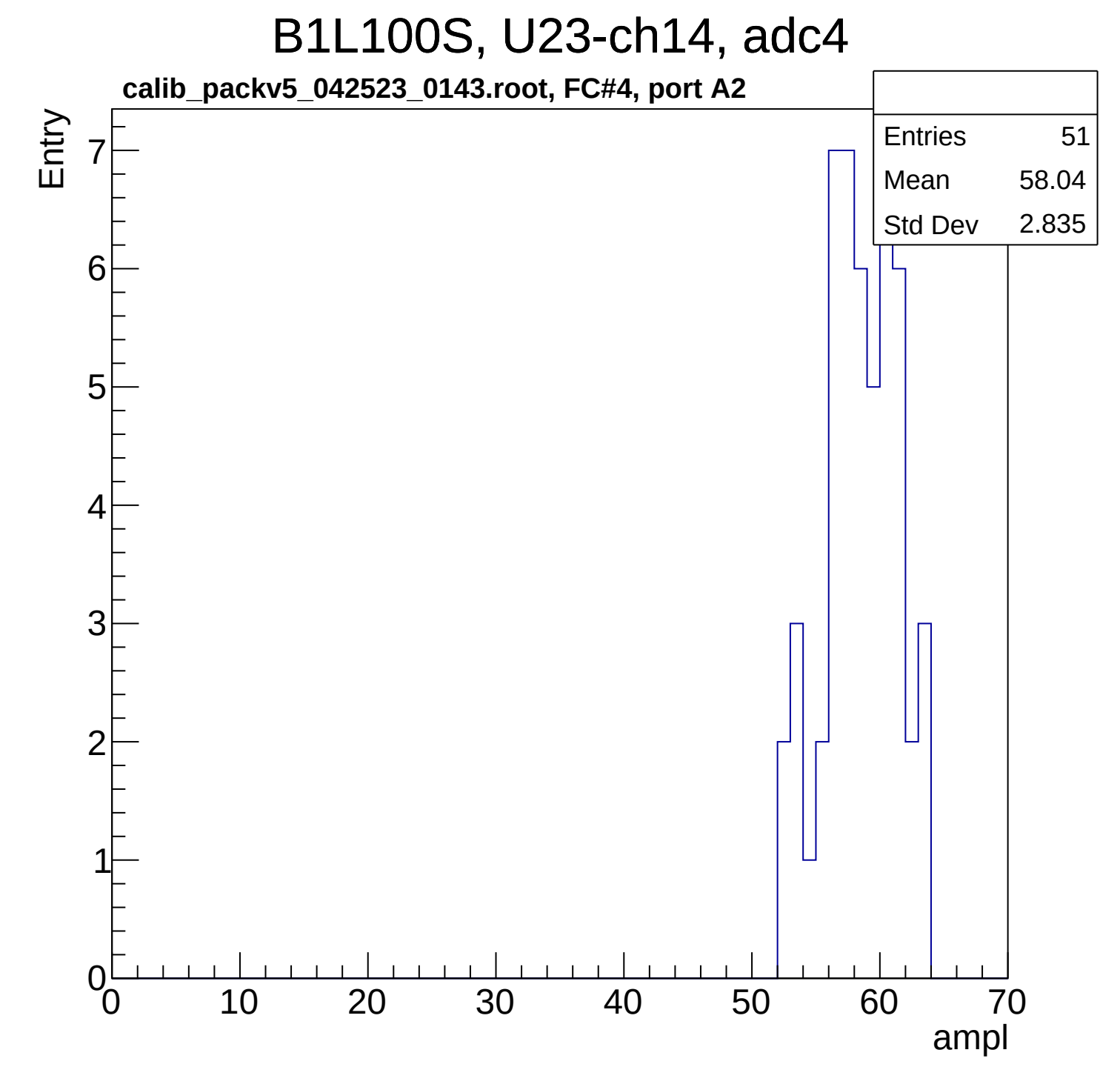
B1L100S, U23-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.04
Std Dev 2.835
Entry
ampl
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1
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4
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0
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60
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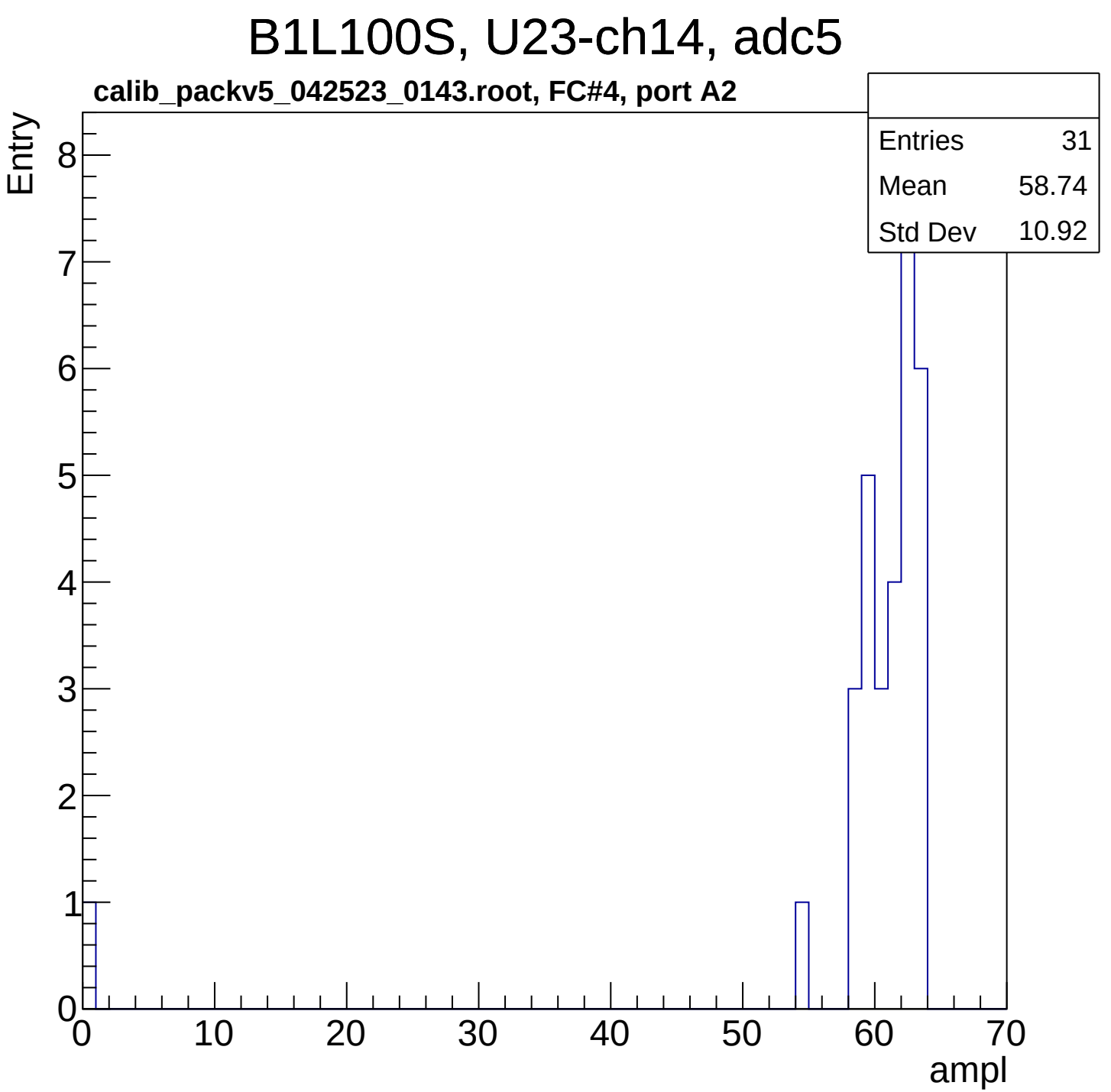
B1L100S, U23-ch14, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.74
Std Dev 10.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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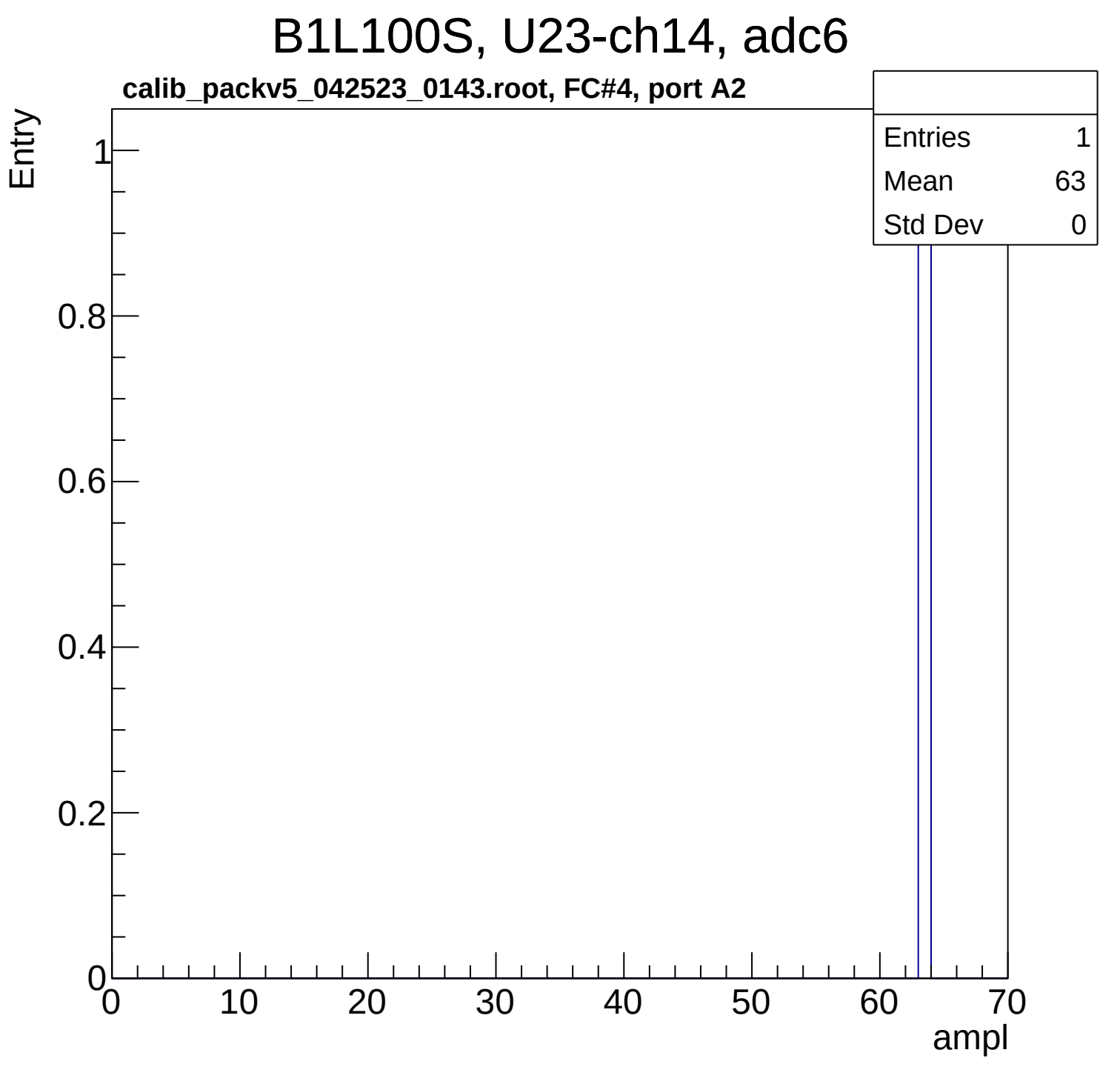
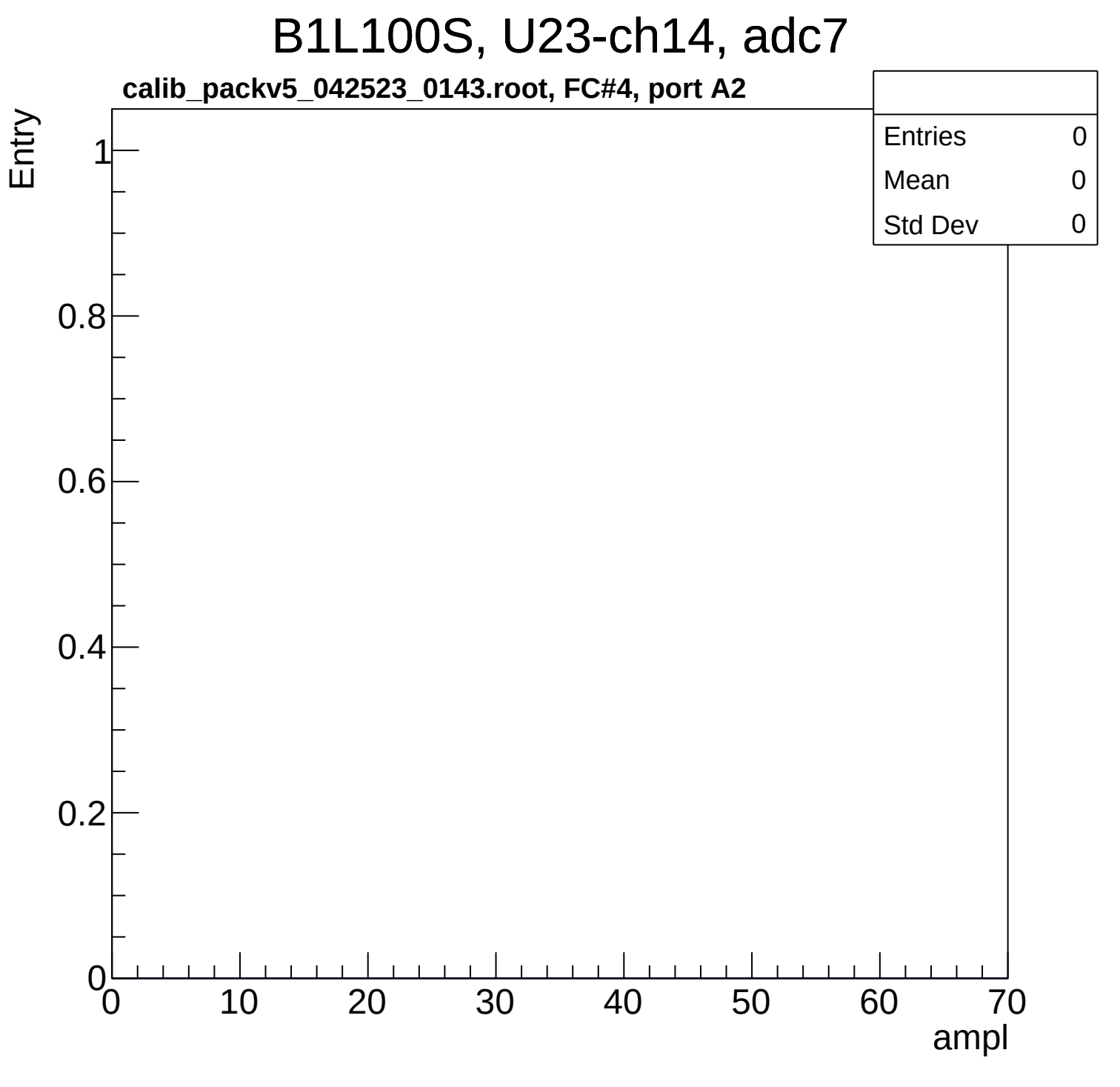

B1L100S, U23-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch14, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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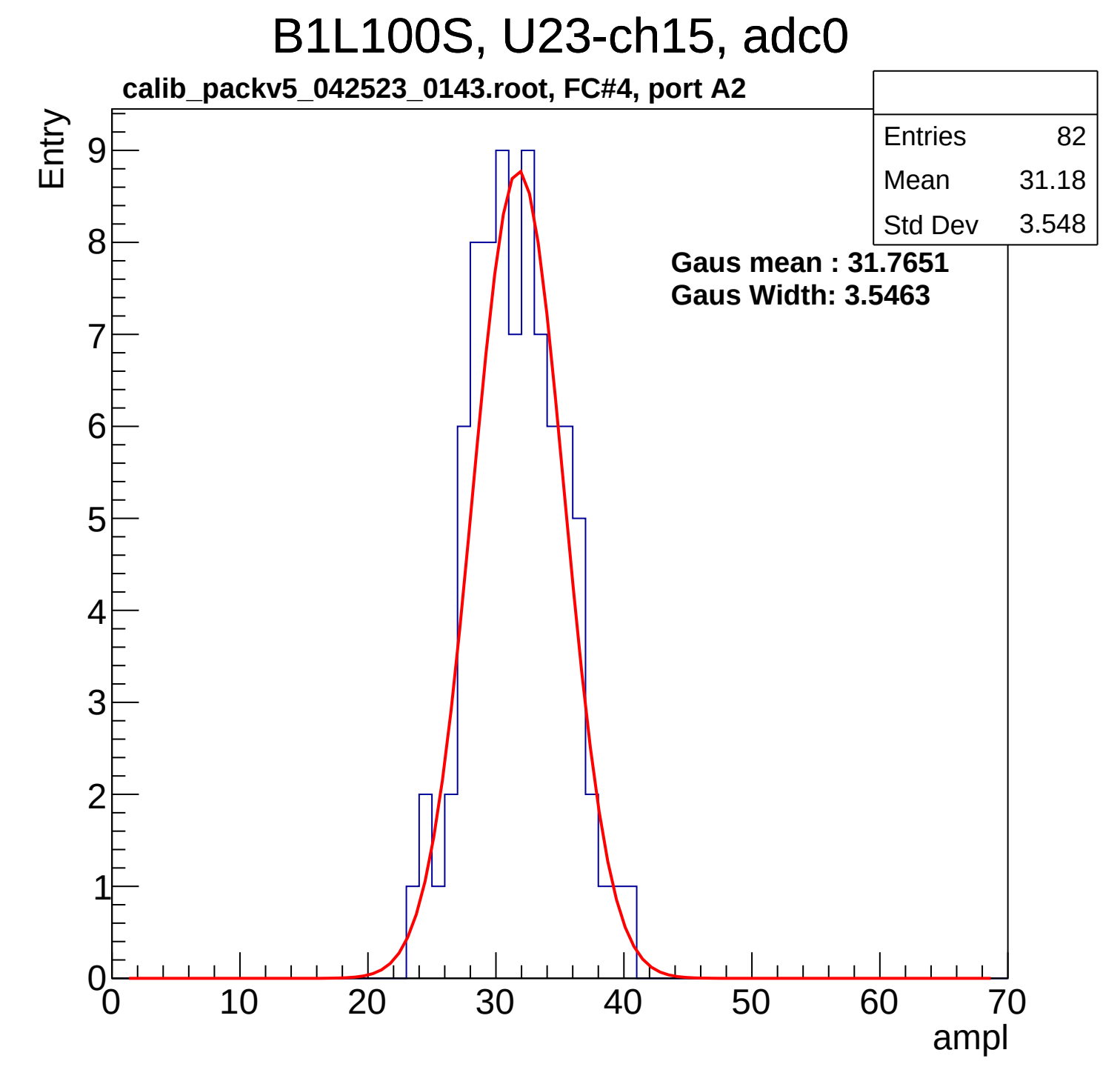
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 31.18
Std Dev 3.548
Gaus mean : 31.7651
Gaus Width: 3.5463
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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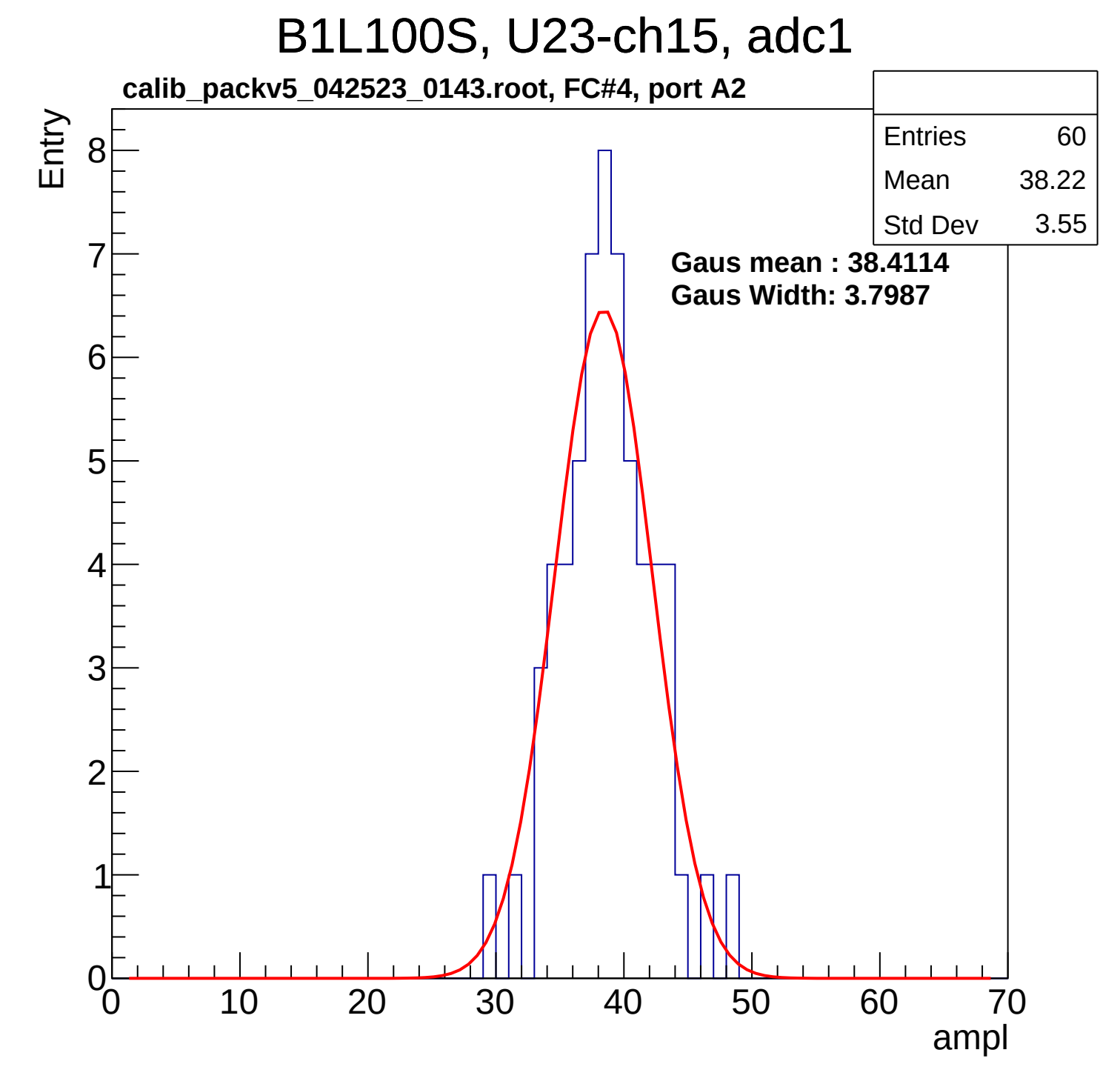
B1L100S, U23-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 38.22
Std Dev 3.55
Gaus mean : 38.4114
Gaus Width: 3.7987
Entry
ampl
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1
2
3
4
5
6
7
8
0
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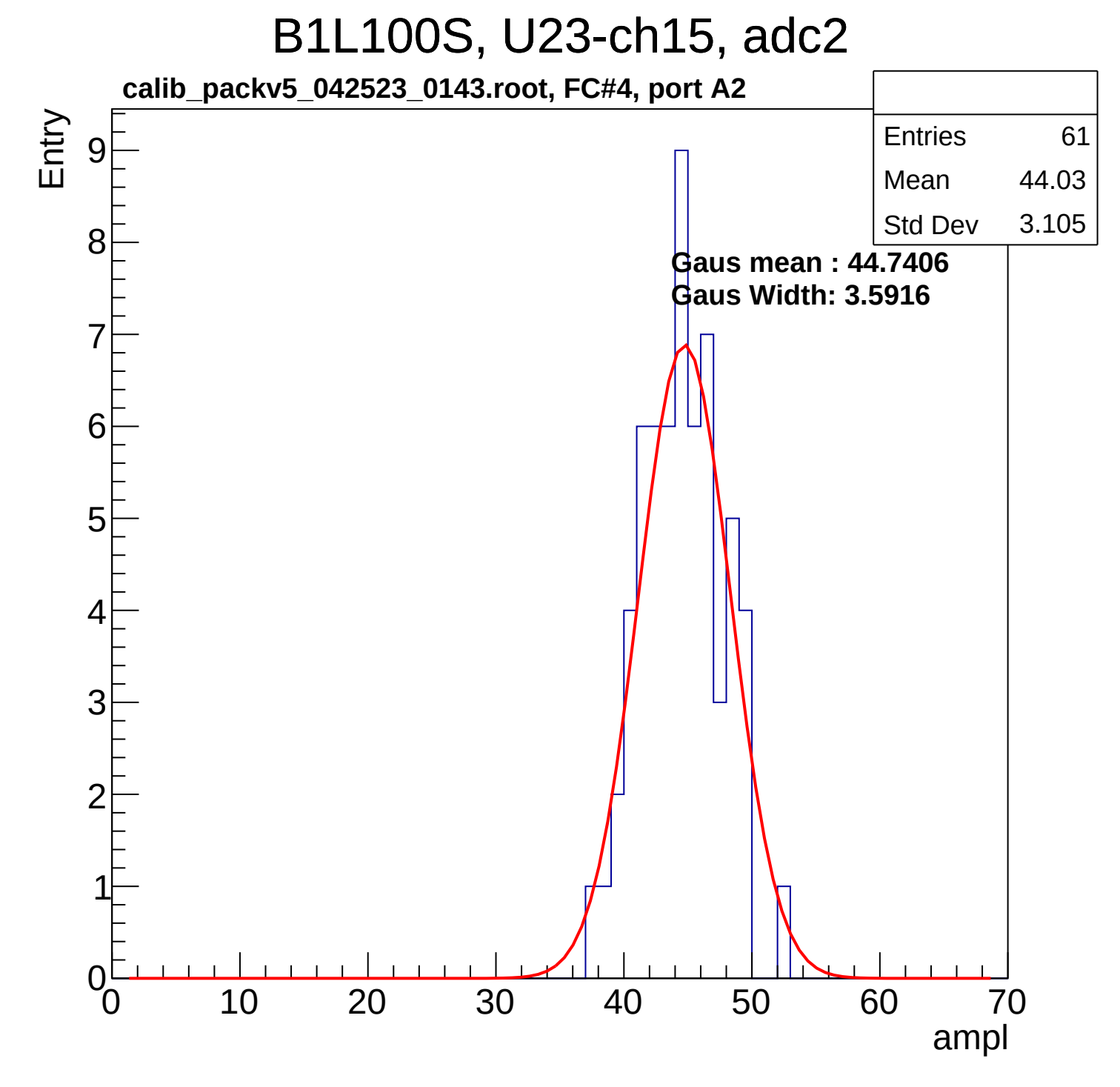
B1L100S, U23-ch15, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 44.03
Std Dev 3.105
Gaus mean : 44.7406
Gaus Width: 3.5916
Entry
ampl
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3
4
5
6
7
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0
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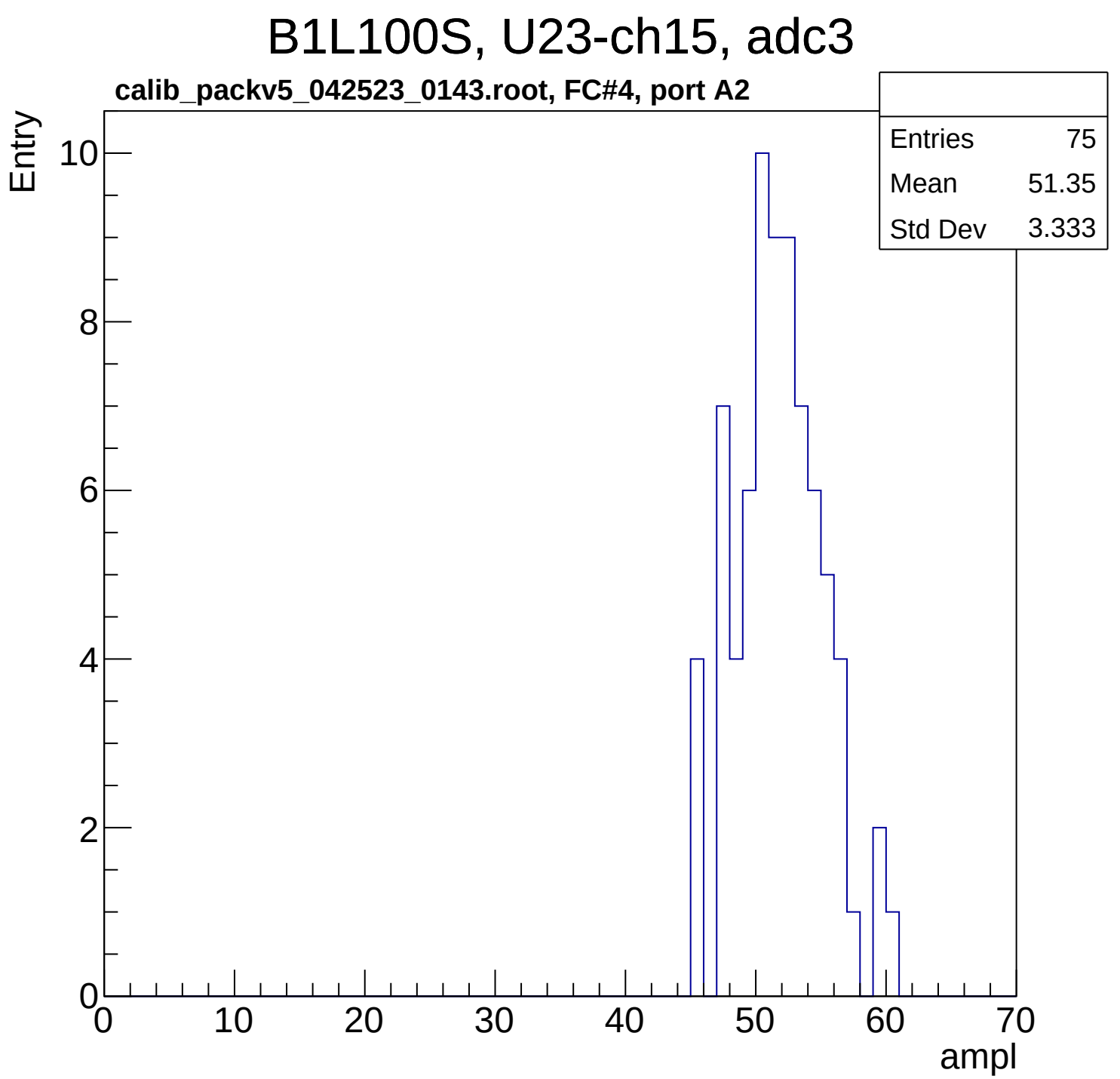
B1L100S, U23-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 51.35
Std Dev 3.333
Entry
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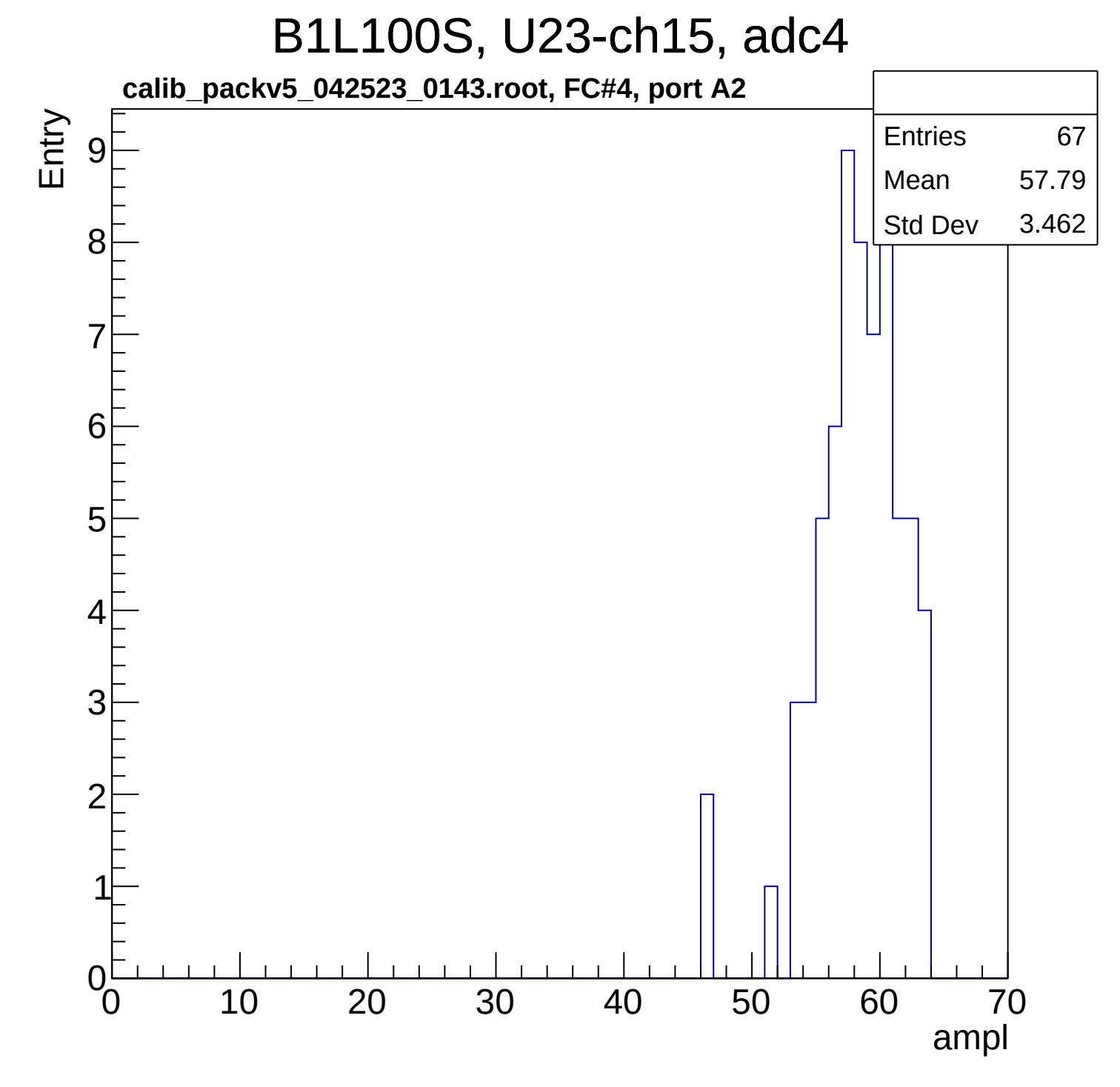
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 57.79
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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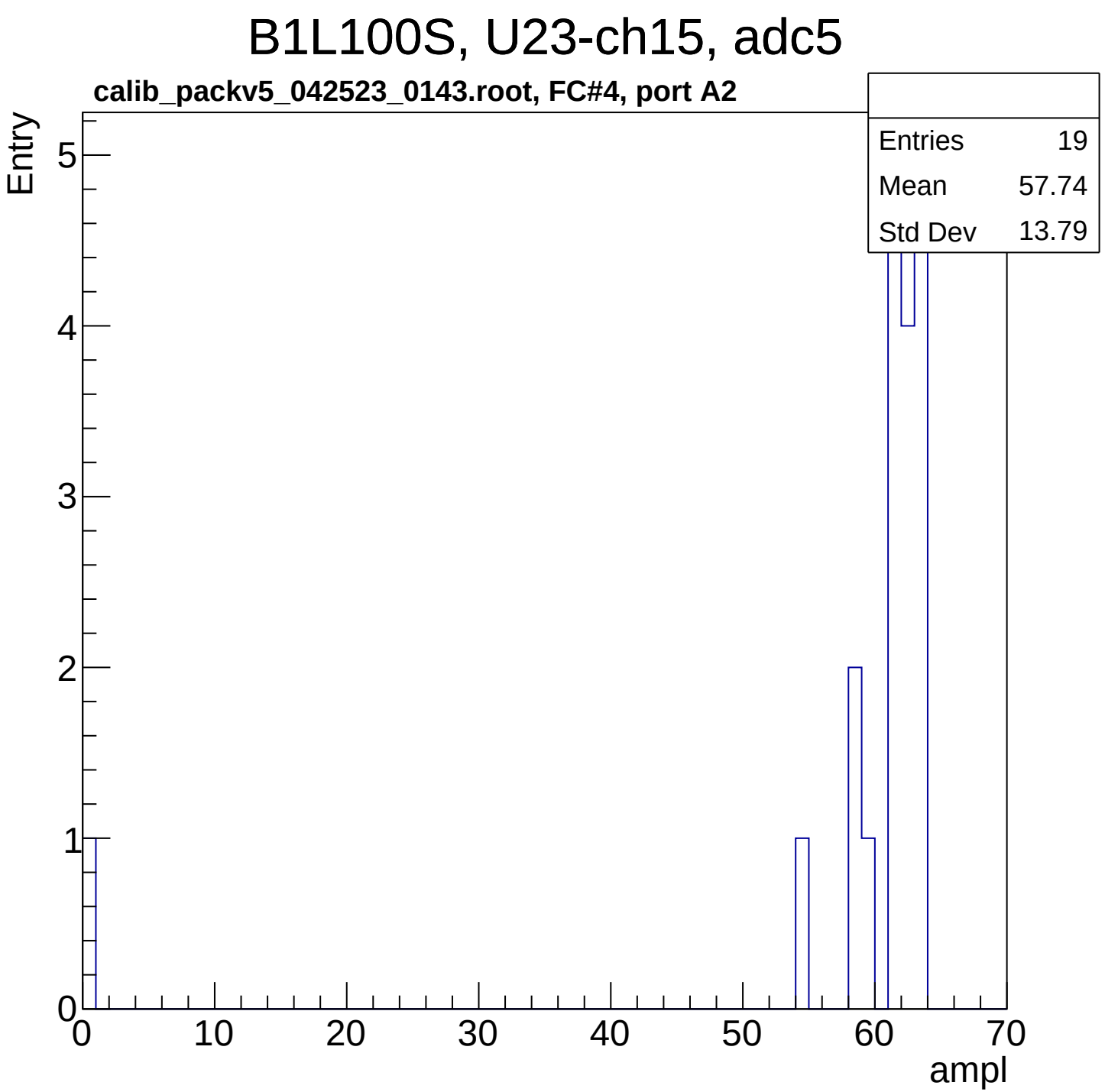
B1L100S, U23-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 19
Mean 57.74
Std Dev 13.79
Entry
0
1
2
3
4
5
0
10
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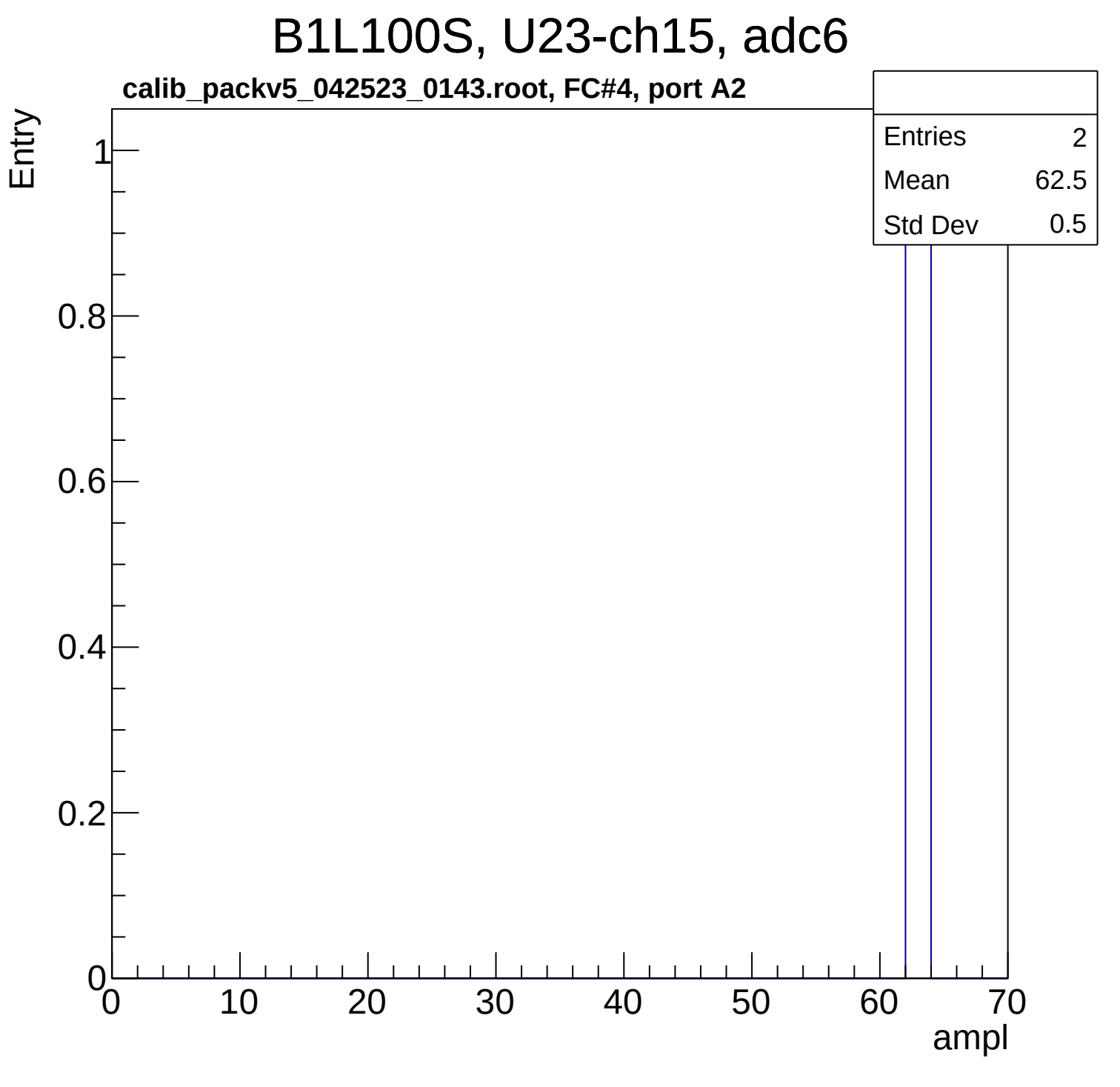
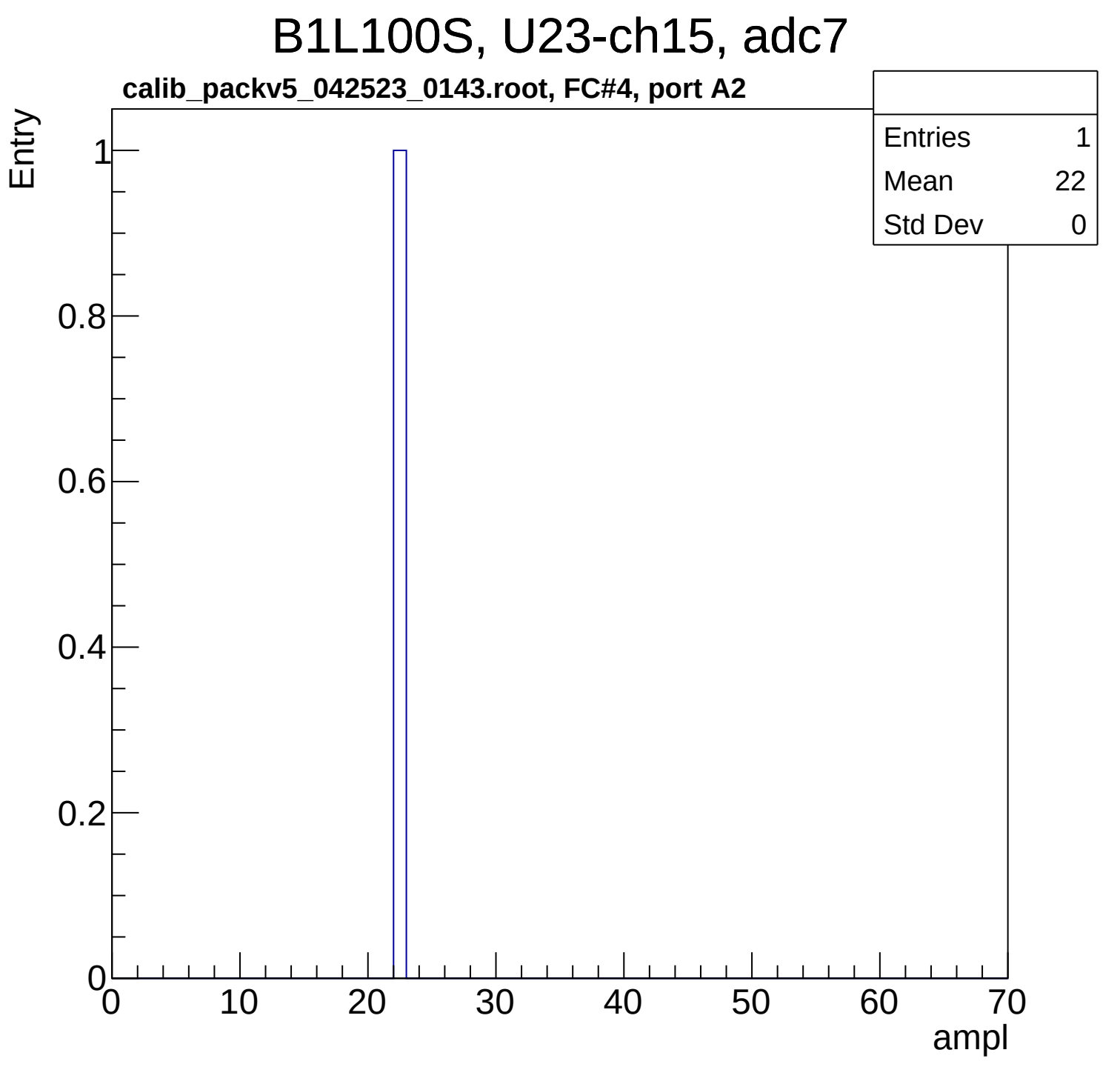
50
60
70
ampl


B1L100S, U23-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch15, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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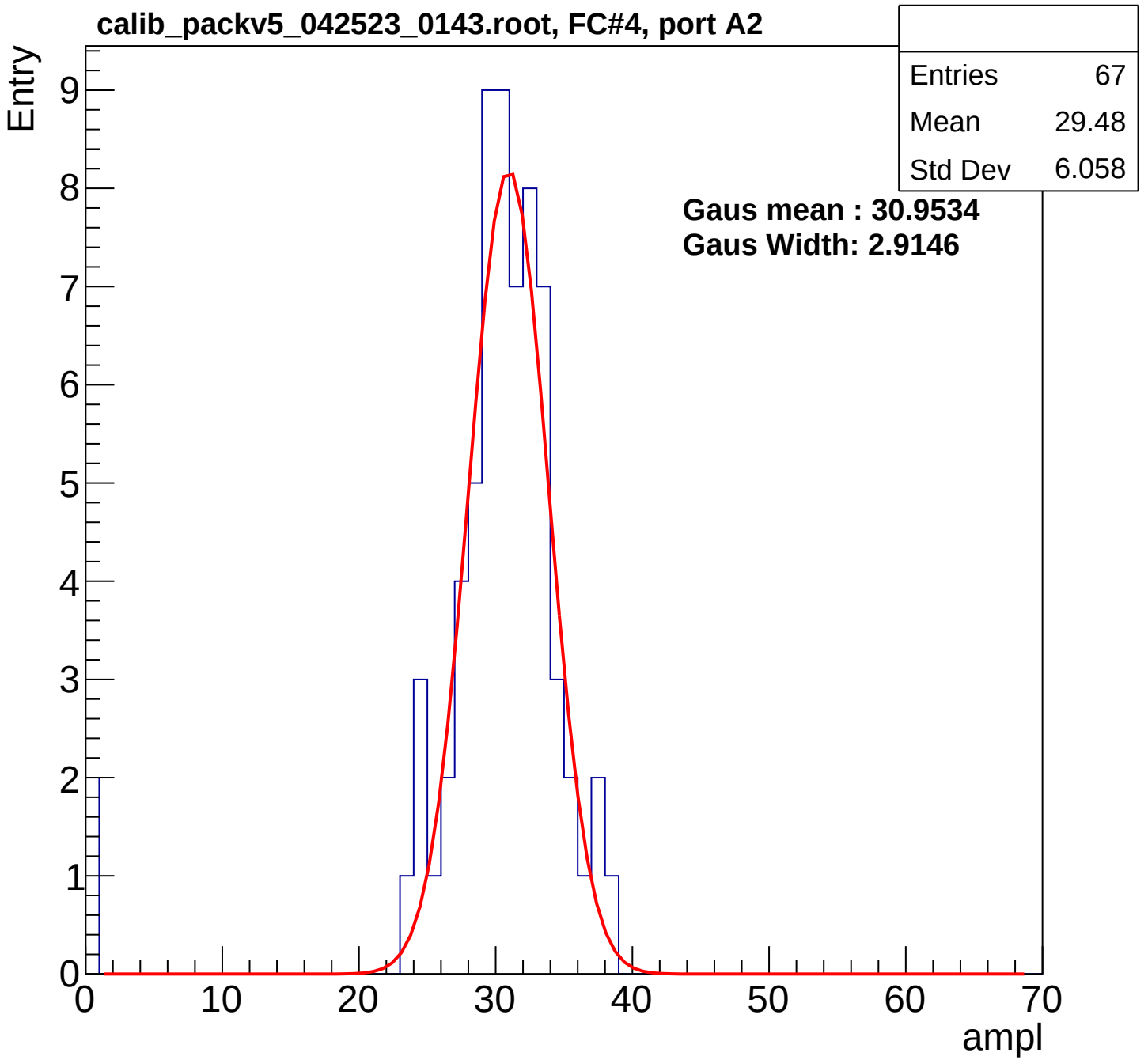
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch16, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.48
Std Dev 6.058
Gaus mean : 30.9534
Gaus Width: 2.9146
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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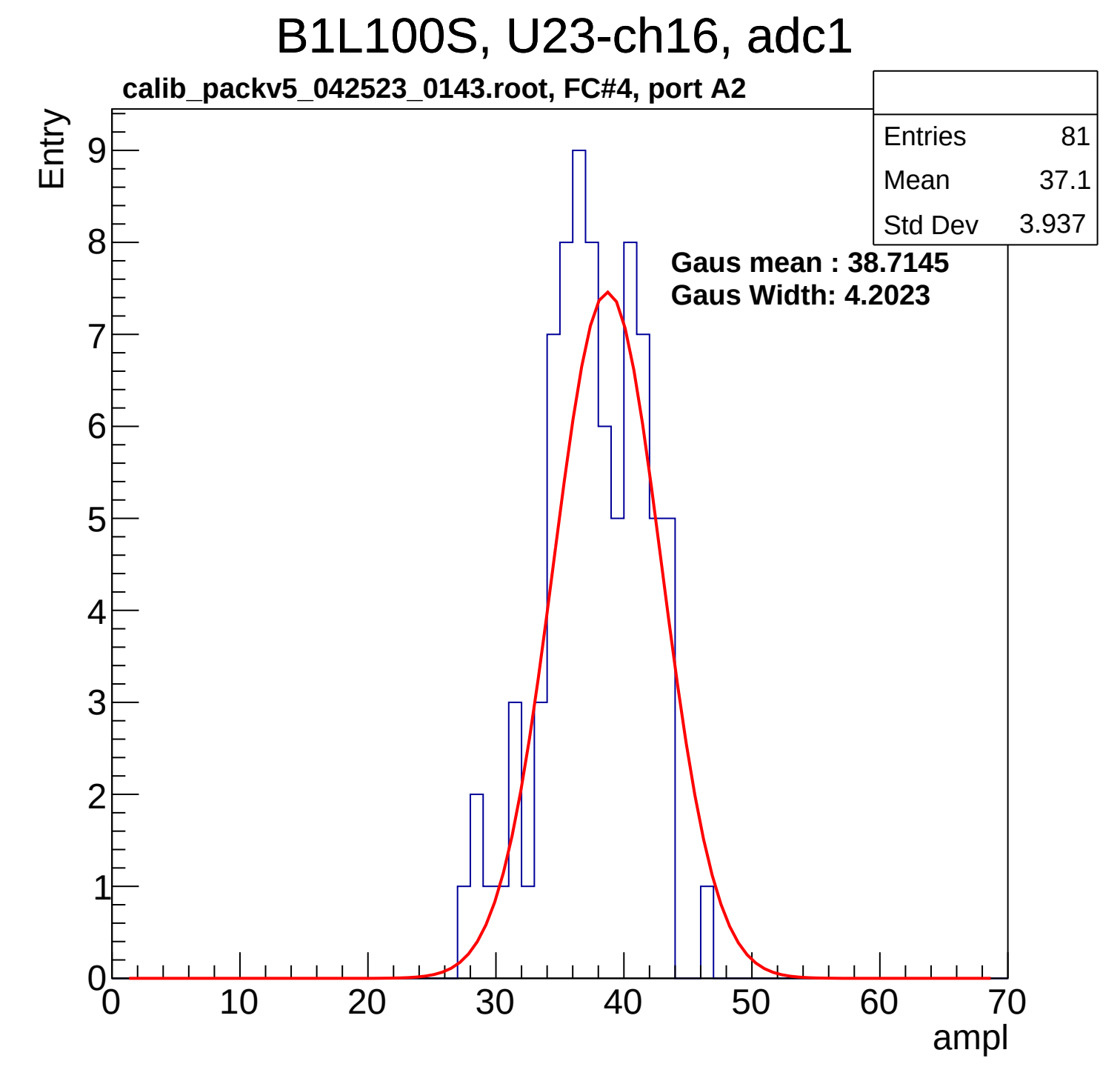
B1L100S, U23-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 37.1
Std Dev 3.937
Gaus mean : 38.7145
Gaus Width: 4.2023
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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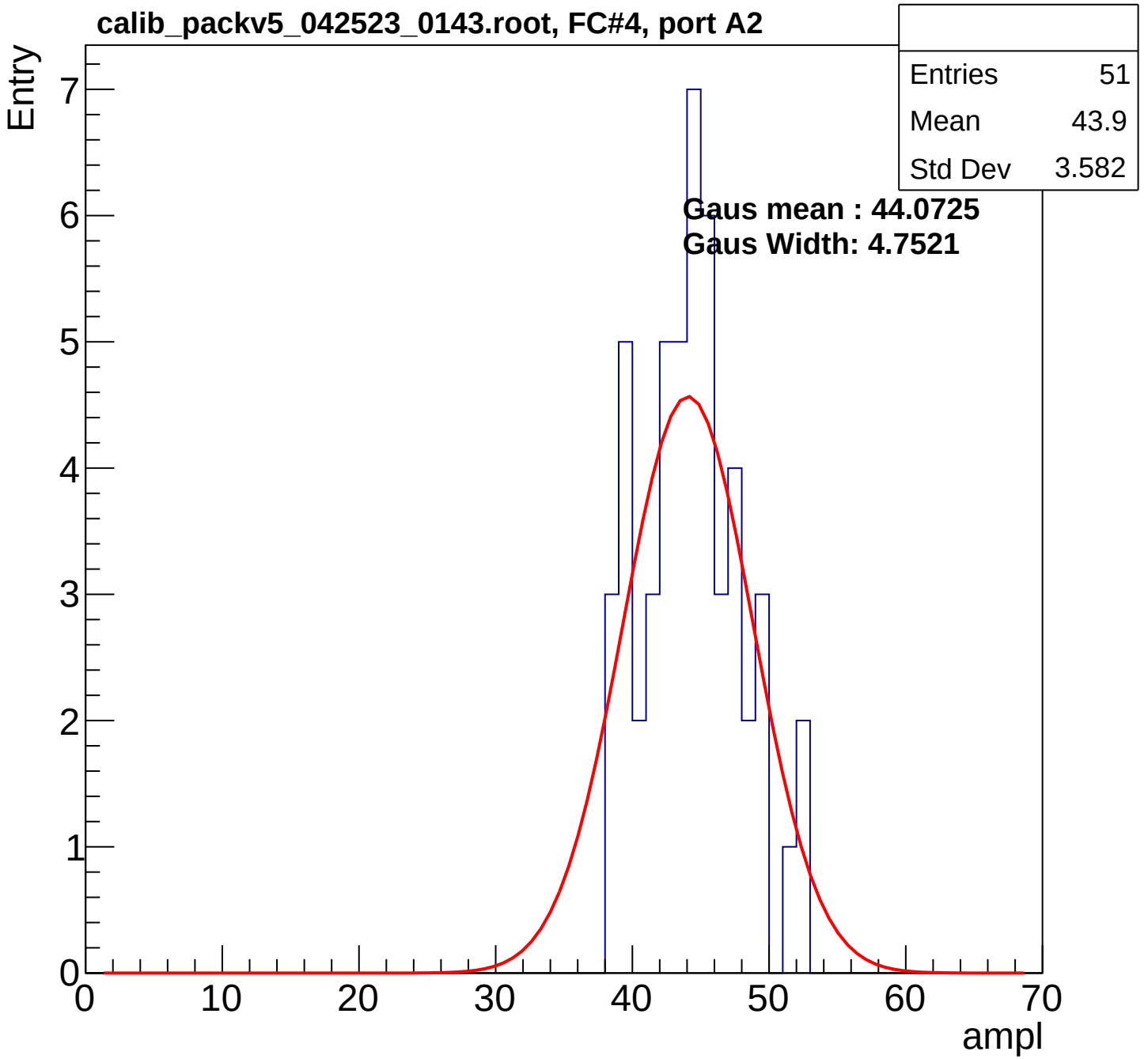
B1L100S, U23-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 43.9
Std Dev 3.582
Gaus mean : 44.0725
Gaus Width: 4.7521
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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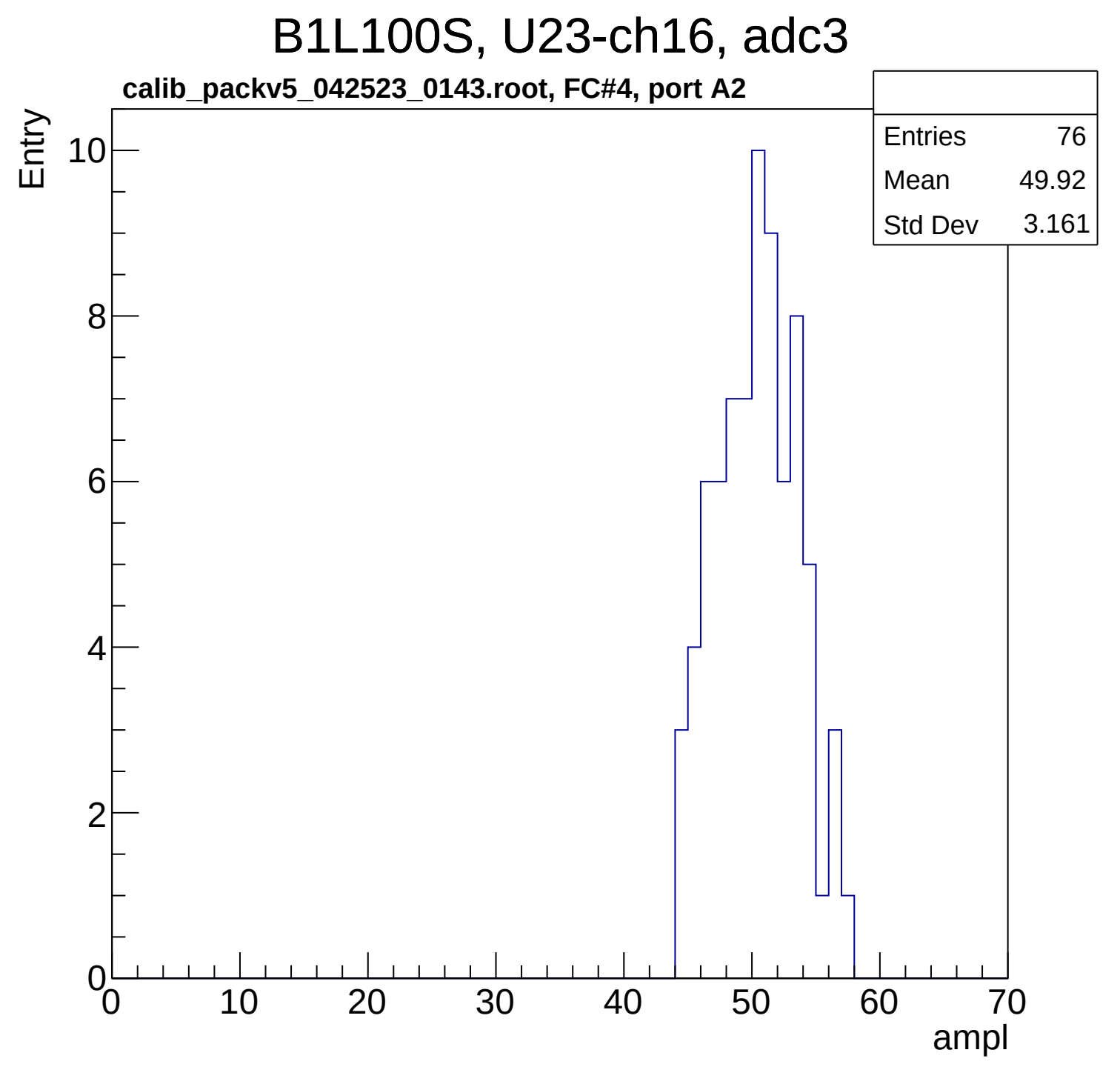
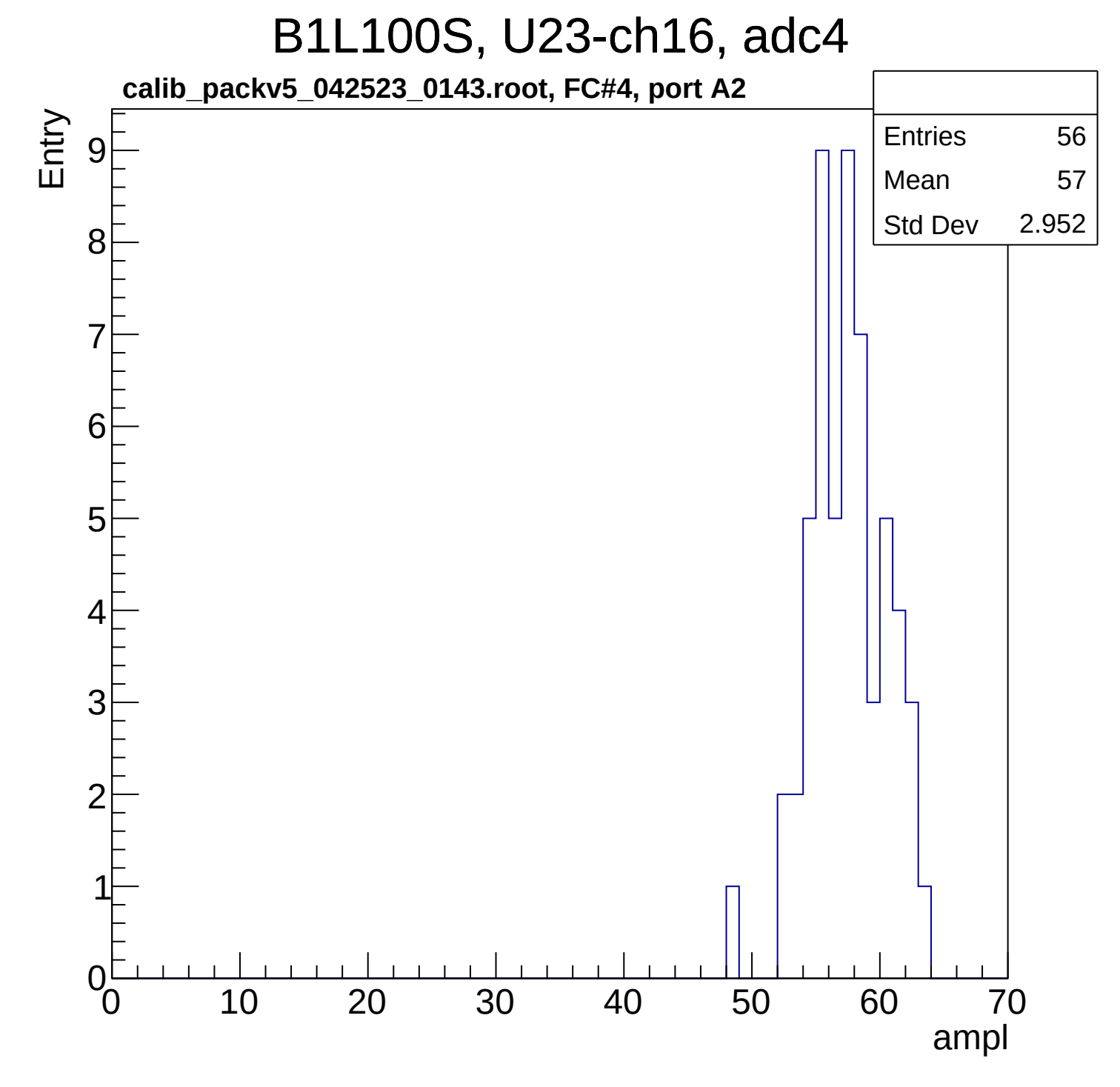
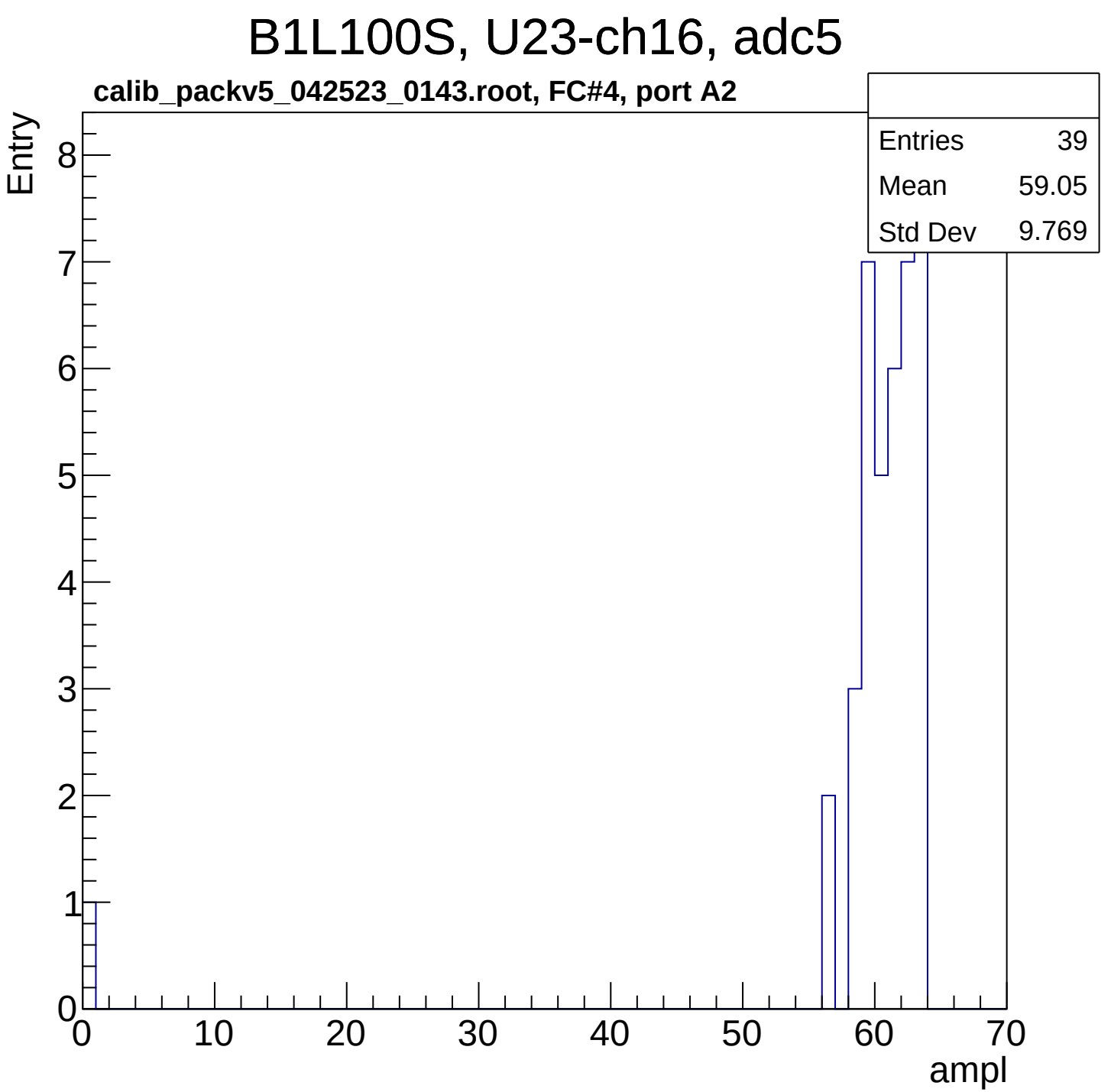
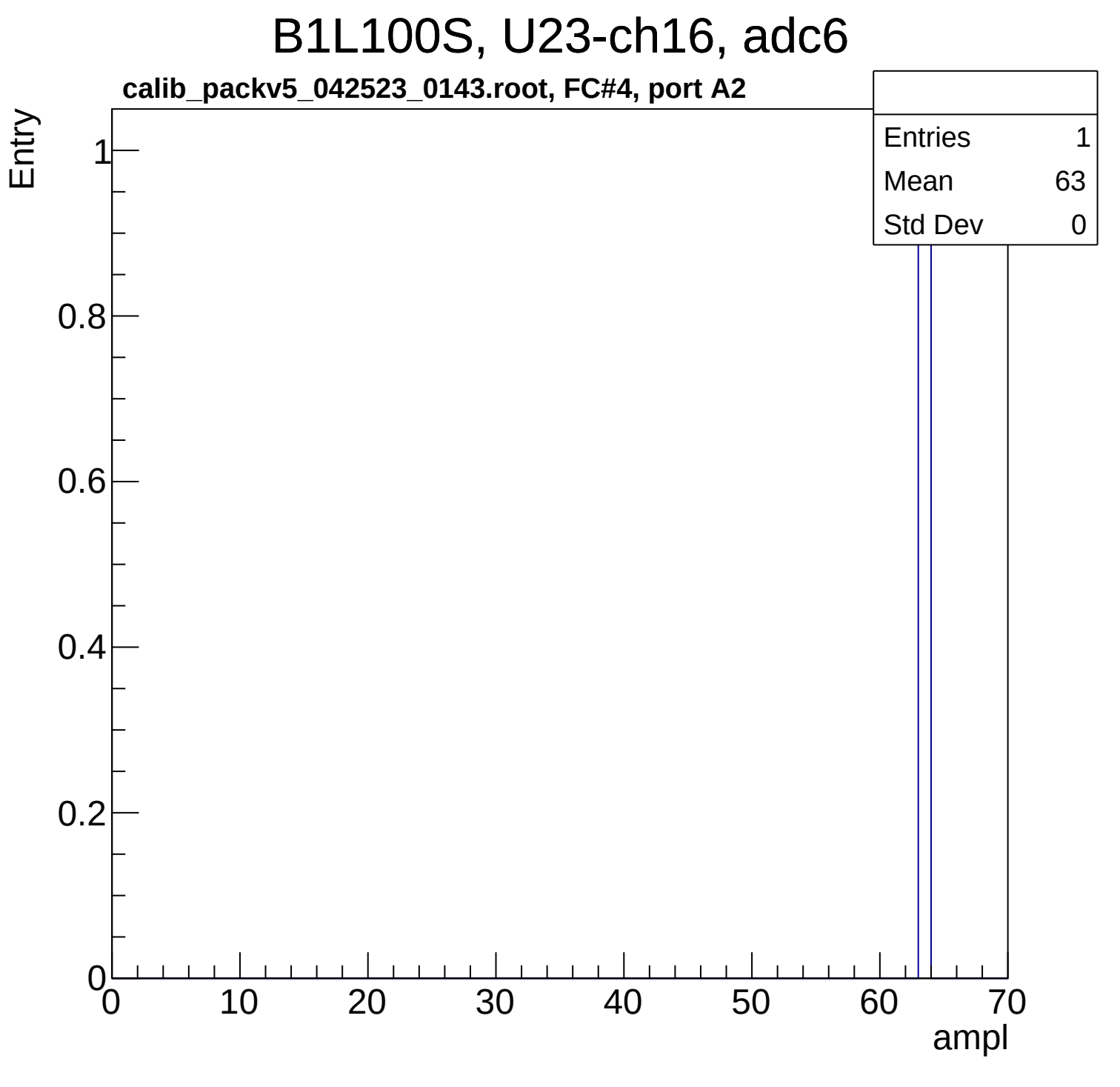
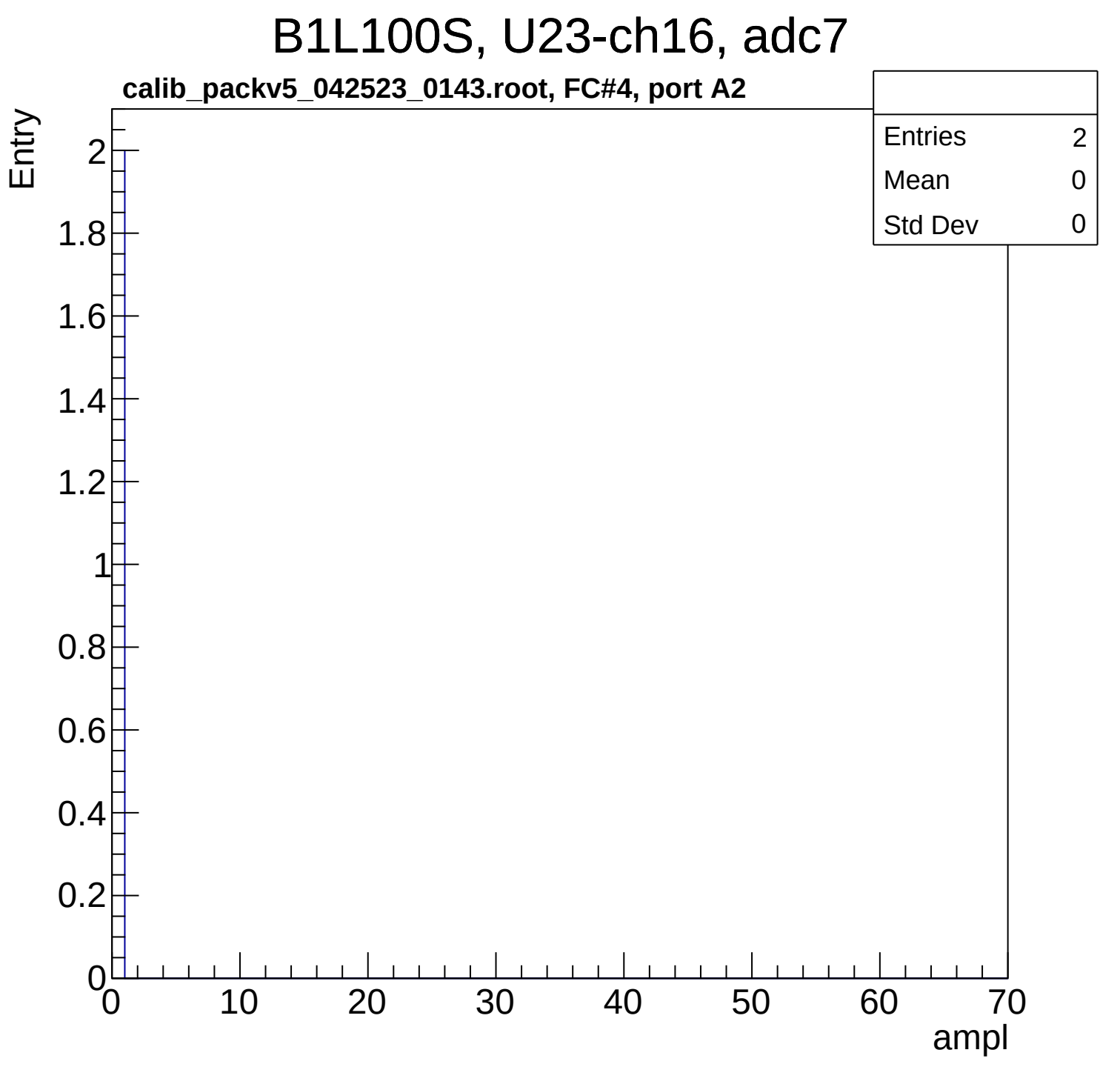
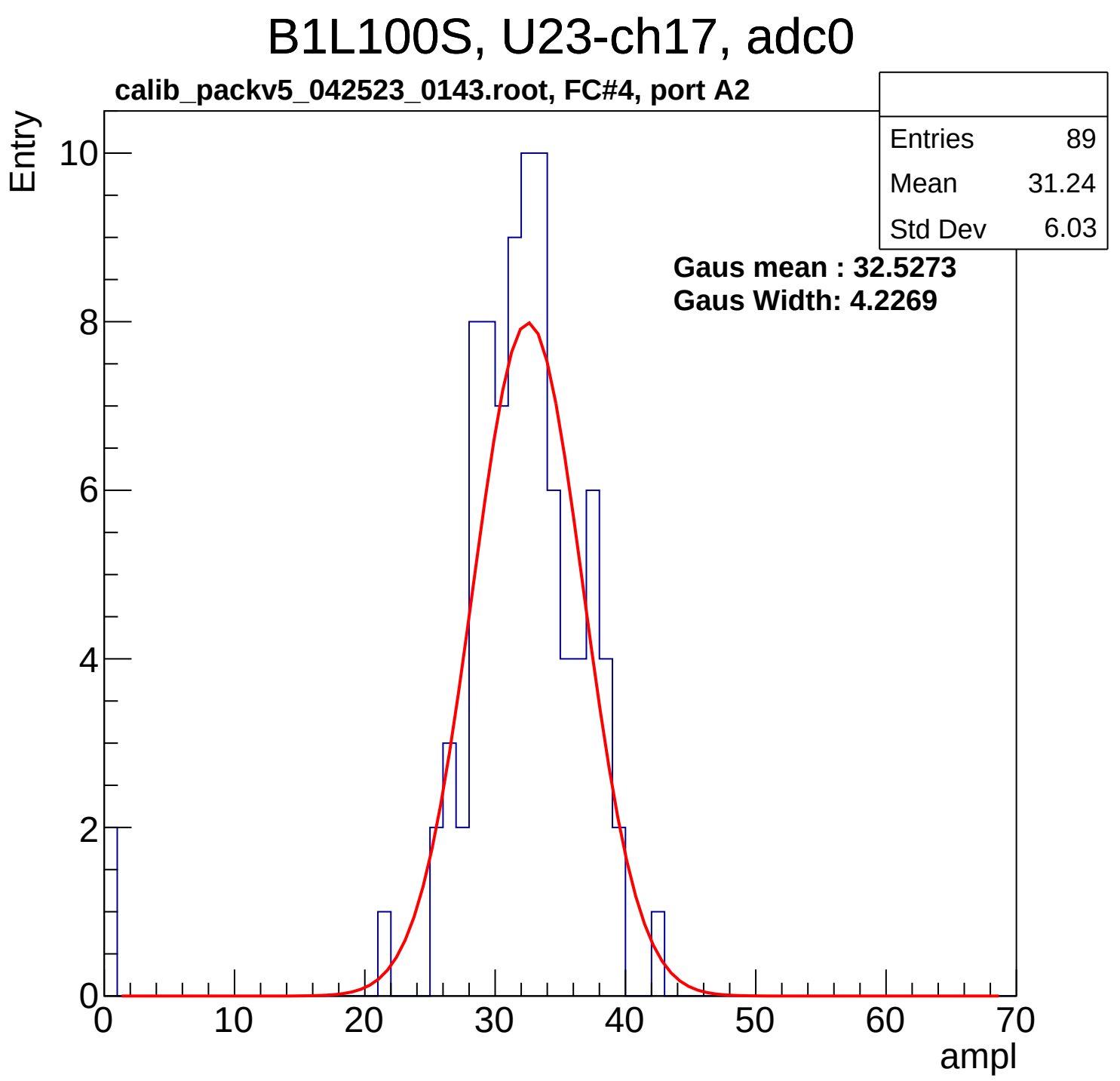

B1L100S, U23-ch16, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.92
Std Dev 3.161
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

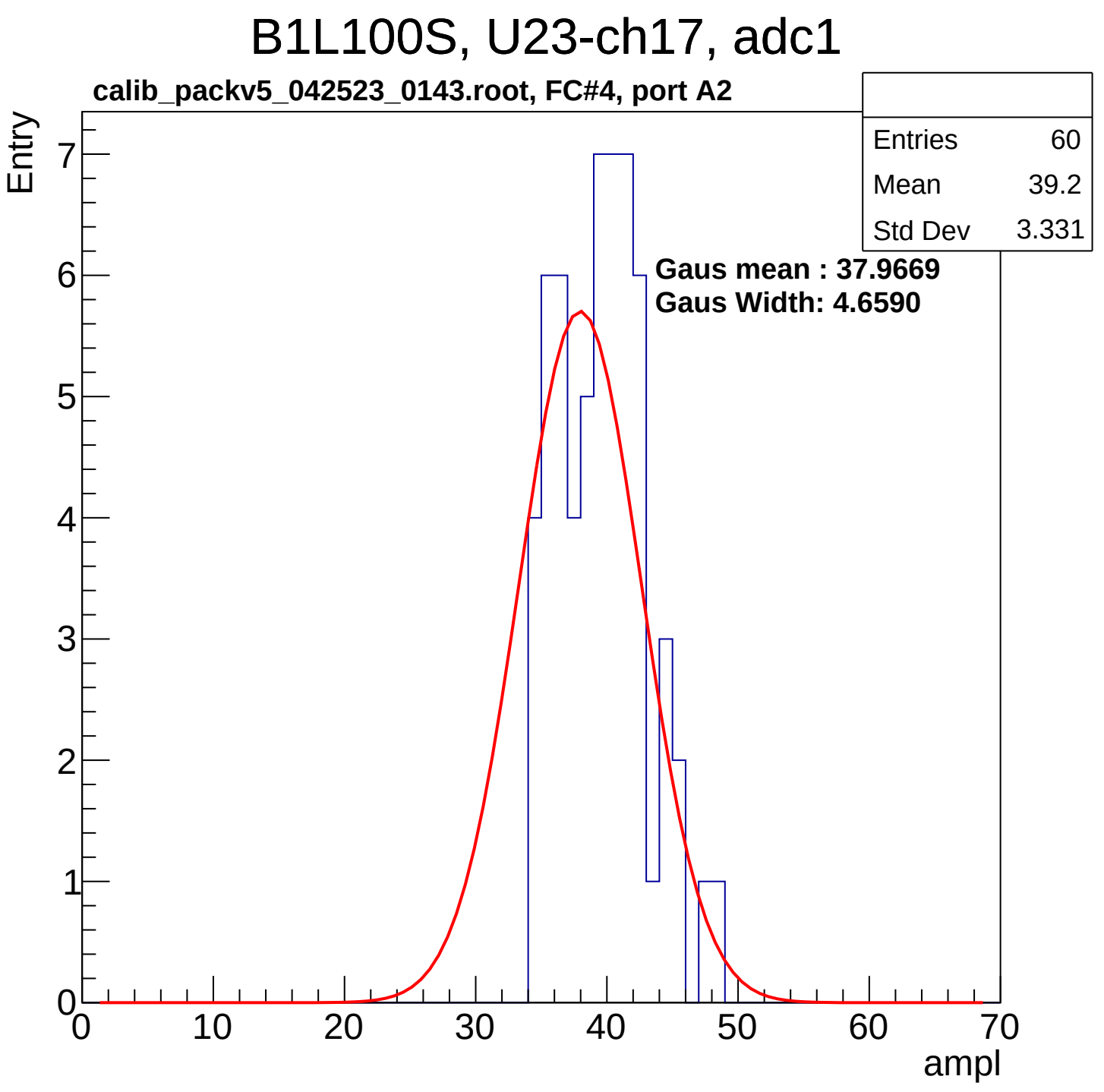

B1L100S, U23-ch16, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57
Std Dev 2.952
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

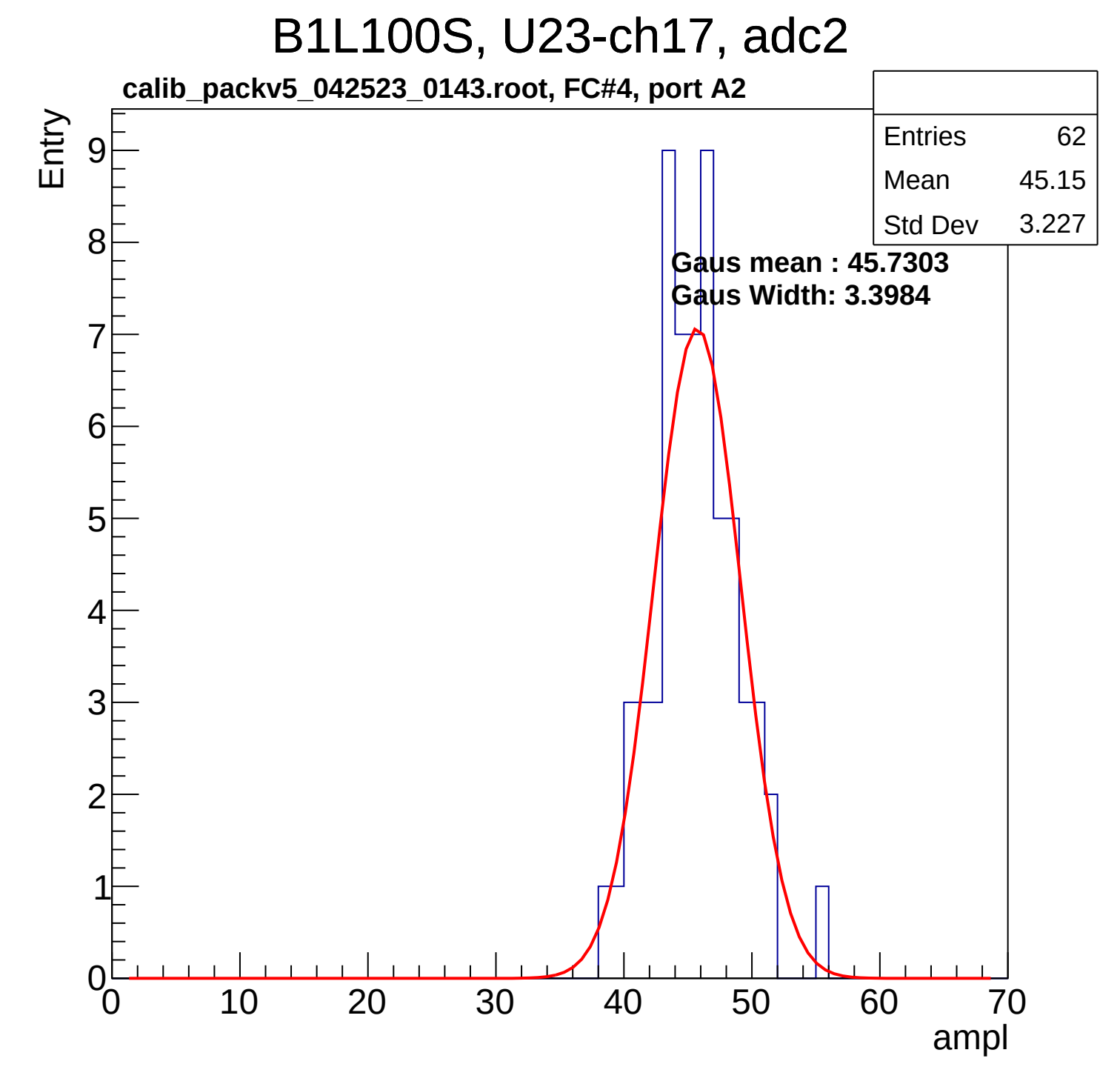

B1L100S, U23-ch16, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.05
Std Dev 9.769
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch16, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 31.24
Std Dev 6.03
Gaus mean : 32.5273
Gaus Width: 4.2269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 39.2
Std Dev 3.331
Gaus mean : 37.9669
Gaus Width: 4.6590
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 45.15
Std Dev 3.227
Gaus mean : 45.7303
Gaus Width: 3.3984
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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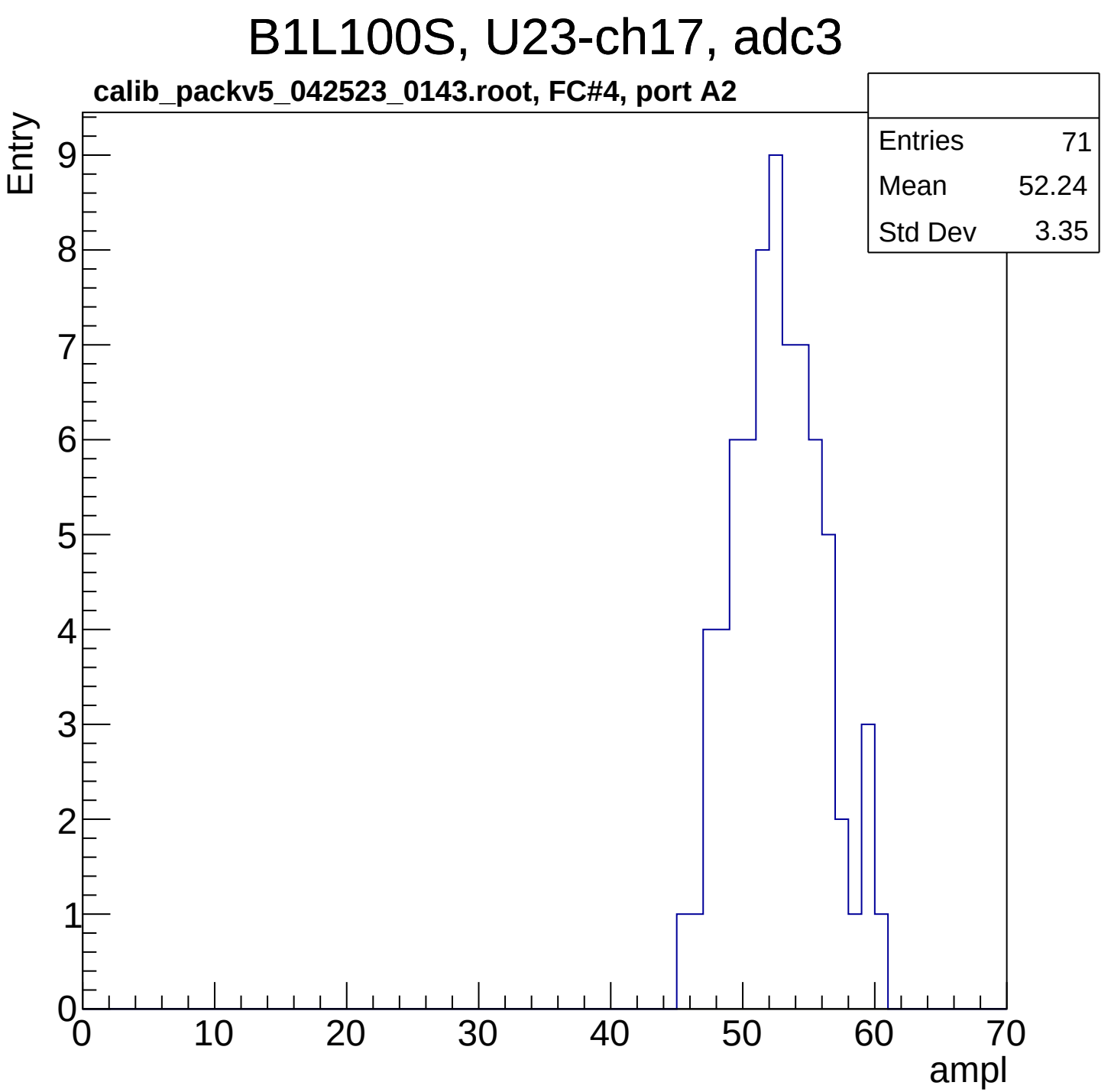
B1L100S, U23-ch17, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 52.24
Std Dev 3.35
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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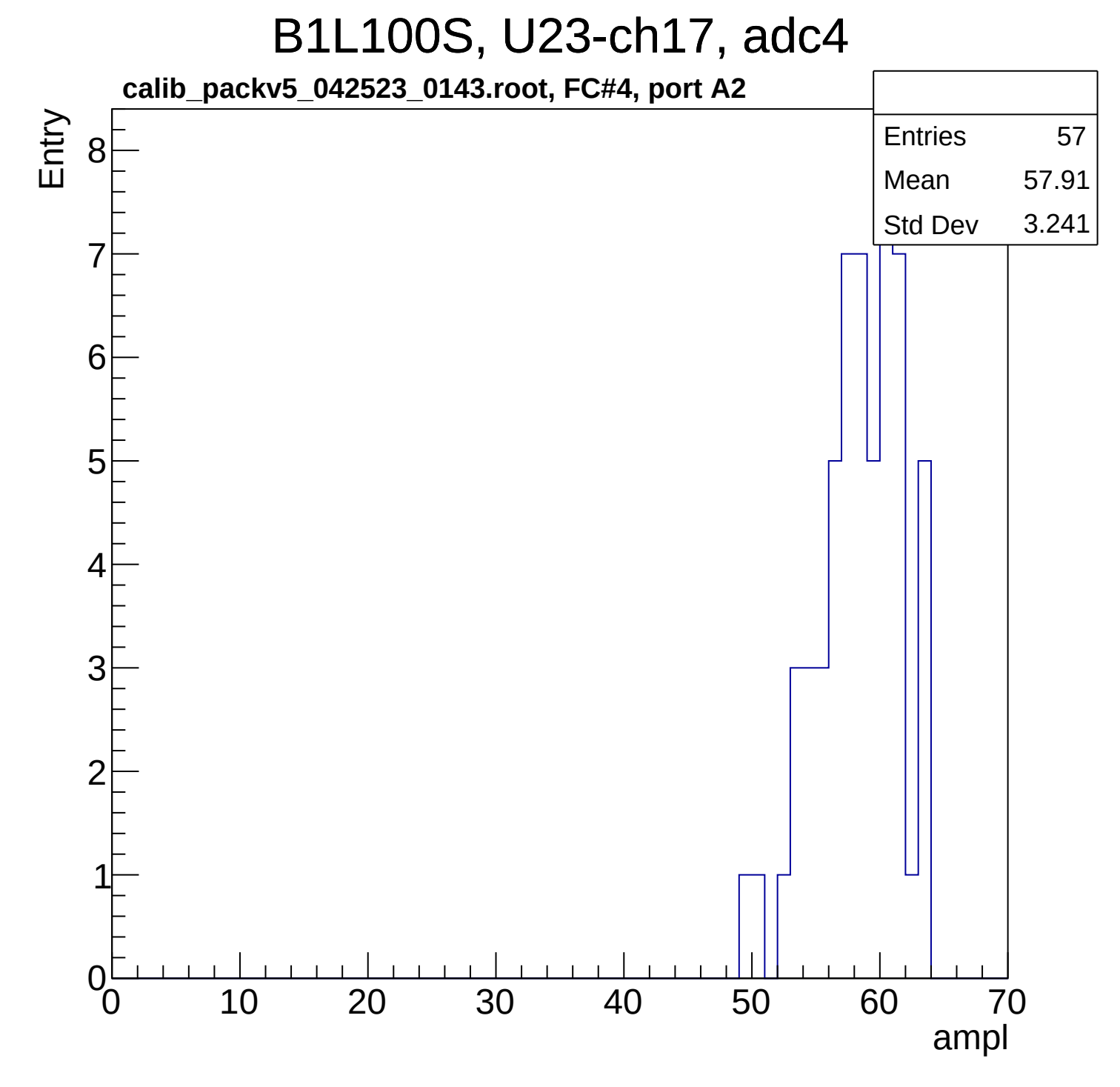
40
50
60
70


B1L100S, U23-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.91
Std Dev 3.241
Entry
ampl
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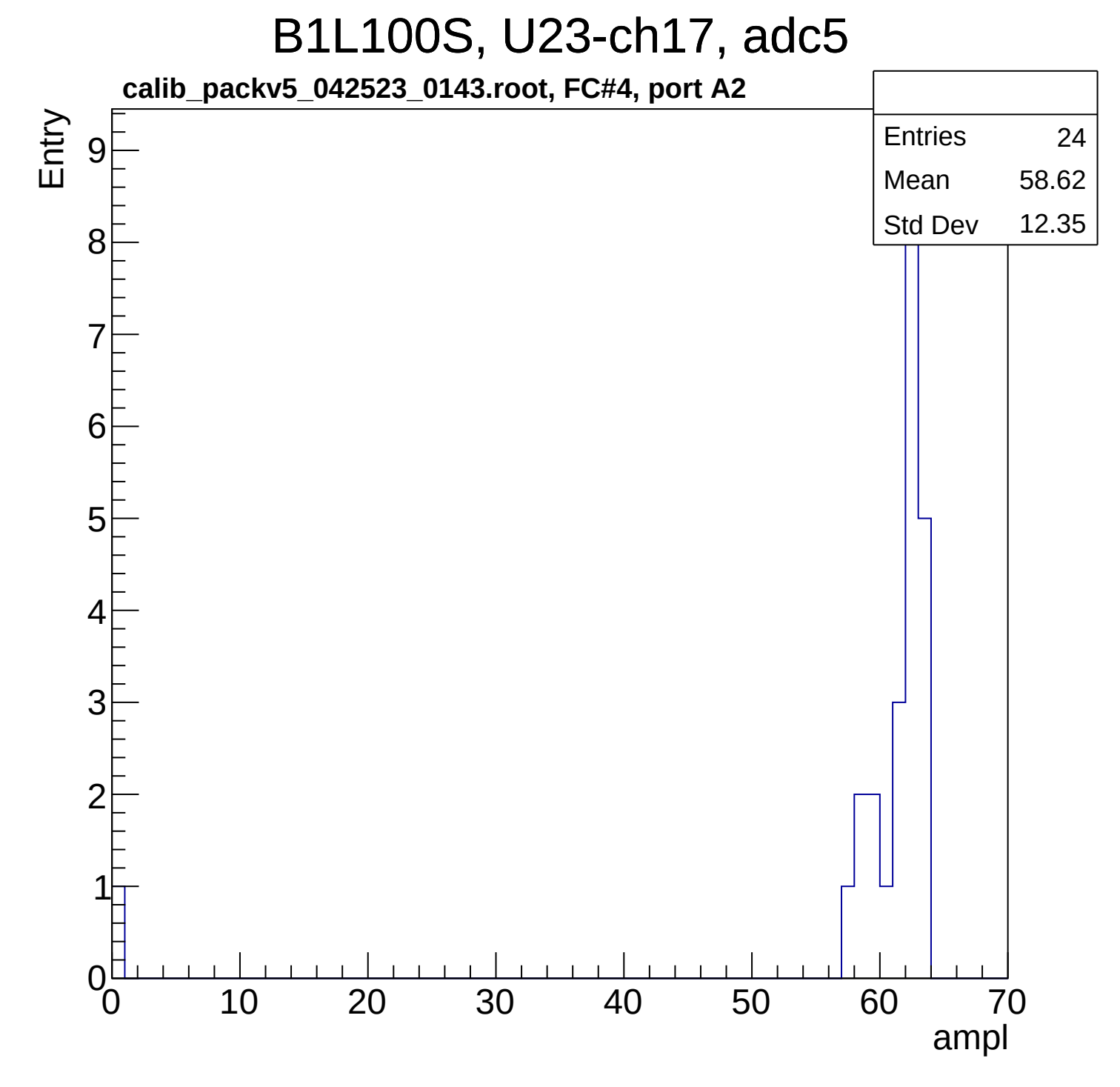
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch17, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.62
Std Dev 12.35
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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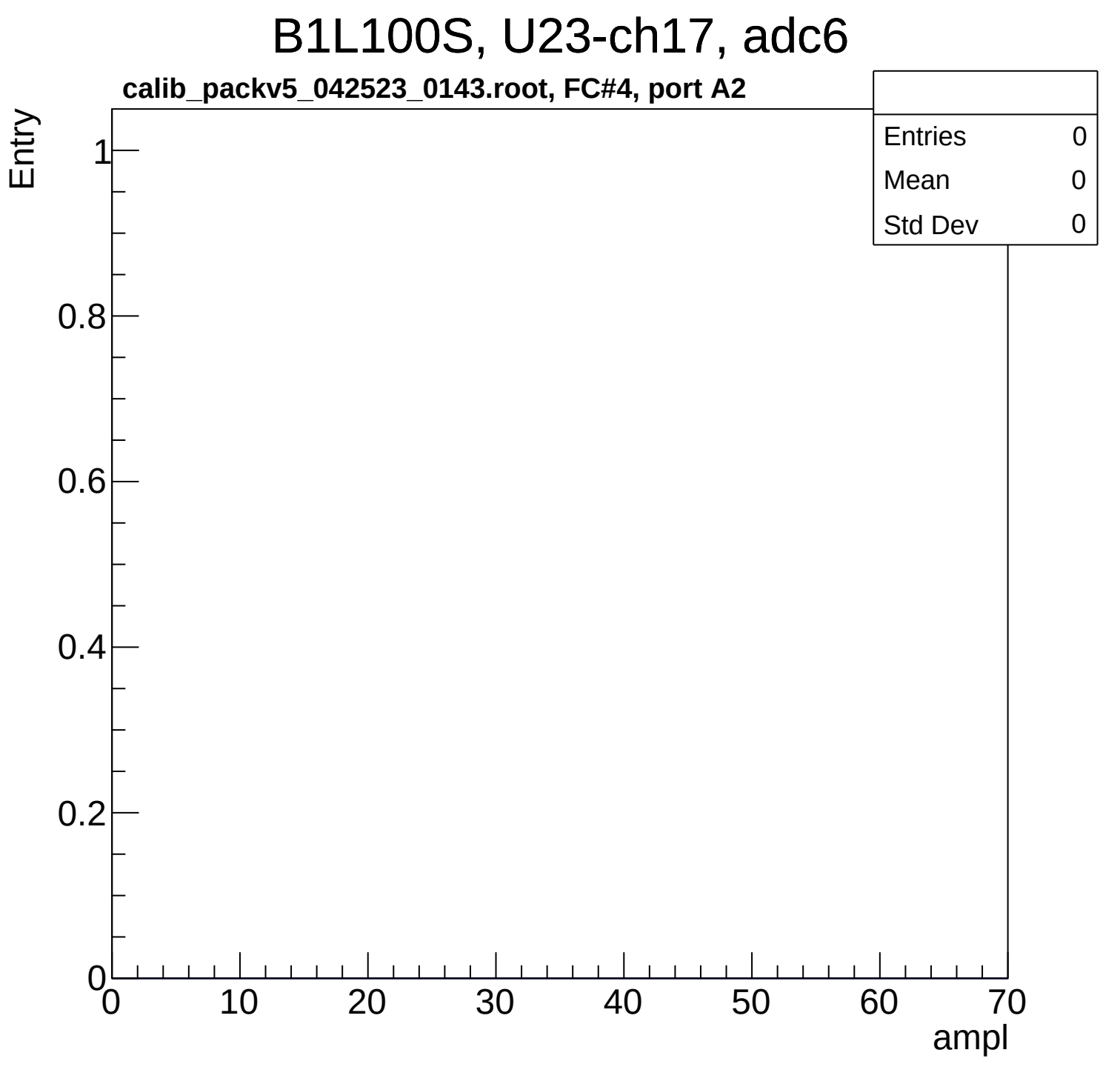
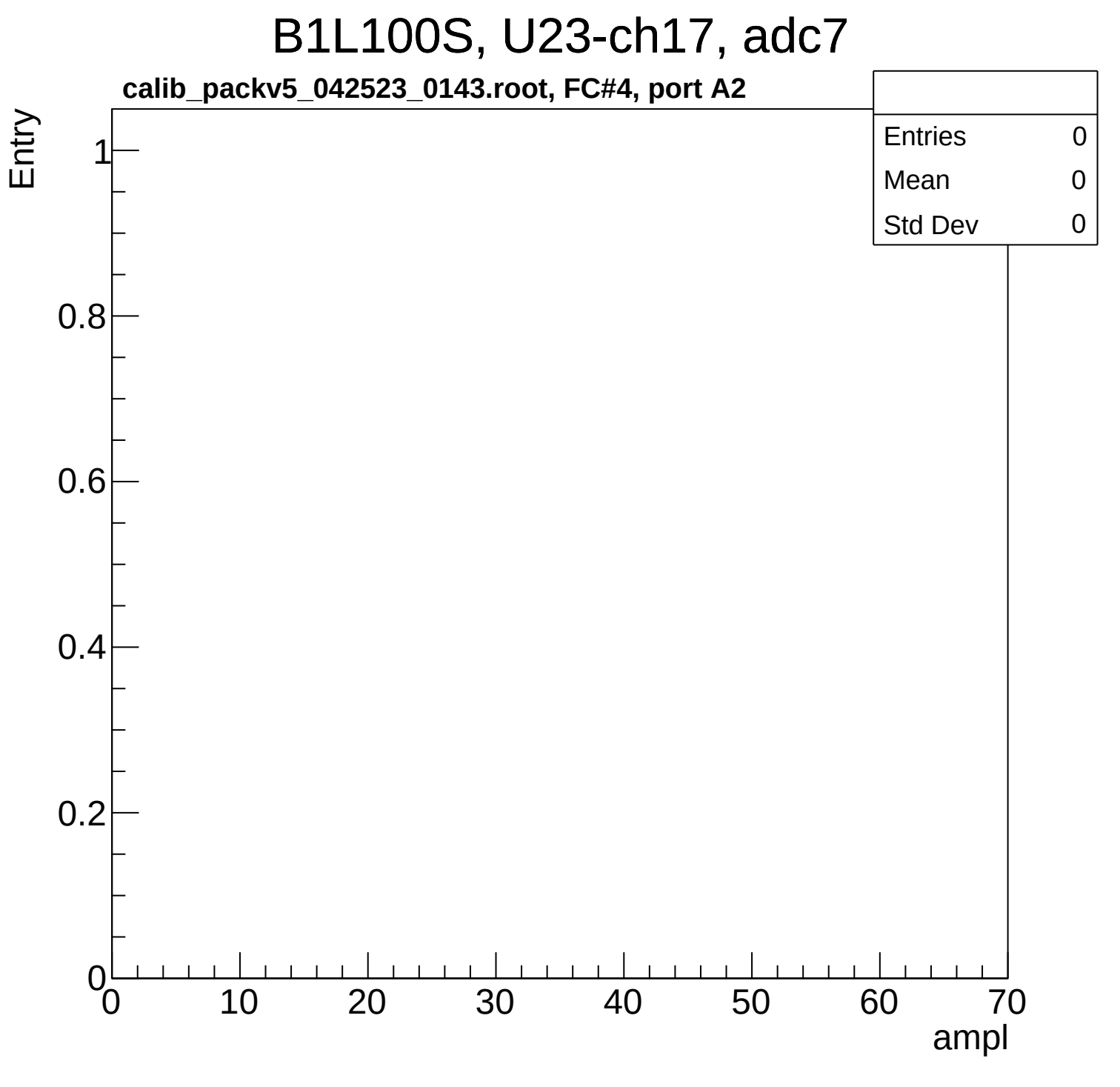

B1L100S, U23-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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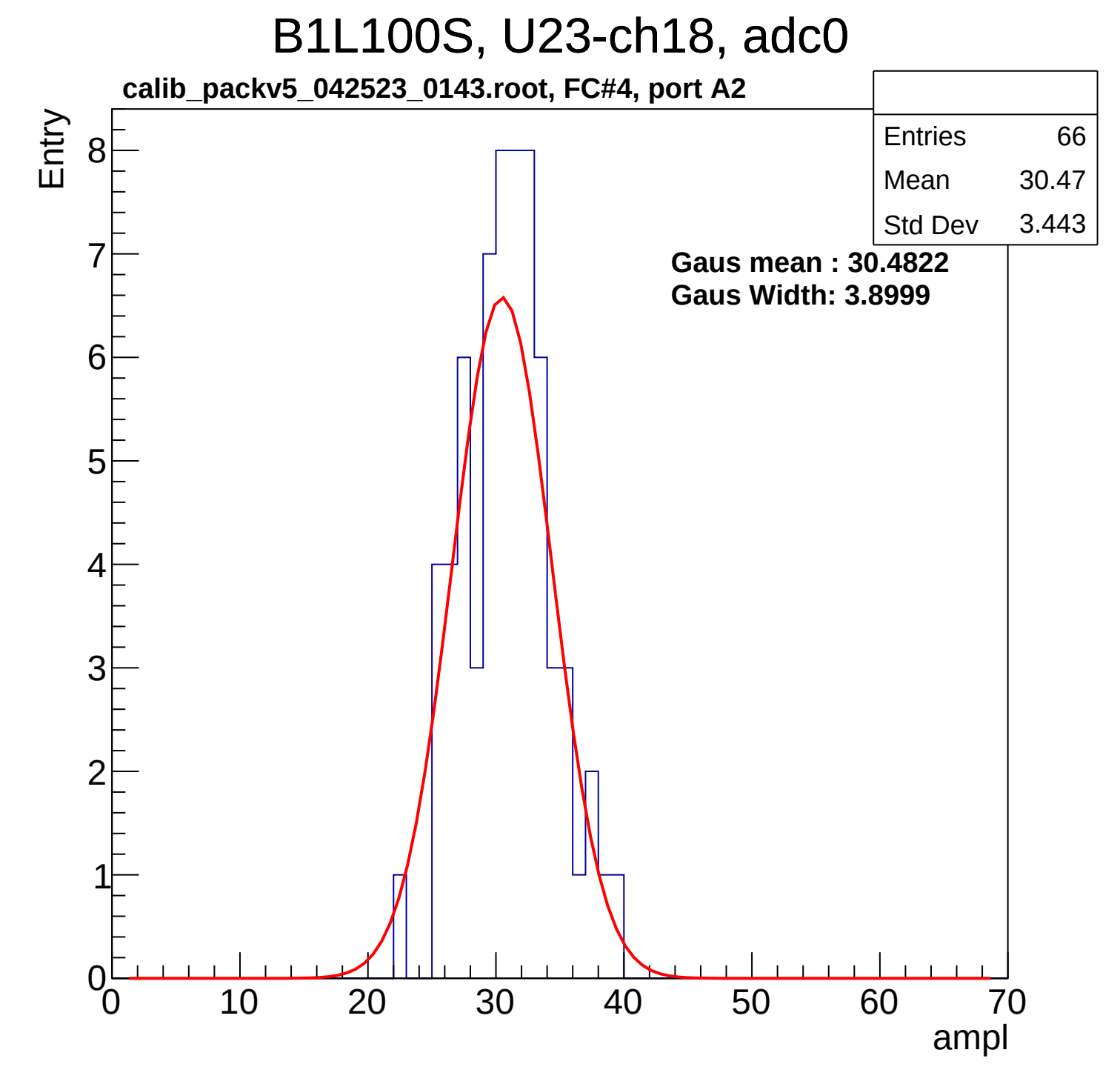
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.47
Std Dev 3.443
Gaus mean : 30.4822
Gaus Width: 3.8999
Entry
ampl
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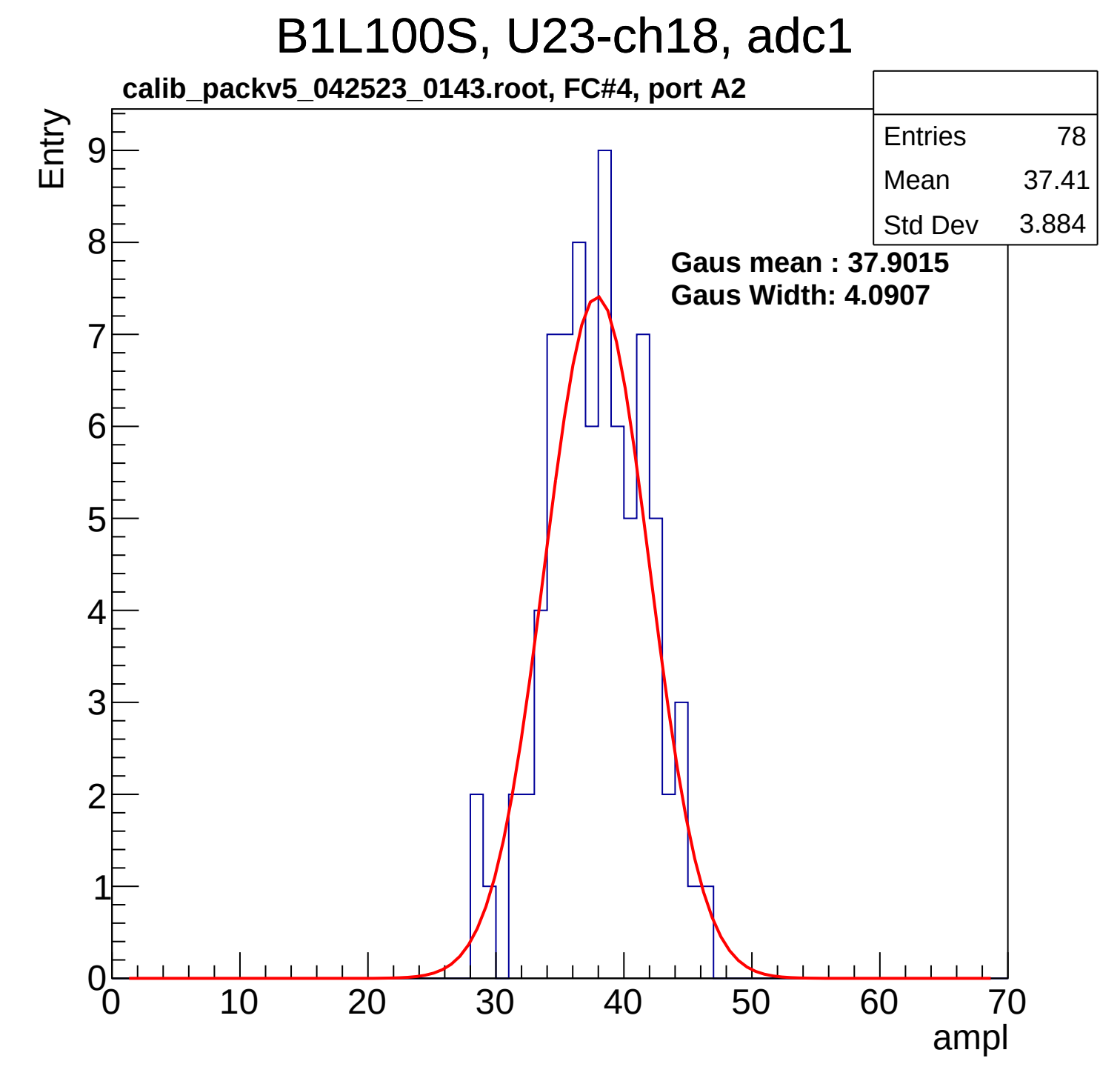
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.41
Std Dev 3.884
Gaus mean : 37.9015
Gaus Width: 4.0907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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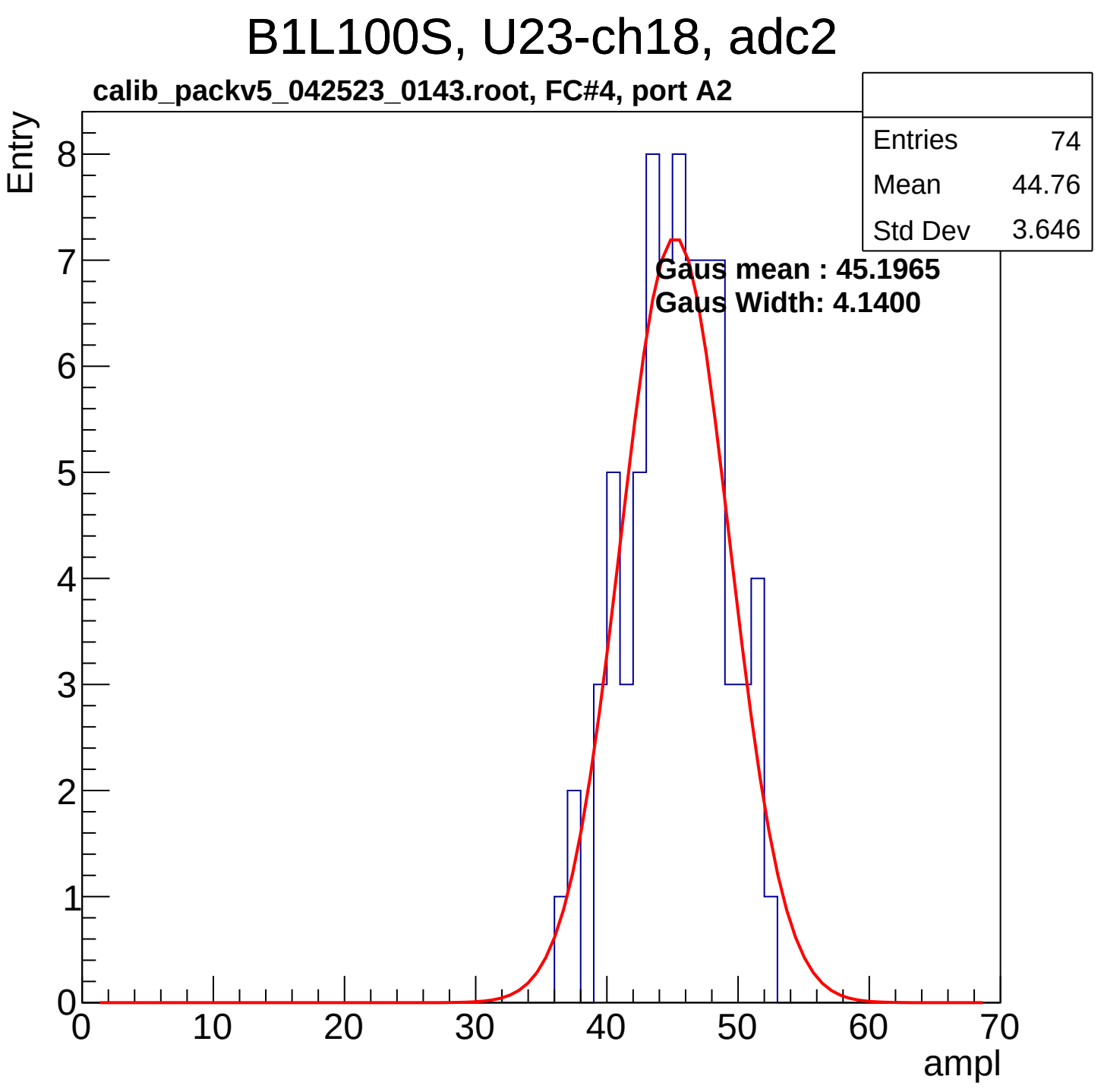
20
30
40
50
60
70


B1L100S, U23-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 44.76
Std Dev 3.646
Gaus mean : 45.1965
Gaus Width: 4.1400
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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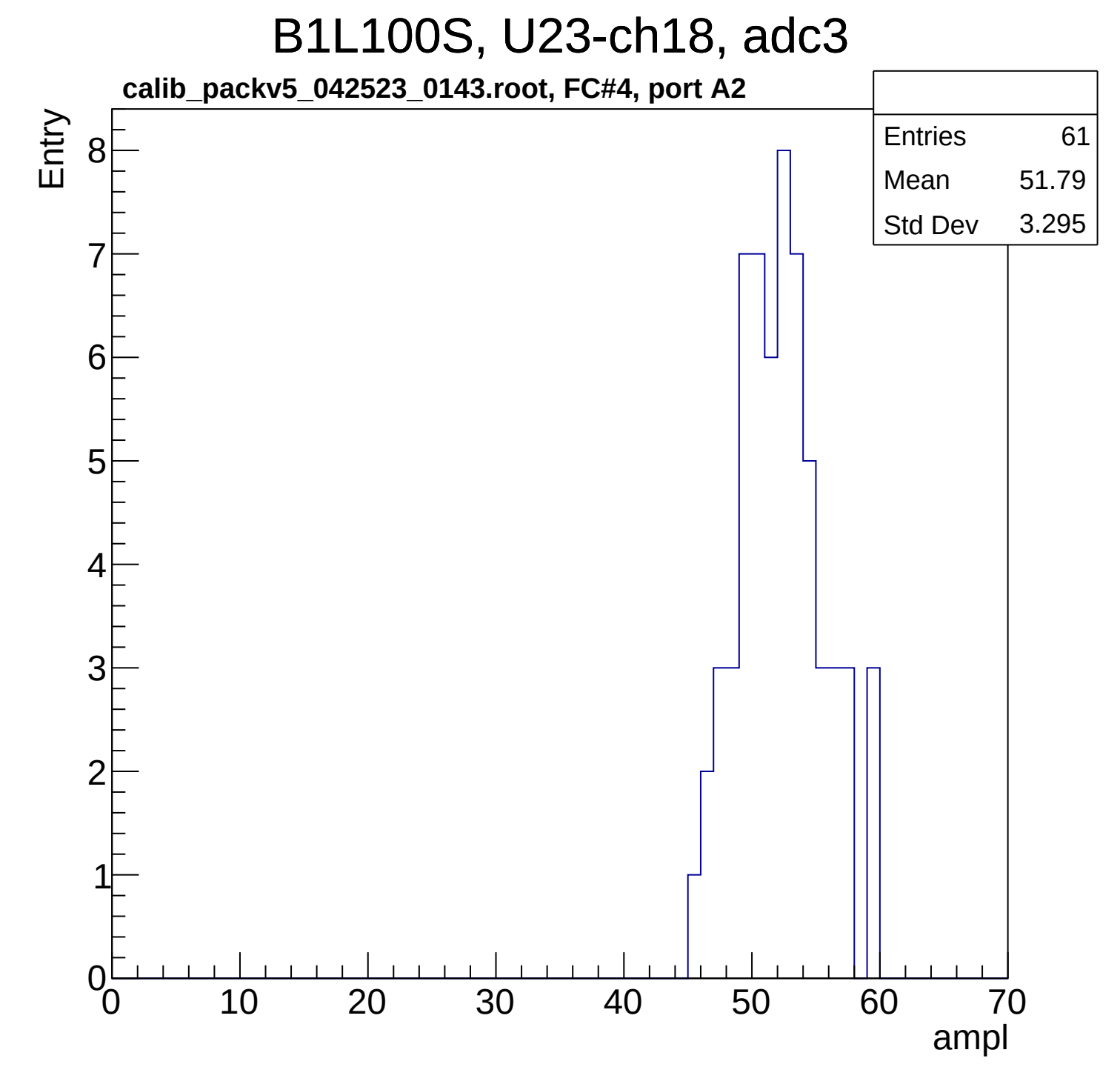
B1L100S, U23-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 51.79
Std Dev 3.295
Entry
ampl
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4
5
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7
8
0
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20
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40
50
60
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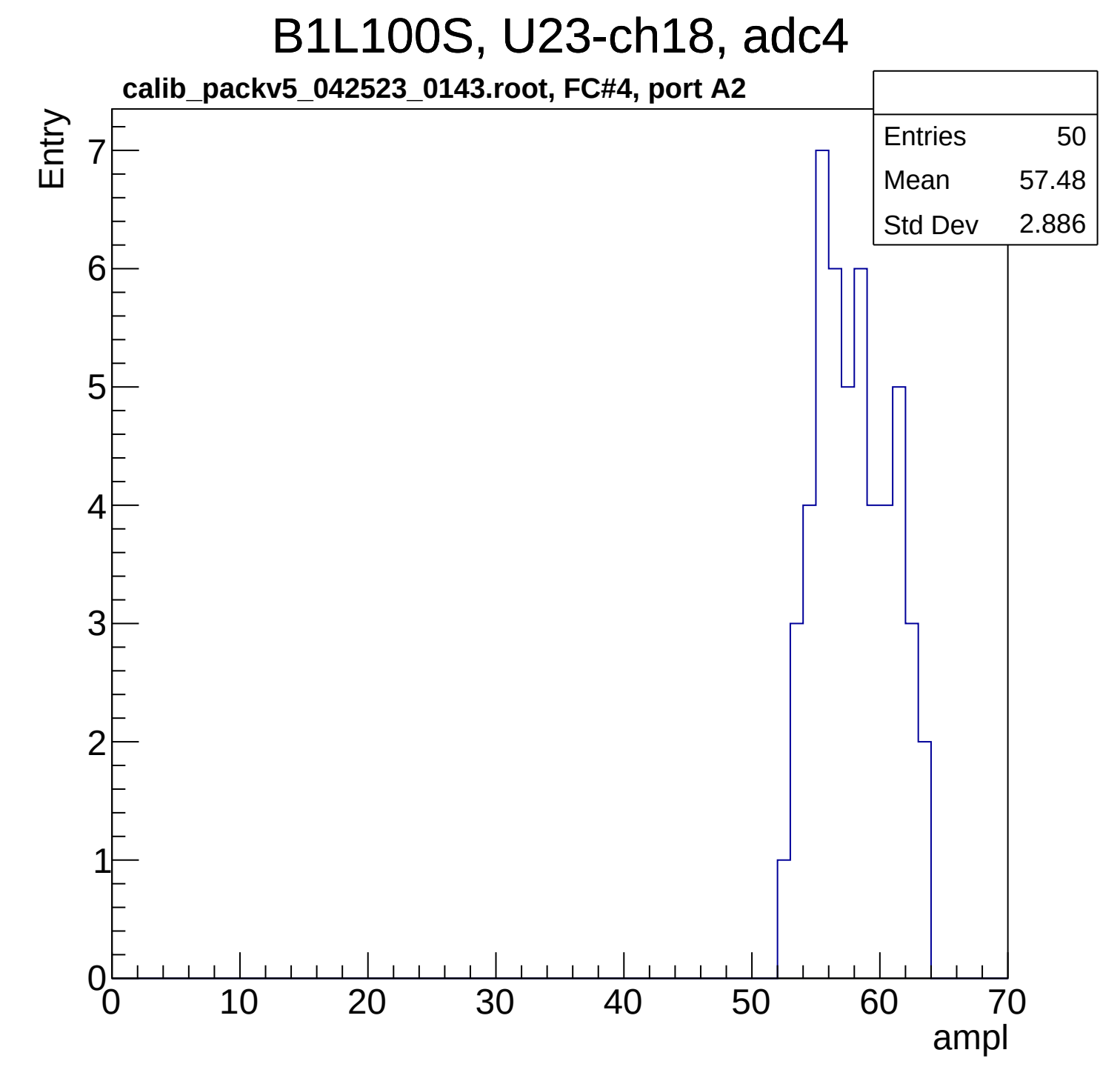
B1L100S, U23-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.48
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
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0
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40
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60
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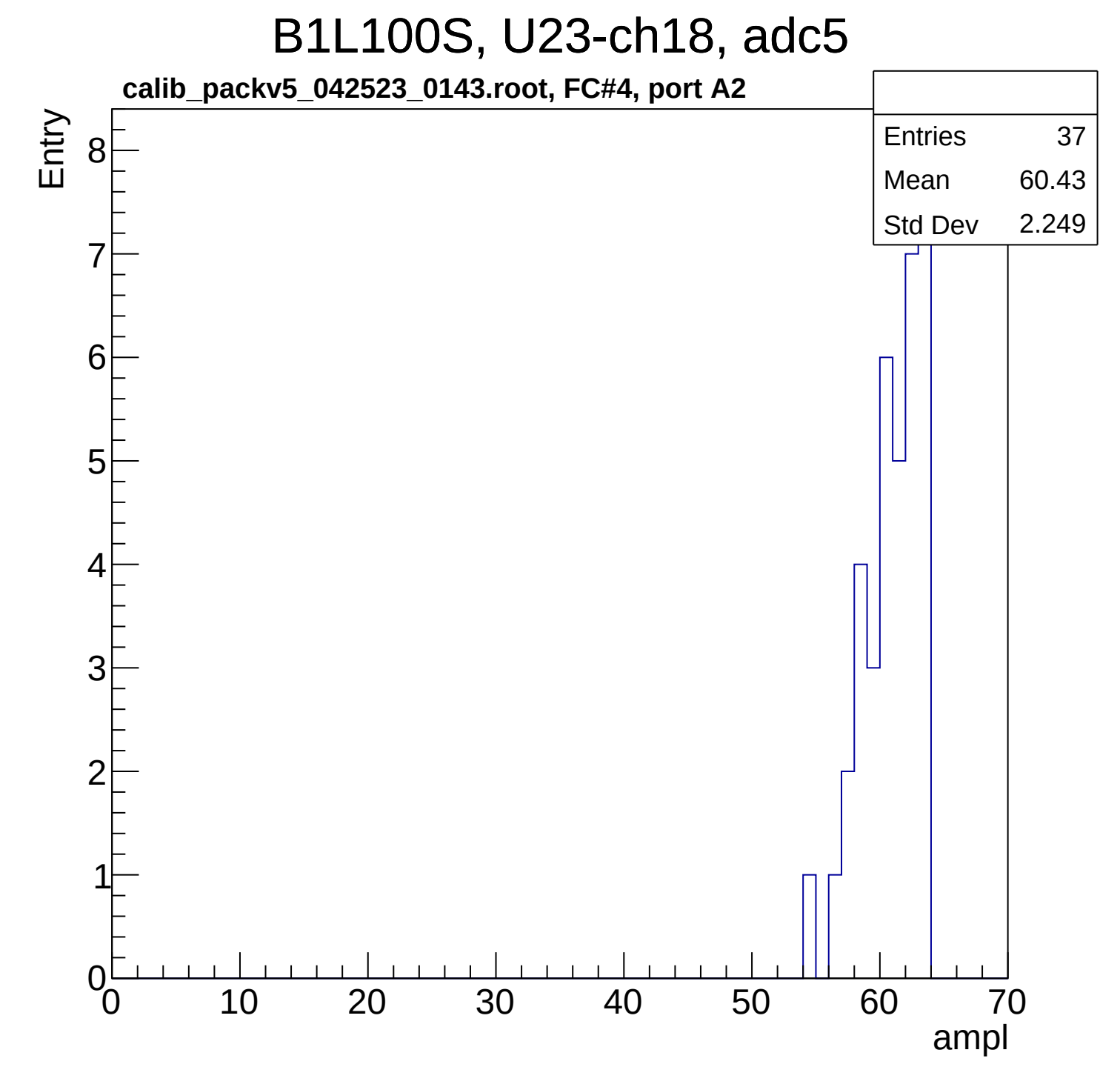
B1L100S, U23-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.43
Std Dev 2.249
Entry
ampl
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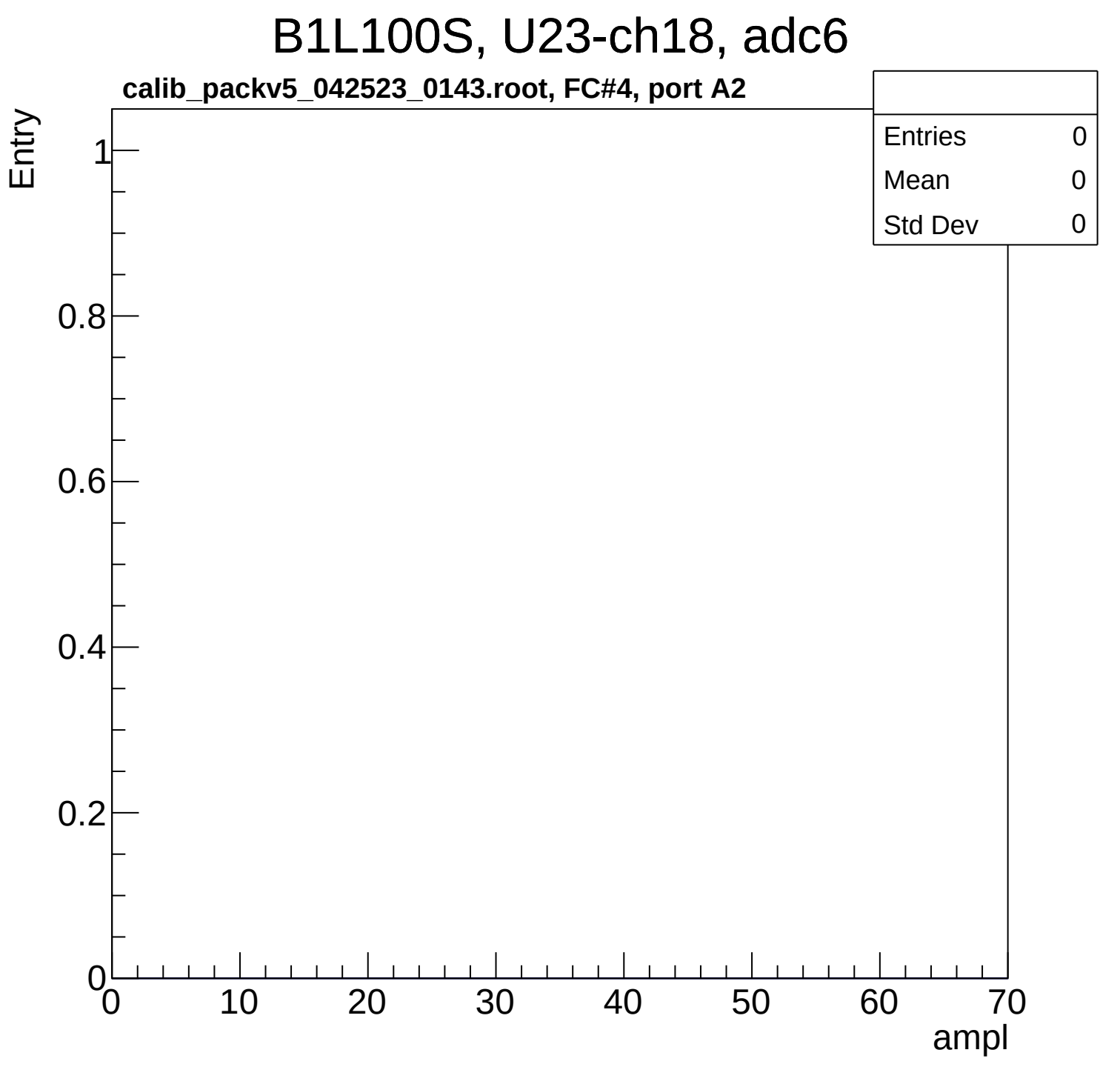
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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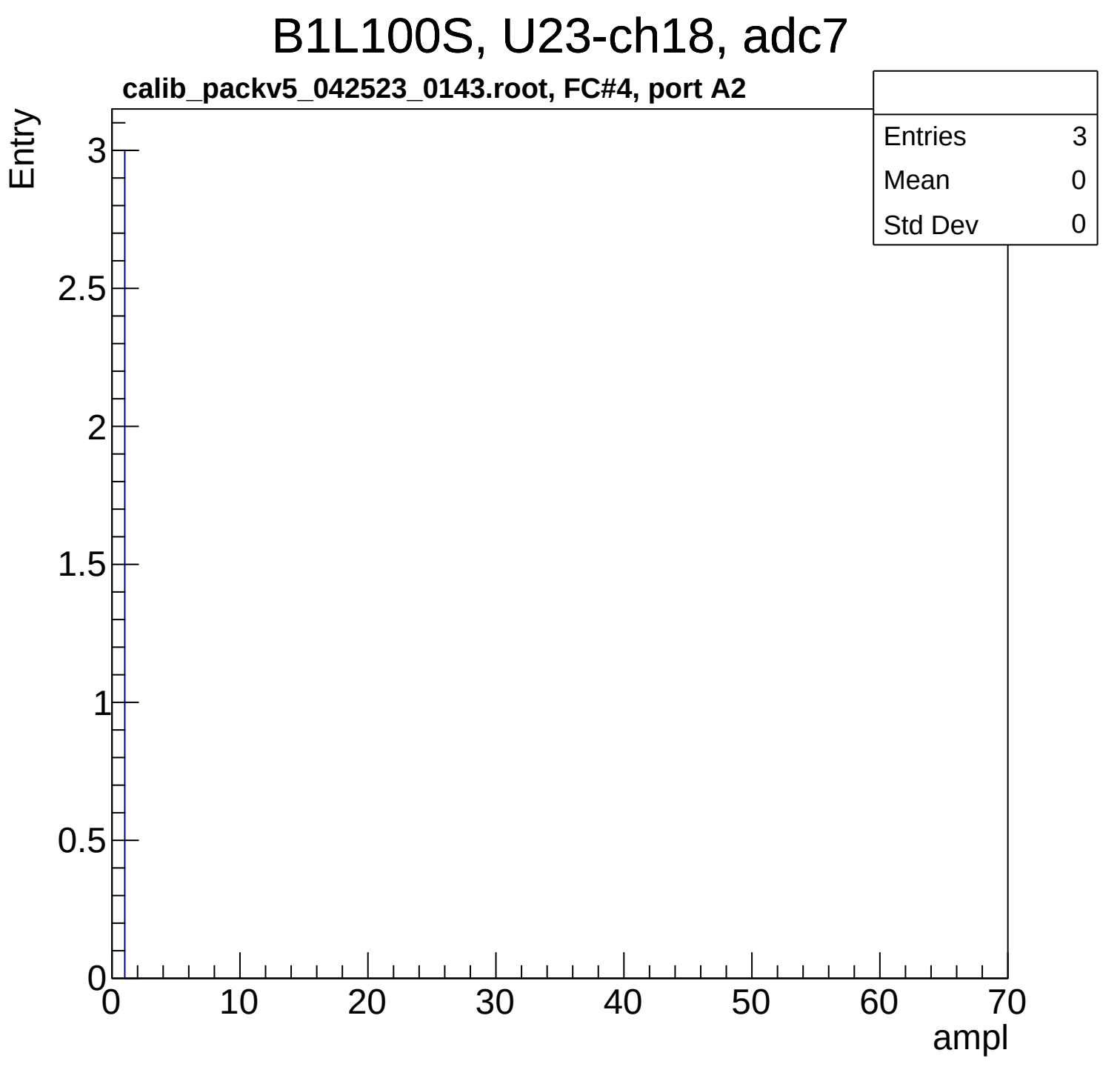
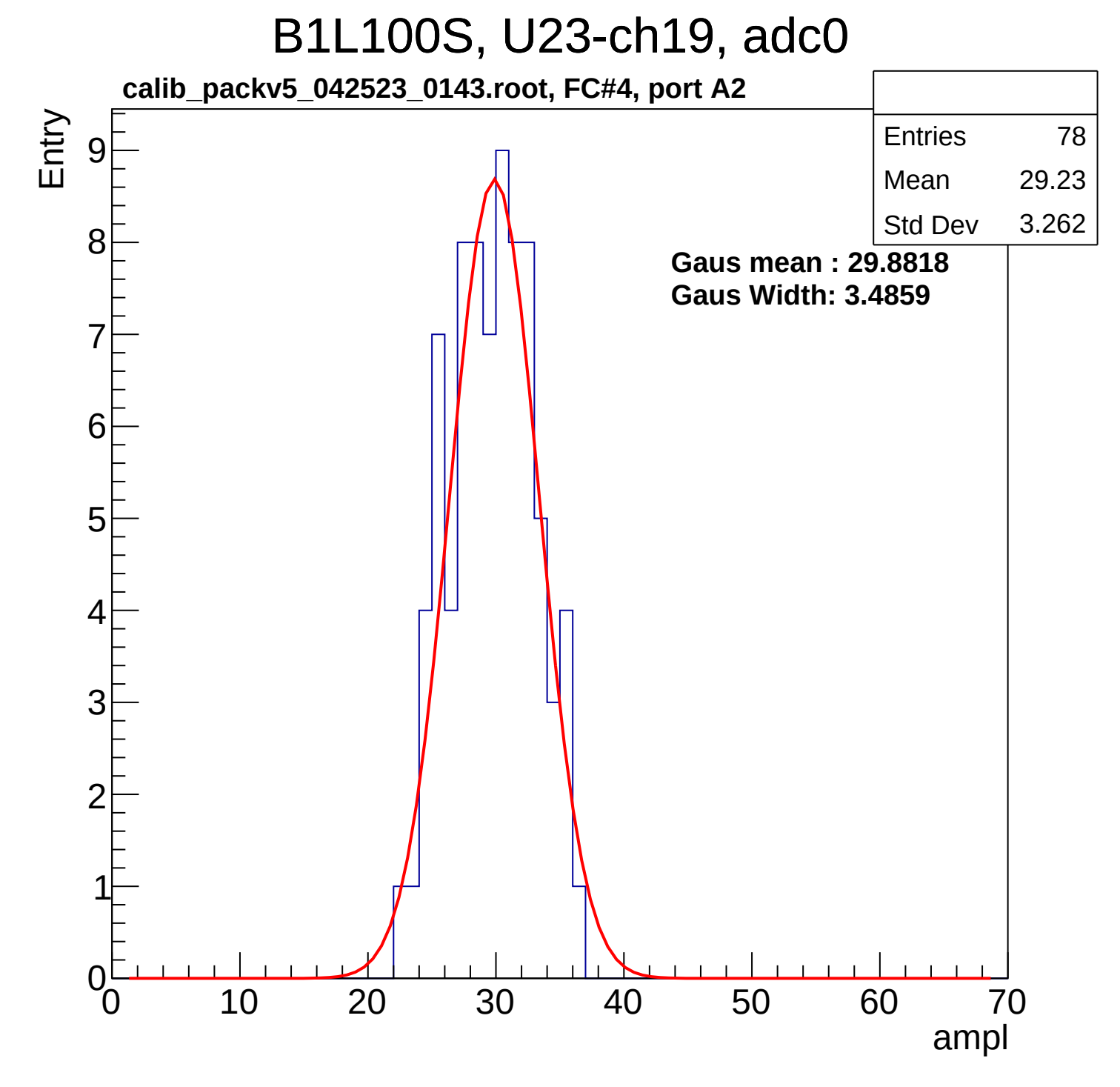
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.23
Std Dev 3.262
Gaus mean : 29.8818
Gaus Width: 3.4859
Entry
ampl
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2
3
4
5
6
7
8
9
0
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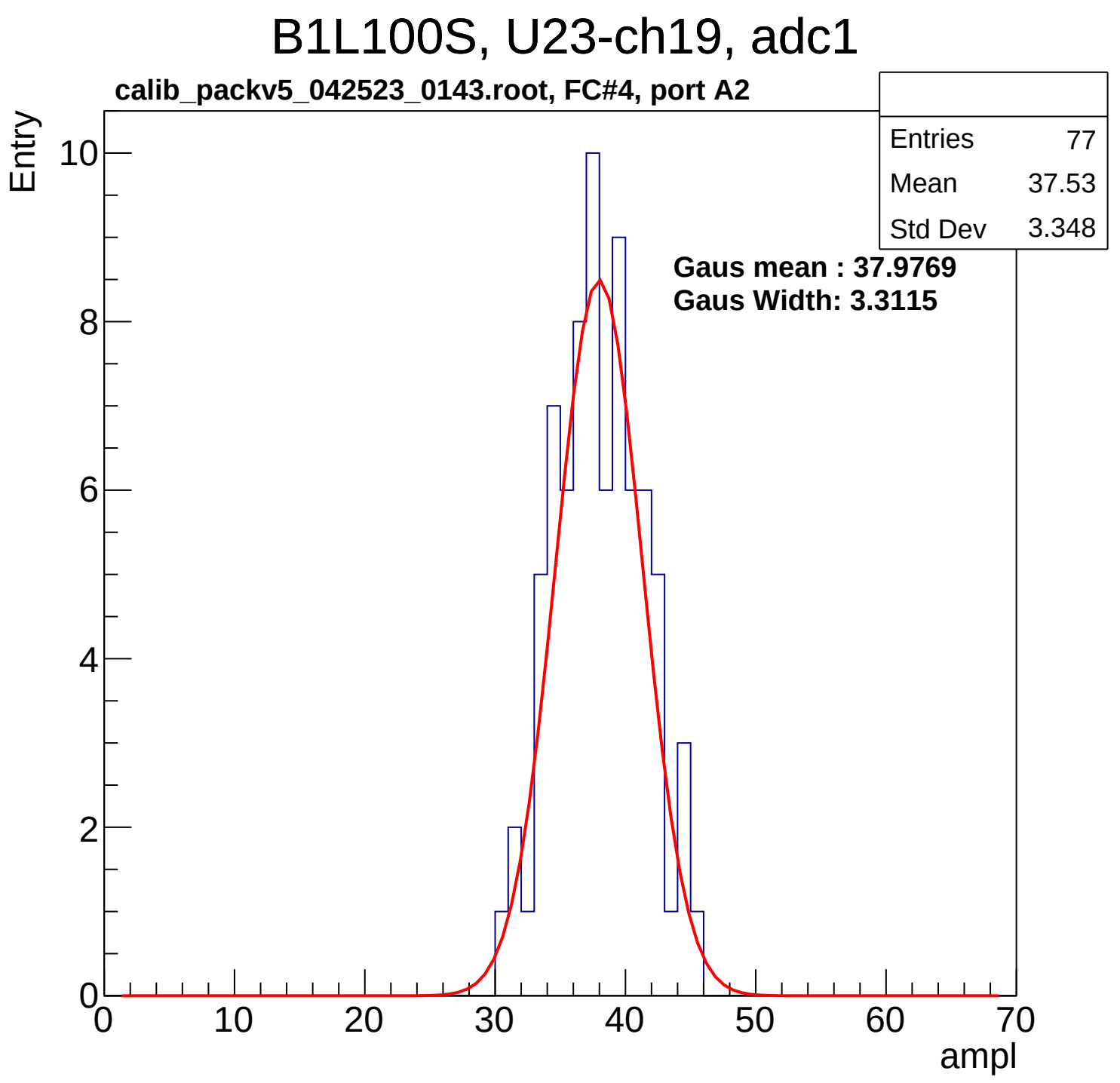
B1L100S, U23-ch19, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.53
Std Dev 3.348
Gaus mean : 37.9769
Gaus Width: 3.3115
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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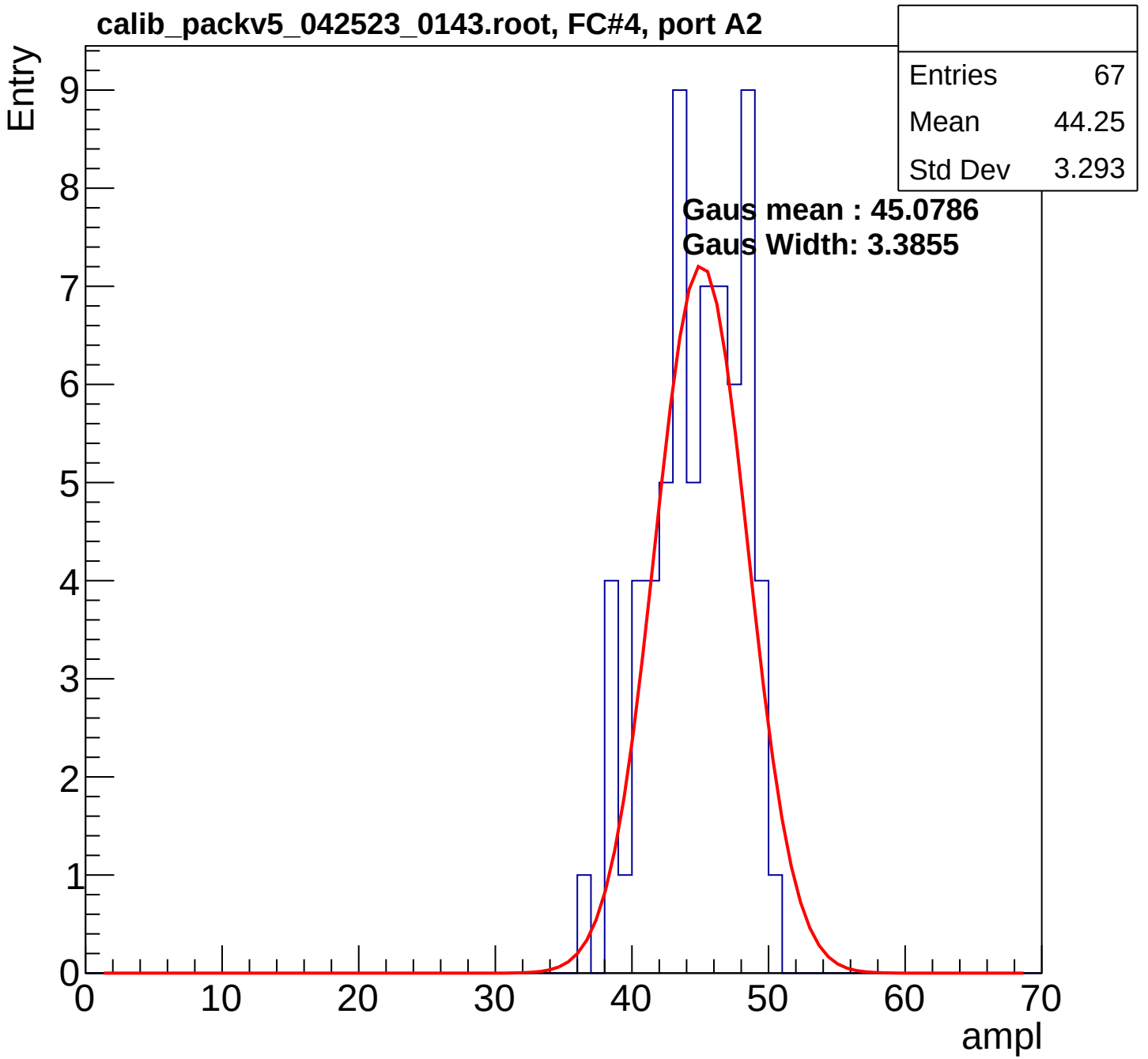
B1L100S, U23-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.25
Std Dev 3.293
Gaus mean : 45.0786
Gaus Width: 3.3855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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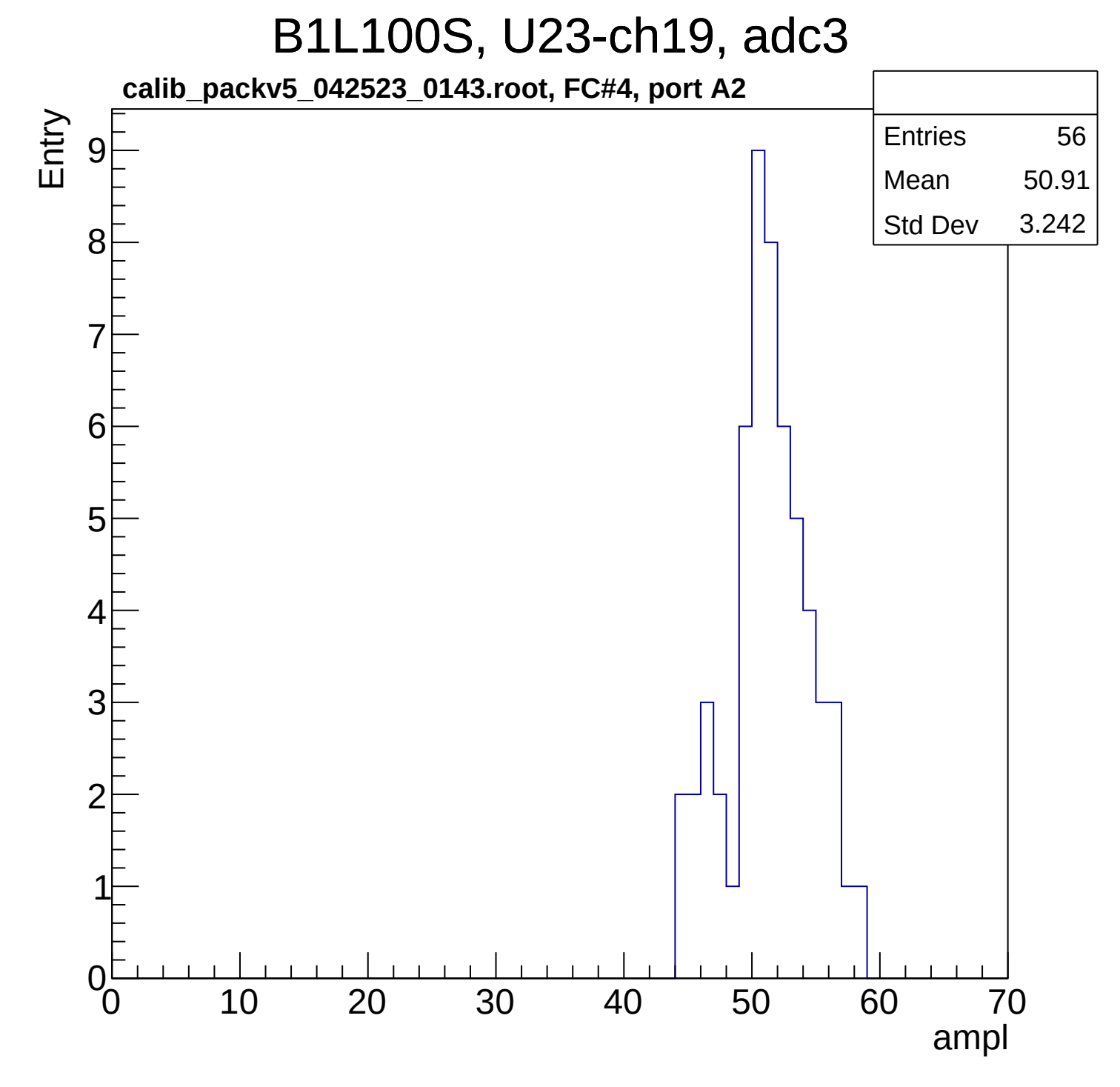
B1L100S, U23-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.91
Std Dev 3.242
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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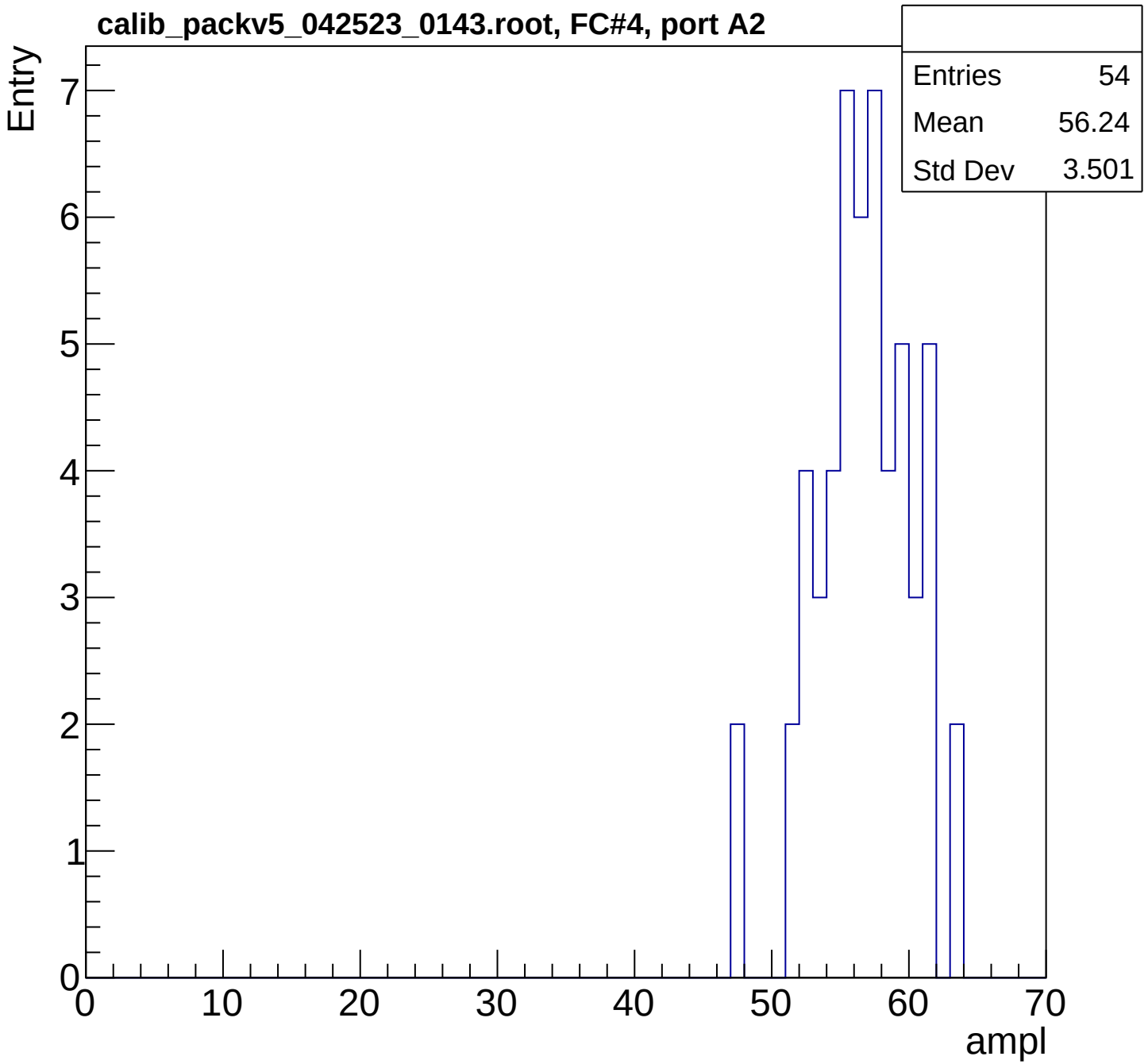
B1L100S, U23-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.24
Std Dev 3.501
Entry
ampl
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1
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4
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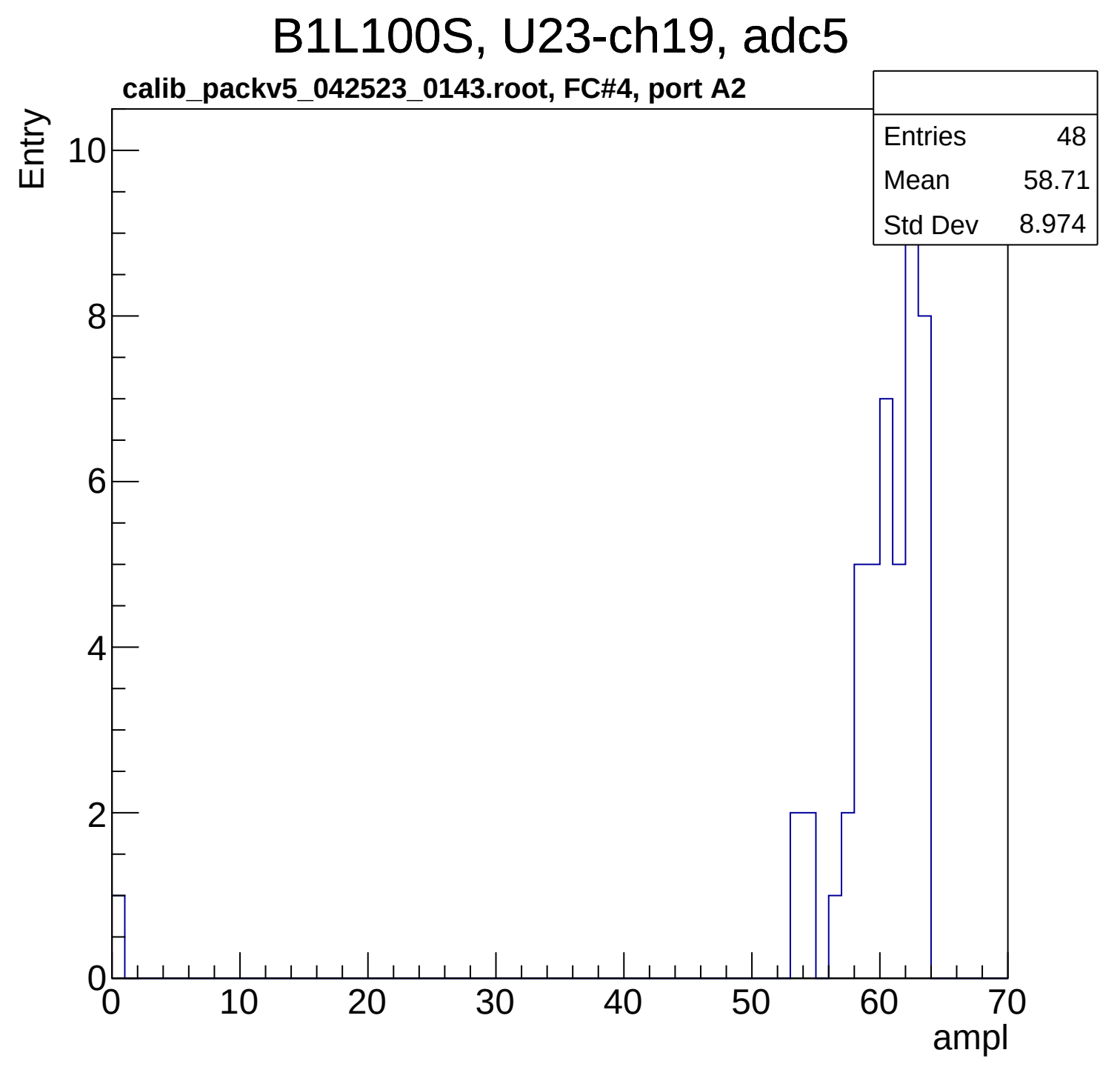
B1L100S, U23-ch19, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.71
Std Dev 8.974
Entry
ampl
0
2
4
6
8
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0
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40
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60
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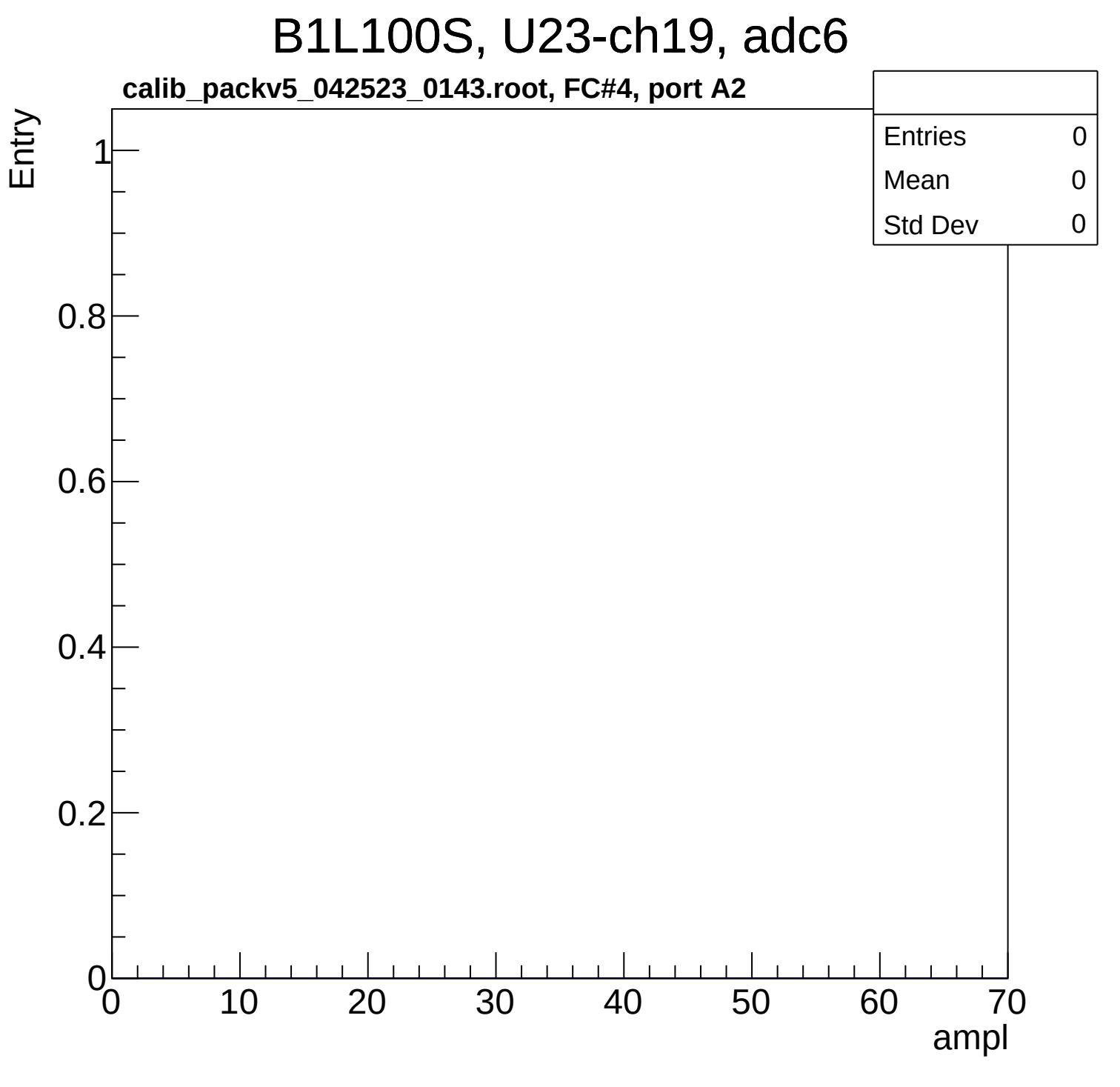
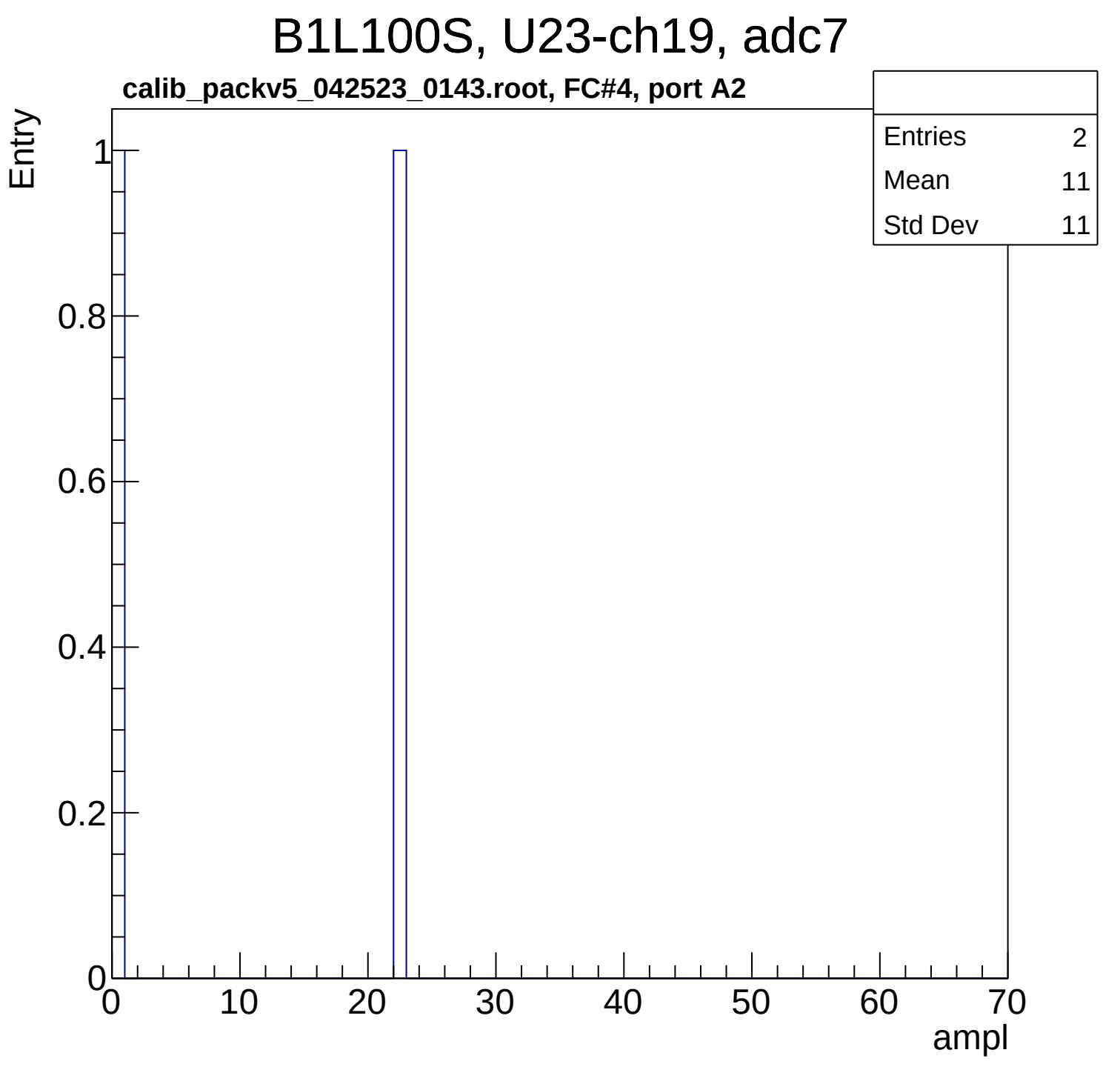

B1L100S, U23-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
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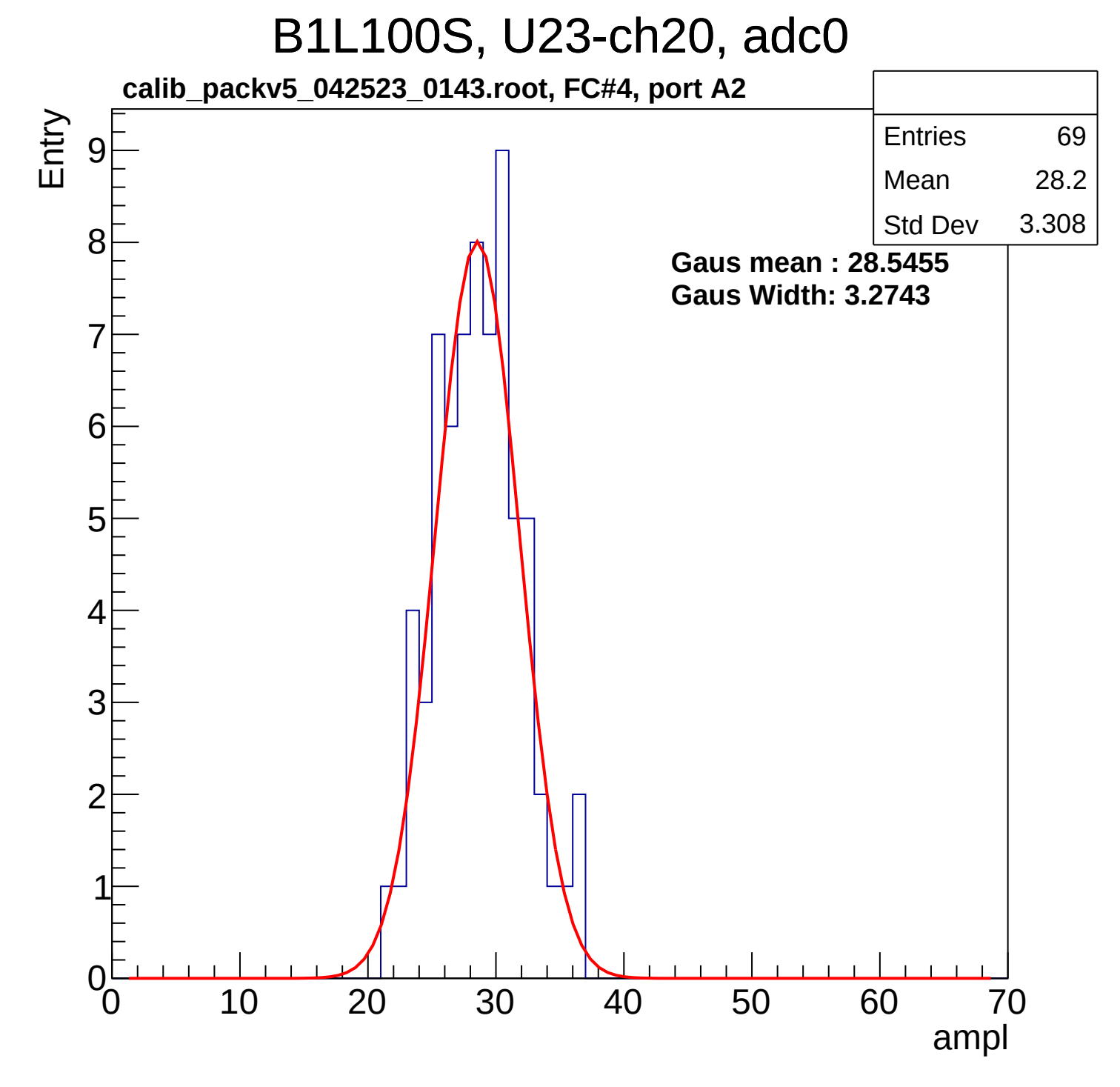
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.2
Std Dev 3.308
Gaus mean : 28.5455
Gaus Width: 3.2743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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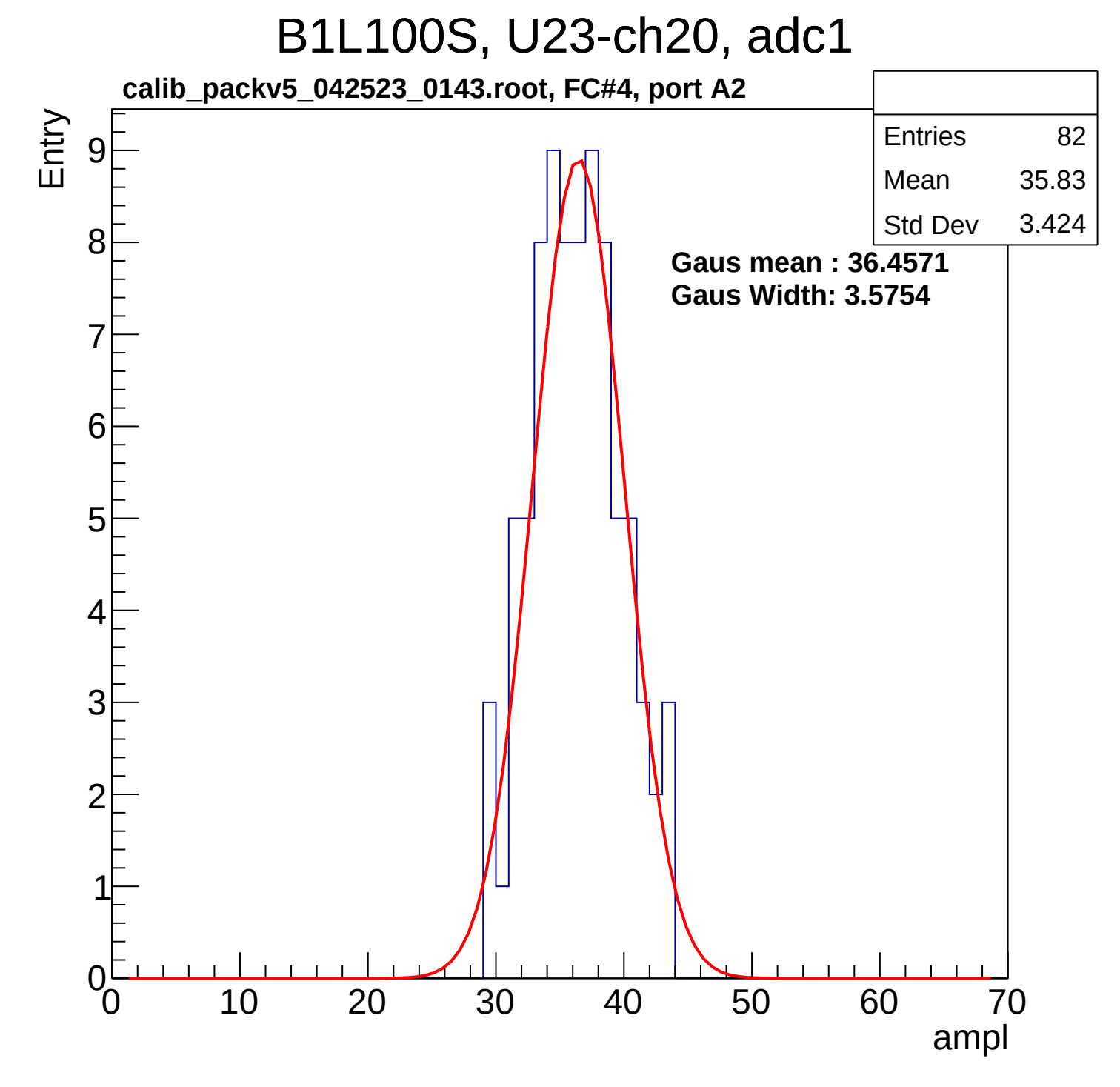
B1L100S, U23-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 35.83
Std Dev 3.424
Gaus mean : 36.4571
Gaus Width: 3.5754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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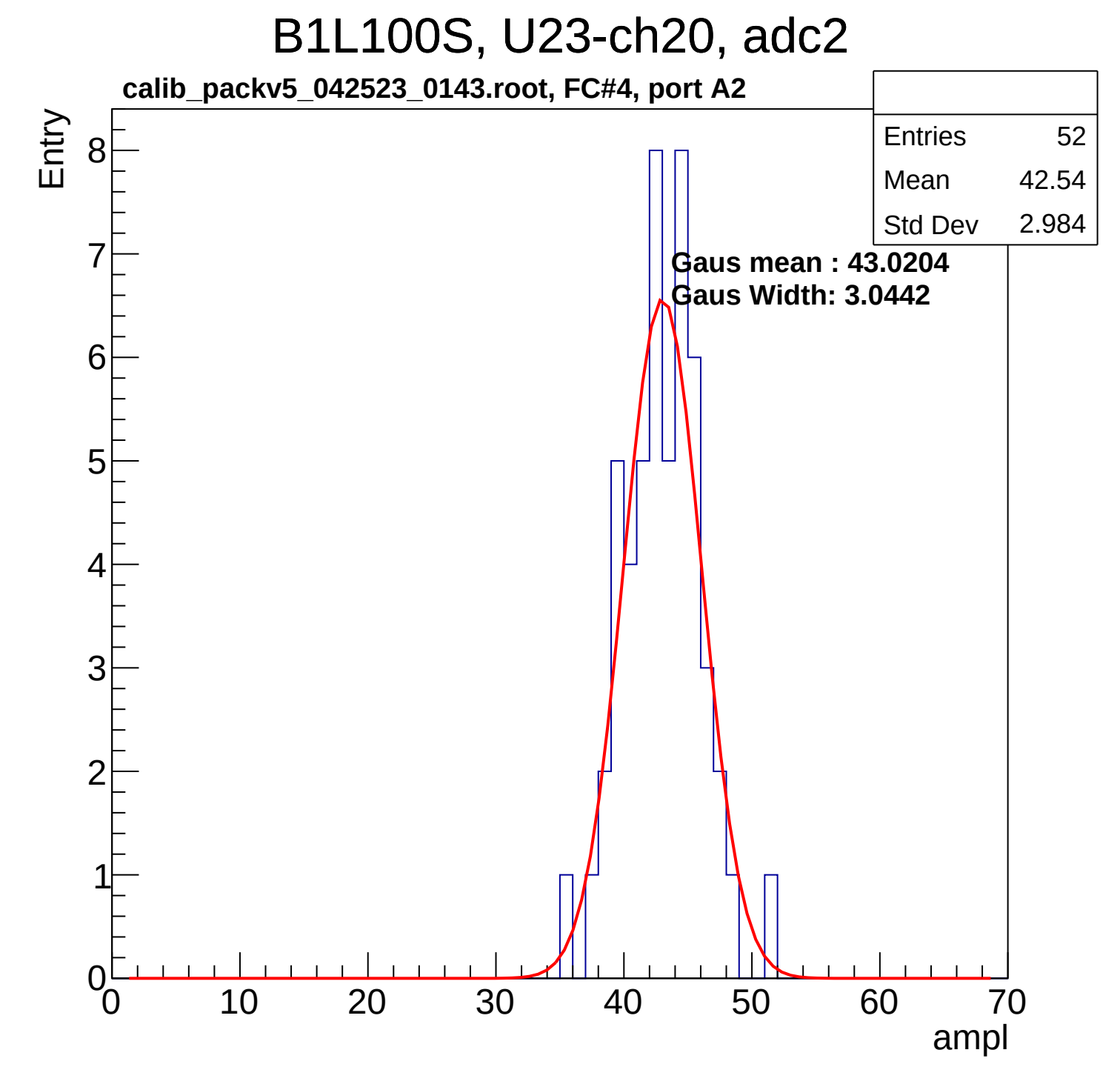
B1L100S, U23-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 42.54
Std Dev 2.984
Gaus mean : 43.0204
Gaus Width: 3.0442
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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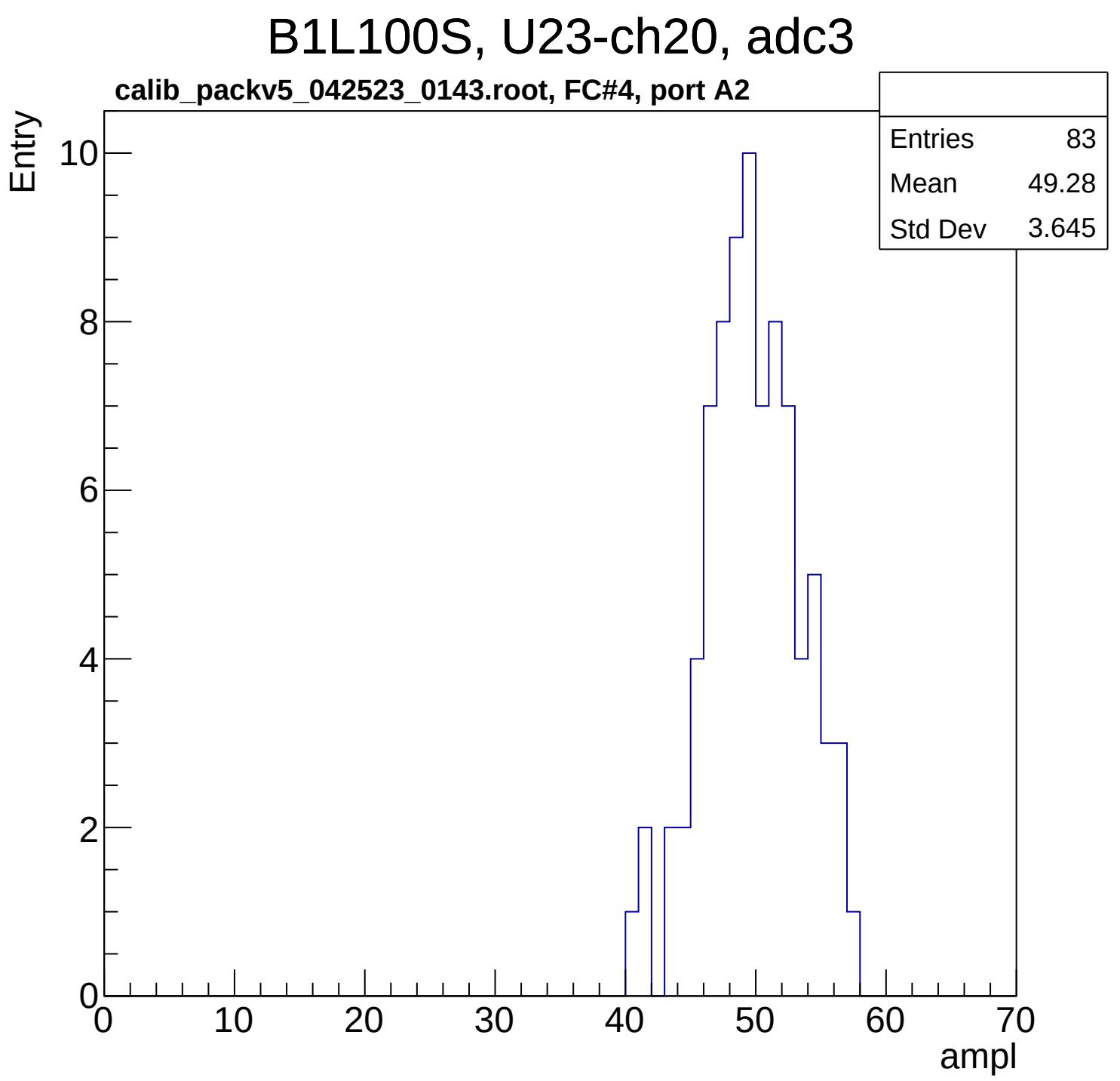
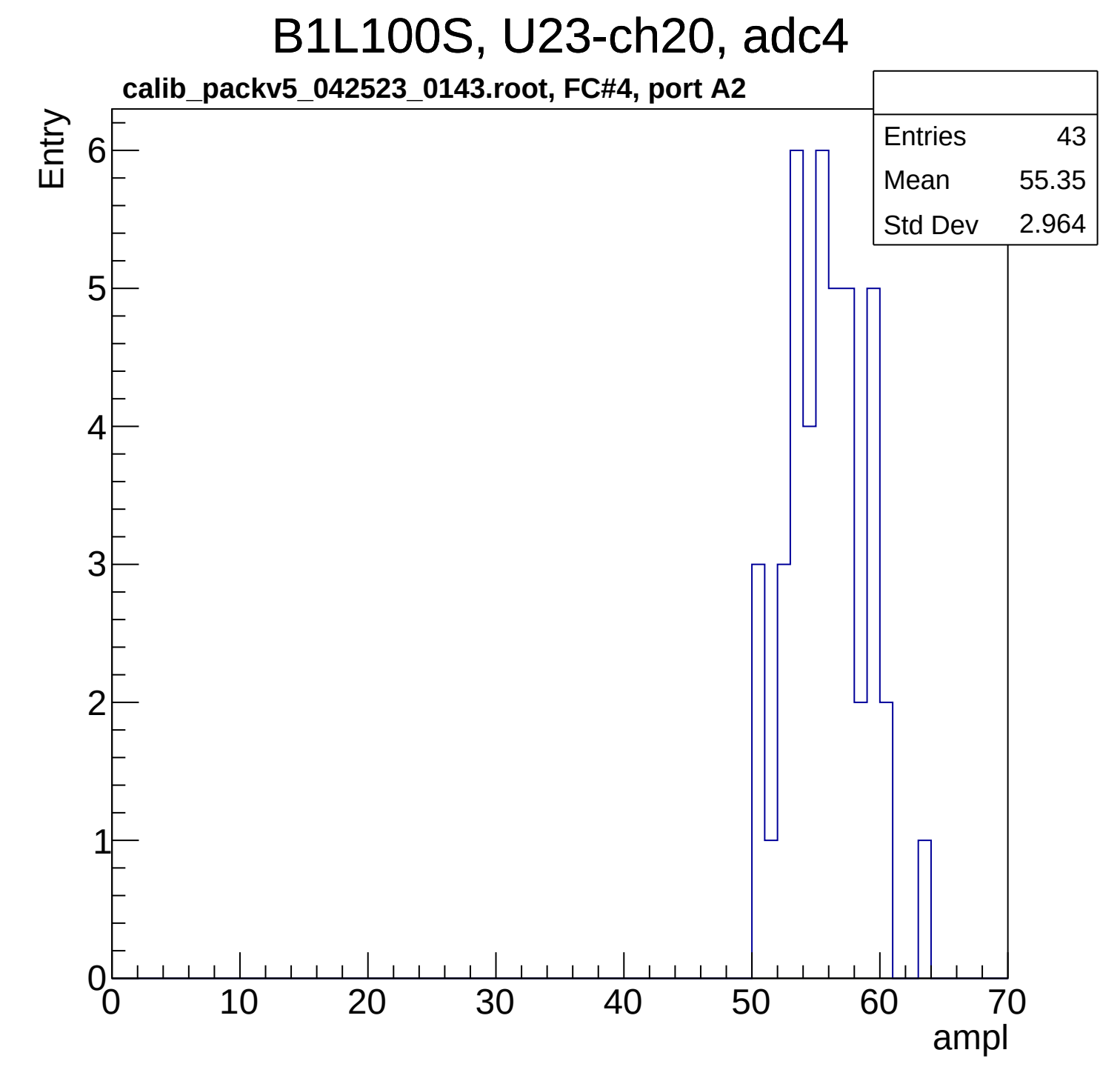
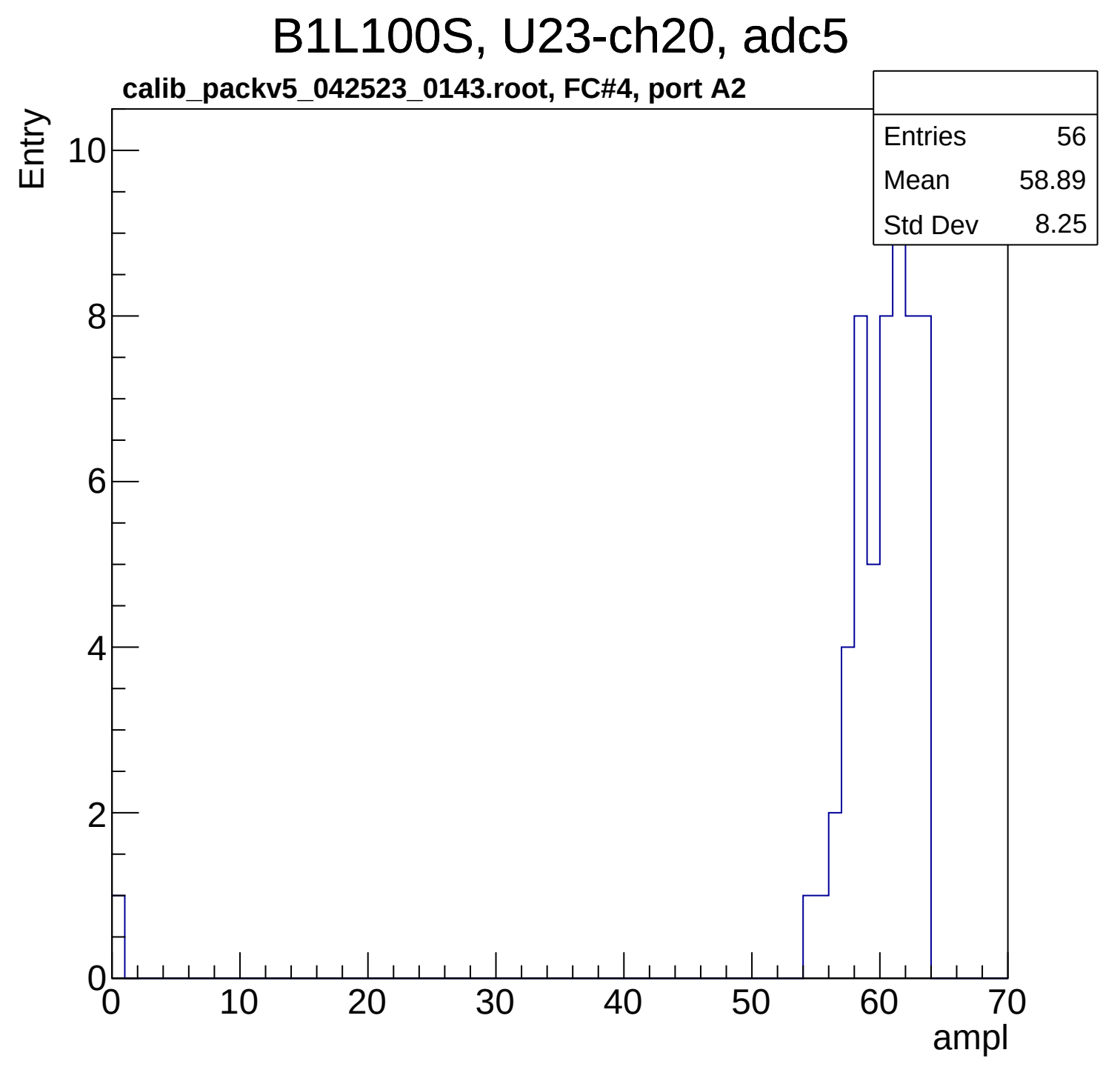
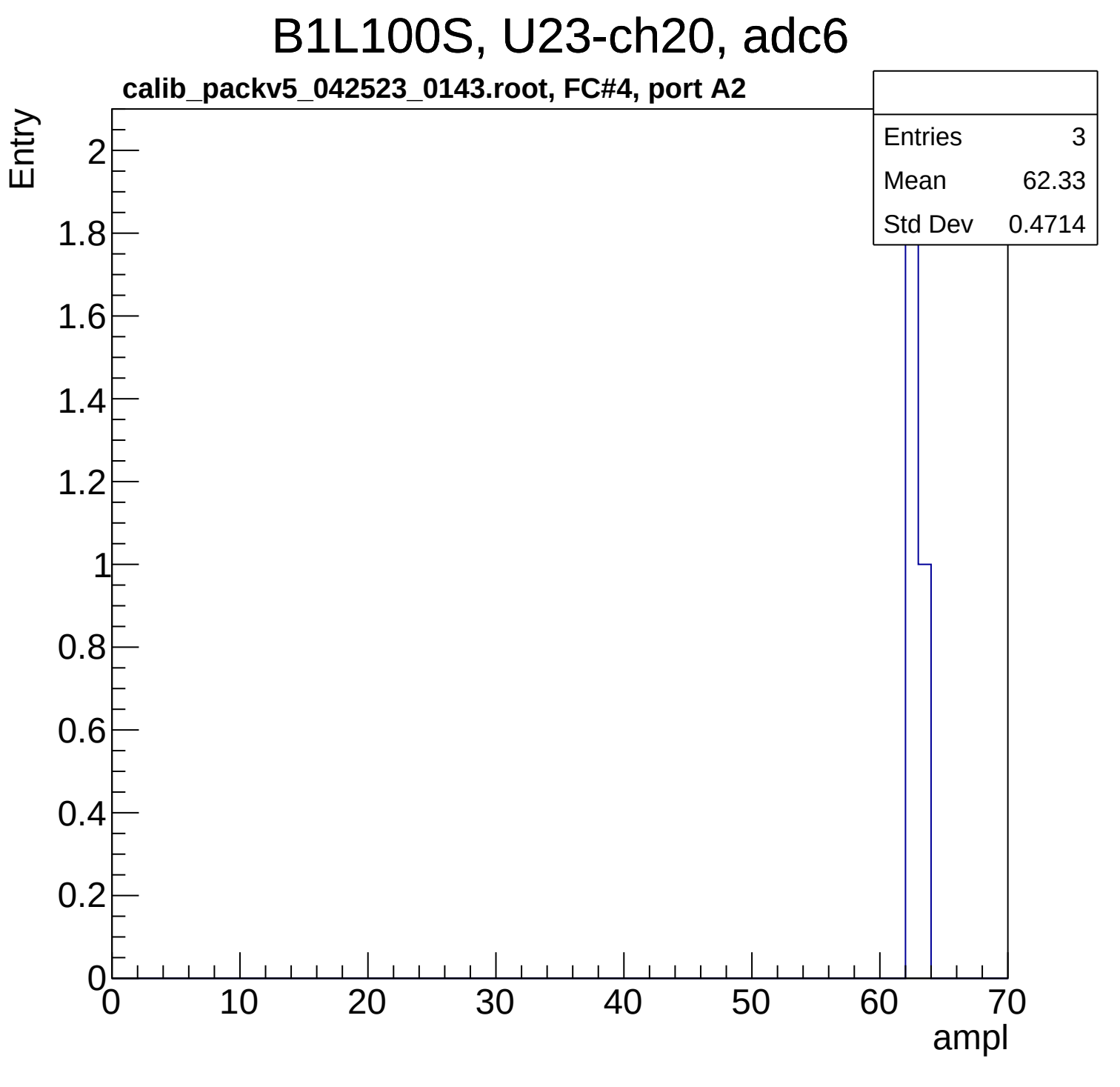
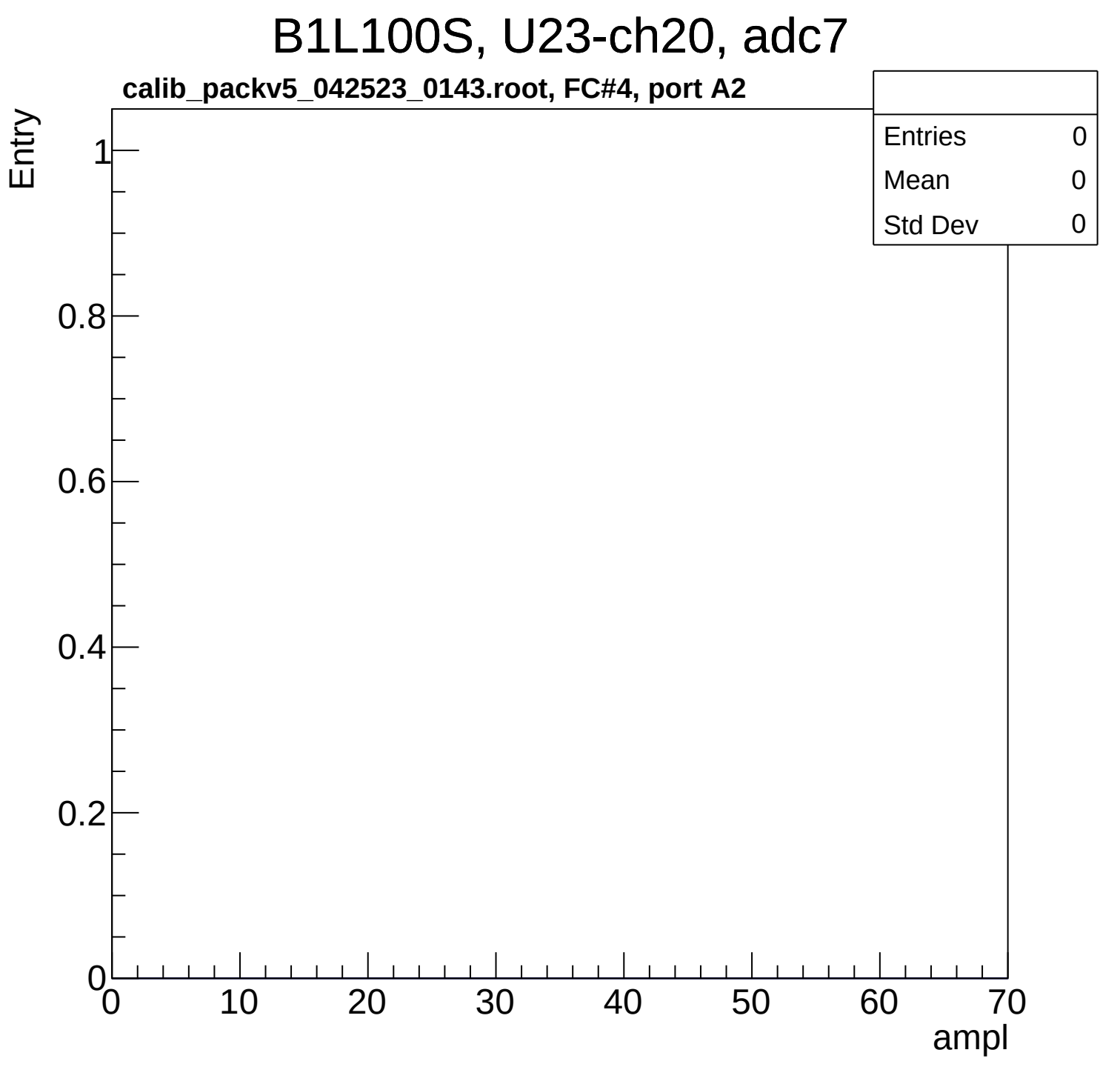
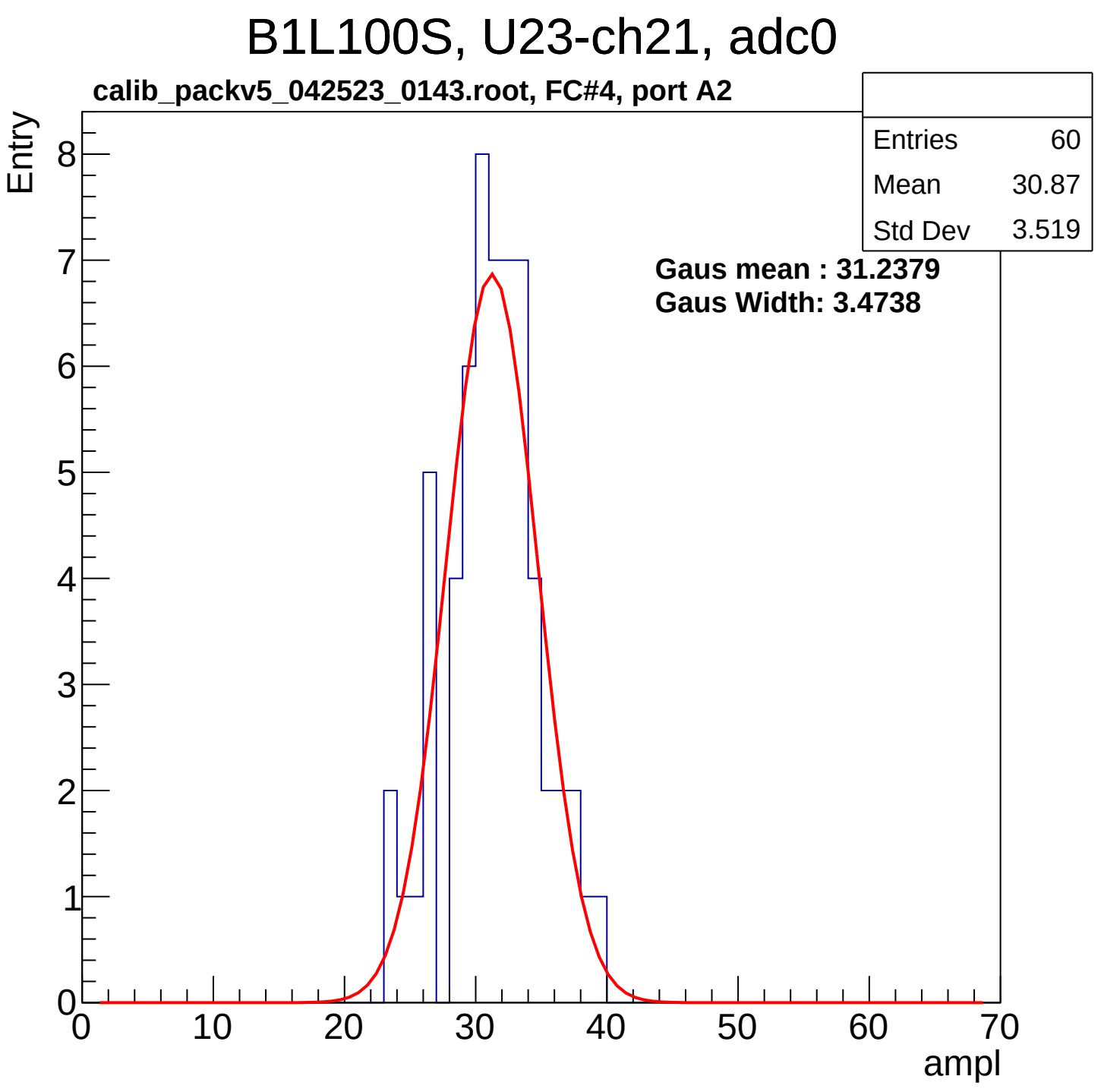

B1L100S, U23-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 49.28
Std Dev 3.645
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

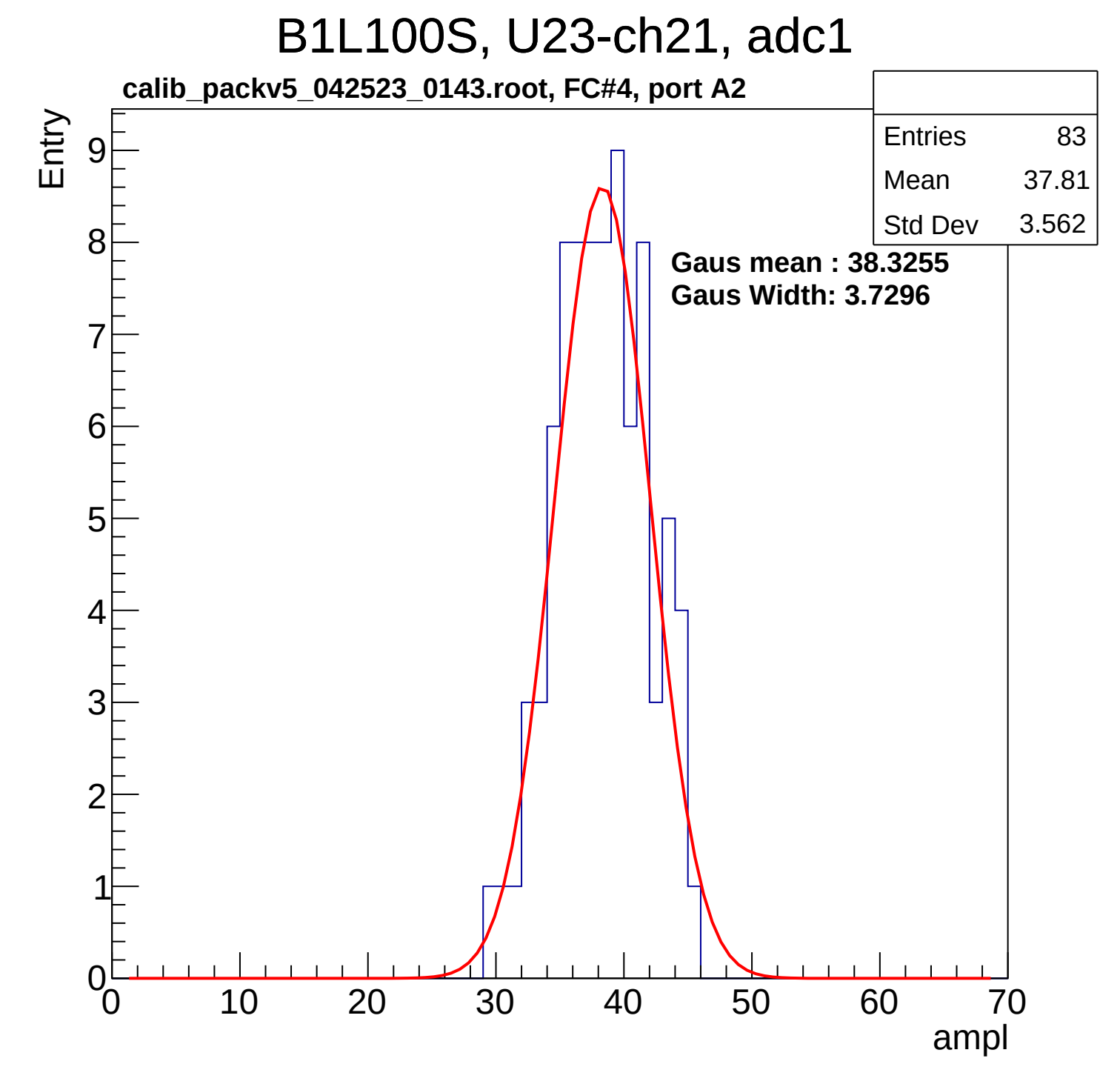

B1L100S, U23-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 55.35
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

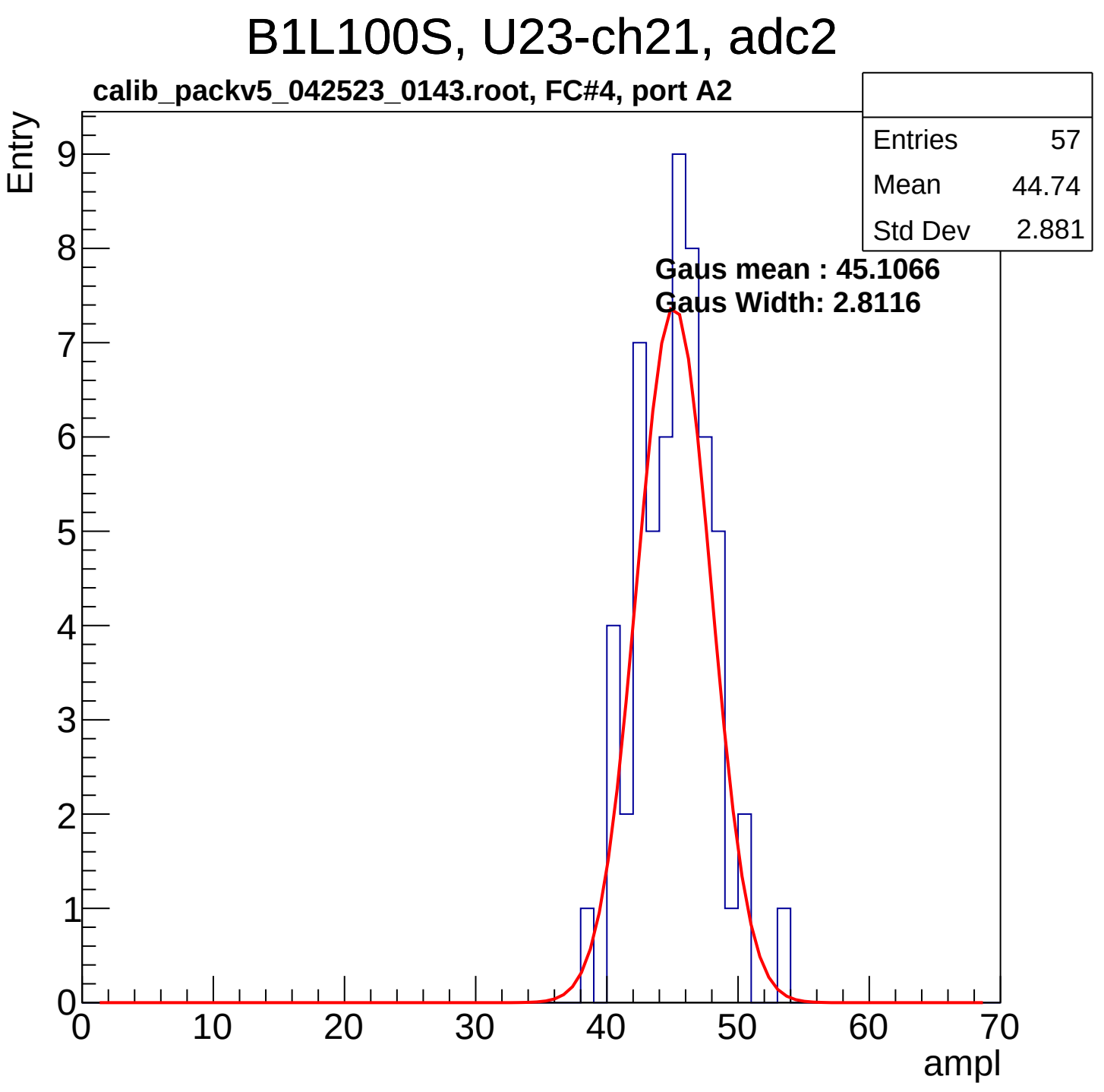

B1L100S, U23-ch20, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.89
Std Dev 8.25
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

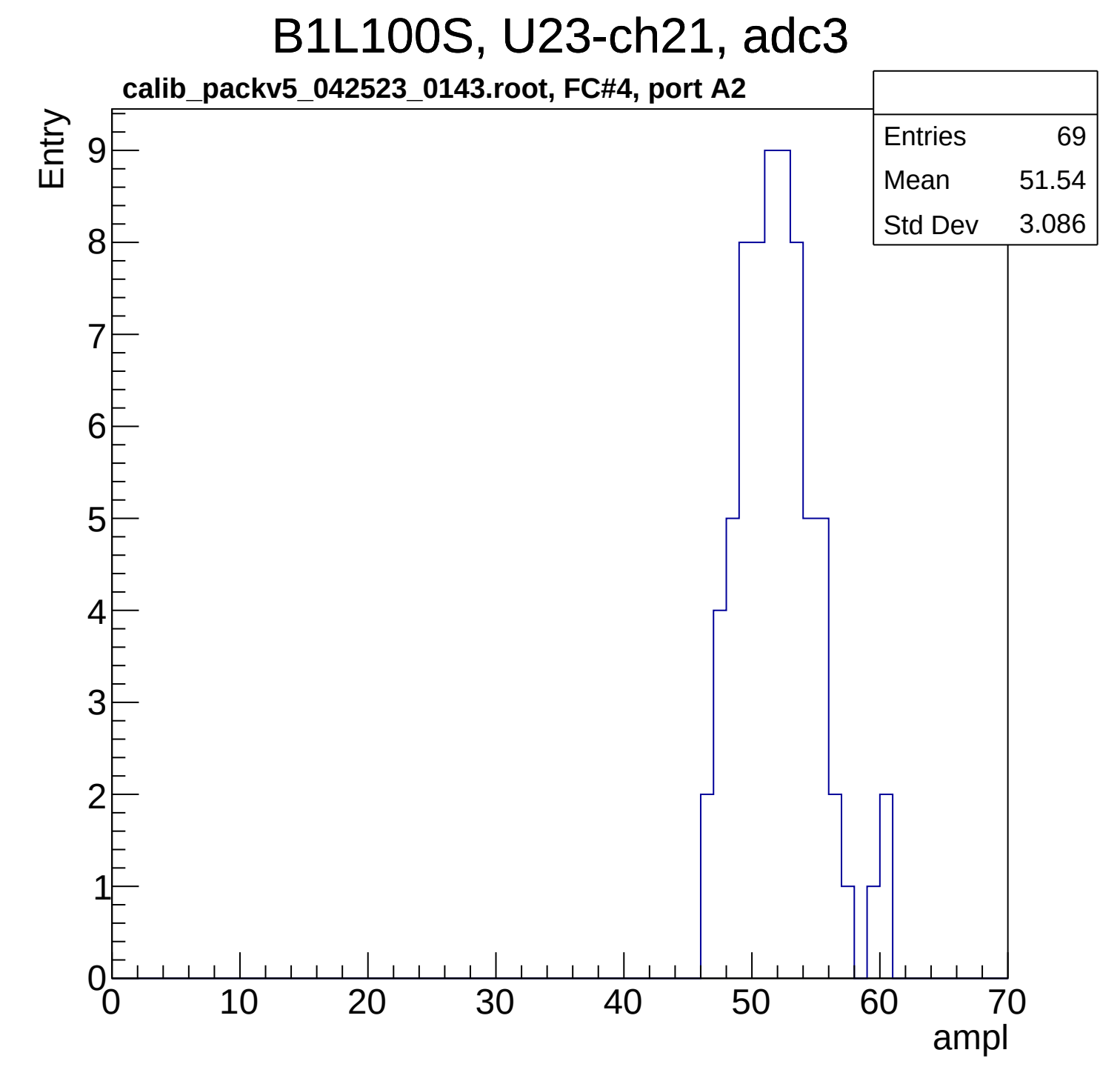

B1L100S, U23-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch20, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 30.87
Std Dev 3.519
Gaus mean : 31.2379
Gaus Width: 3.4738
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 37.81
Std Dev 3.562
Gaus mean : 38.3255
Gaus Width: 3.7296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.74
Std Dev 2.881
Gaus mean : 45.1066
Gaus Width: 2.8116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch21, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.54
Std Dev 3.086
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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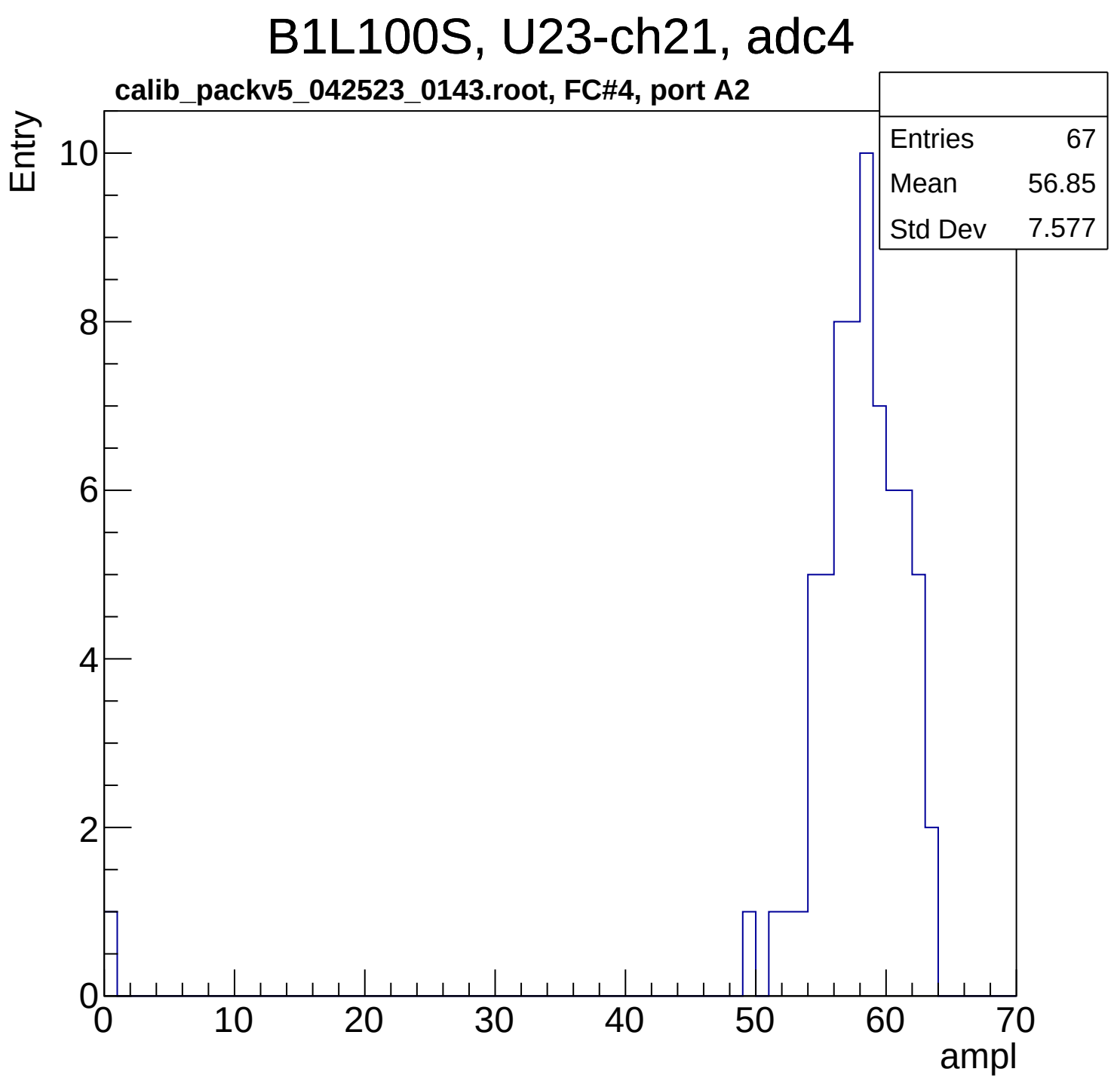
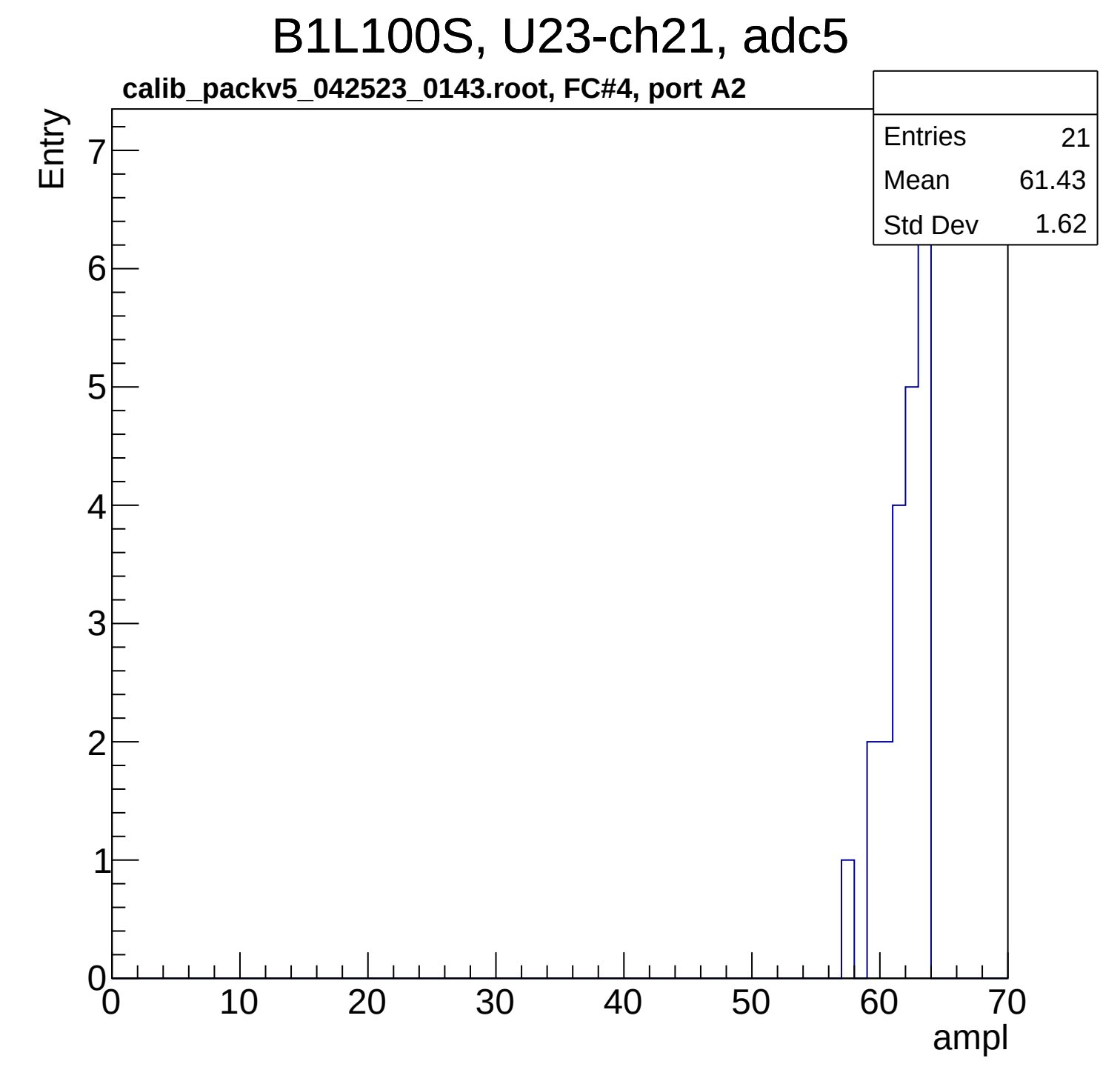
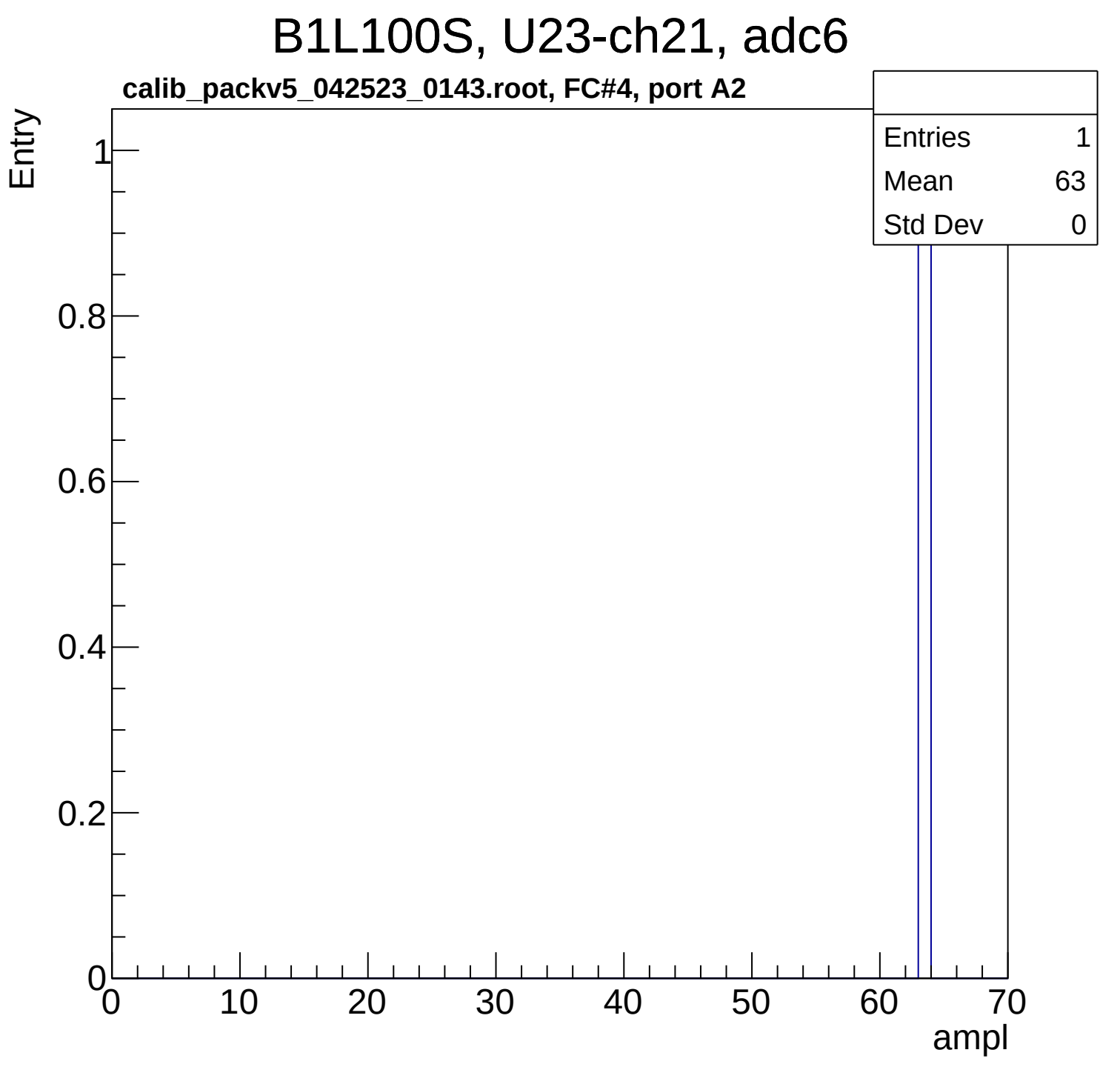
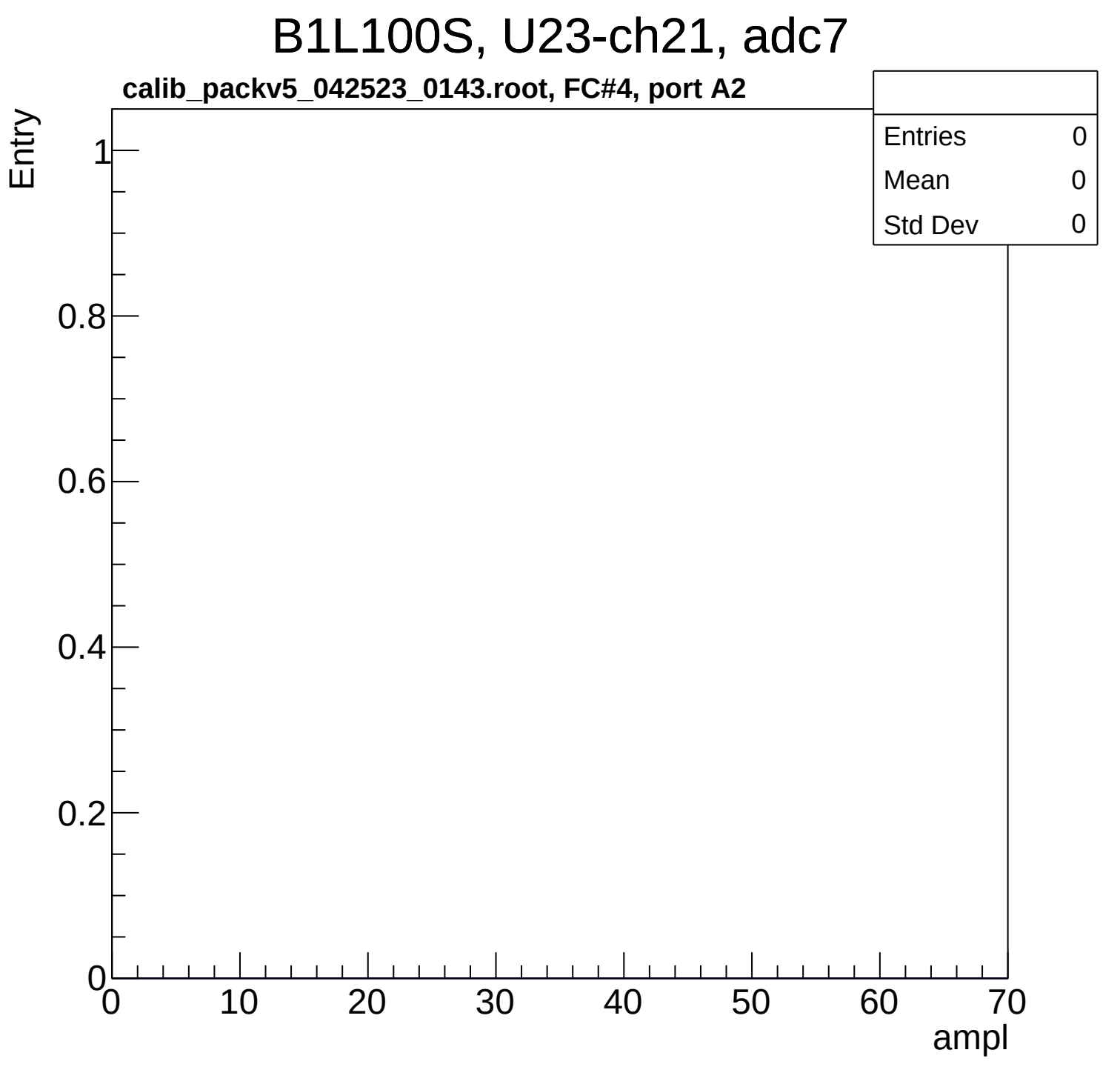
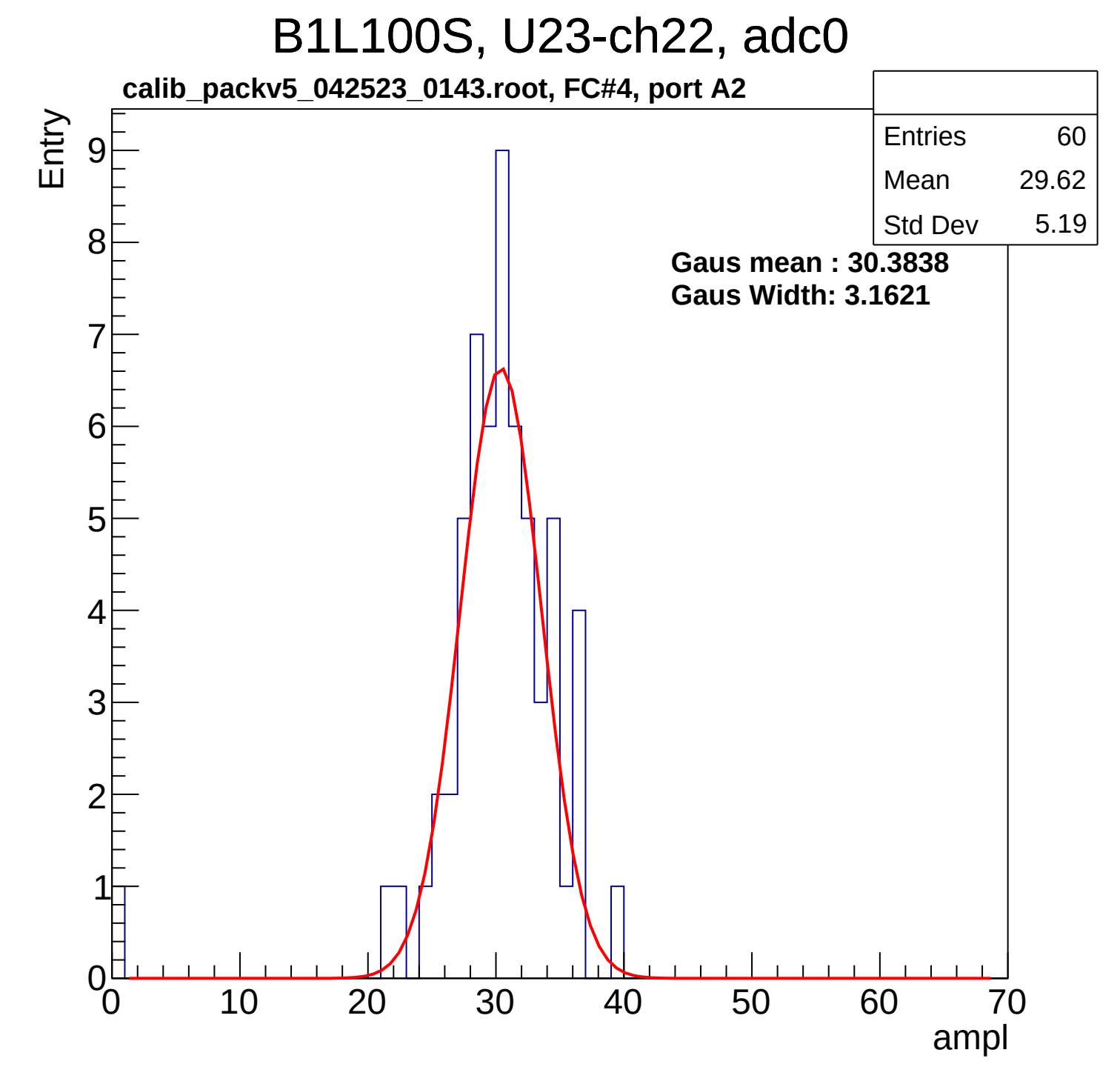
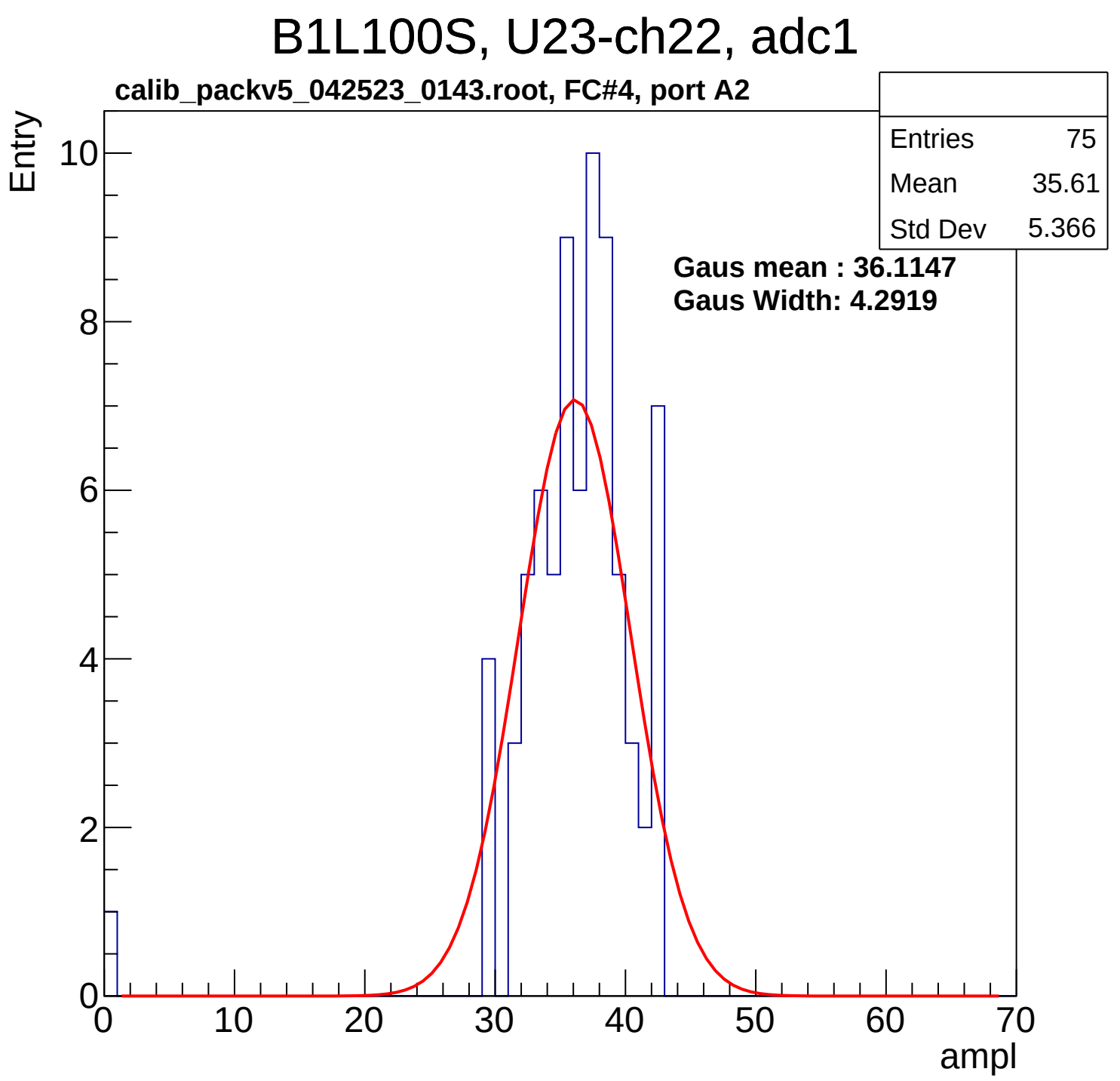

B1L100S, U23-ch21, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.85
Std Dev 7.577
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

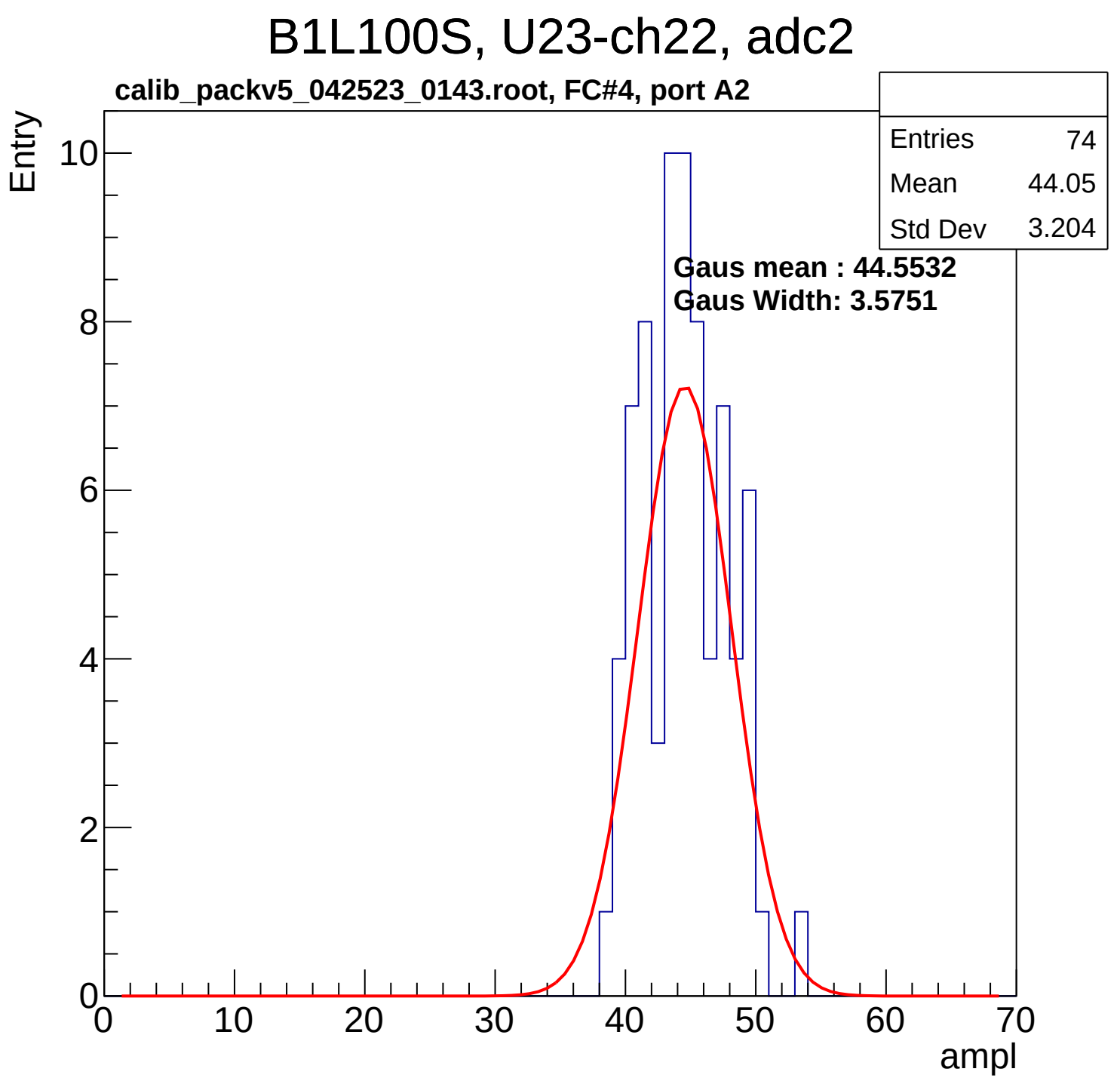

B1L100S, U23-ch21, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 21
Mean 61.43
Std Dev 1.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

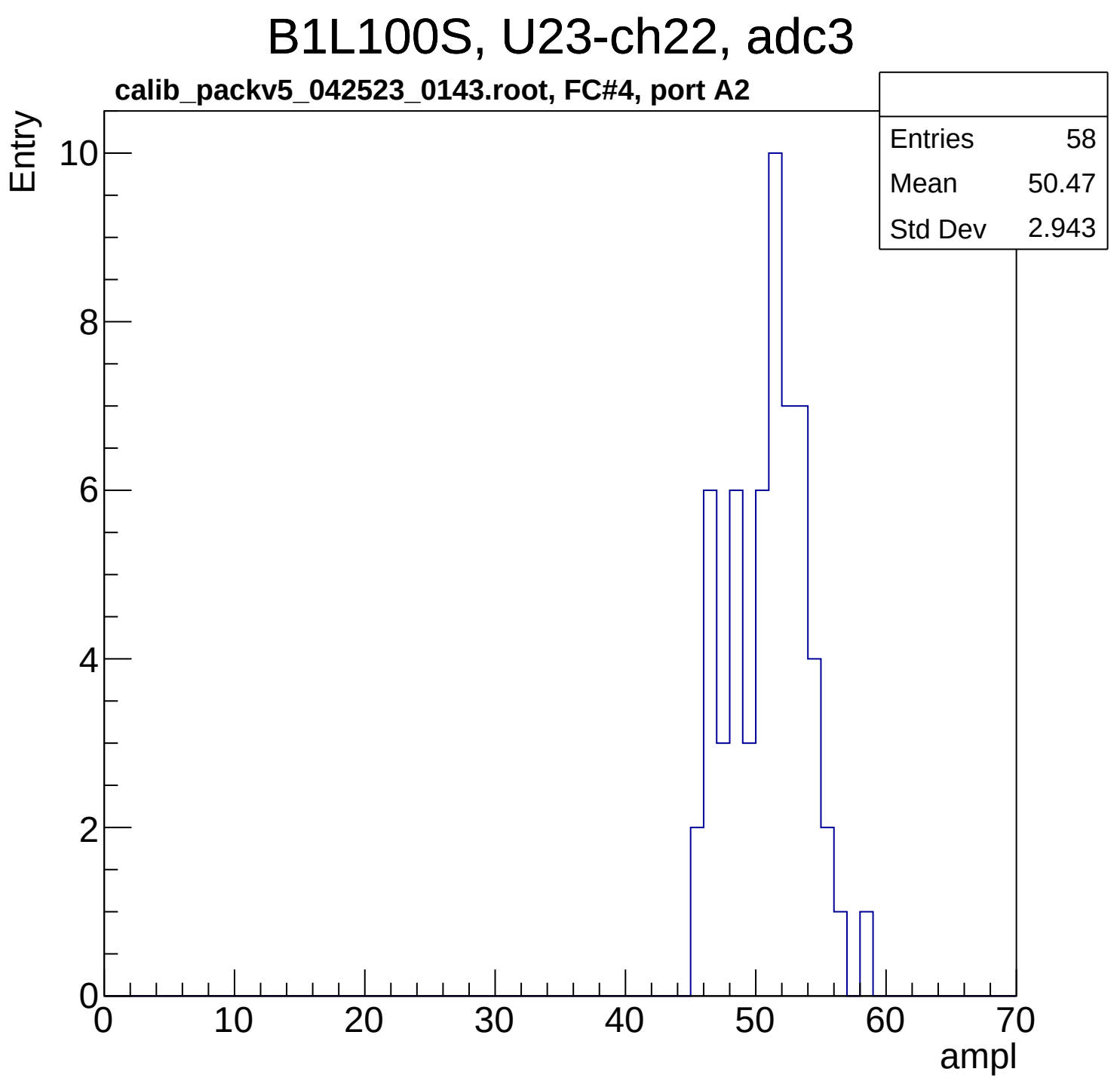

B1L100S, U23-ch21, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

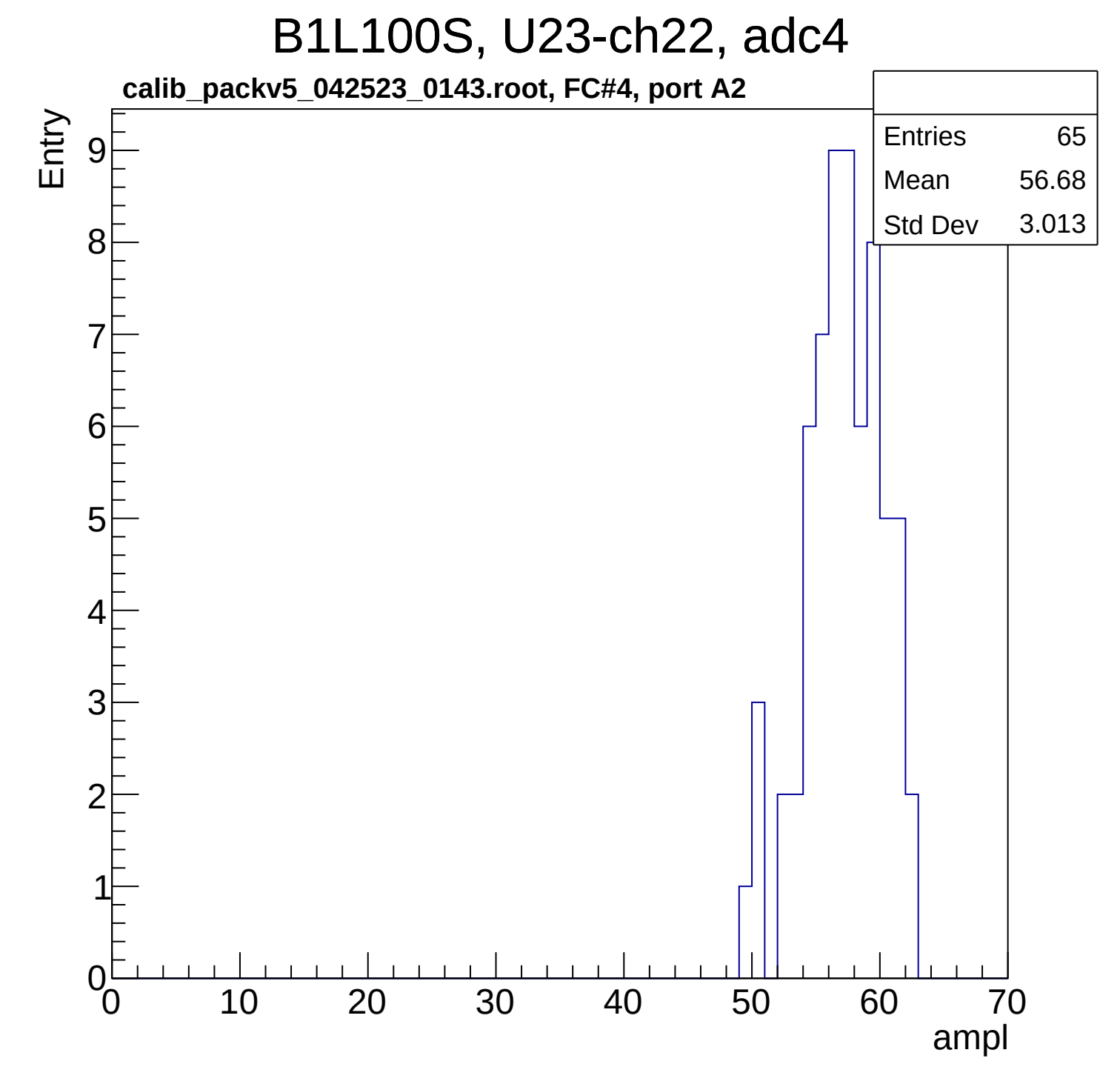

B1L100S, U23-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch22, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.62
Std Dev 5.19
Gaus mean : 30.3838
Gaus Width: 3.1621
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.61
Std Dev 5.366
Gaus mean : 36.1147
Gaus Width: 4.2919
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 44.05
Std Dev 3.204
Gaus mean : 44.5532
Gaus Width: 3.5751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch22, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 50.47
Std Dev 2.943
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch22, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.68
Std Dev 3.013
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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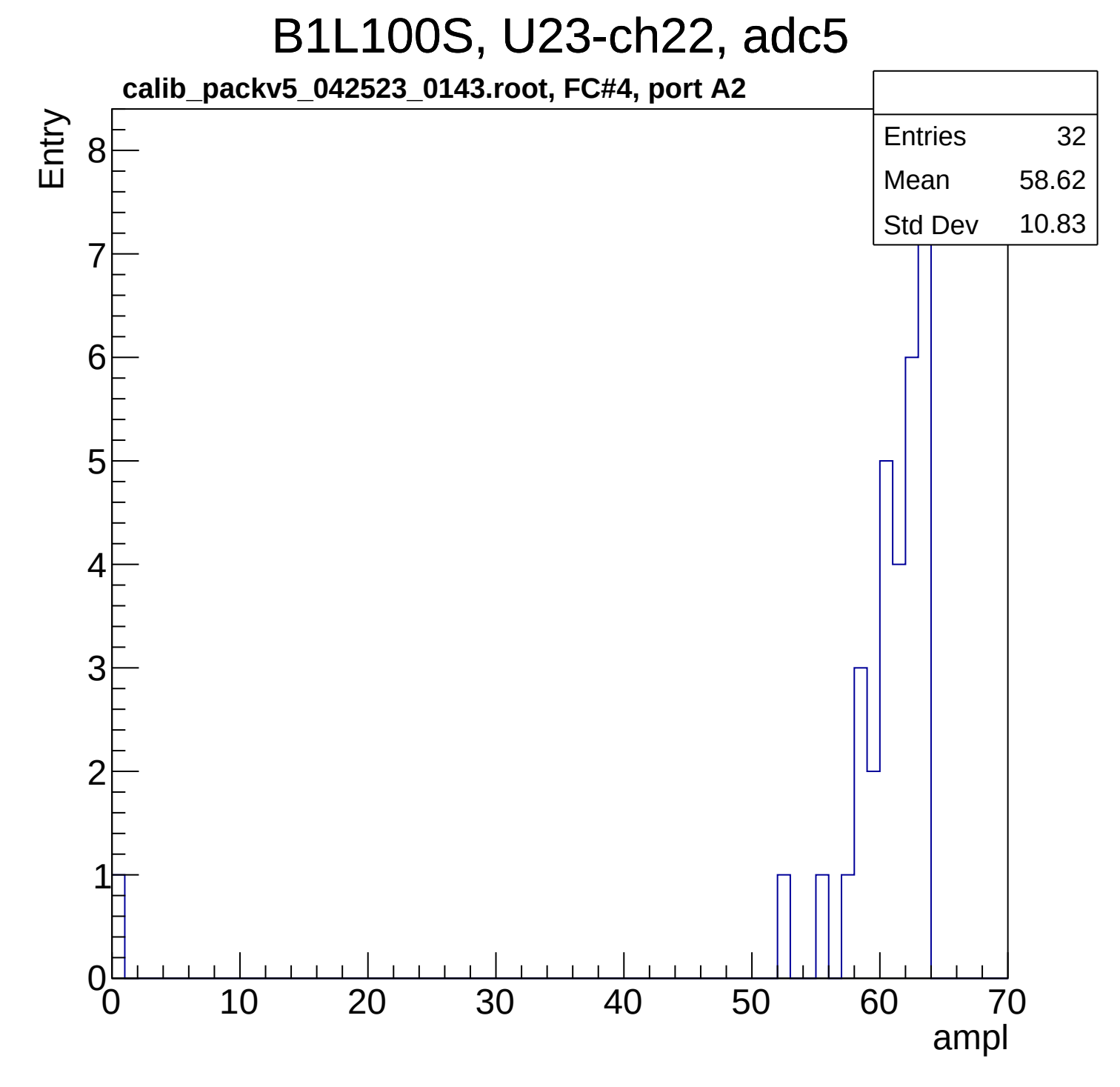
B1L100S, U23-ch22, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.62
Std Dev 10.83
Entry
ampl
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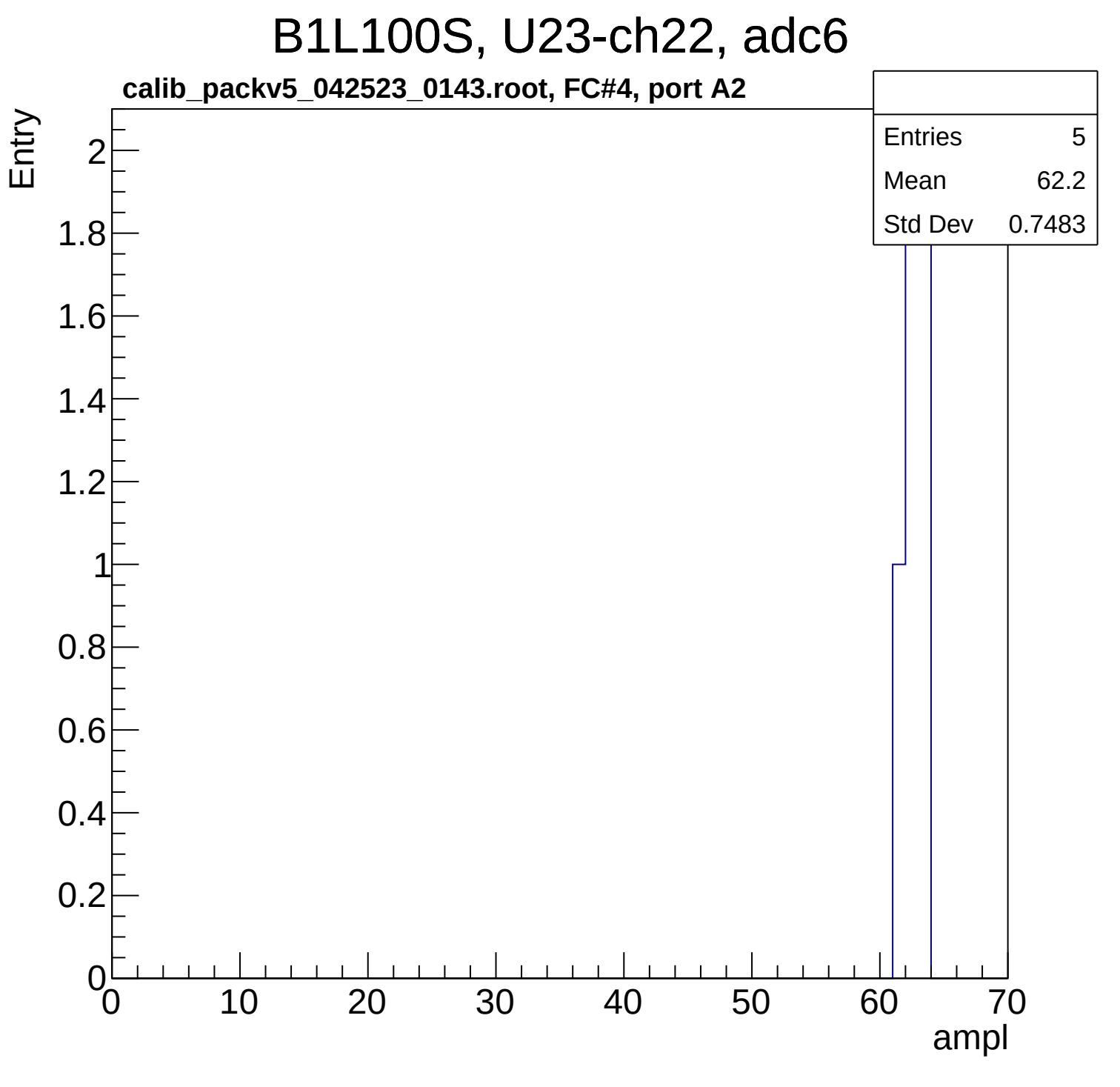
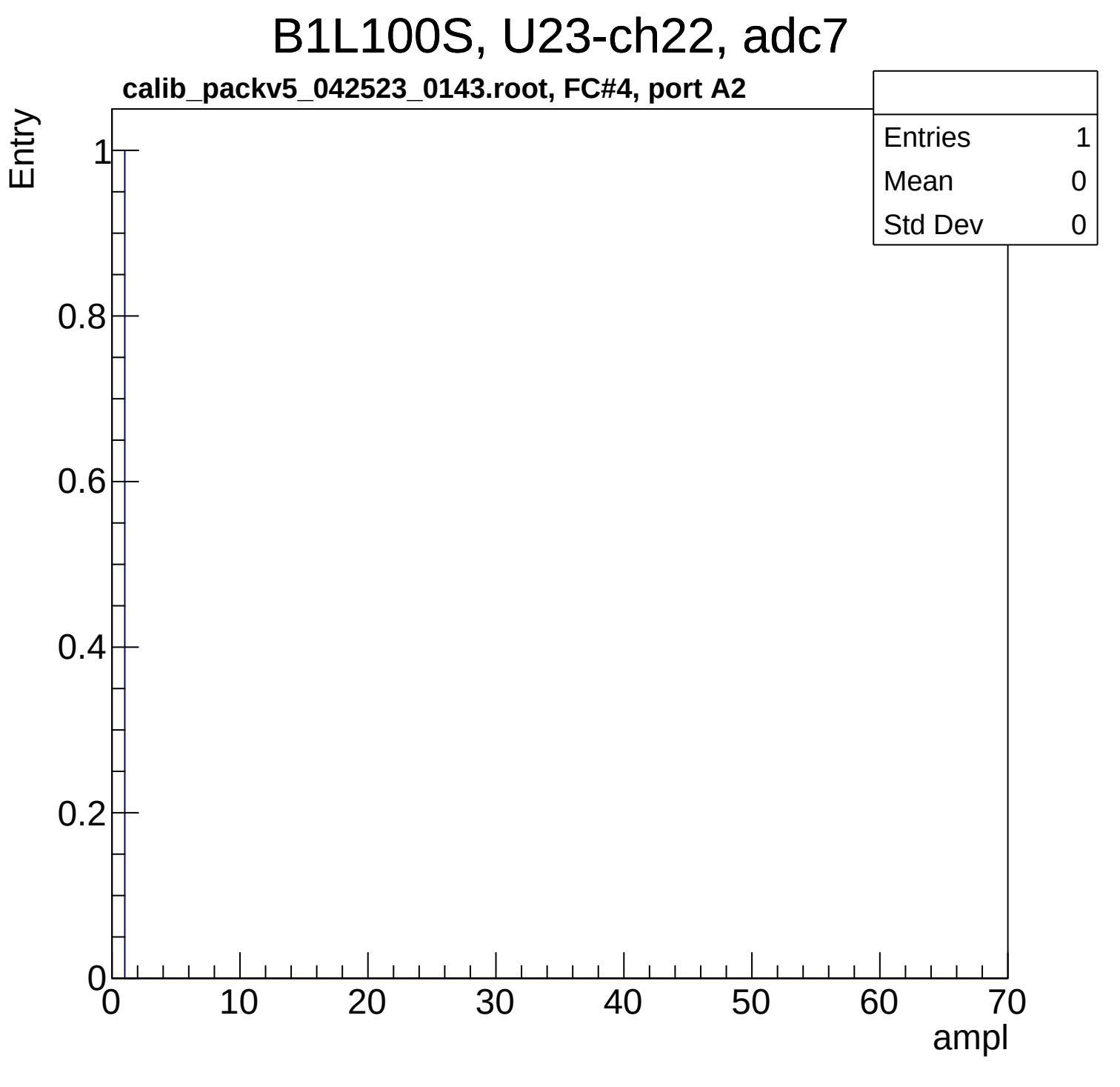
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch22, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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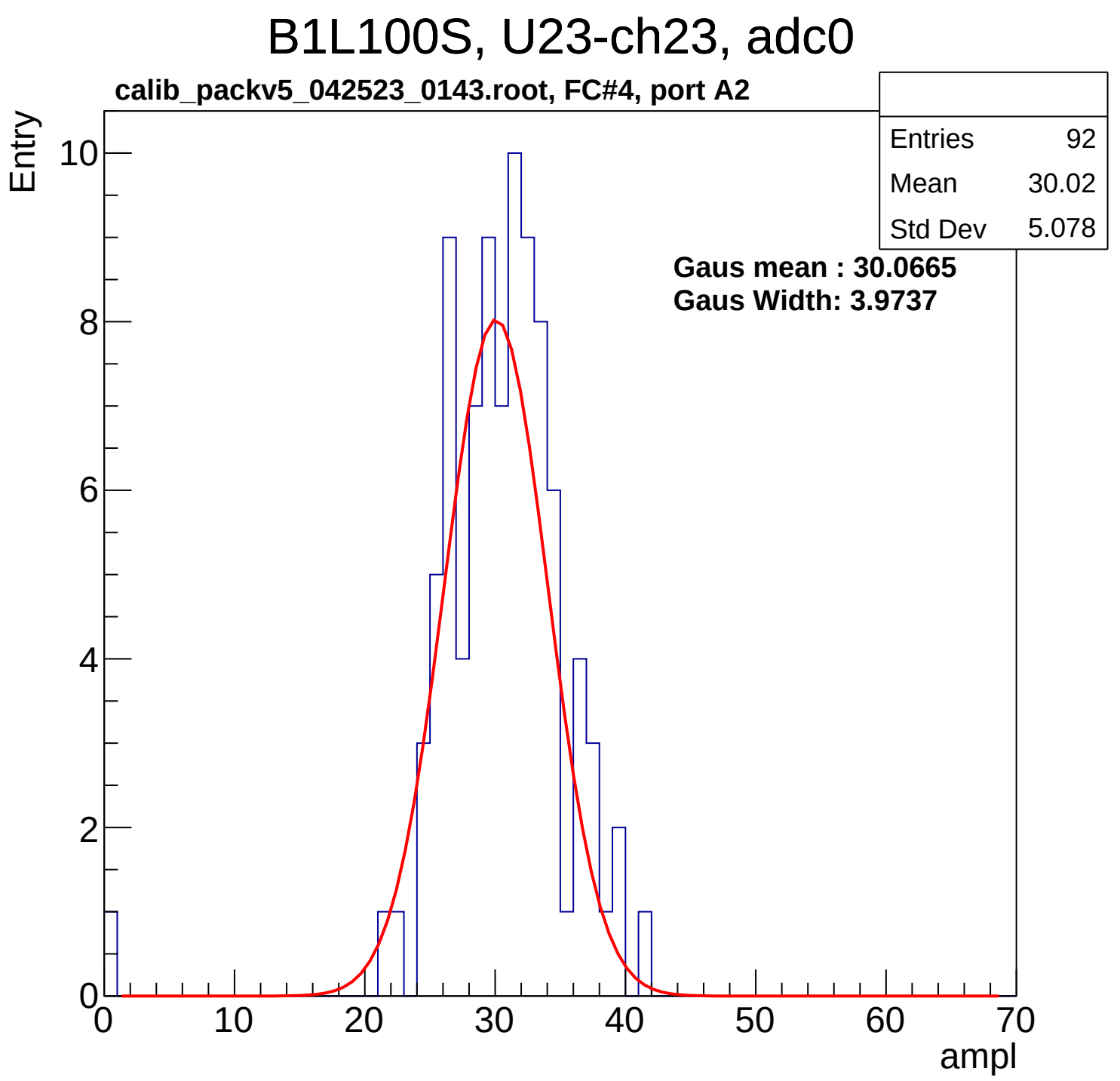
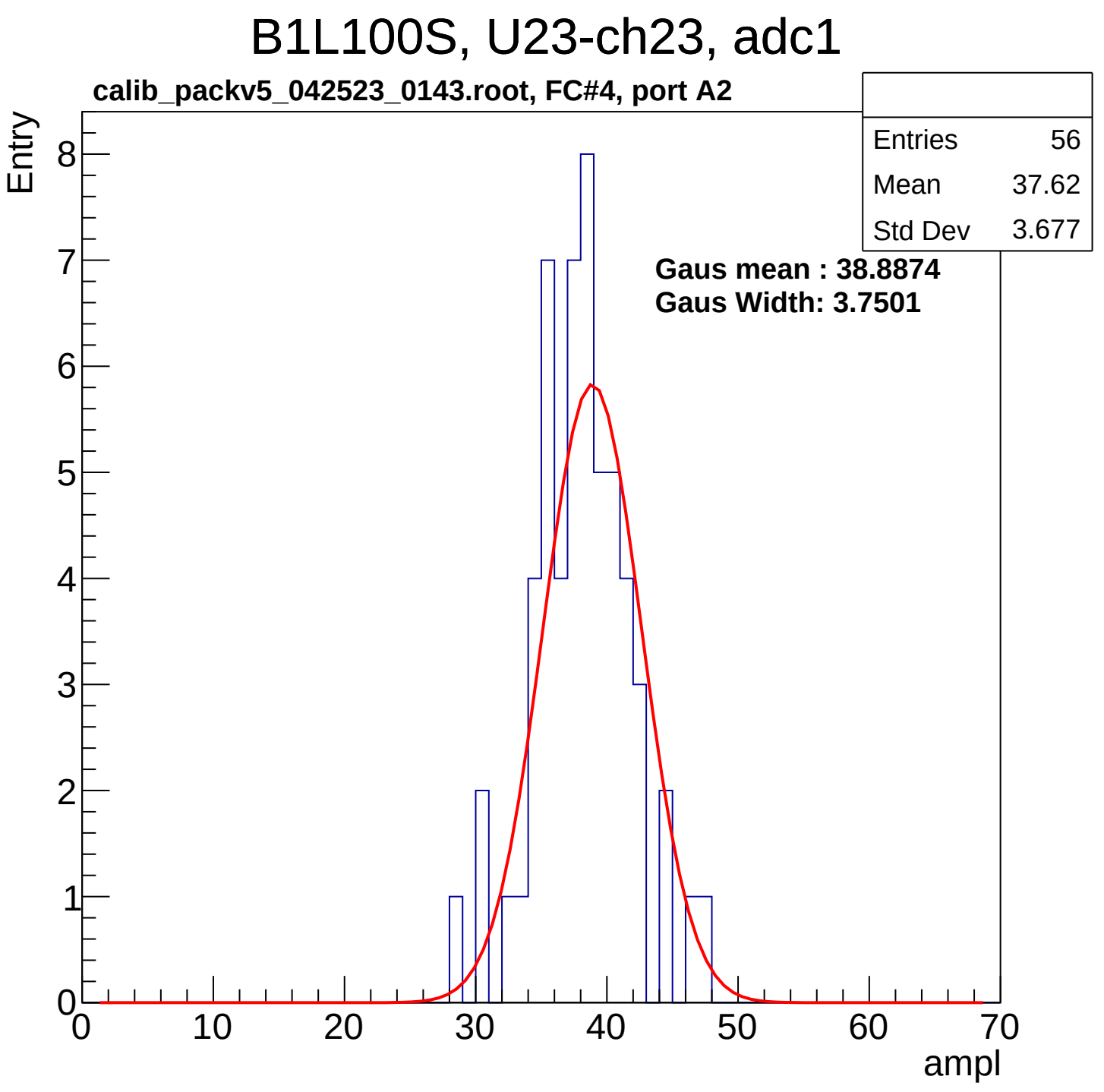
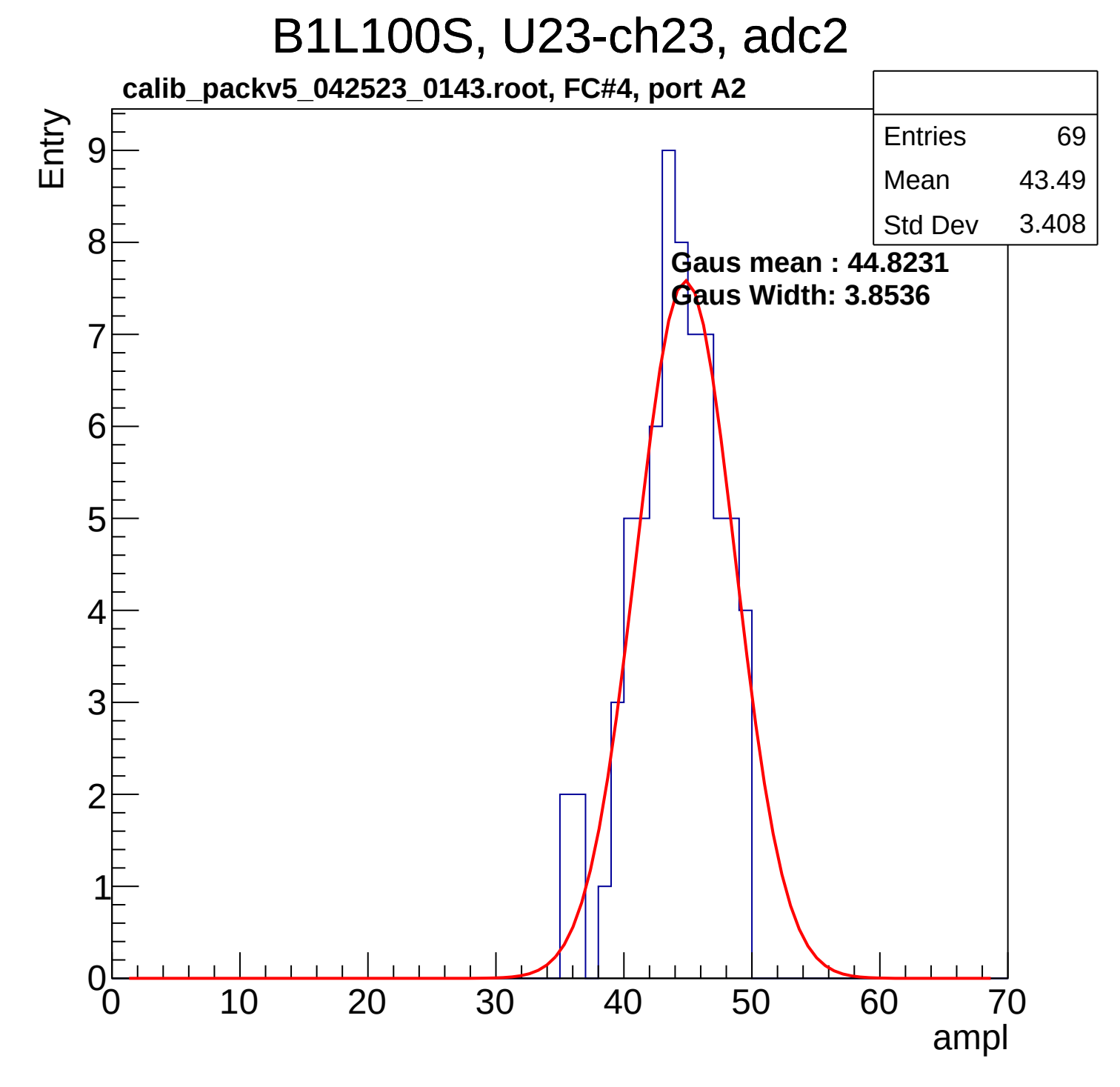
50
60
70
ampl


B1L100S, U23-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 92
Mean 30.02
Std Dev 5.078
Gaus mean : 30.0665
Gaus Width: 3.9737
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 37.62
Std Dev 3.677
Gaus mean : 38.8874
Gaus Width: 3.7501
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.49
Std Dev 3.408
Gaus mean : 44.8231
Gaus Width: 3.8536
Entry
ampl
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2
3
4
5
6
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0
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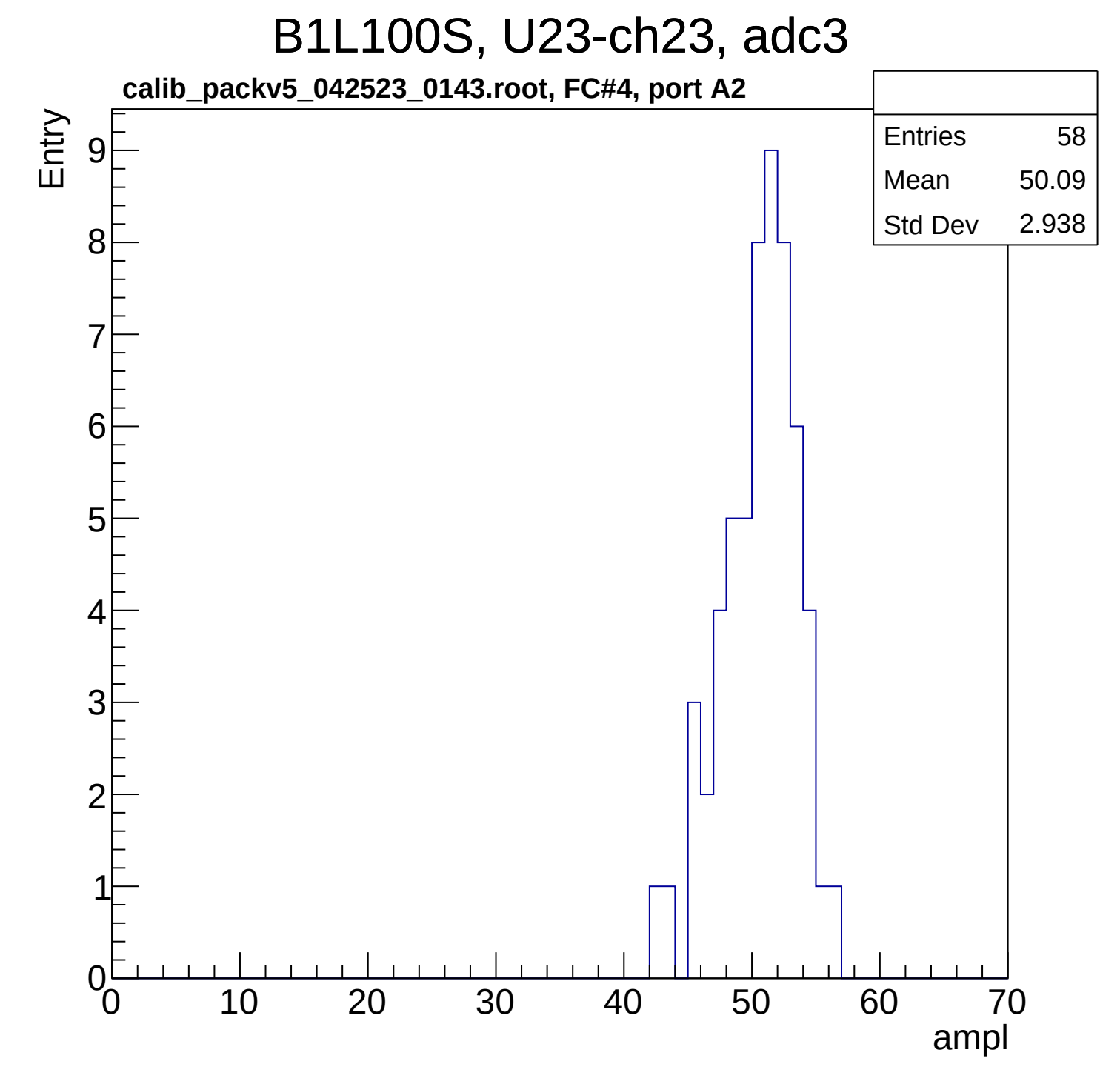
B1L100S, U23-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 50.09
Std Dev 2.938
Entry
ampl
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0
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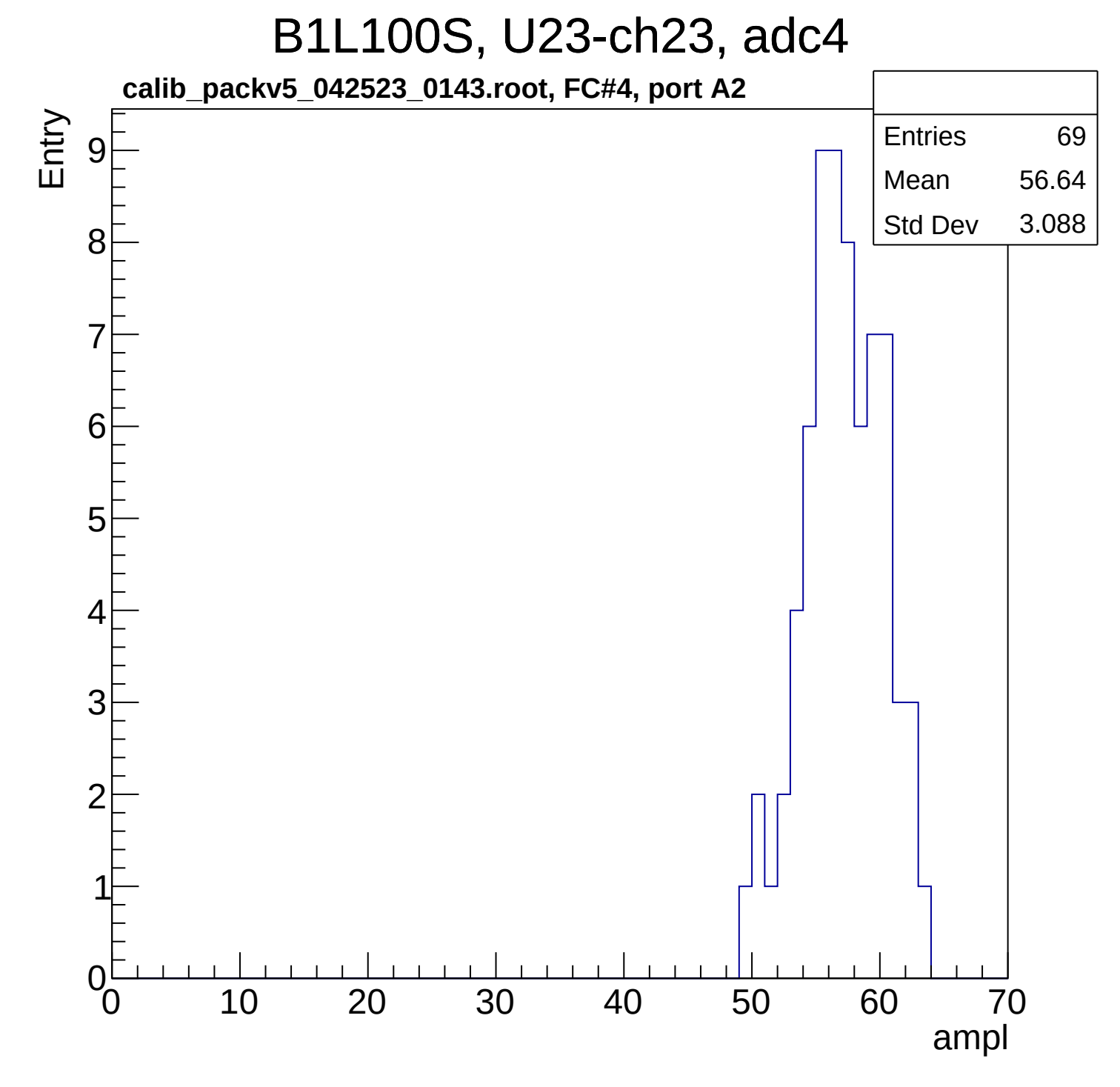
B1L100S, U23-ch23, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.64
Std Dev 3.088
Entry
ampl
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2
3
4
5
6
7
8
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0
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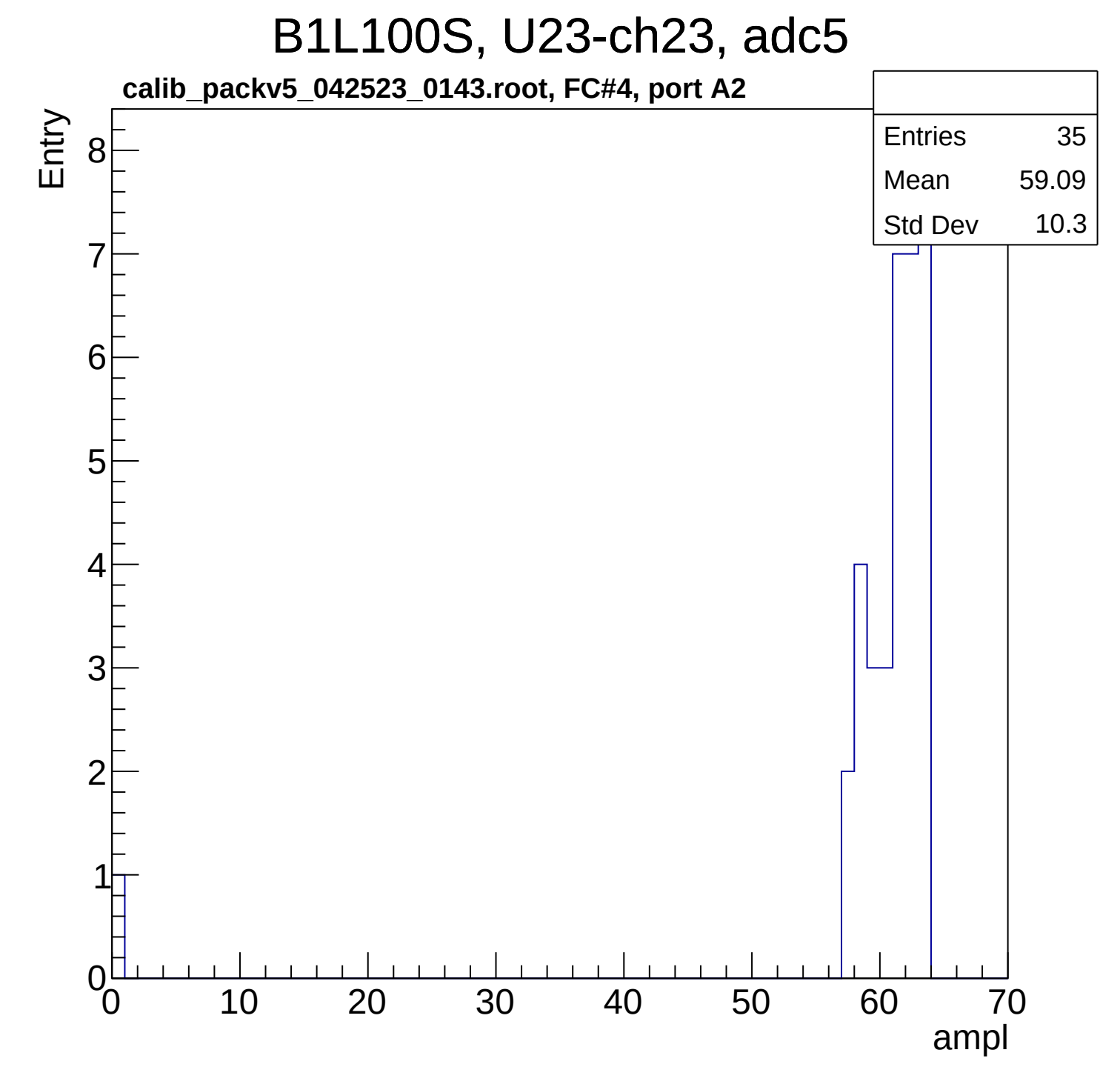
B1L100S, U23-ch23, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.09
Std Dev 10.3
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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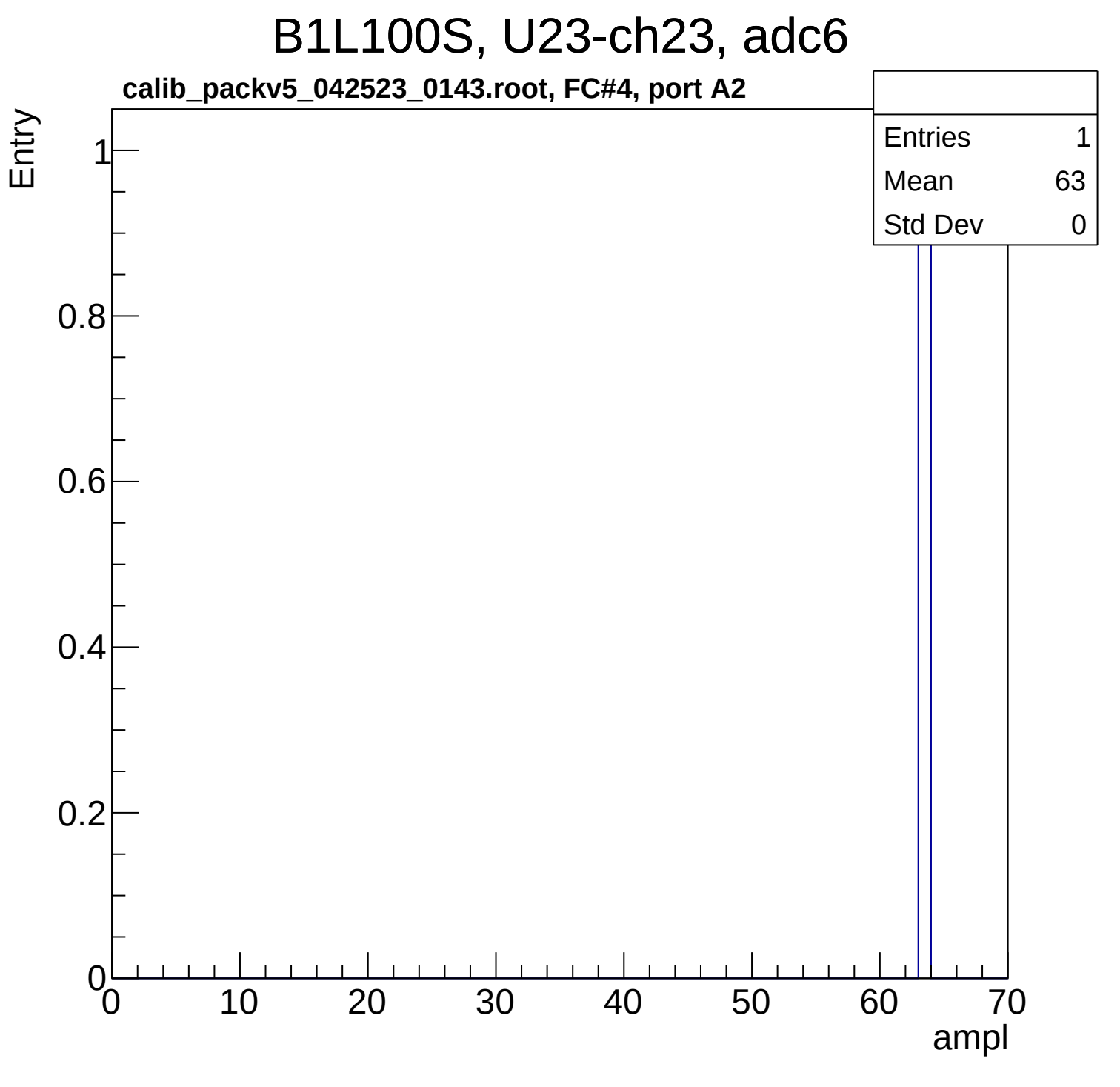
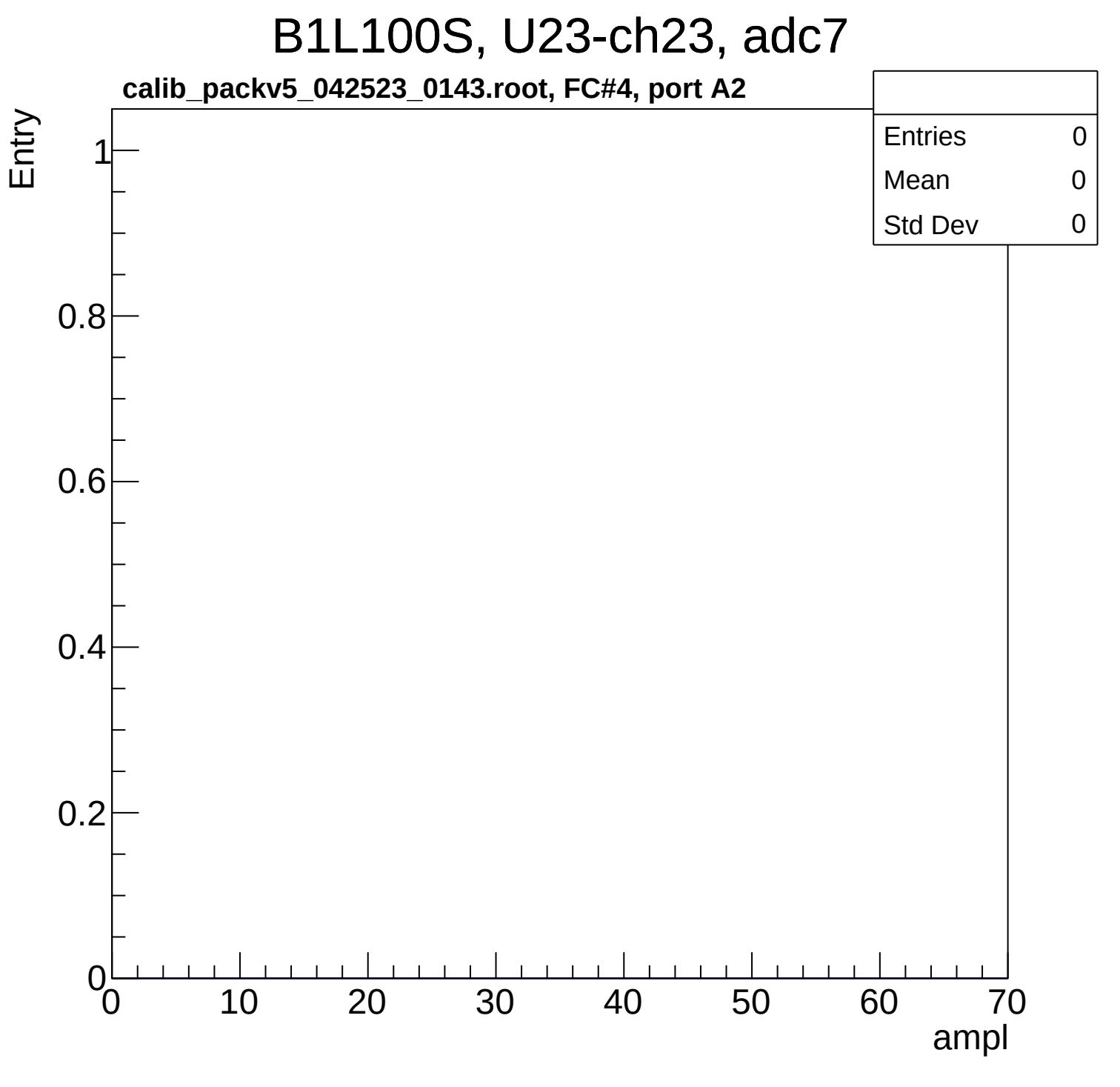
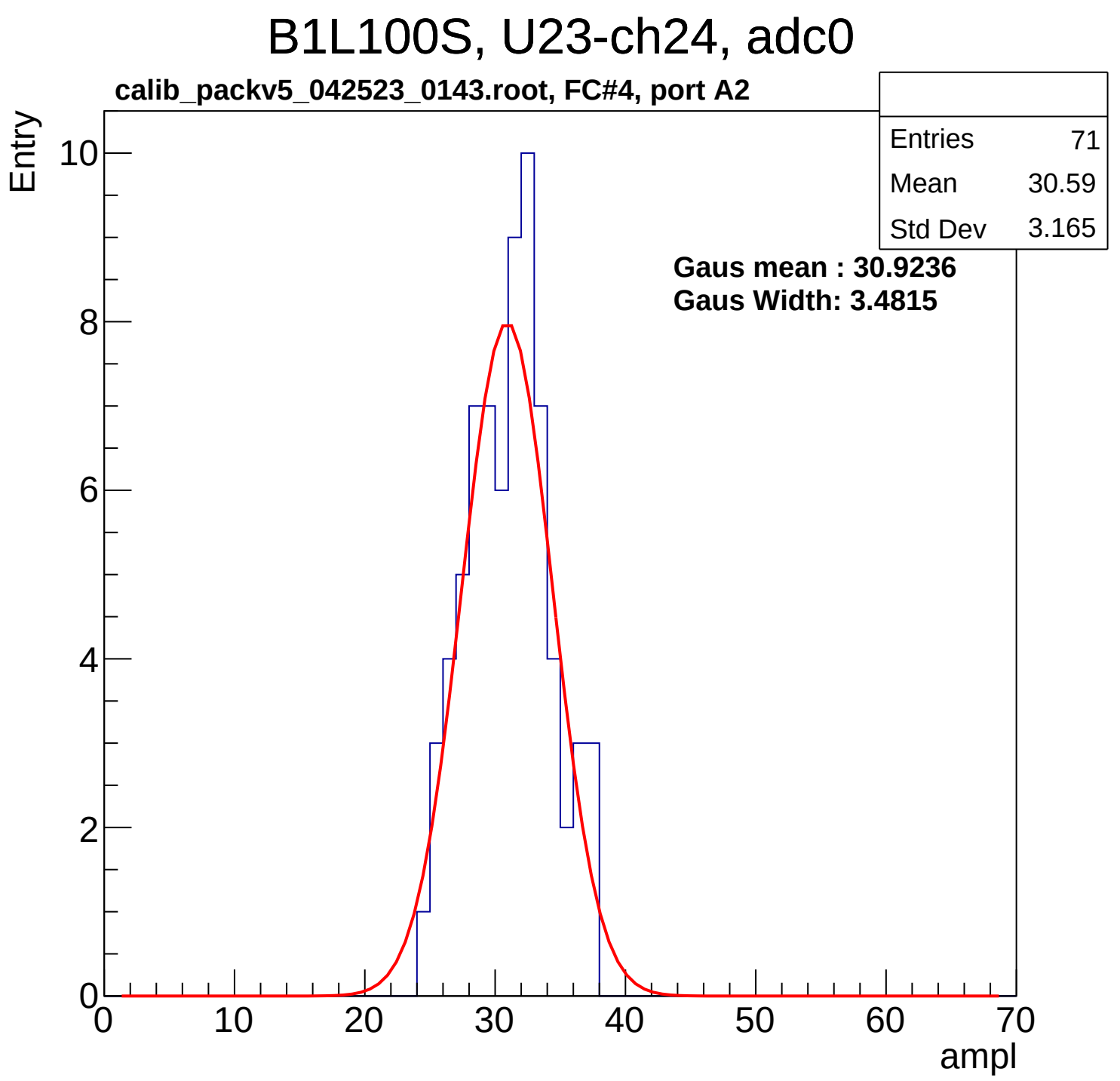

B1L100S, U23-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.59
Std Dev 3.165
Gaus mean : 30.9236
Gaus Width: 3.4815
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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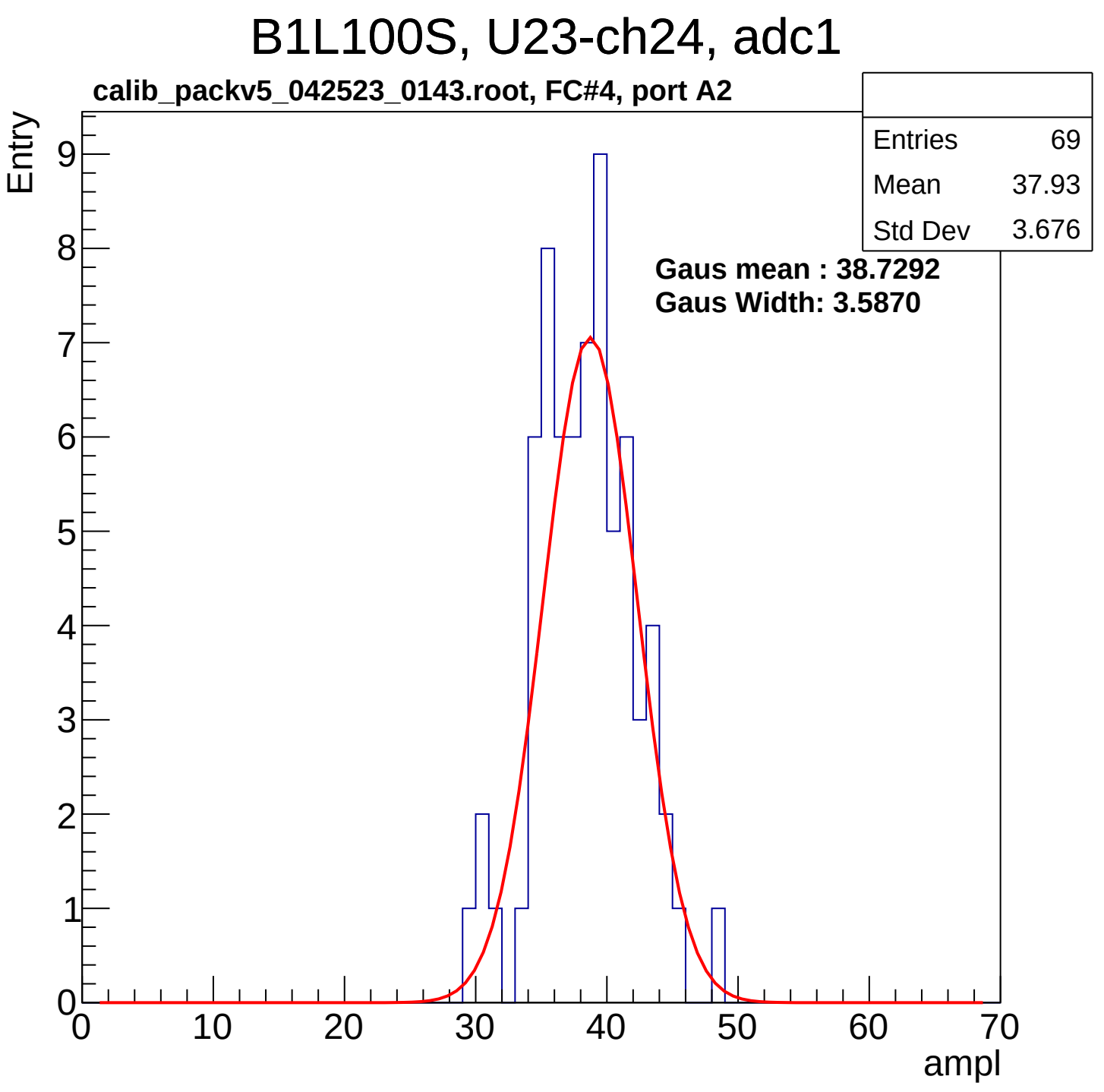
B1L100S, U23-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.93
Std Dev 3.676
Gaus mean : 38.7292
Gaus Width: 3.5870
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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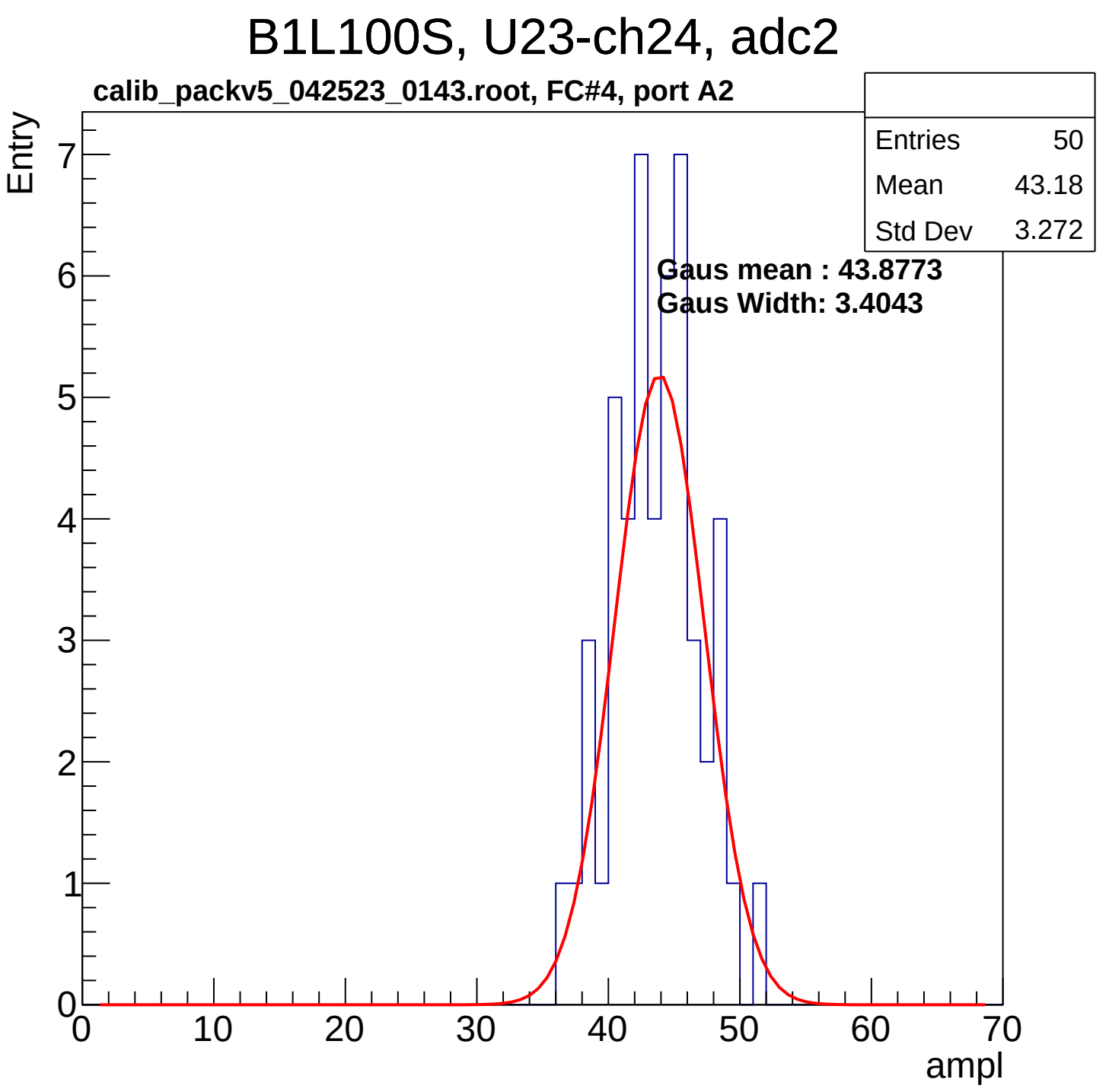
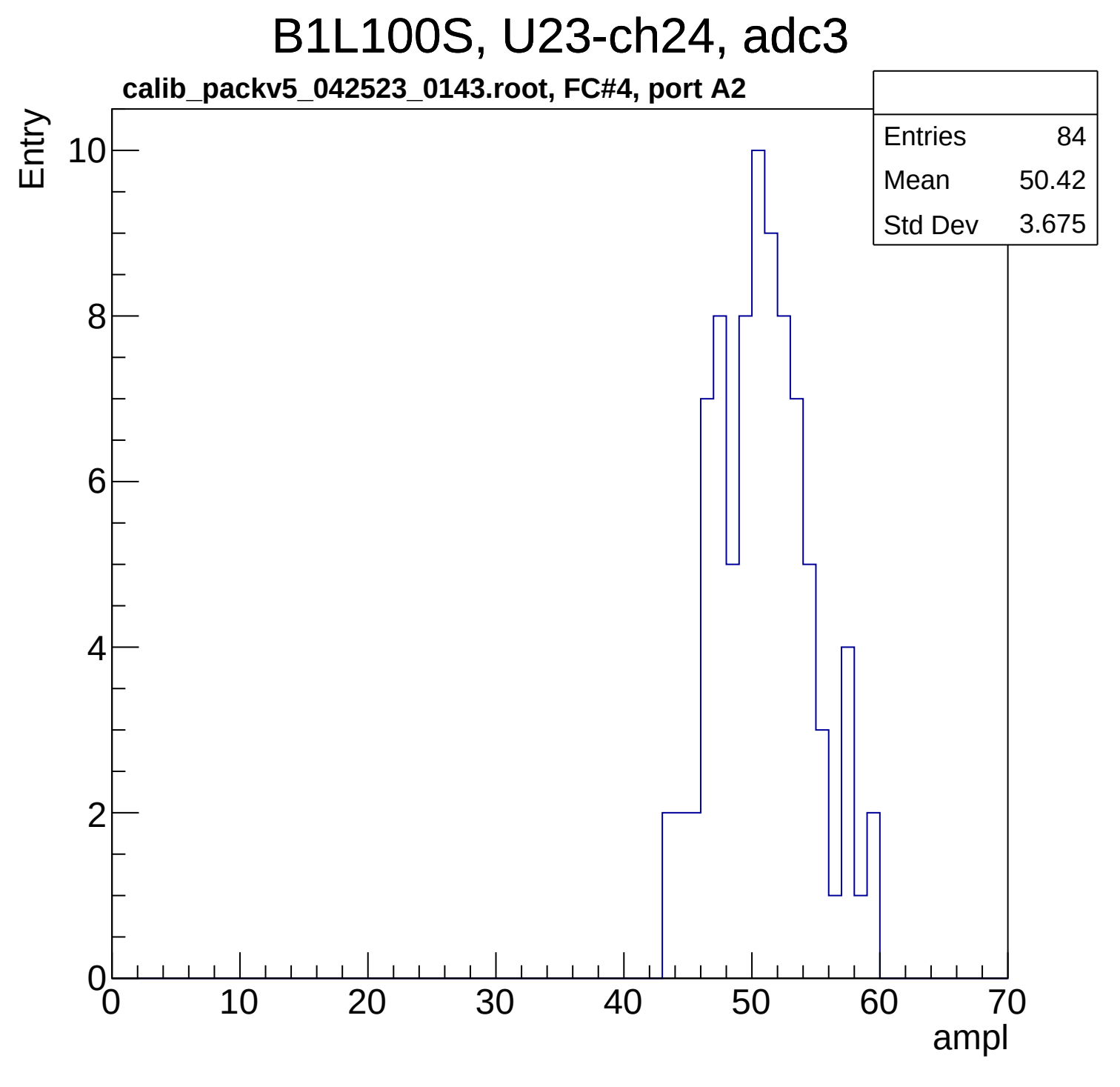
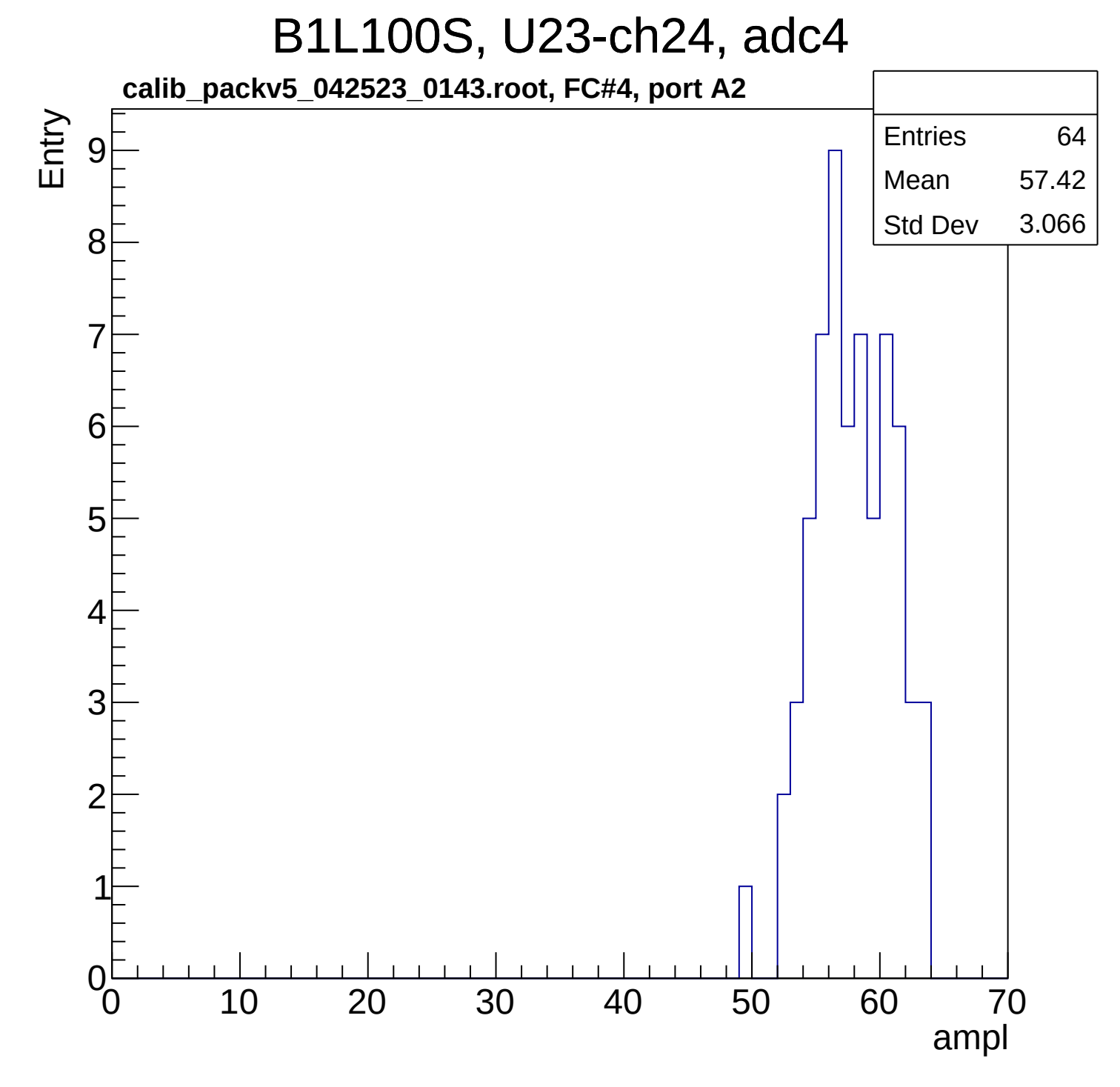

B1L100S, U23-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 43.18
Std Dev 3.272
Gaus mean : 43.8773
Gaus Width: 3.4043
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch24, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 50.42
Std Dev 3.675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch24, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.42
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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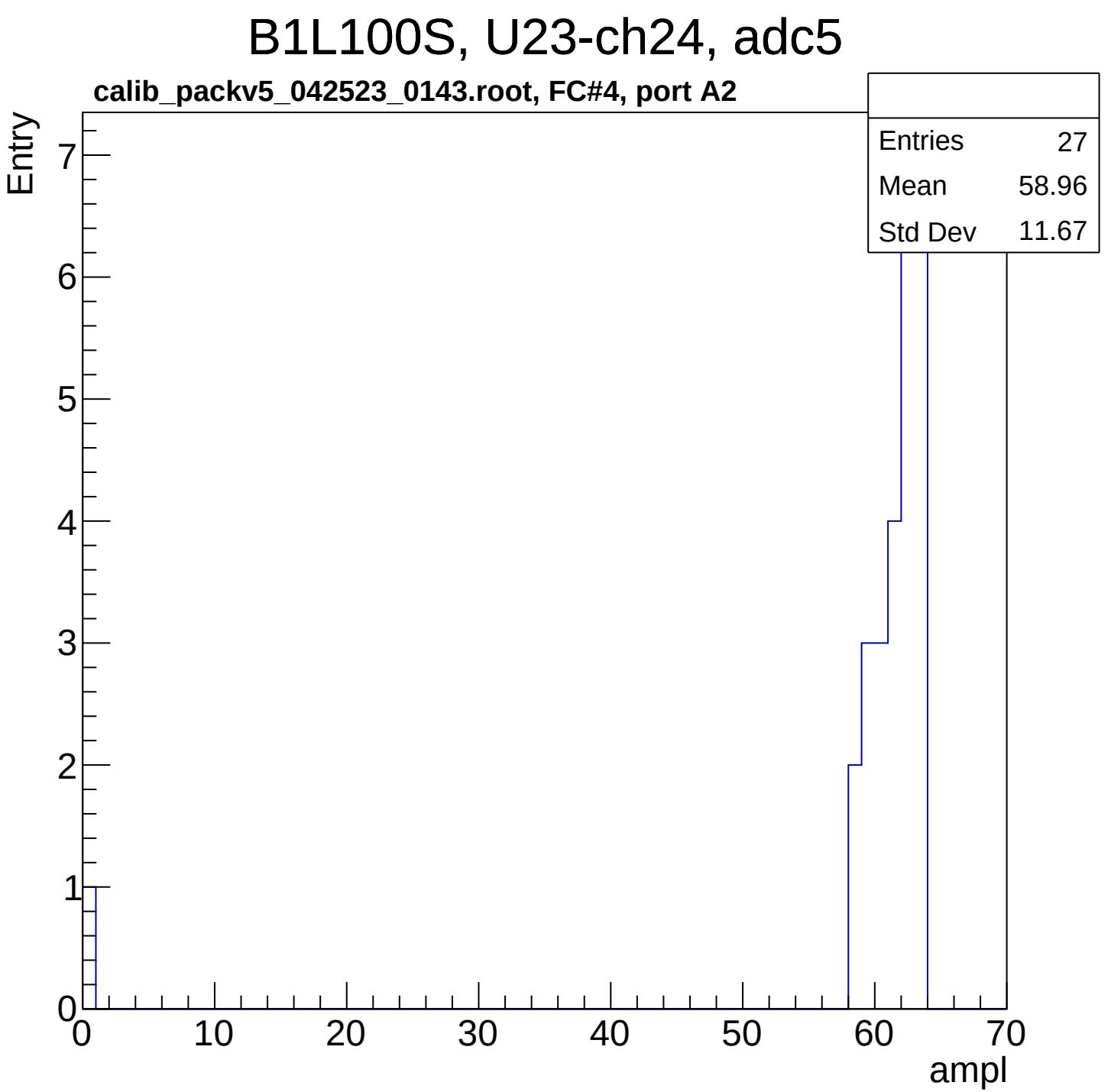
B1L100S, U23-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.96
Std Dev 11.67
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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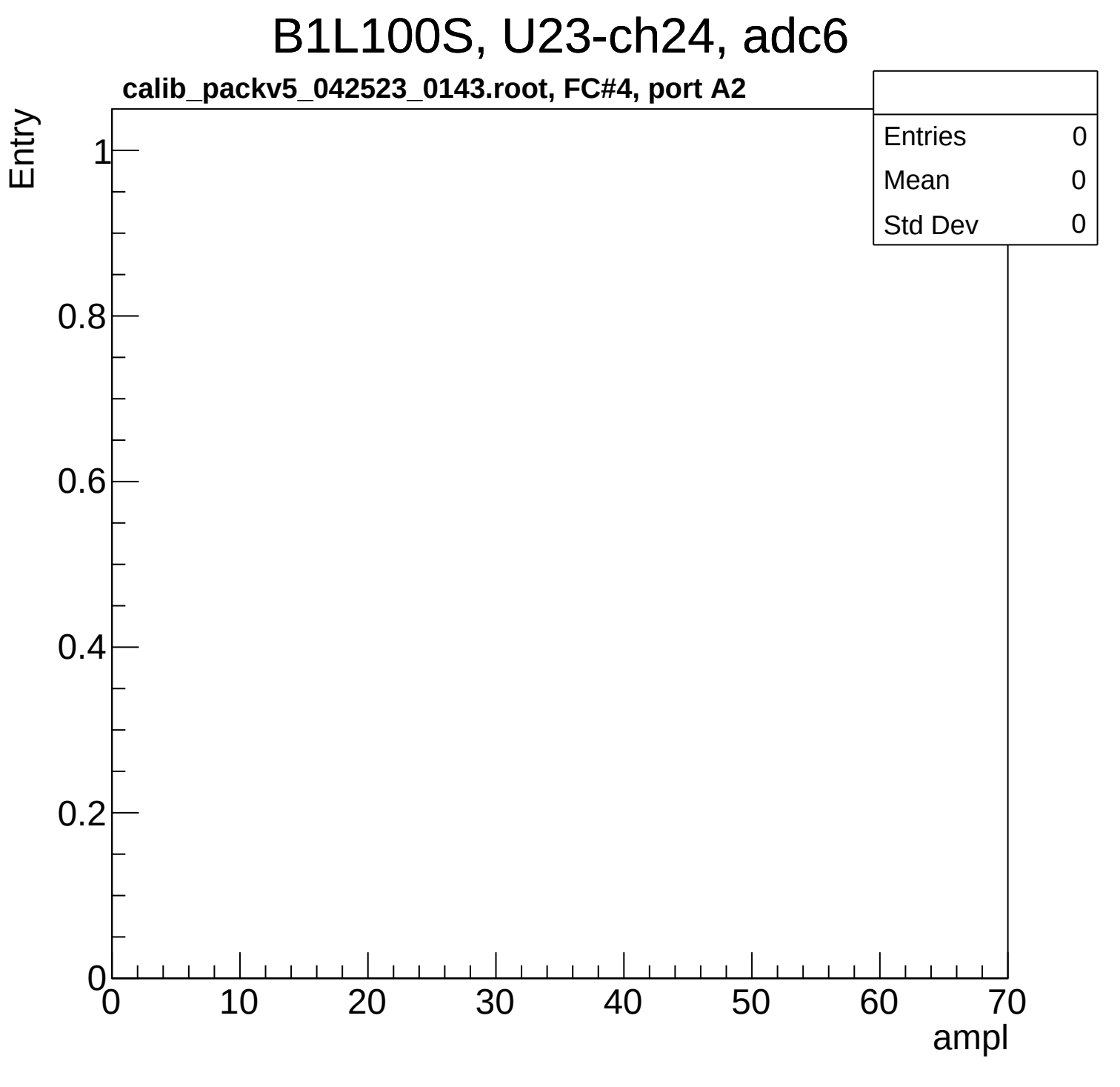
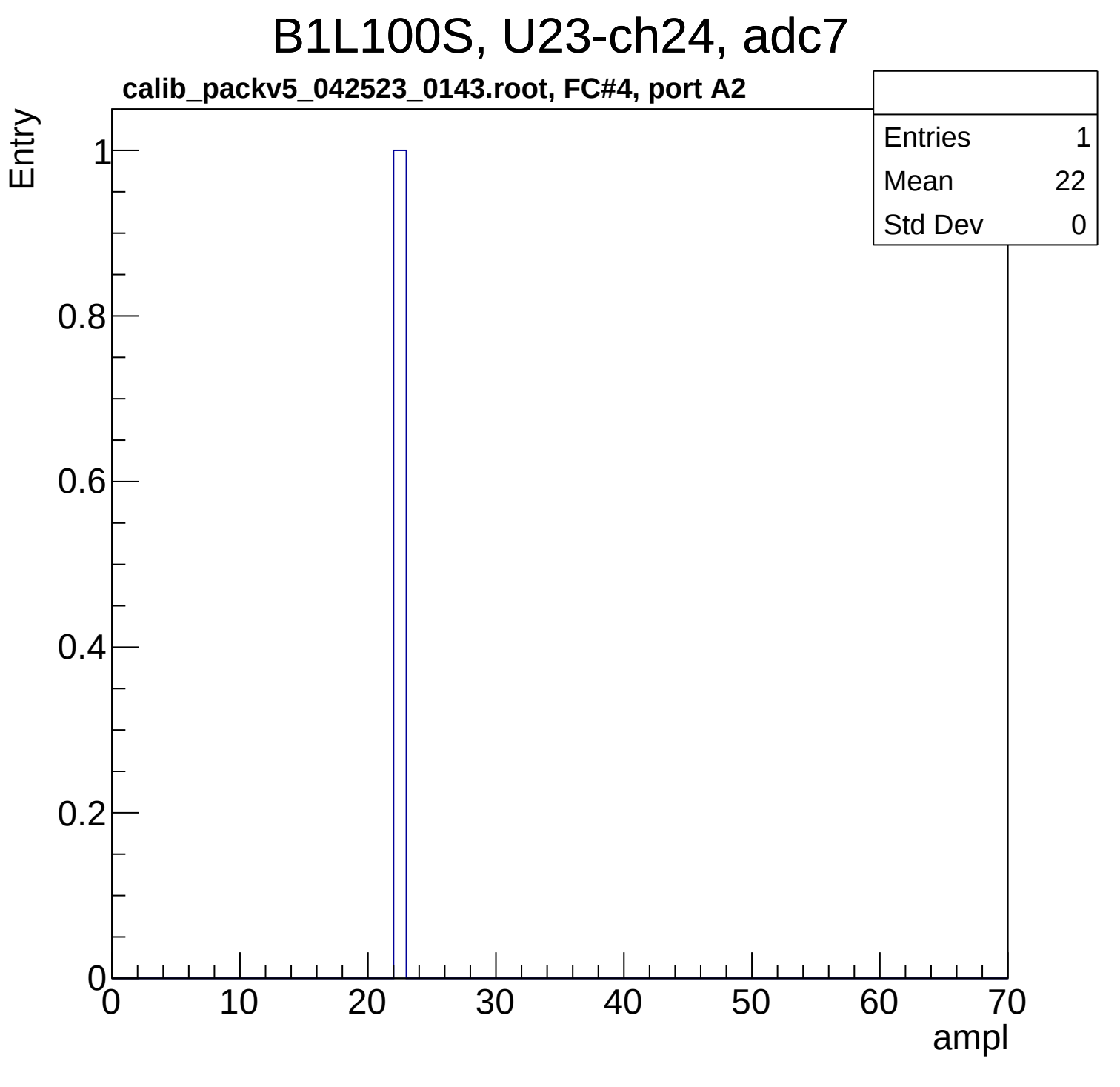

B1L100S, U23-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch24, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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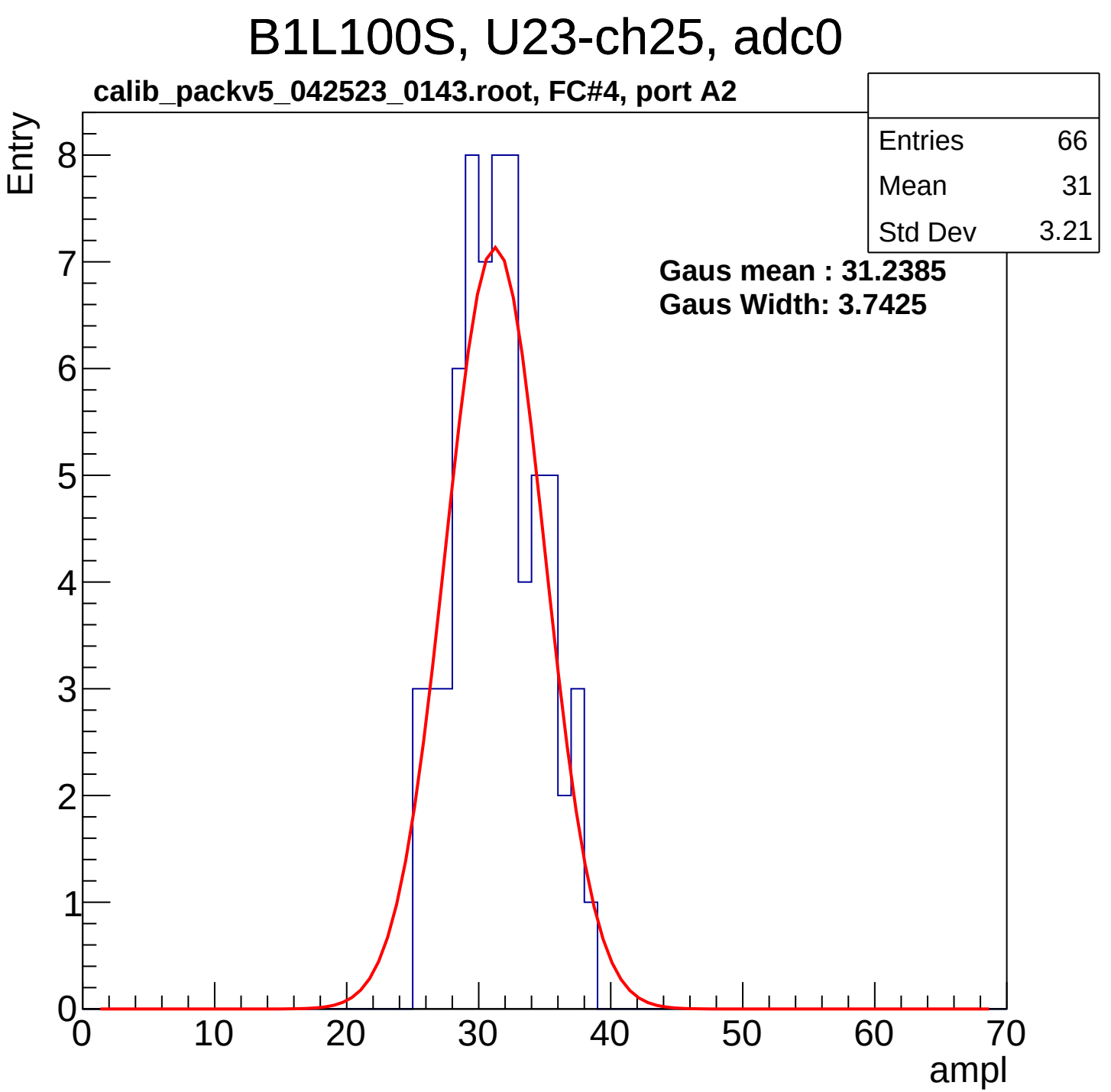
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 31
Std Dev 3.21
Gaus mean : 31.2385
Gaus Width: 3.7425
Entry
ampl
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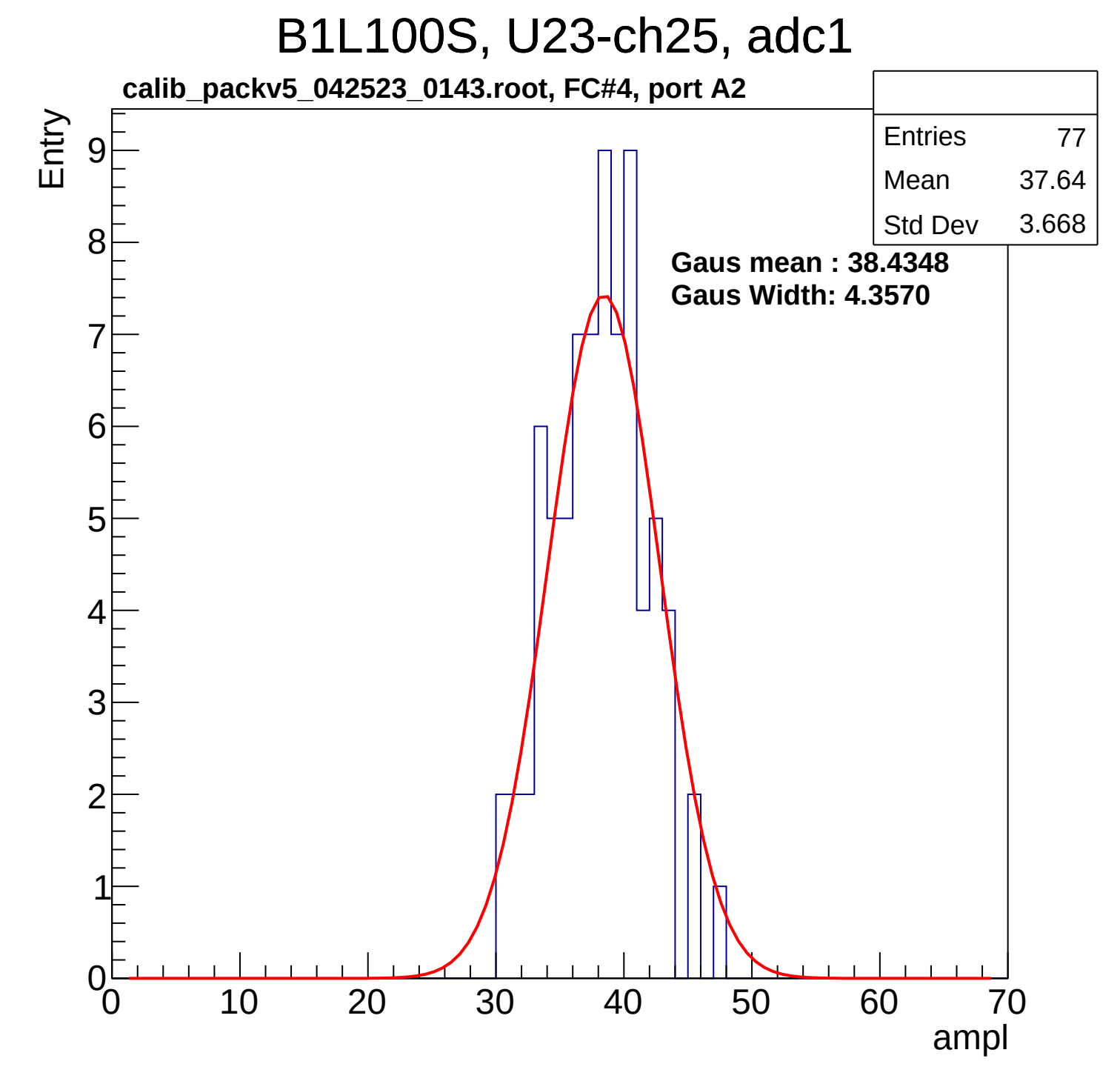
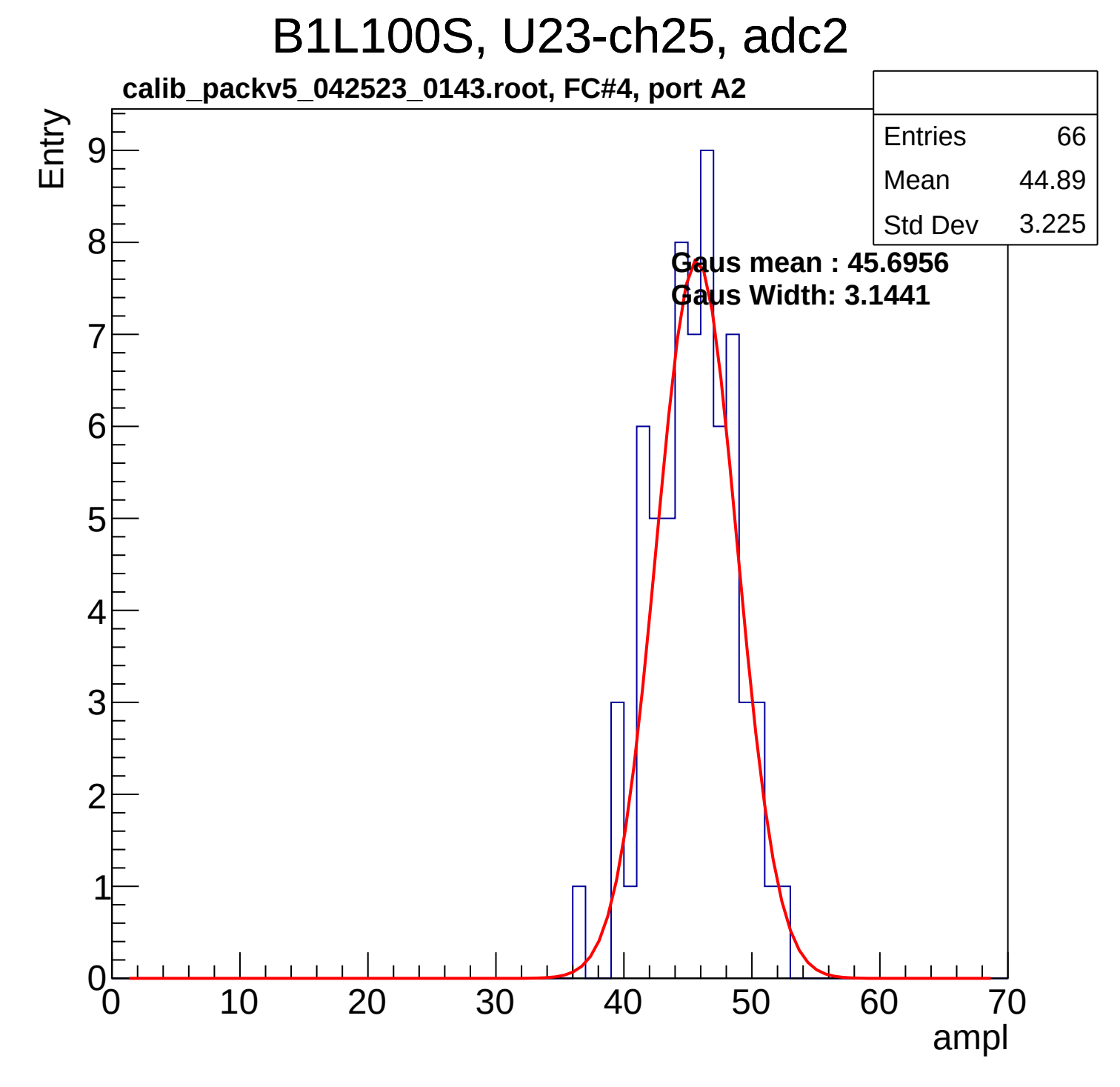
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.64
Std Dev 3.668
Gaus mean : 38.4348
Gaus Width: 4.3570
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 44.89
Std Dev 3.225
Gaus mean : 45.6956
Gaus Width: 3.1441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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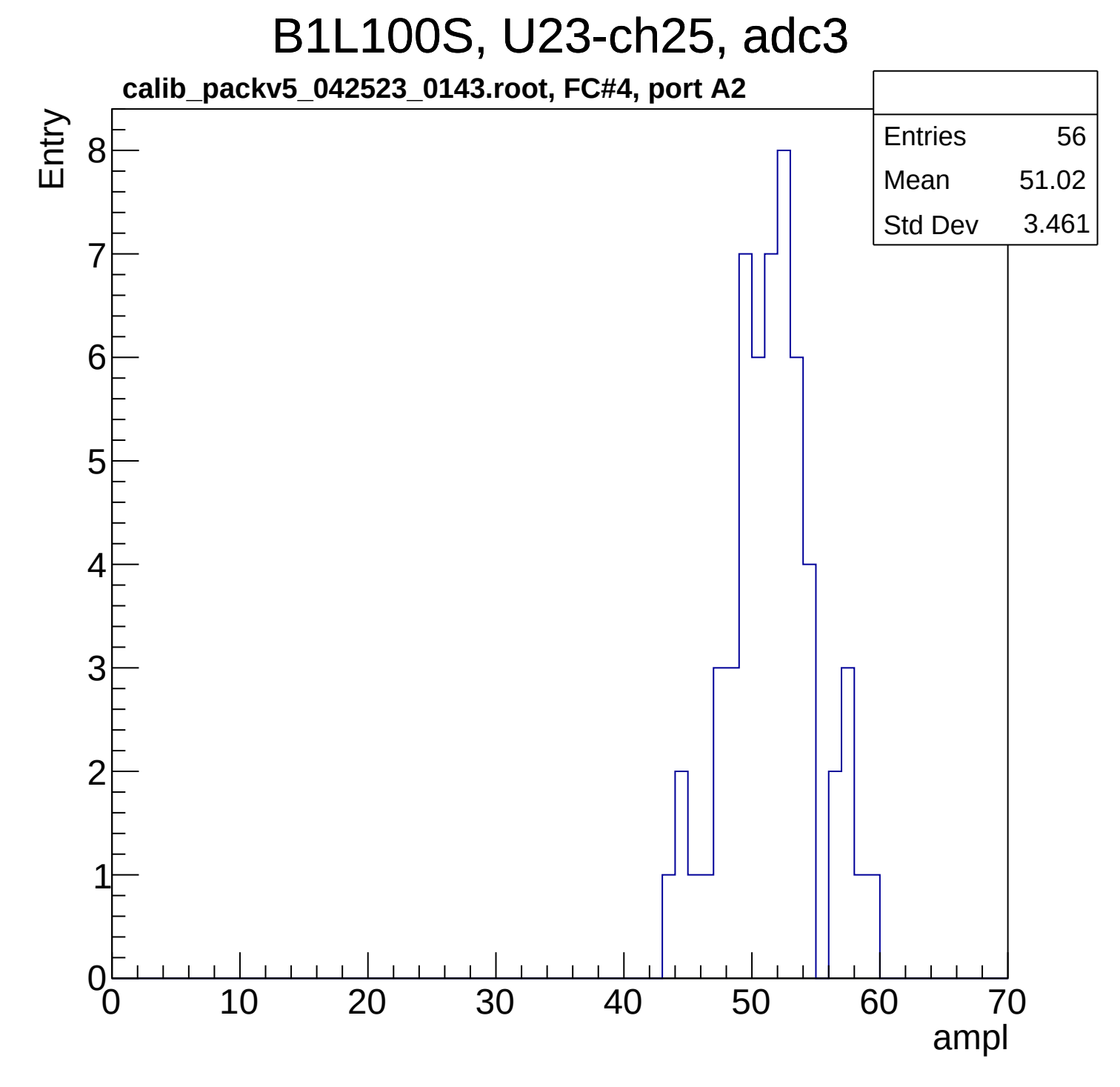
40
50
60
70


B1L100S, U23-ch25, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 51.02
Std Dev 3.461
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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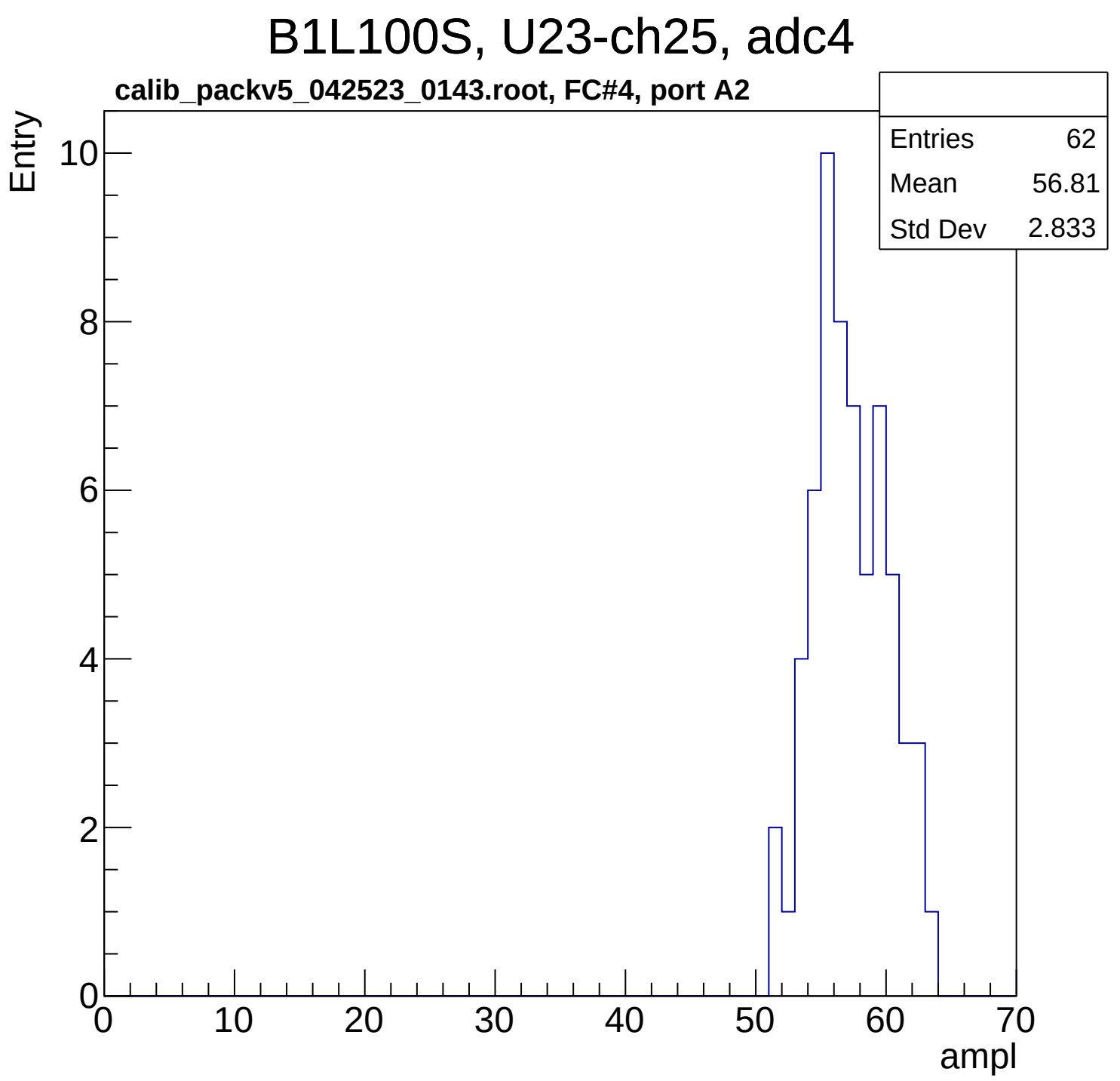
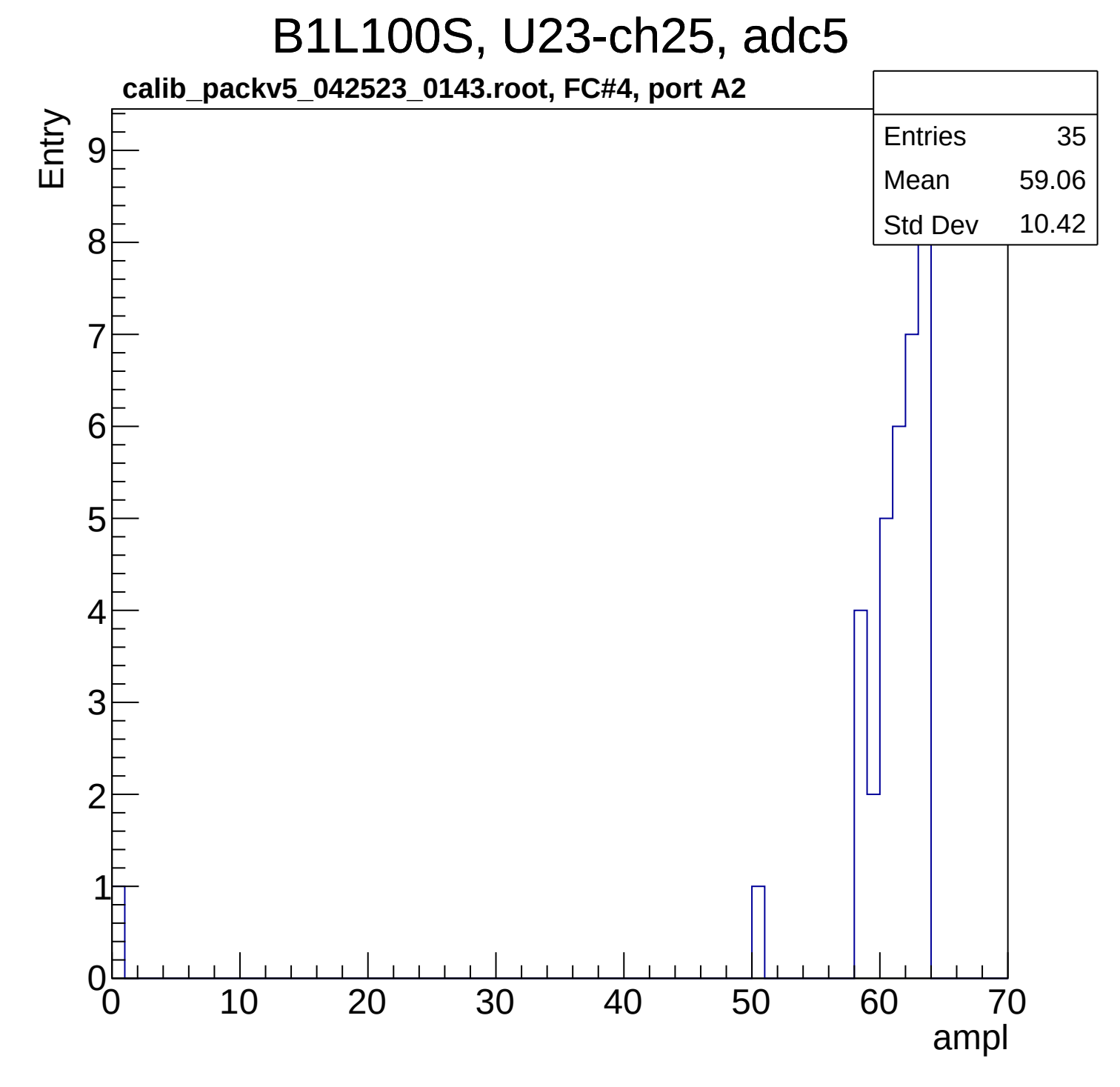
40
50
60
70


B1L100S, U23-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 56.81
Std Dev 2.833
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch25, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.06
Std Dev 10.42
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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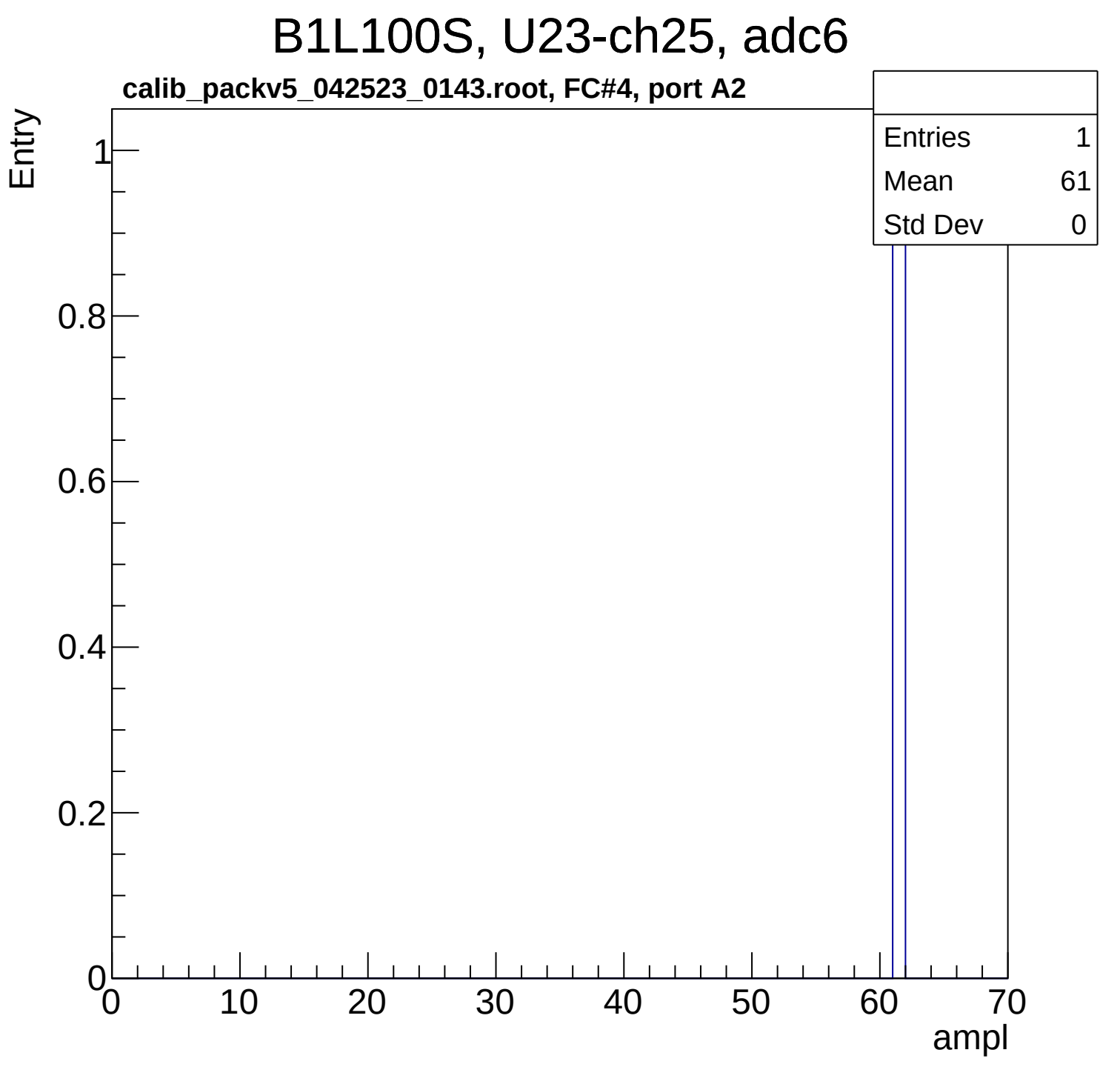
B1L100S, U23-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
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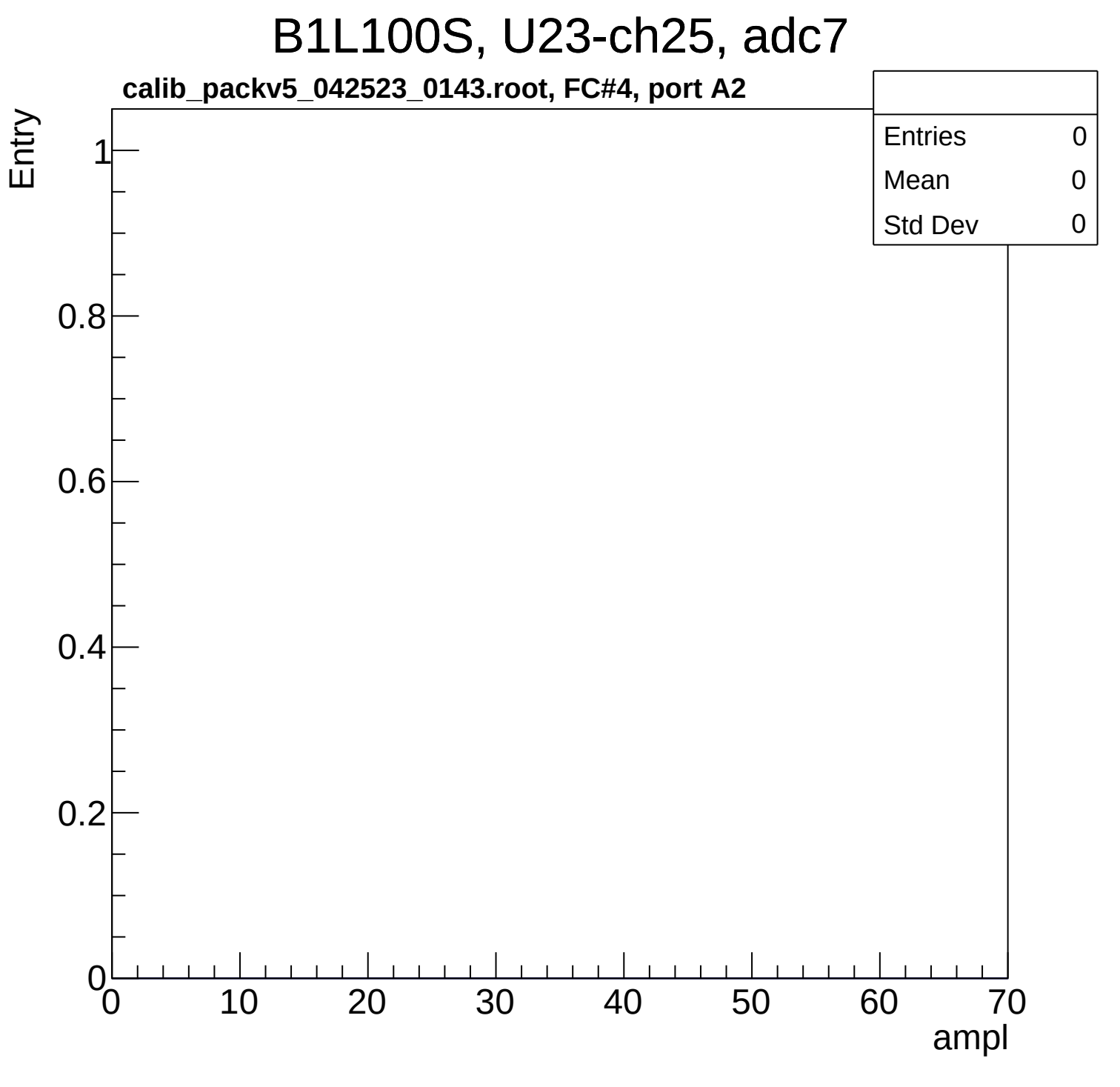
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch25, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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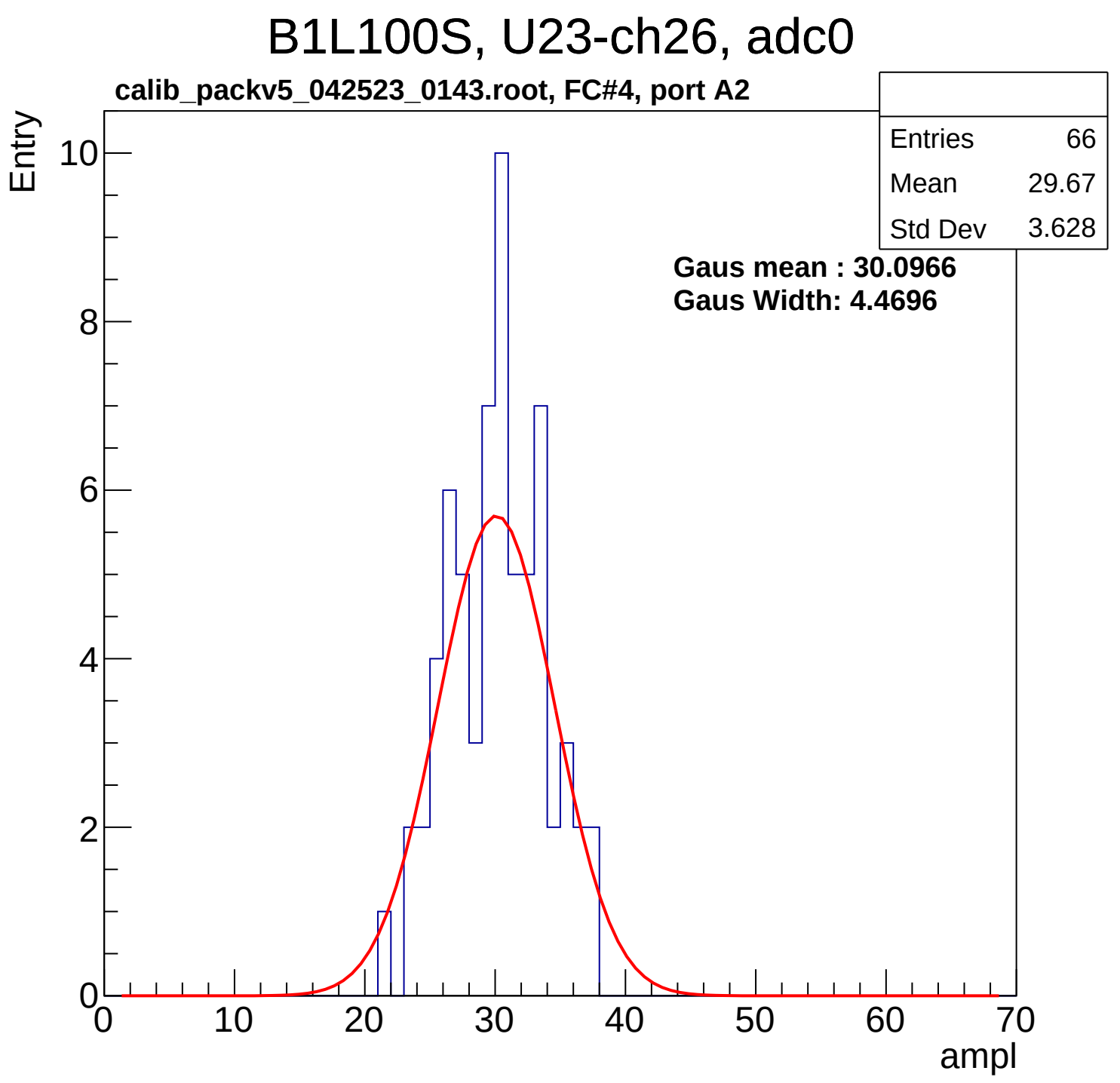
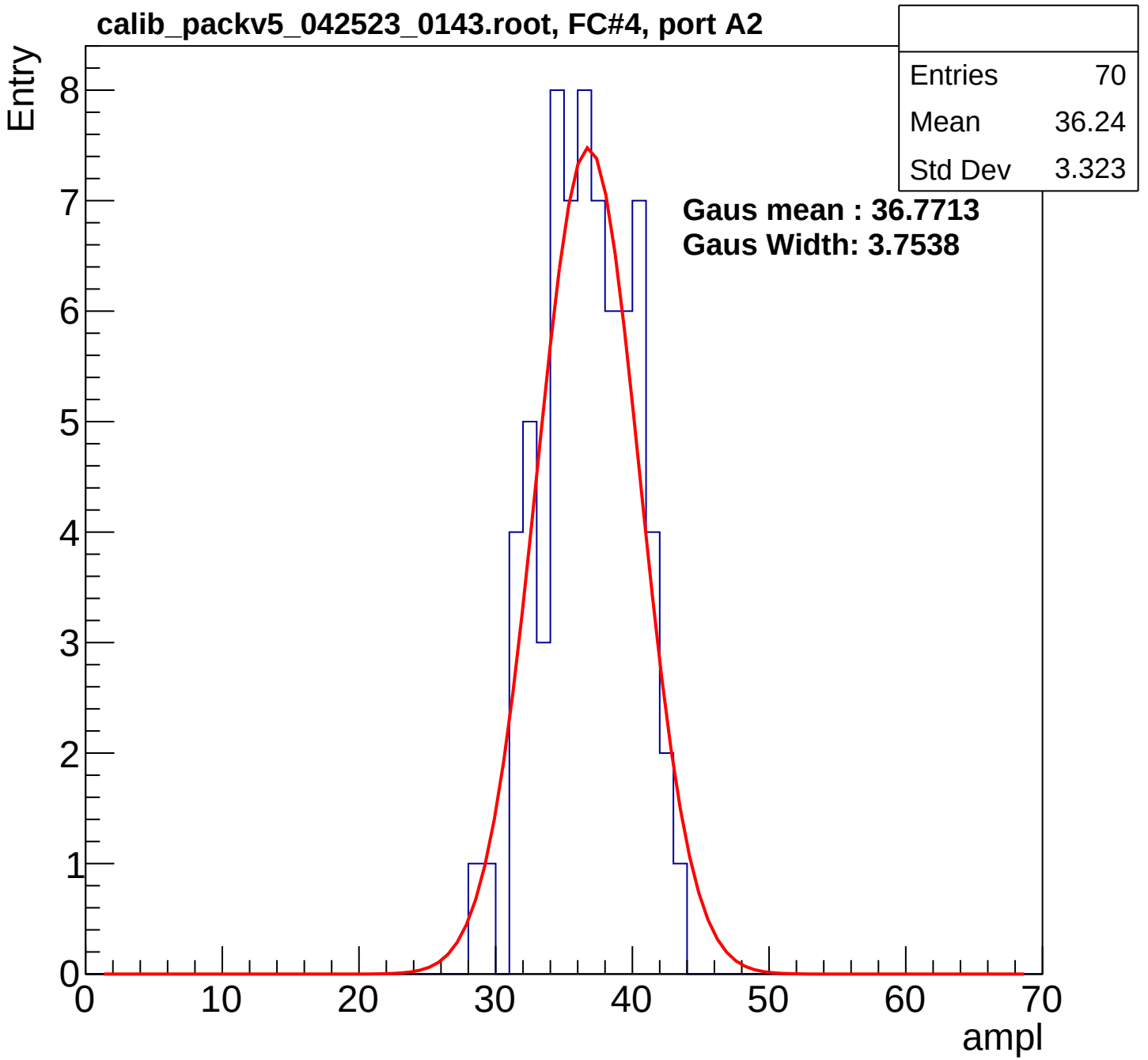
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.67
Std Dev 3.628
Gaus mean : 30.0966
Gaus Width: 4.4696
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.24
Std Dev 3.323
Gaus mean : 36.7713
Gaus Width: 3.7538
Entry
ampl
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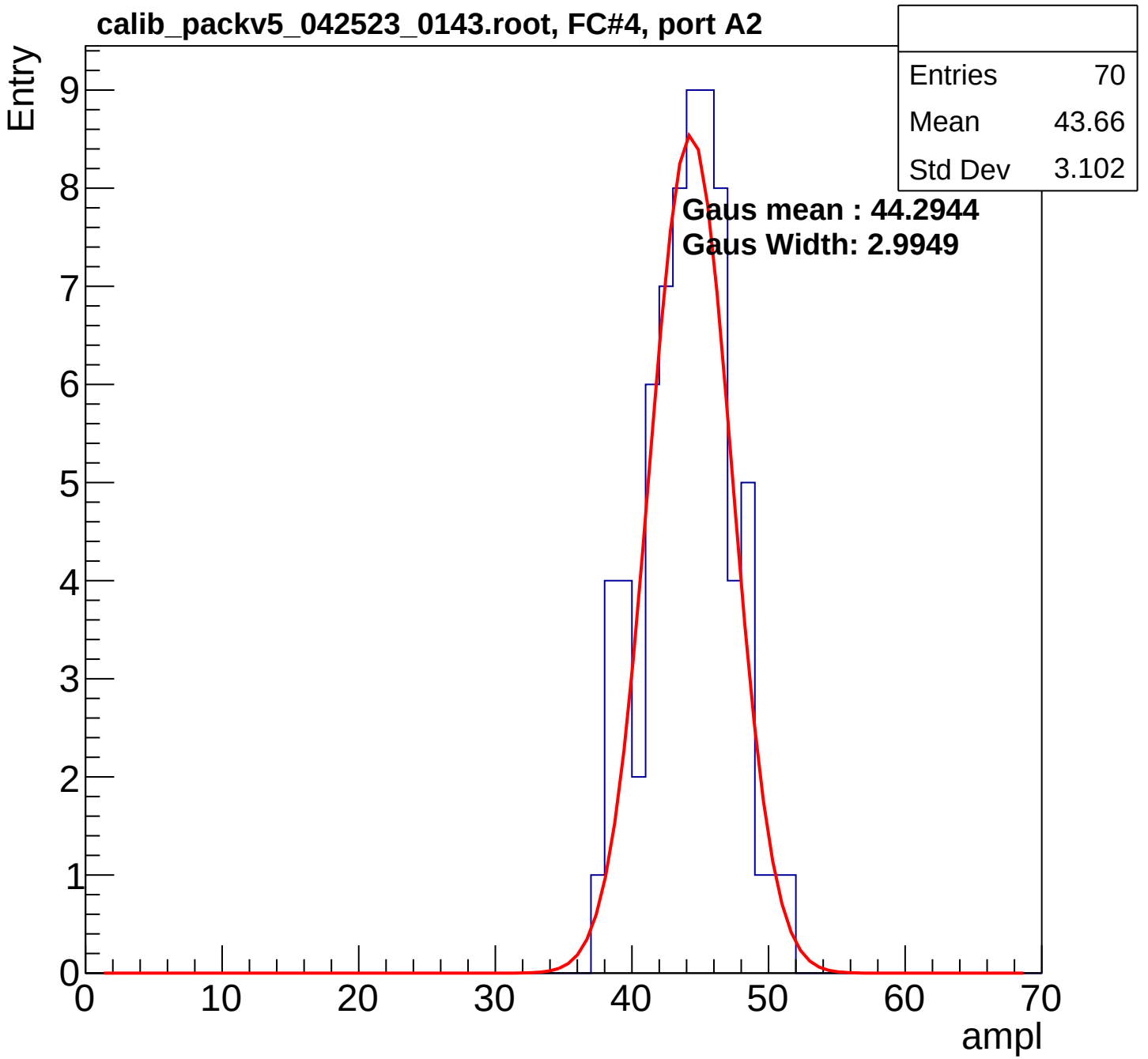
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 43.66
Std Dev 3.102
Gaus mean : 44.2944
Gaus Width: 2.9949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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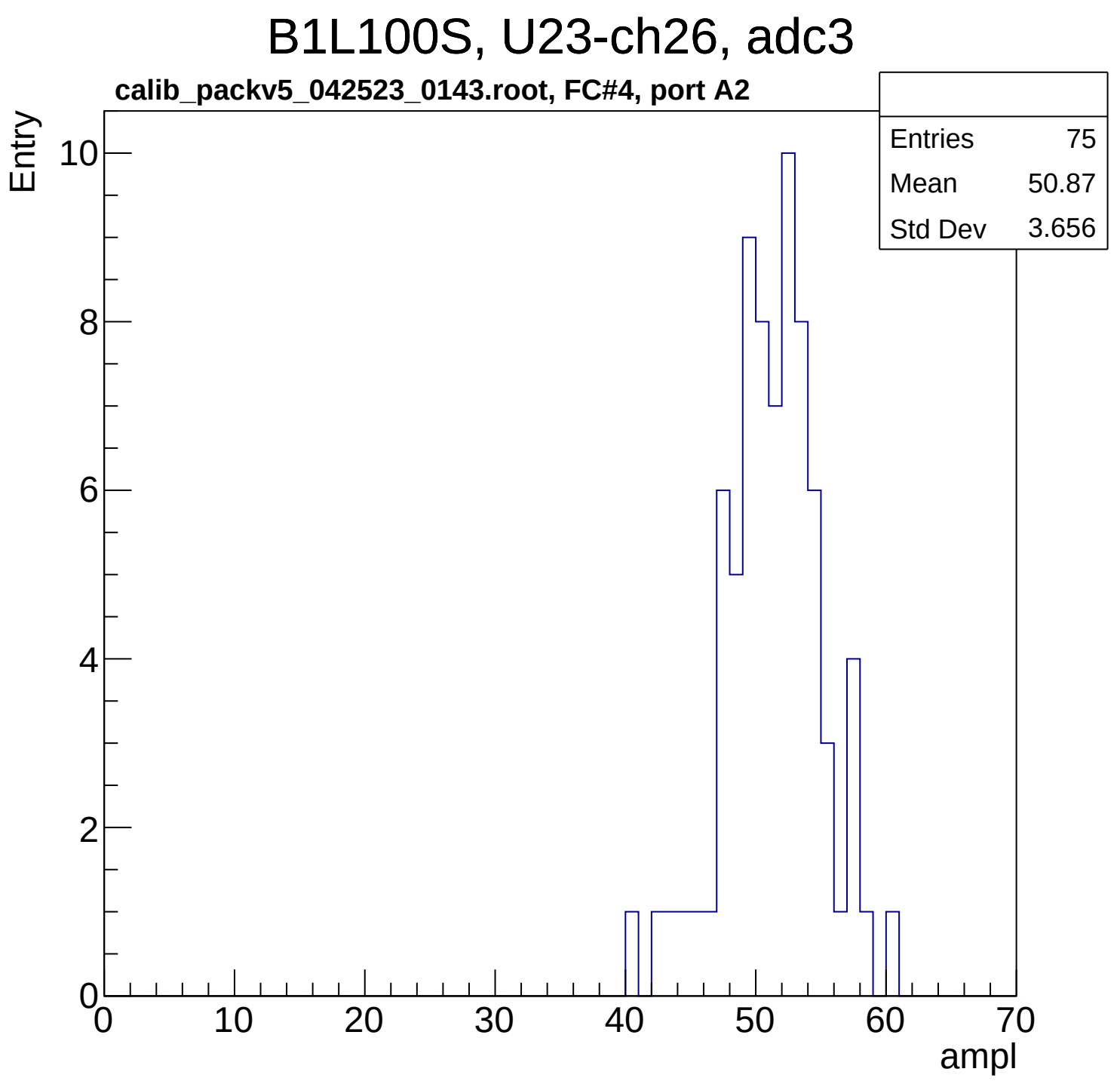
B1L100S, U23-ch26, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.87
Std Dev 3.656
Entry
ampl
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8
10
0
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40
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60
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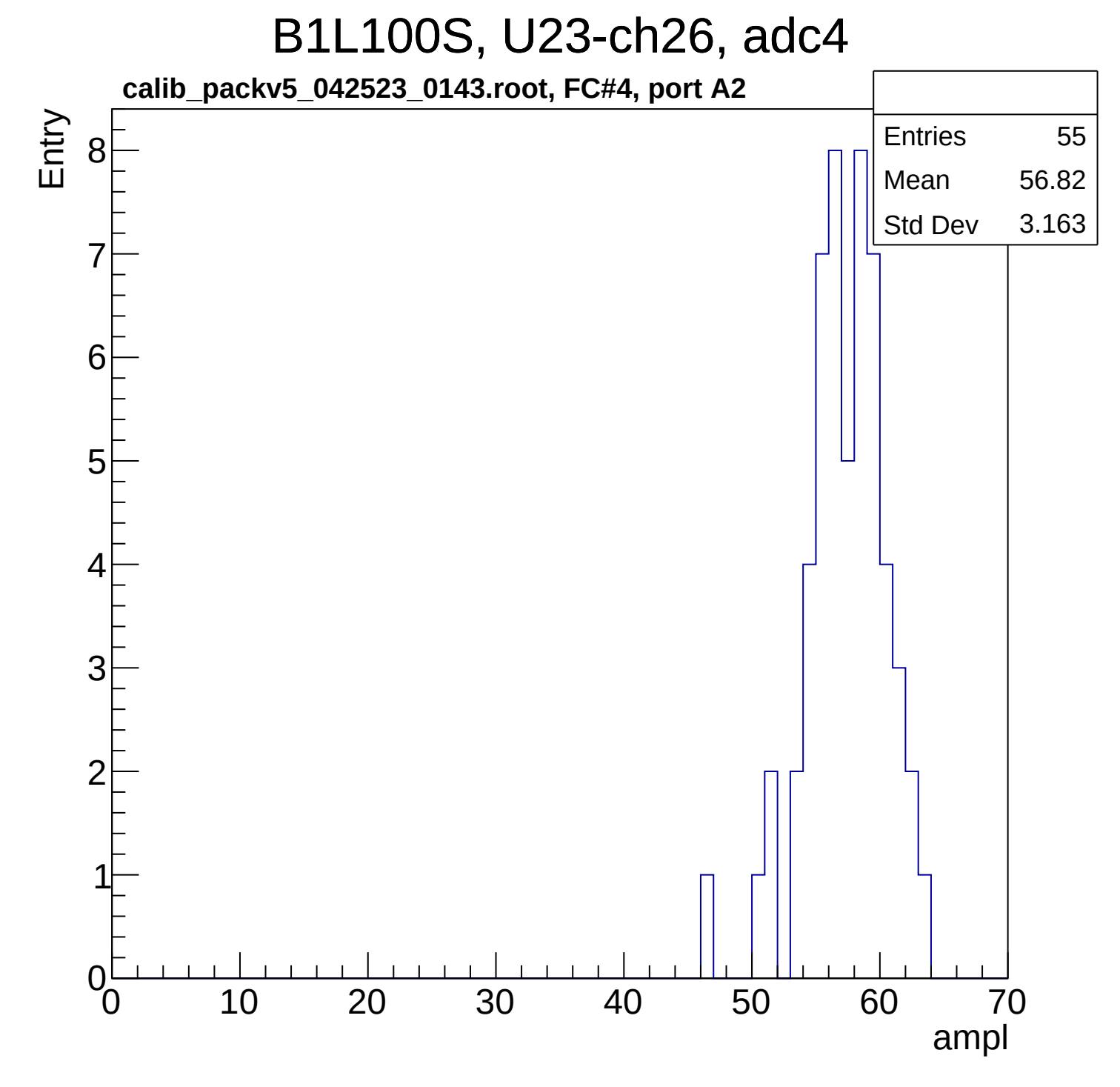
B1L100S, U23-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.82
Std Dev 3.163
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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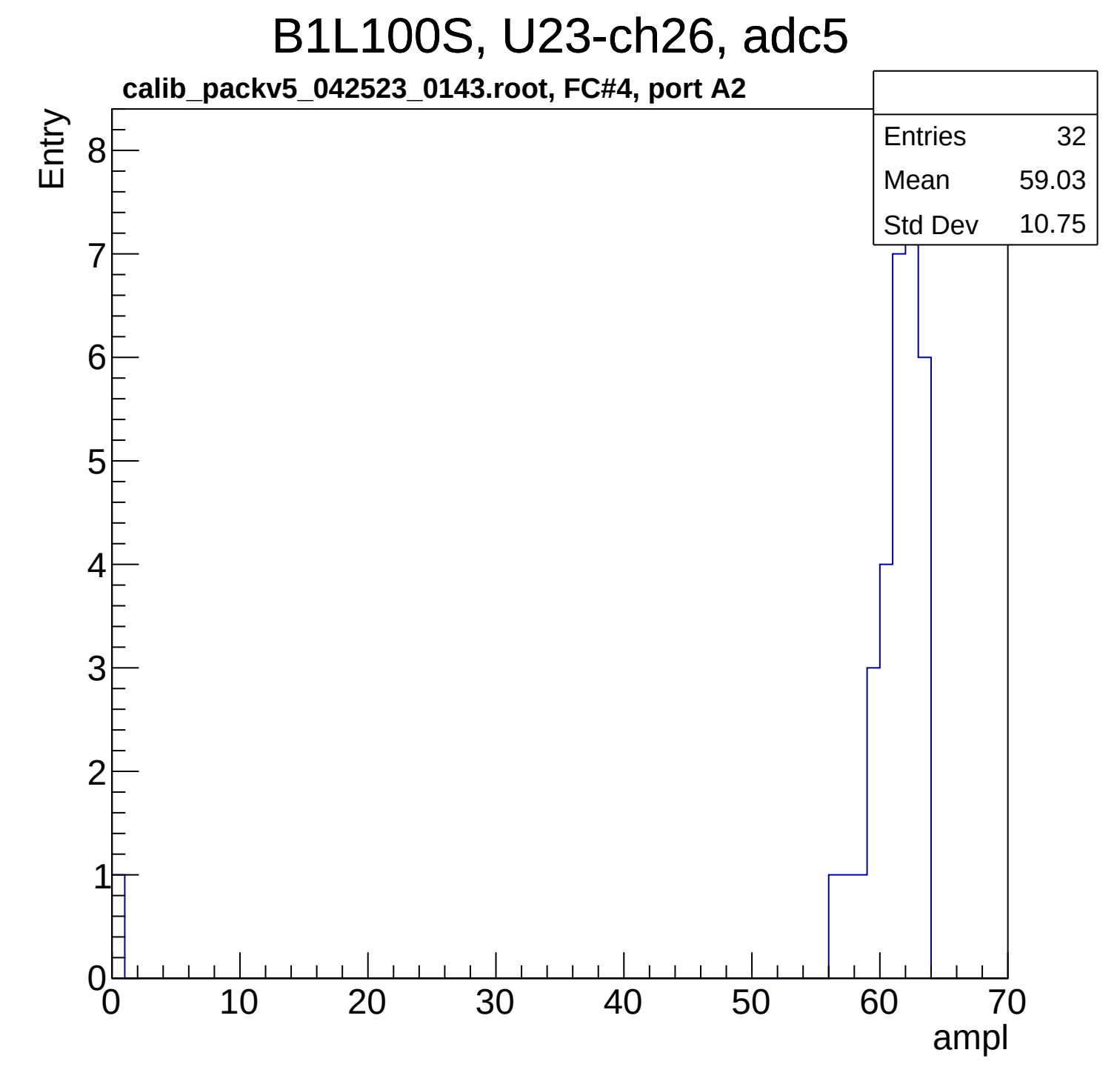
B1L100S, U23-ch26, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.03
Std Dev 10.75
Entry
ampl
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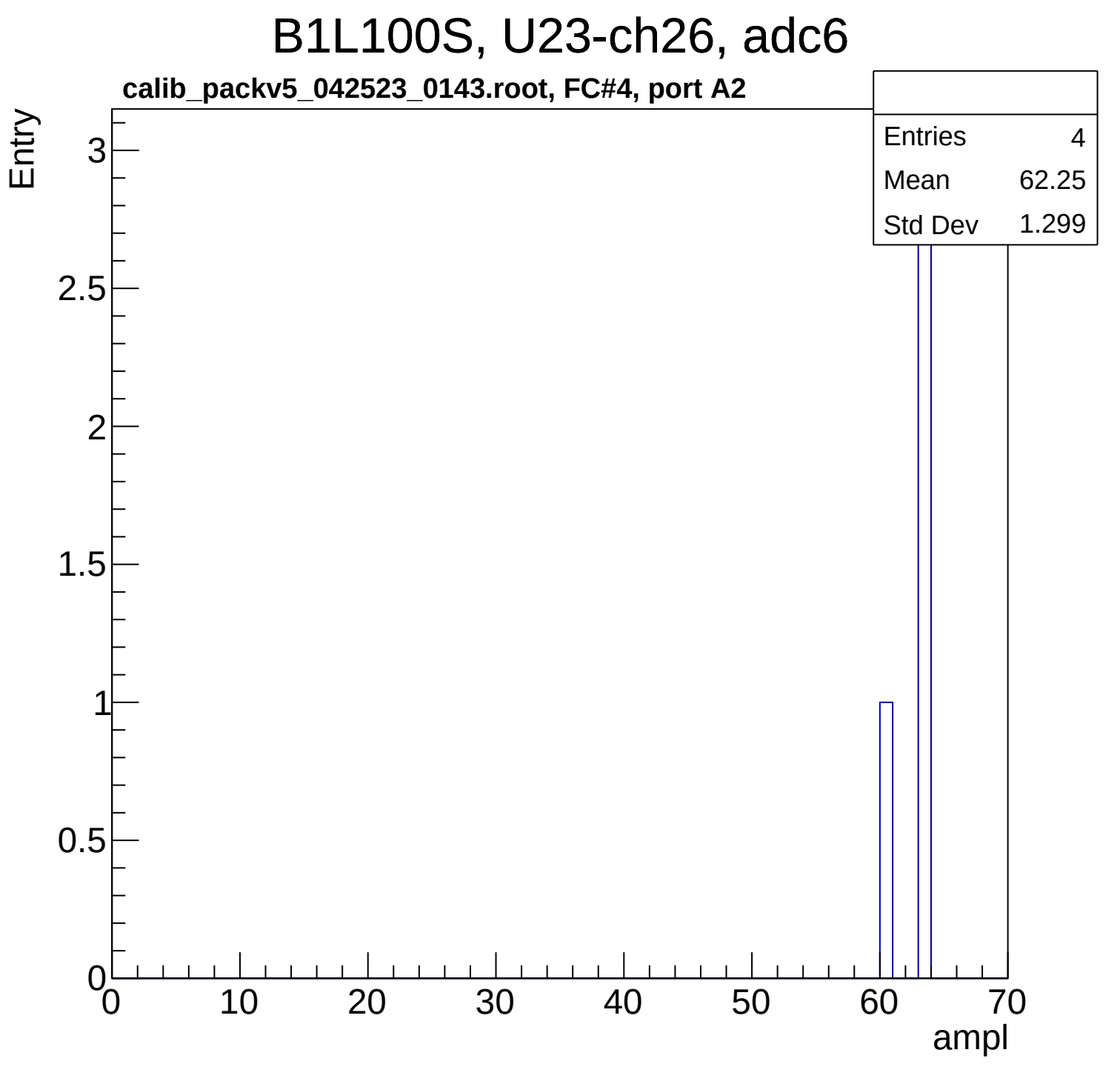
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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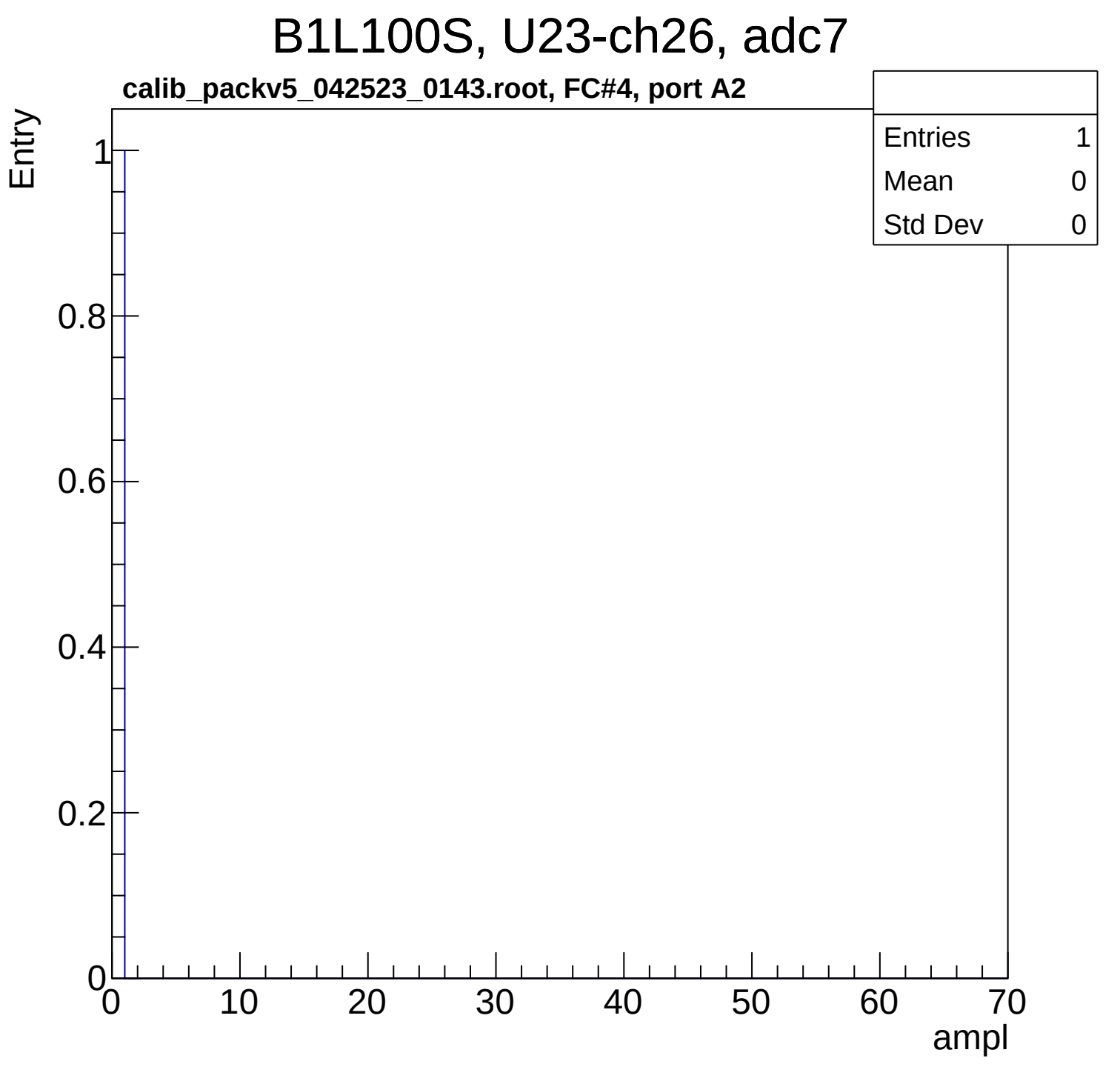
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch26, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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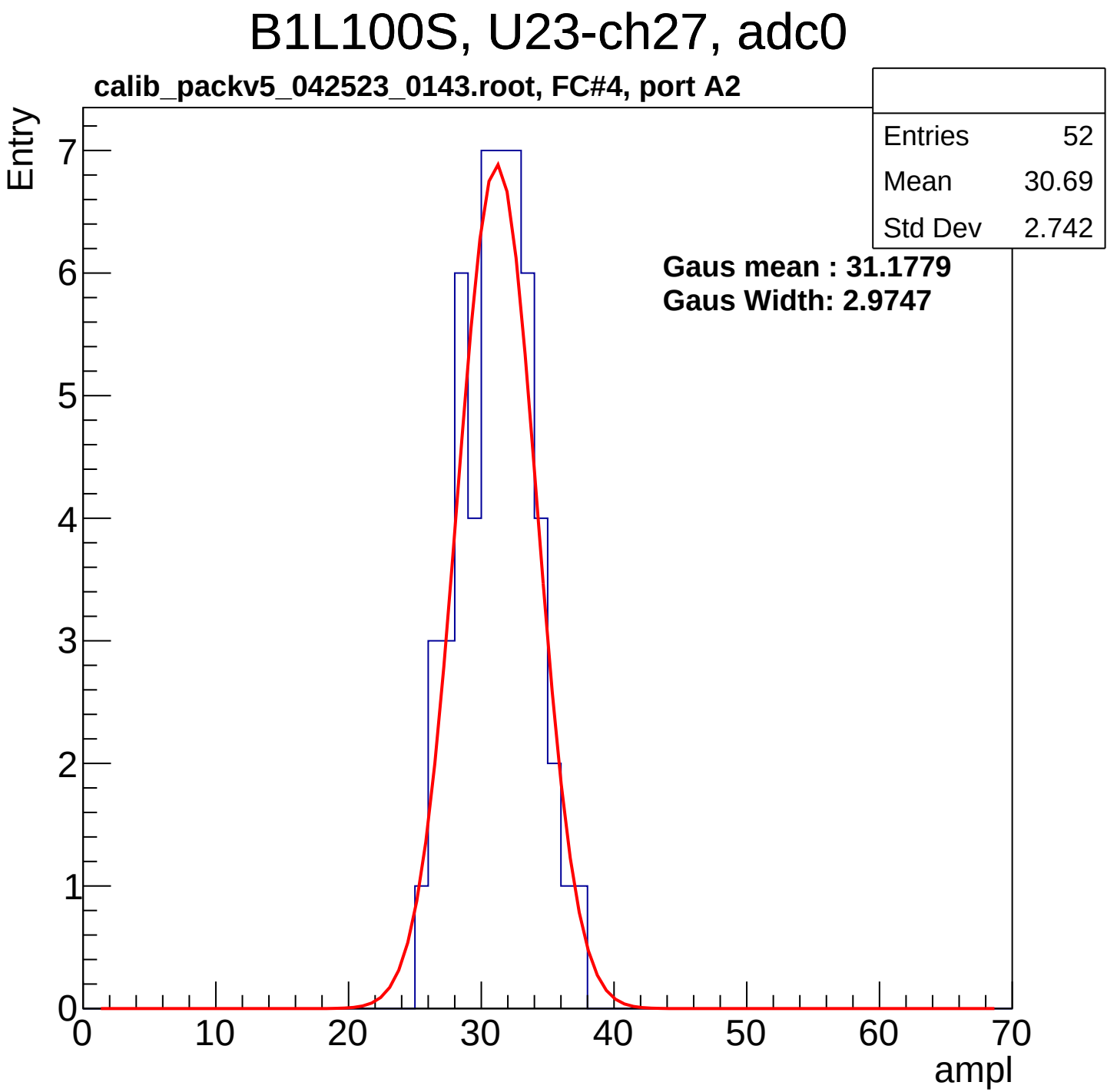
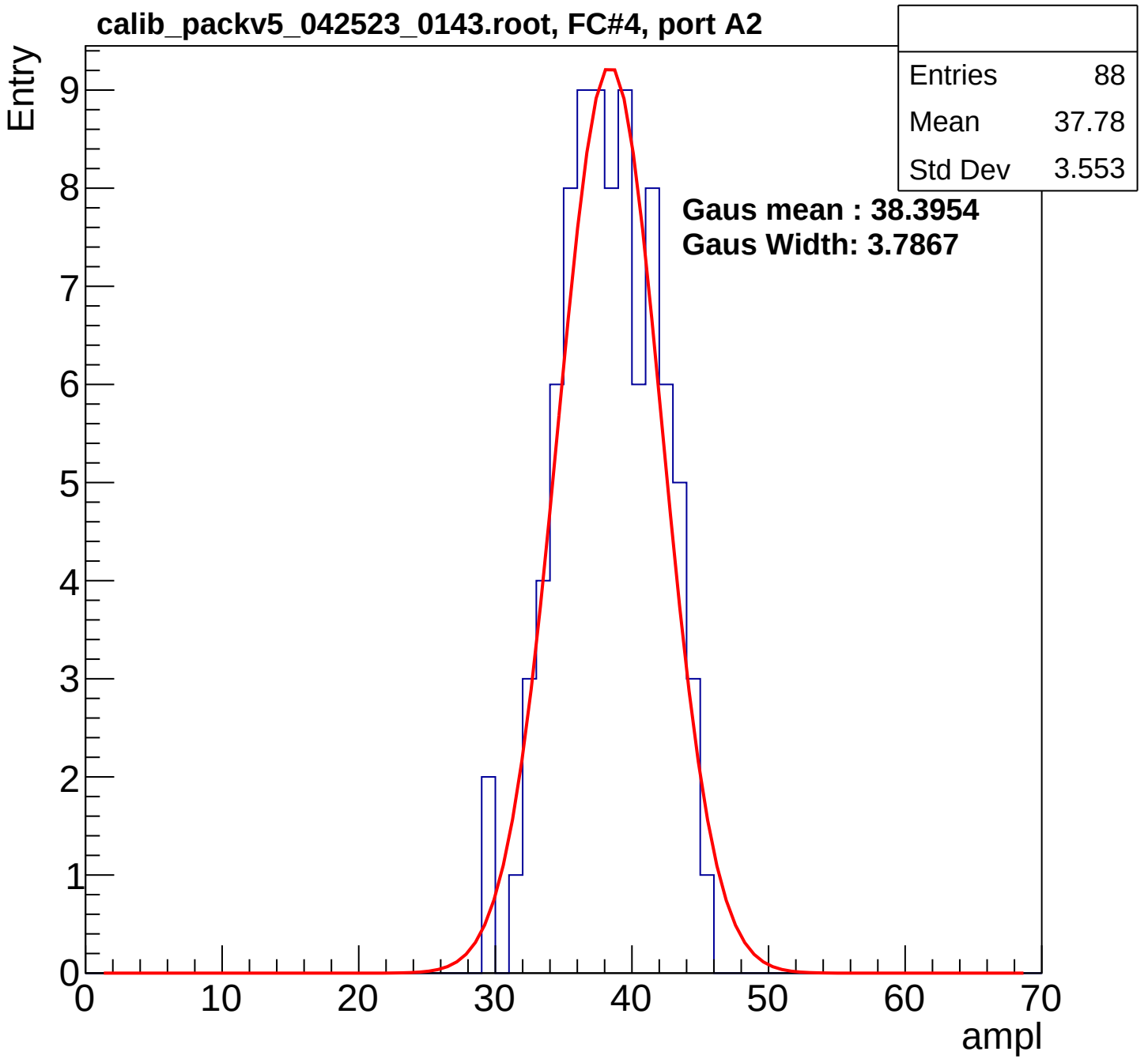
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 30.69
Std Dev 2.742
Gaus mean : 31.1779
Gaus Width: 2.9747
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 37.78
Std Dev 3.553
Gaus mean : 38.3954
Gaus Width: 3.7867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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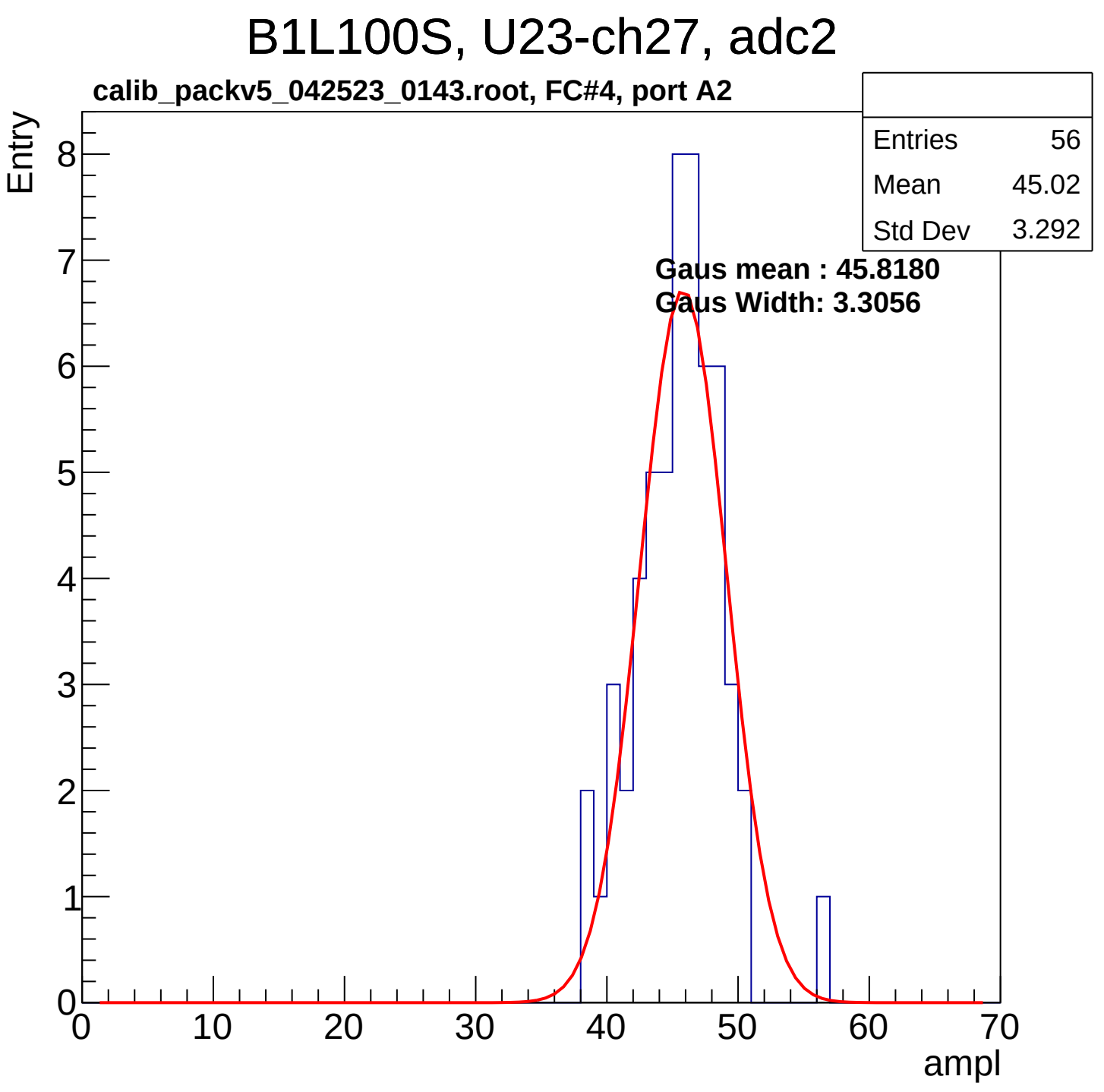
B1L100S, U23-ch27, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 45.02
Std Dev 3.292
Gaus mean : 45.8180
Gaus Width: 3.3056
Entry
ampl
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1
2
3
4
5
6
7
8
0
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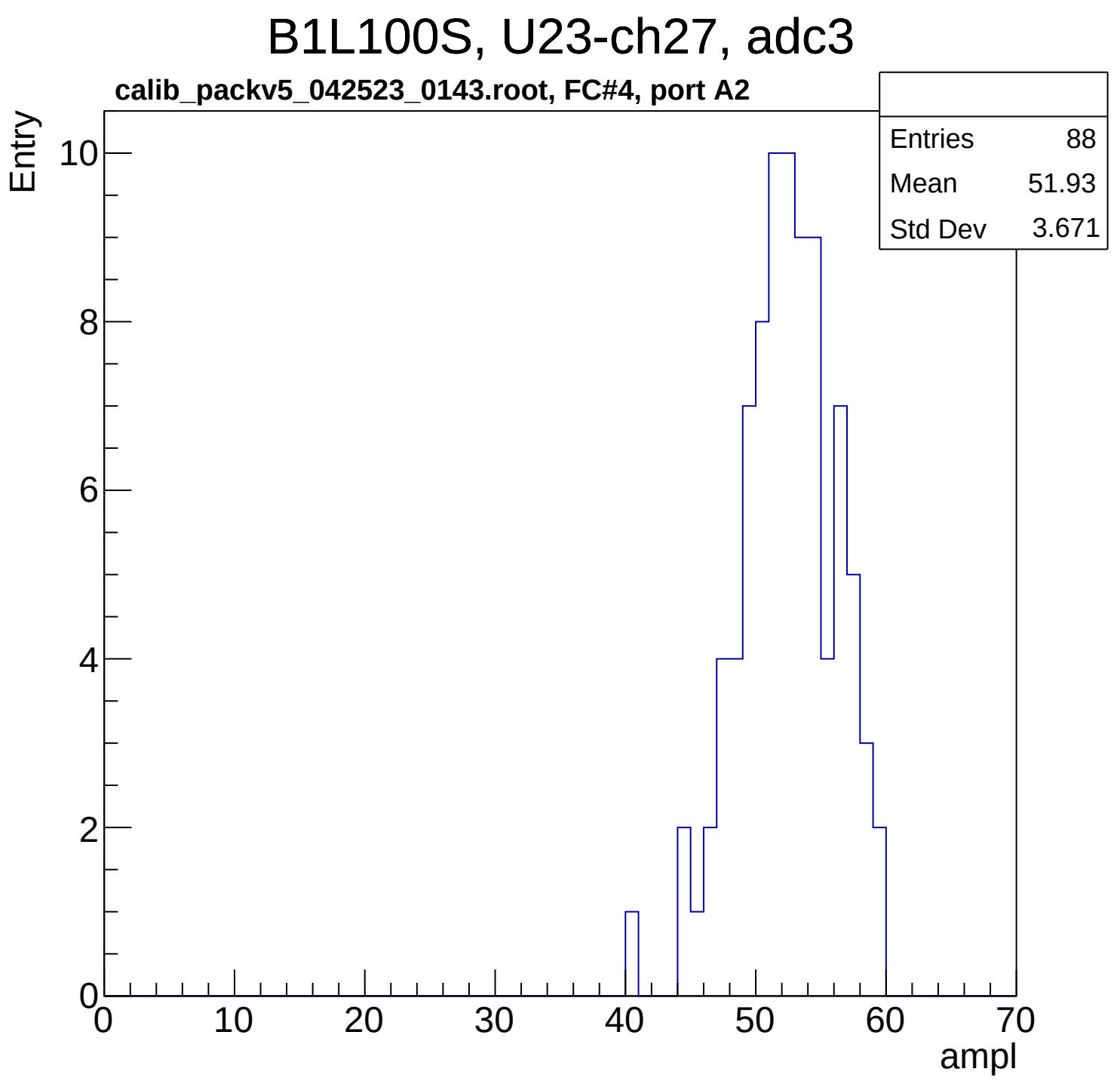
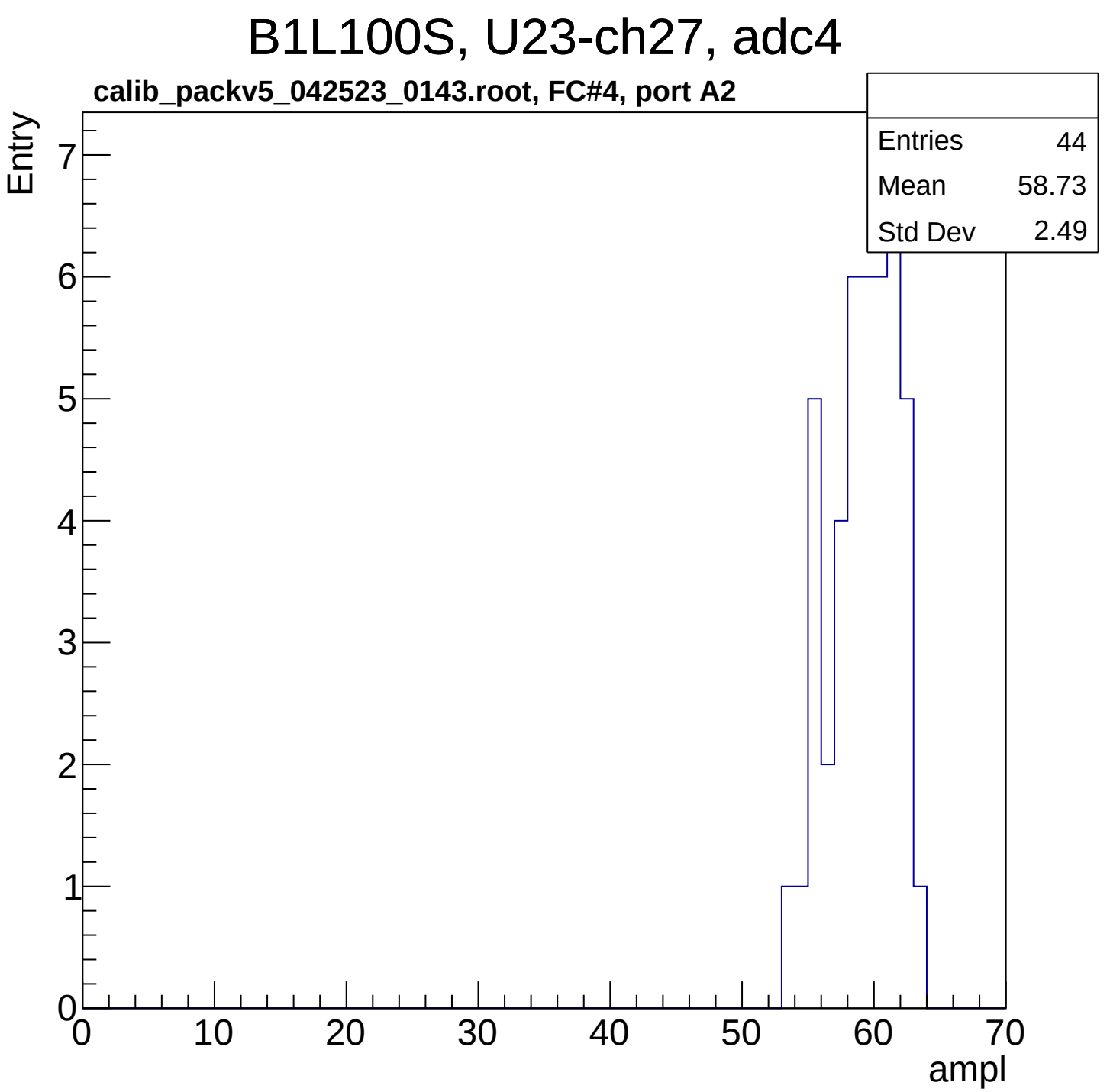

B1L100S, U23-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 51.93
Std Dev 3.671
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.73
Std Dev 2.49
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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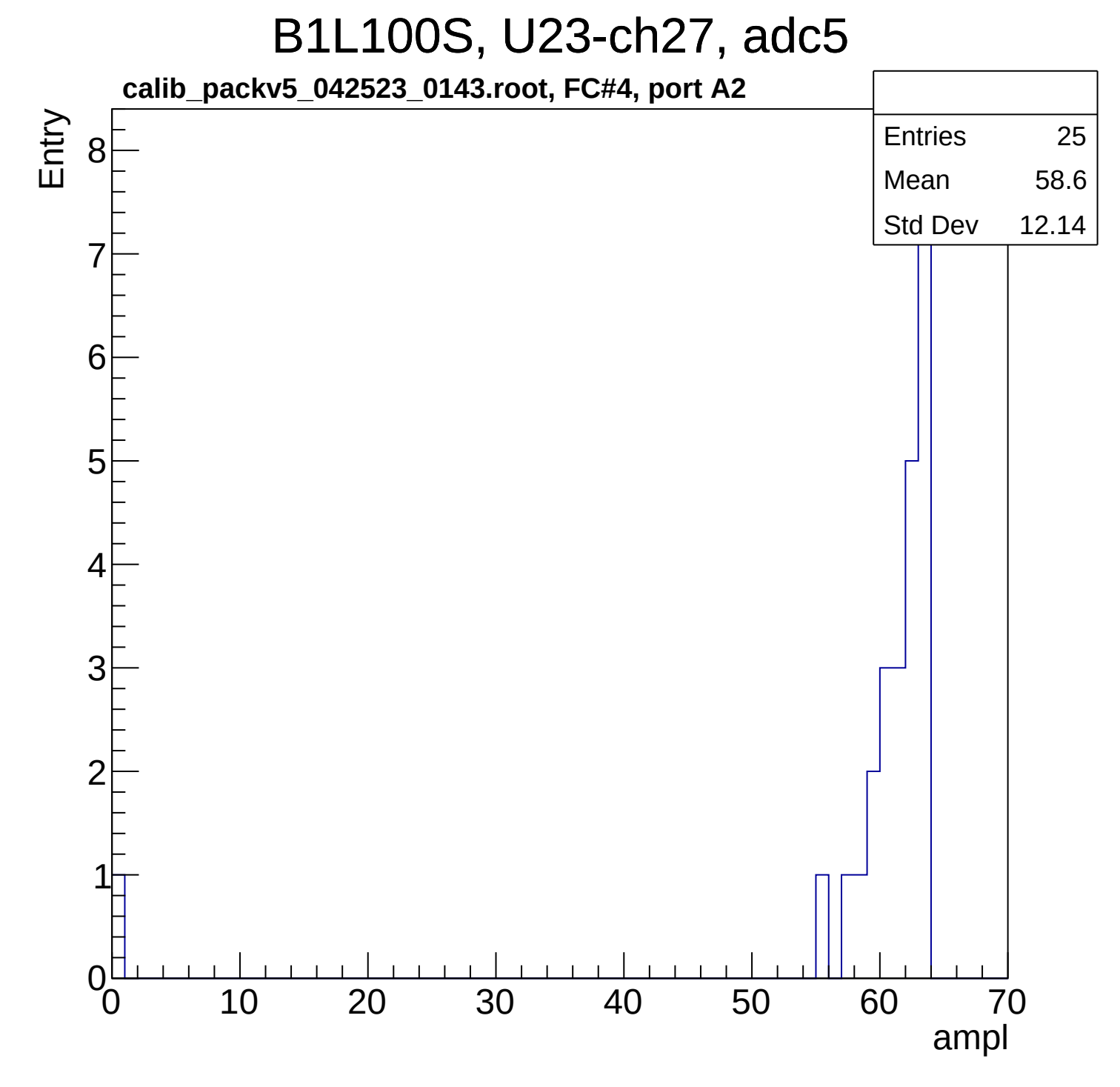
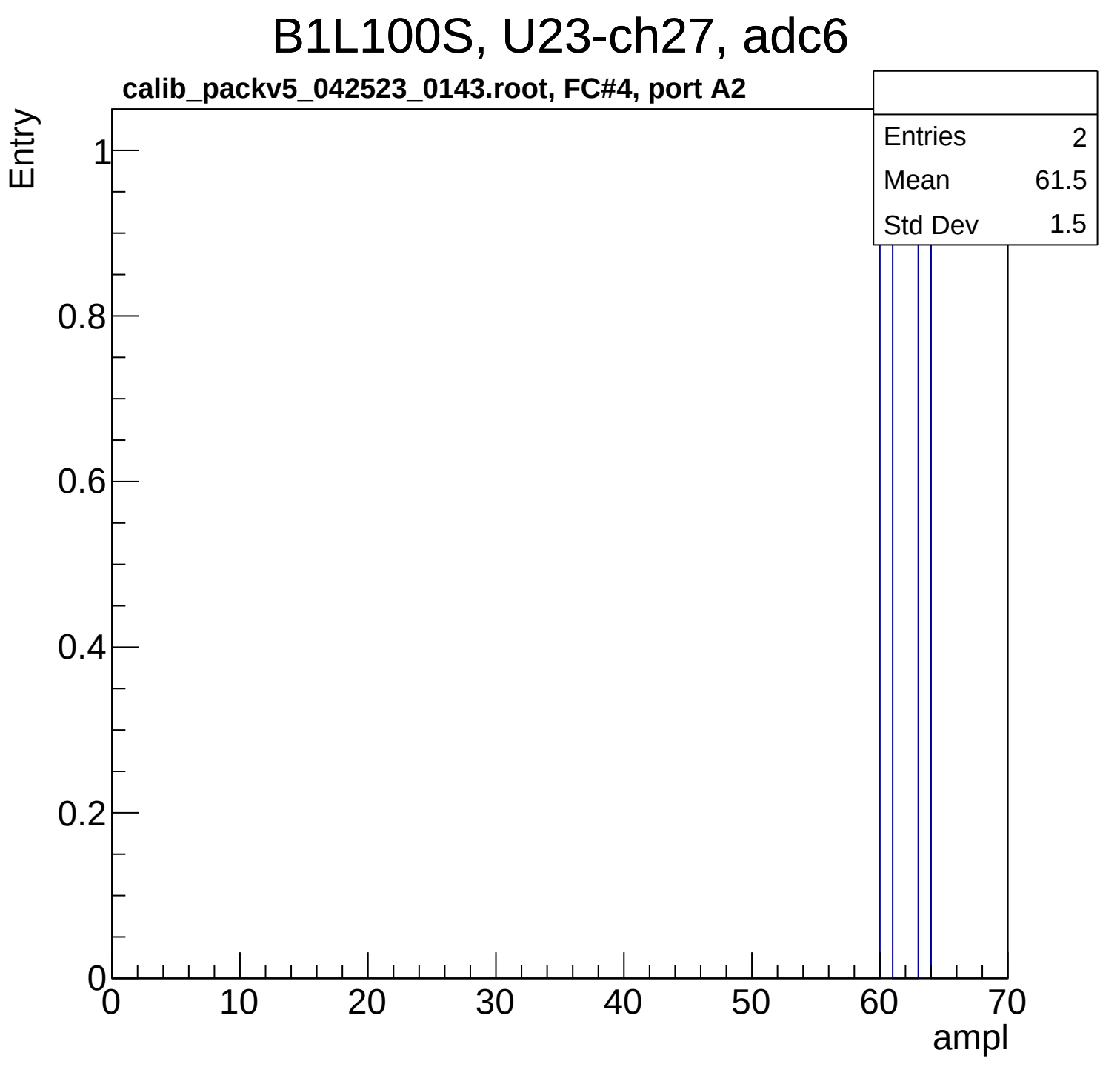
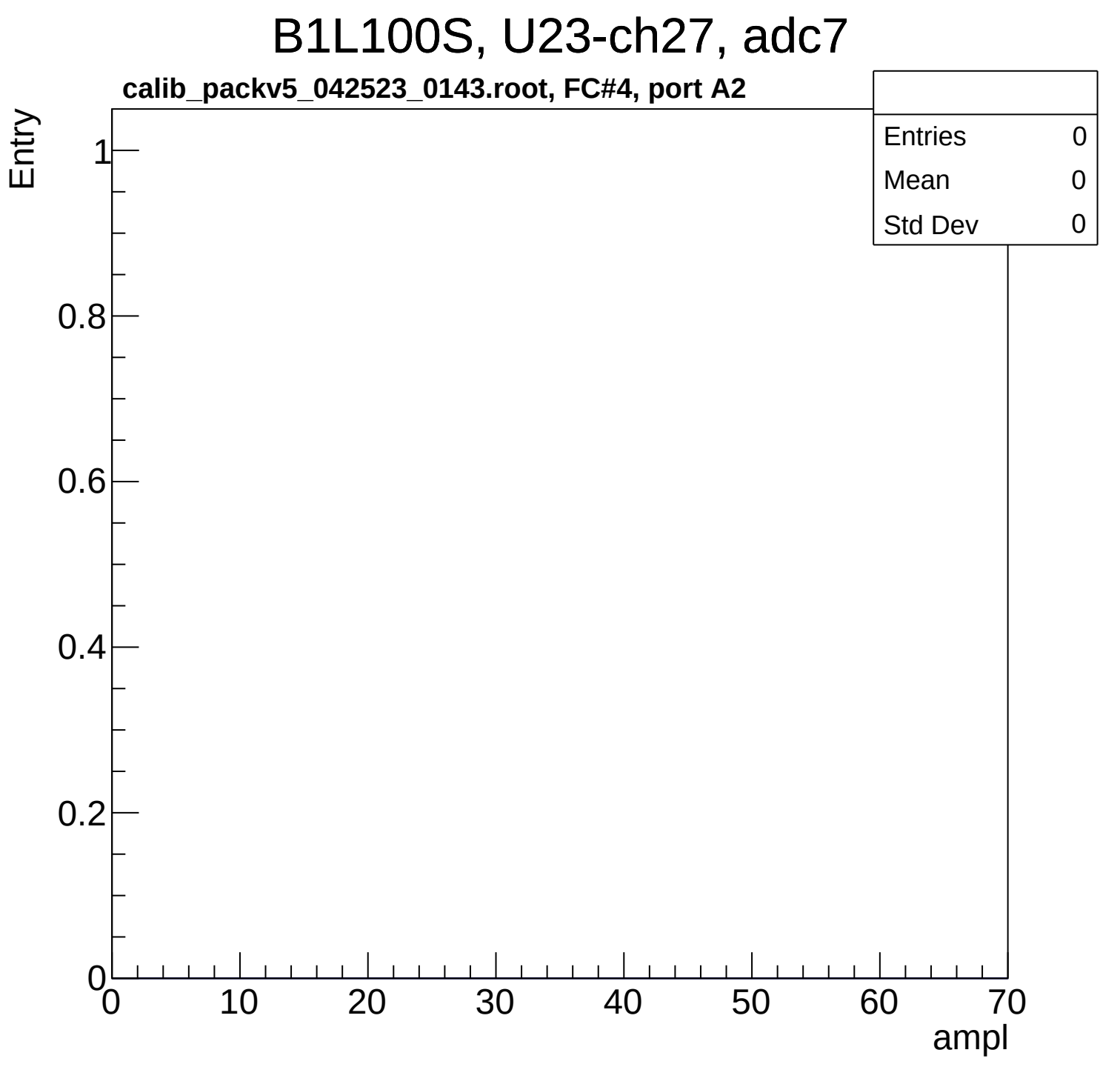

B1L100S, U23-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.6
Std Dev 12.14
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch27, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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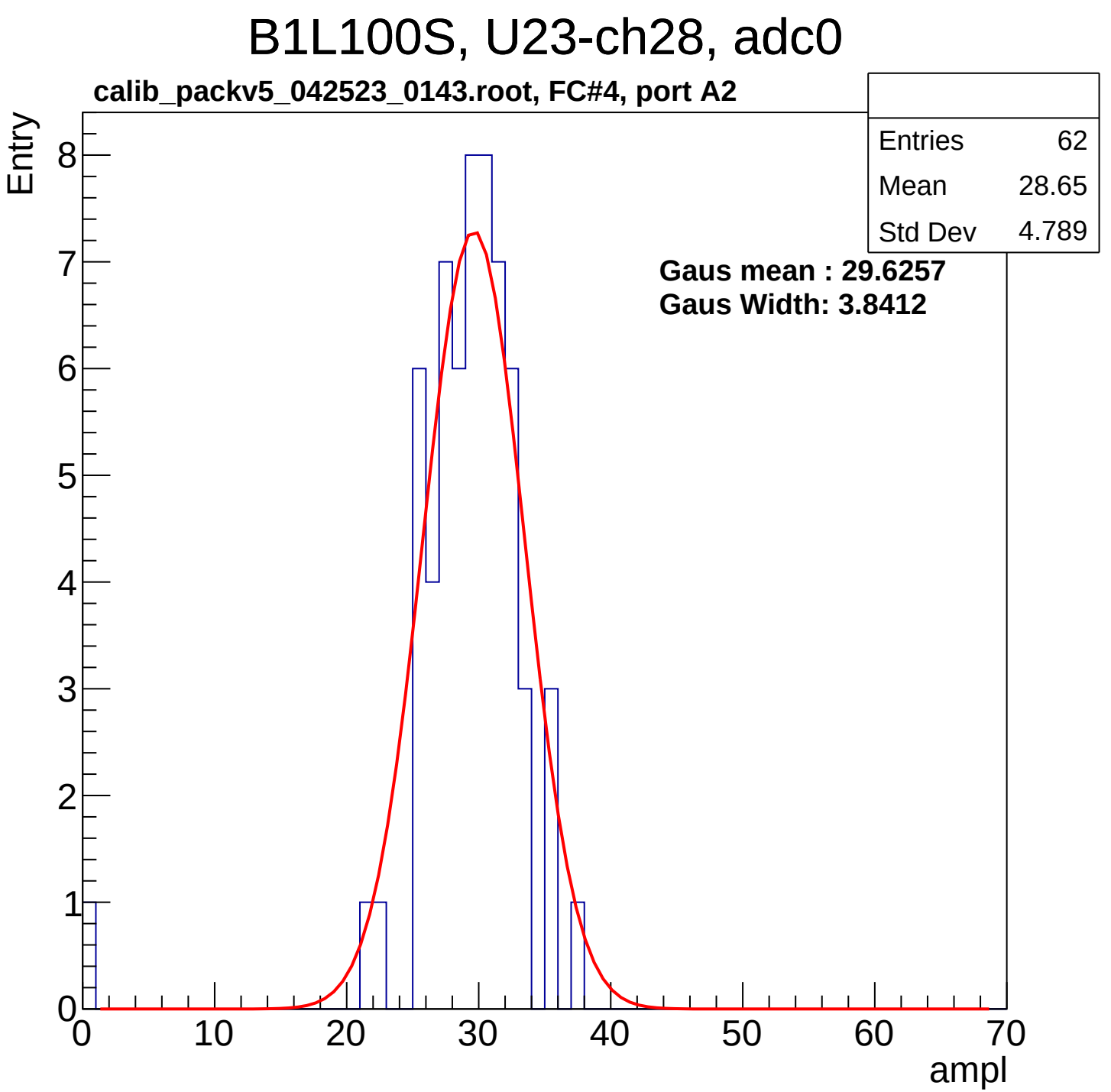
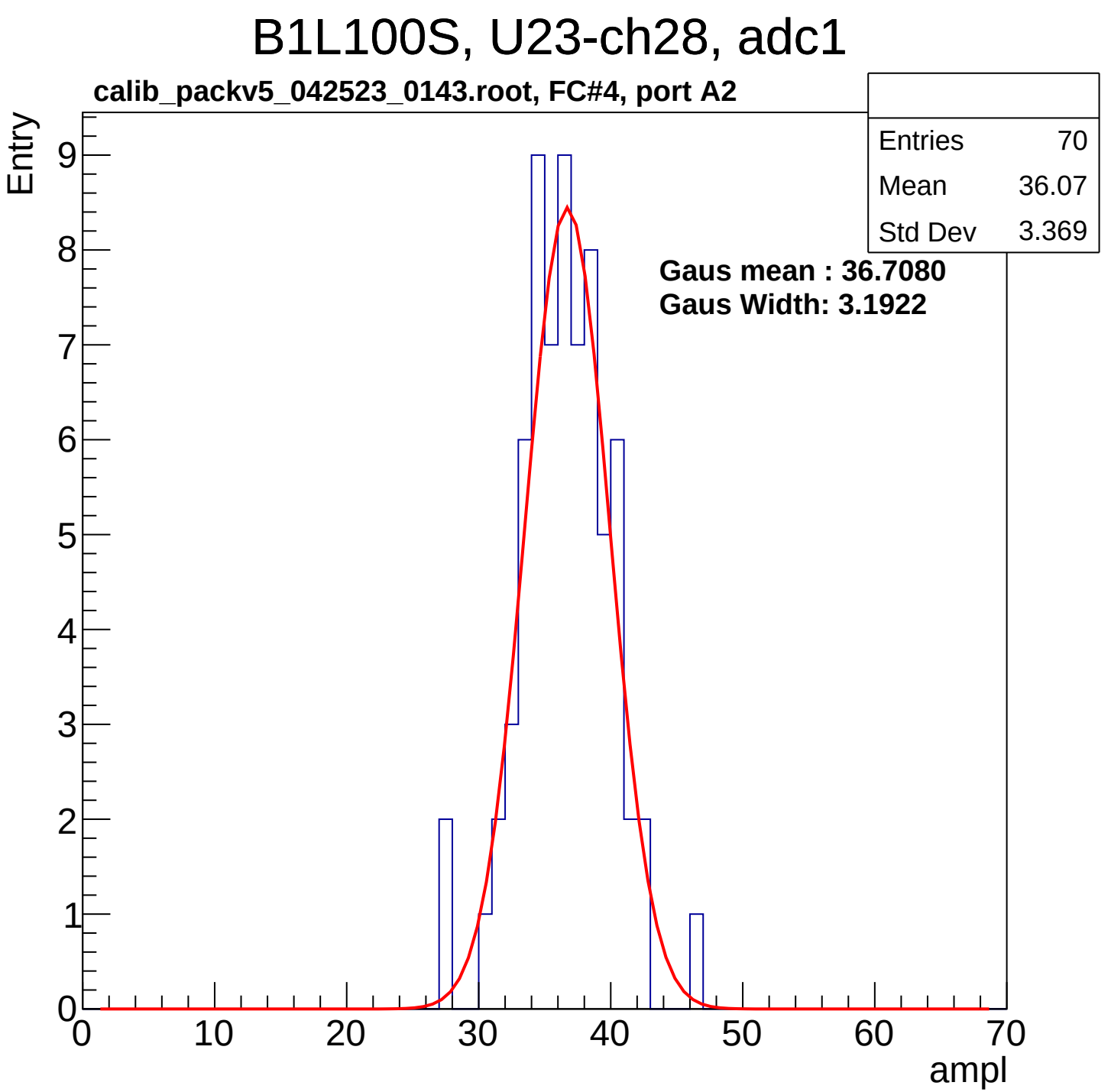
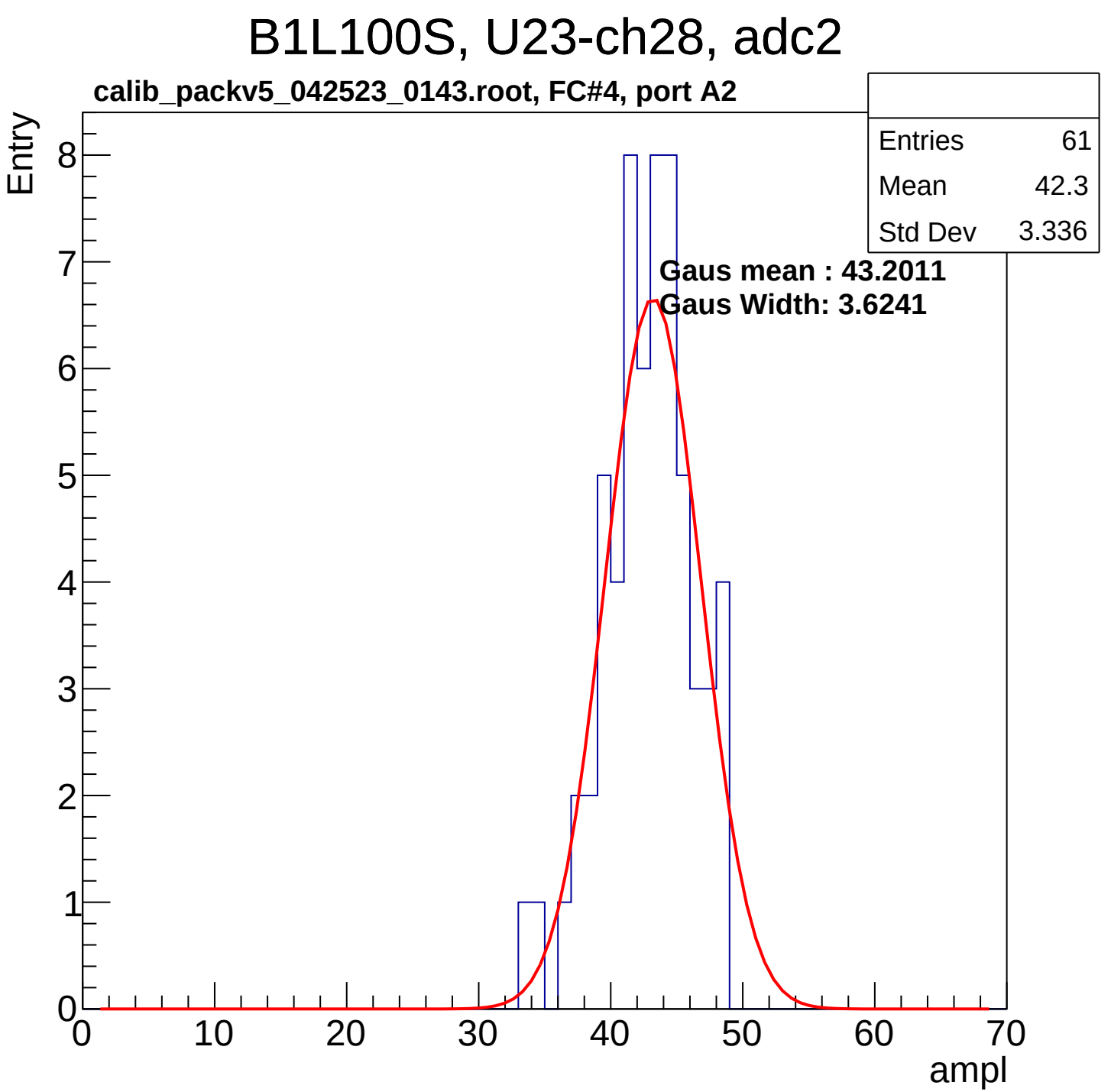
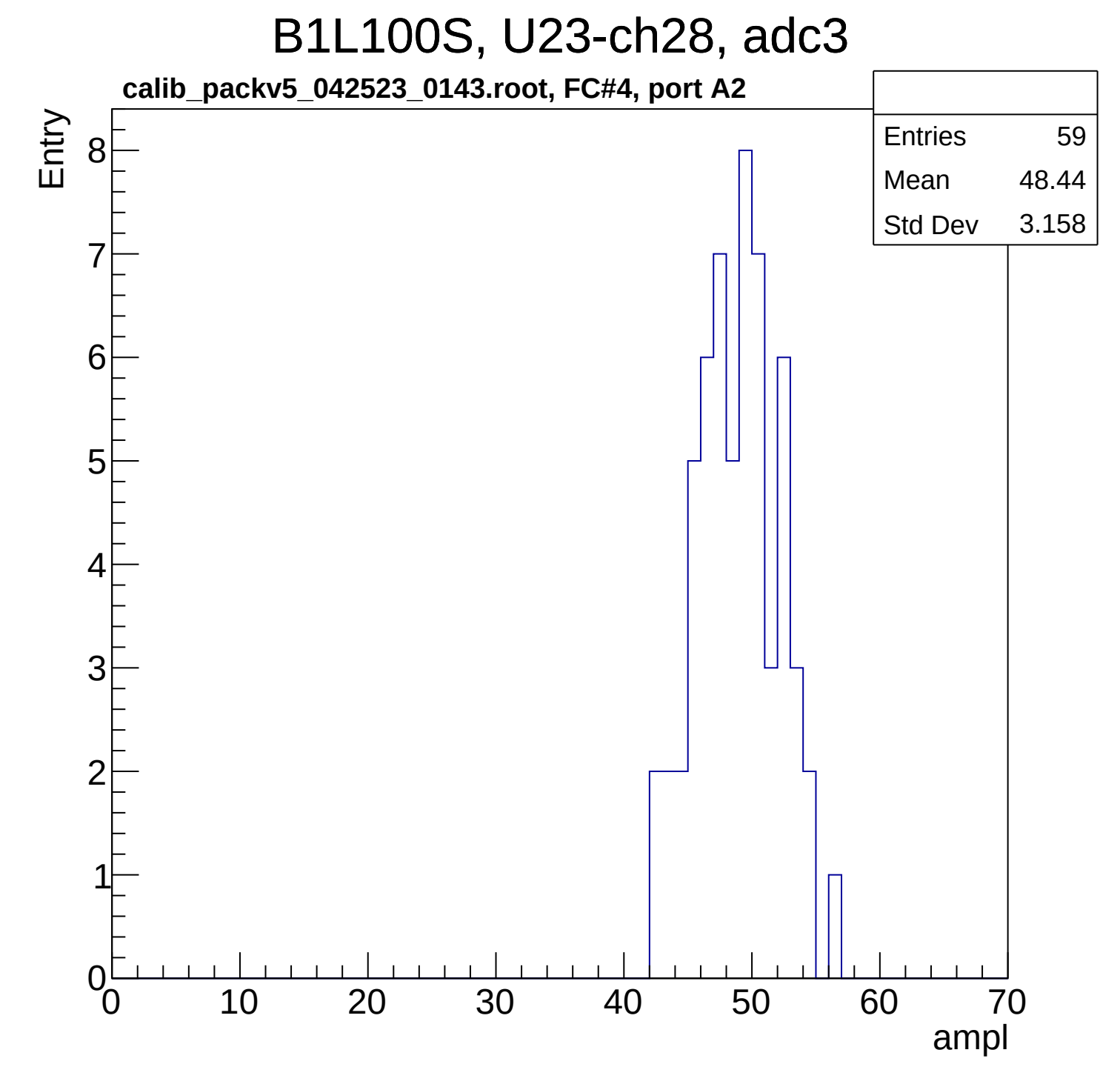
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.65
Std Dev 4.789
Gaus mean : 29.6257
Gaus Width: 3.8412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.07
Std Dev 3.369
Gaus mean : 36.7080
Gaus Width: 3.1922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.3
Std Dev 3.336
Gaus mean : 43.2011
Gaus Width: 3.6241
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch28, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 48.44
Std Dev 3.158
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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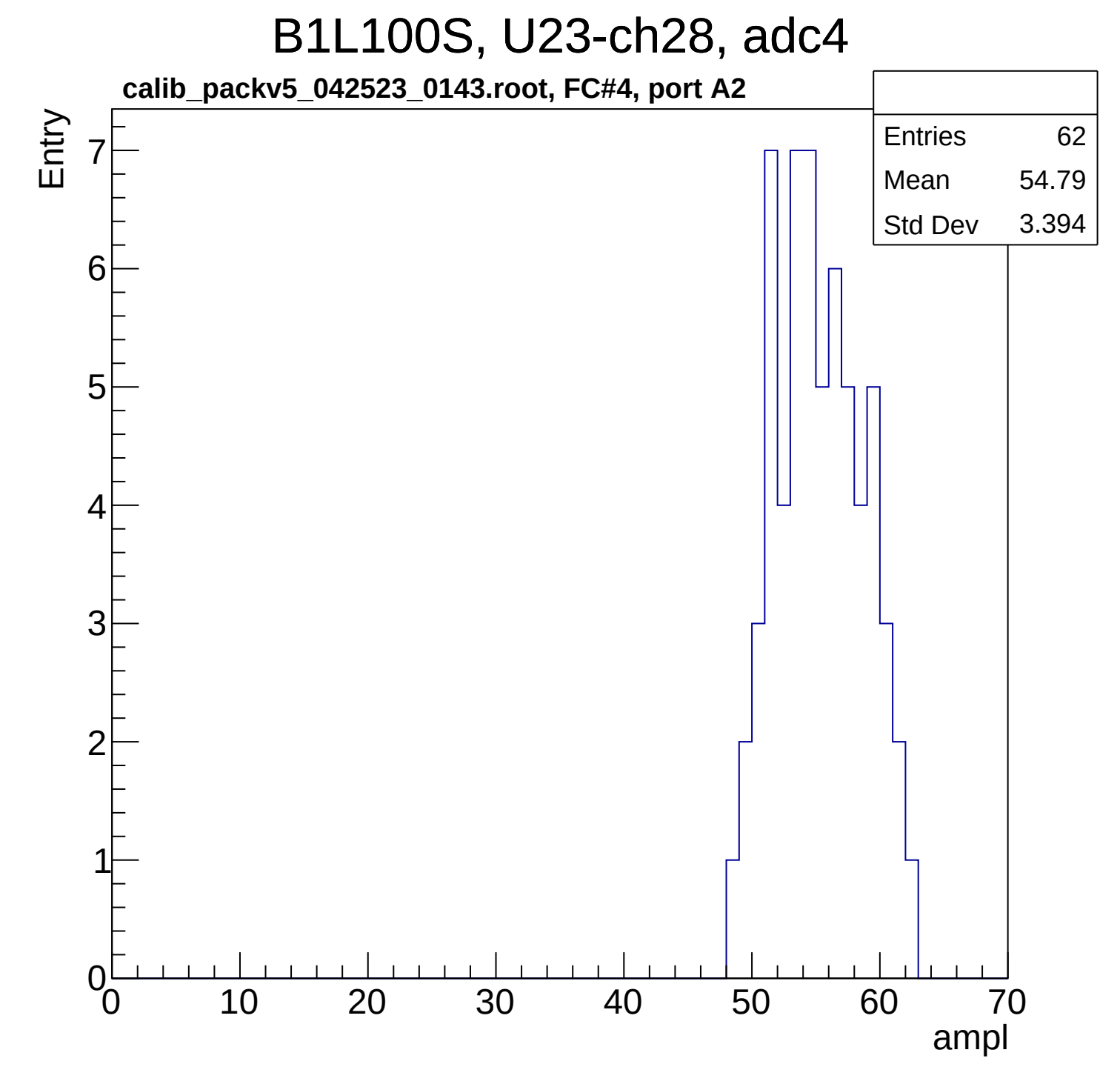
B1L100S, U23-ch28, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 54.79
Std Dev 3.394
Entry
ampl
0
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2
3
4
5
6
7
0
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20
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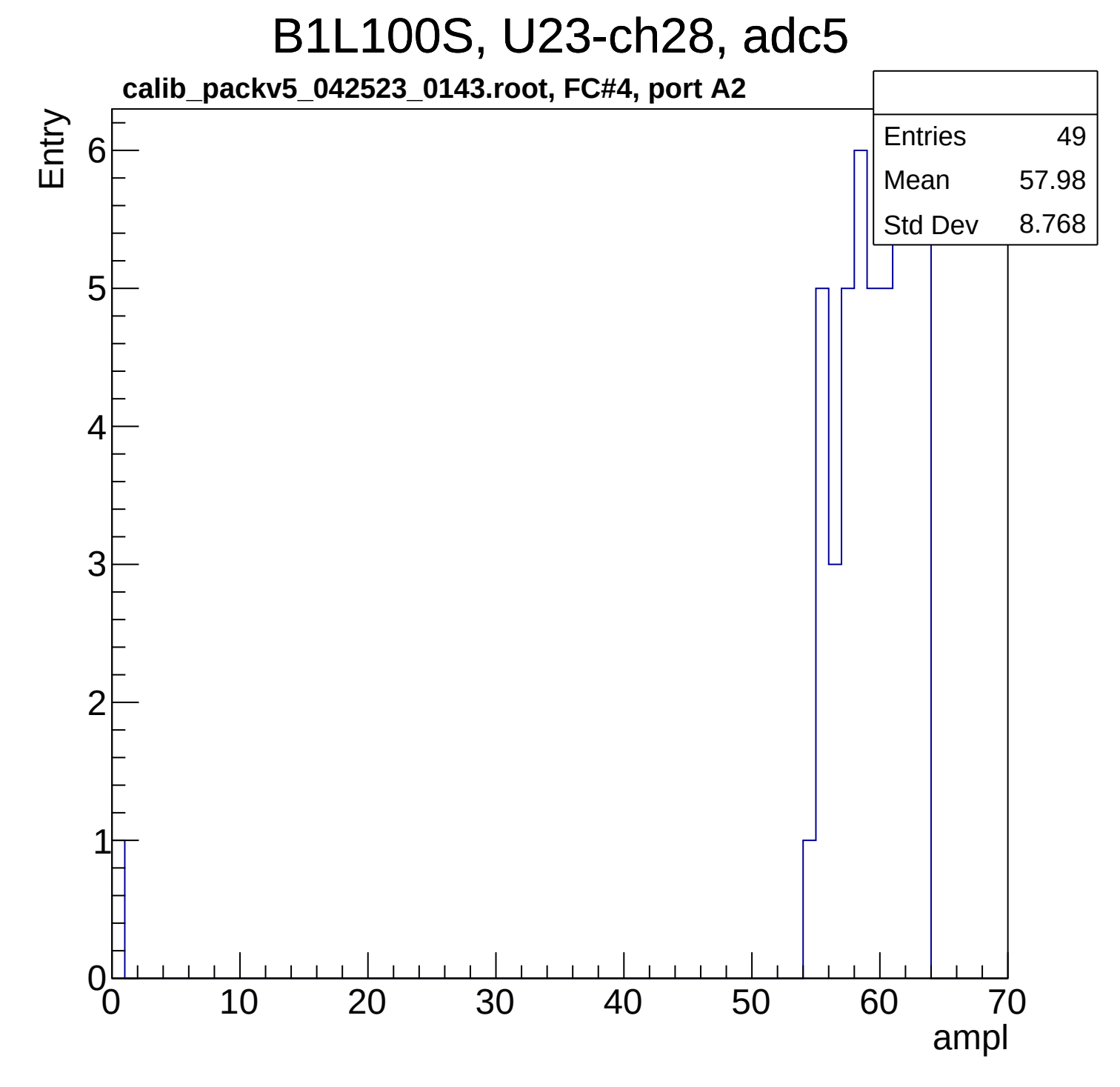
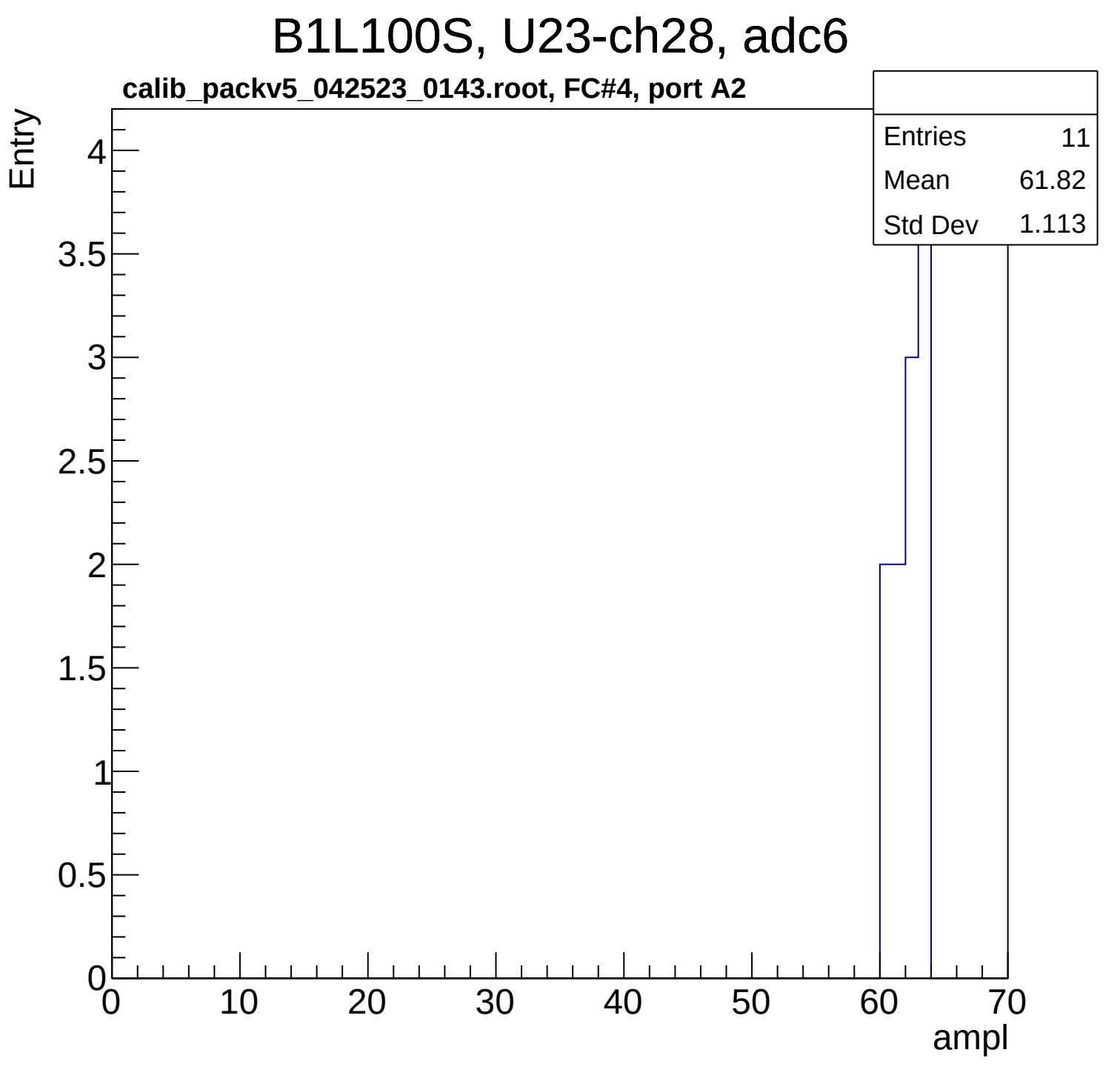
40
50
60
70


B1L100S, U23-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 57.98
Std Dev 8.768
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.82
Std Dev 1.113
Entry
ampl
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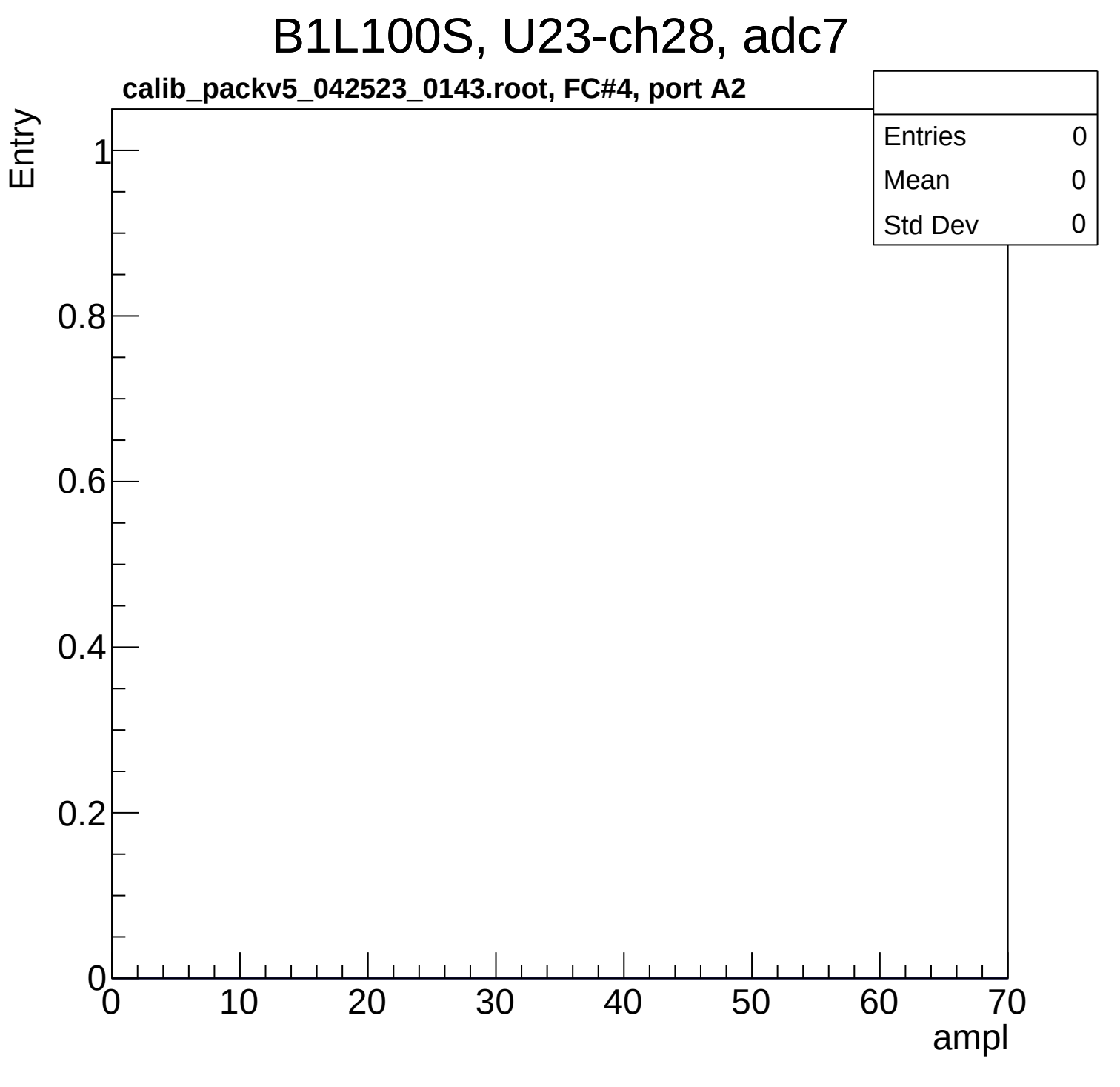
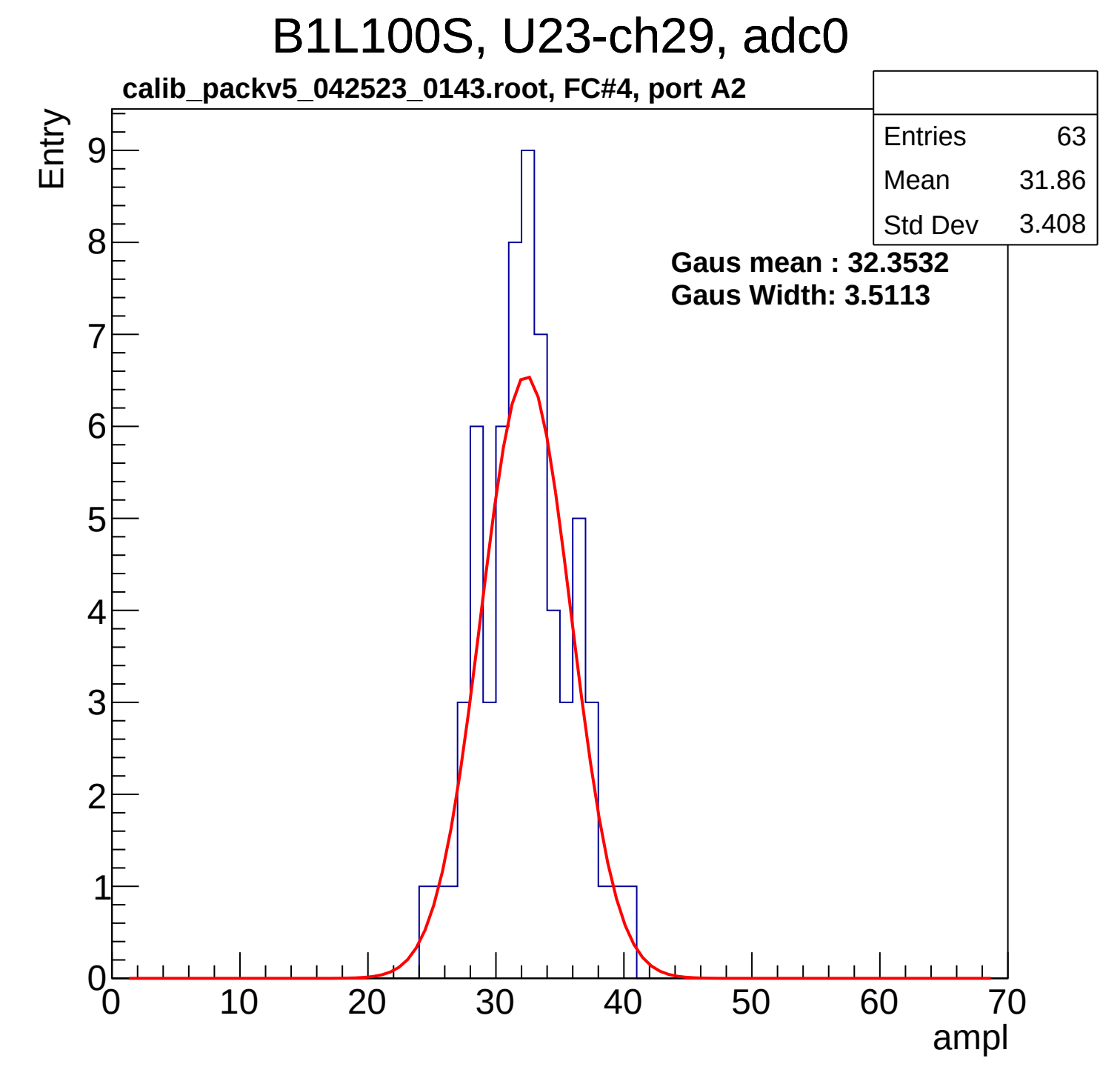
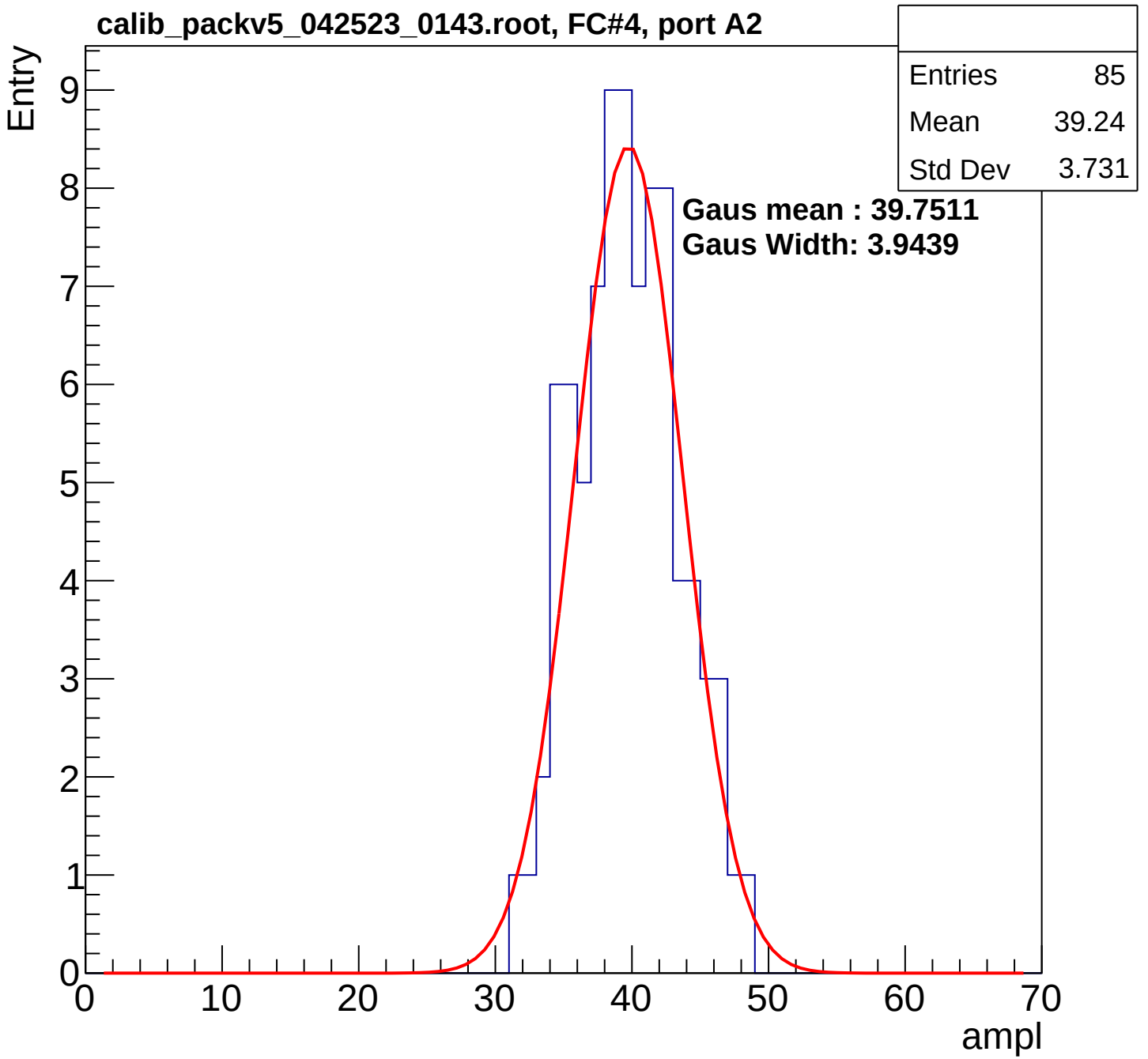
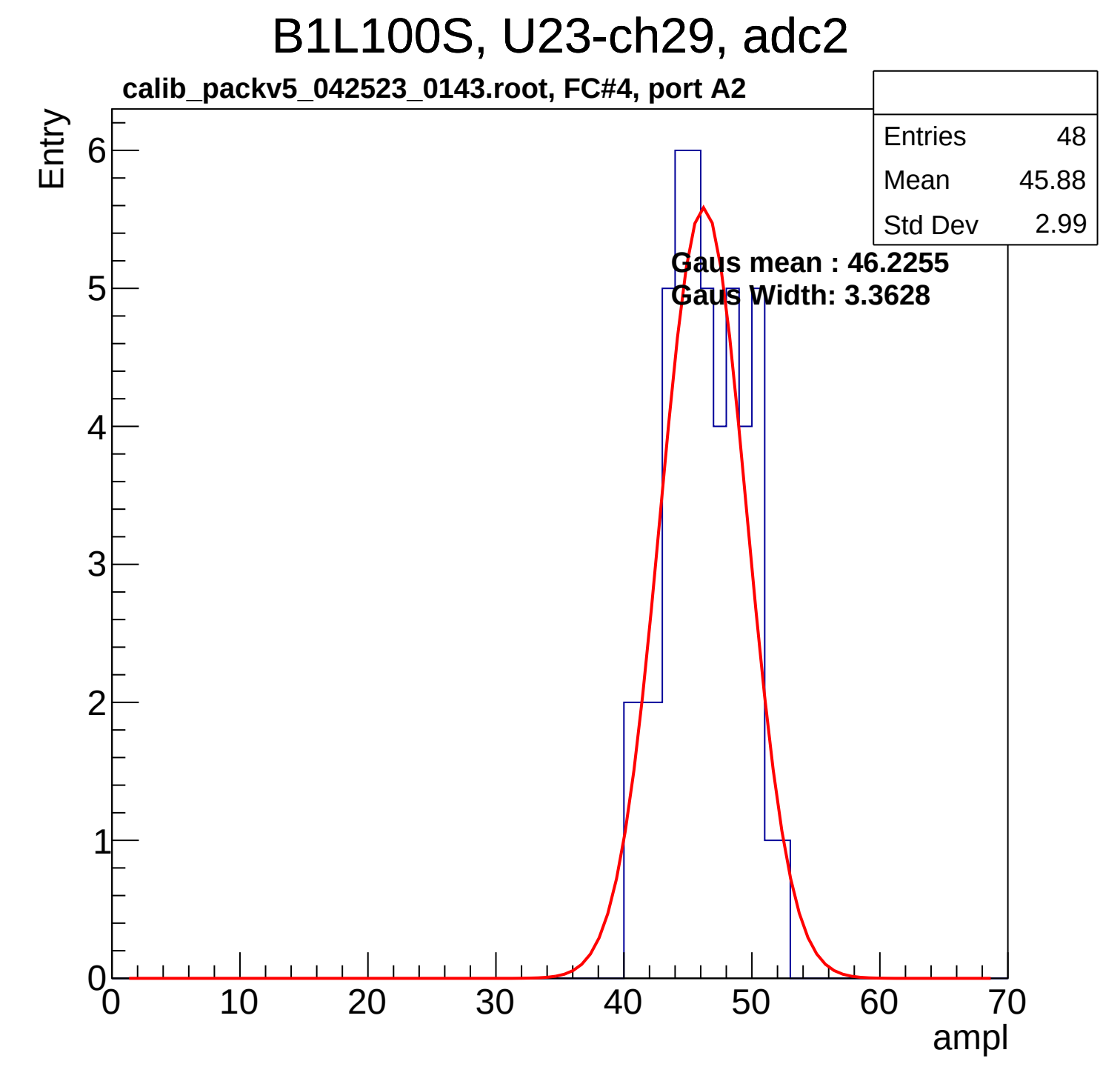
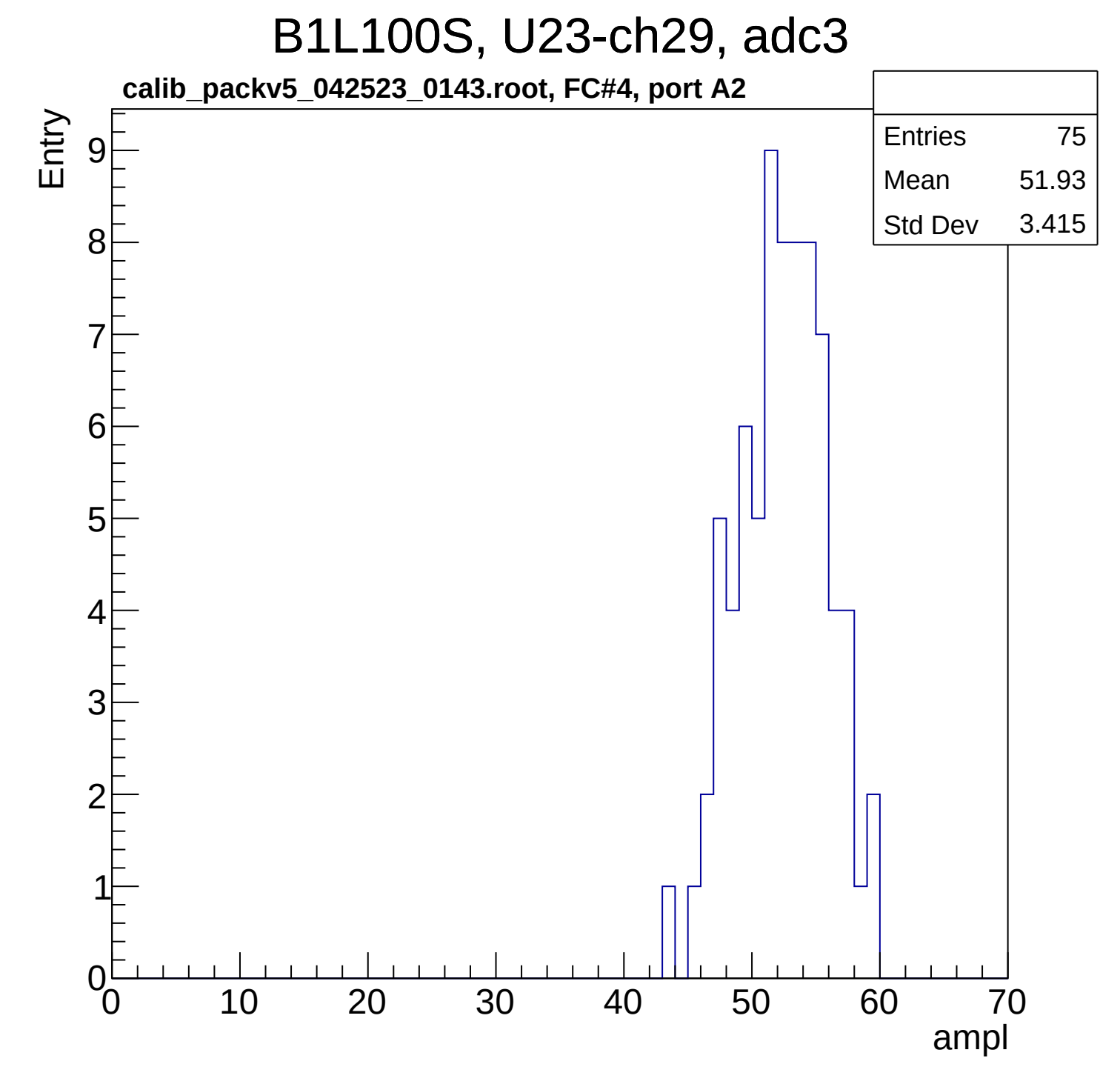
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U23-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.86
Std Dev 3.408
Gaus mean : 32.3532
Gaus Width: 3.5113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 39.24
Std Dev 3.731
Gaus mean : 39.7511
Gaus Width: 3.9439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 45.88
Std Dev 2.99
Gaus mean : 46.2255
Gaus Width: 3.3628
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch29, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 51.93
Std Dev 3.415
Entry
ampl
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1
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3
4
5
6
7
8
9
0
10
20
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60
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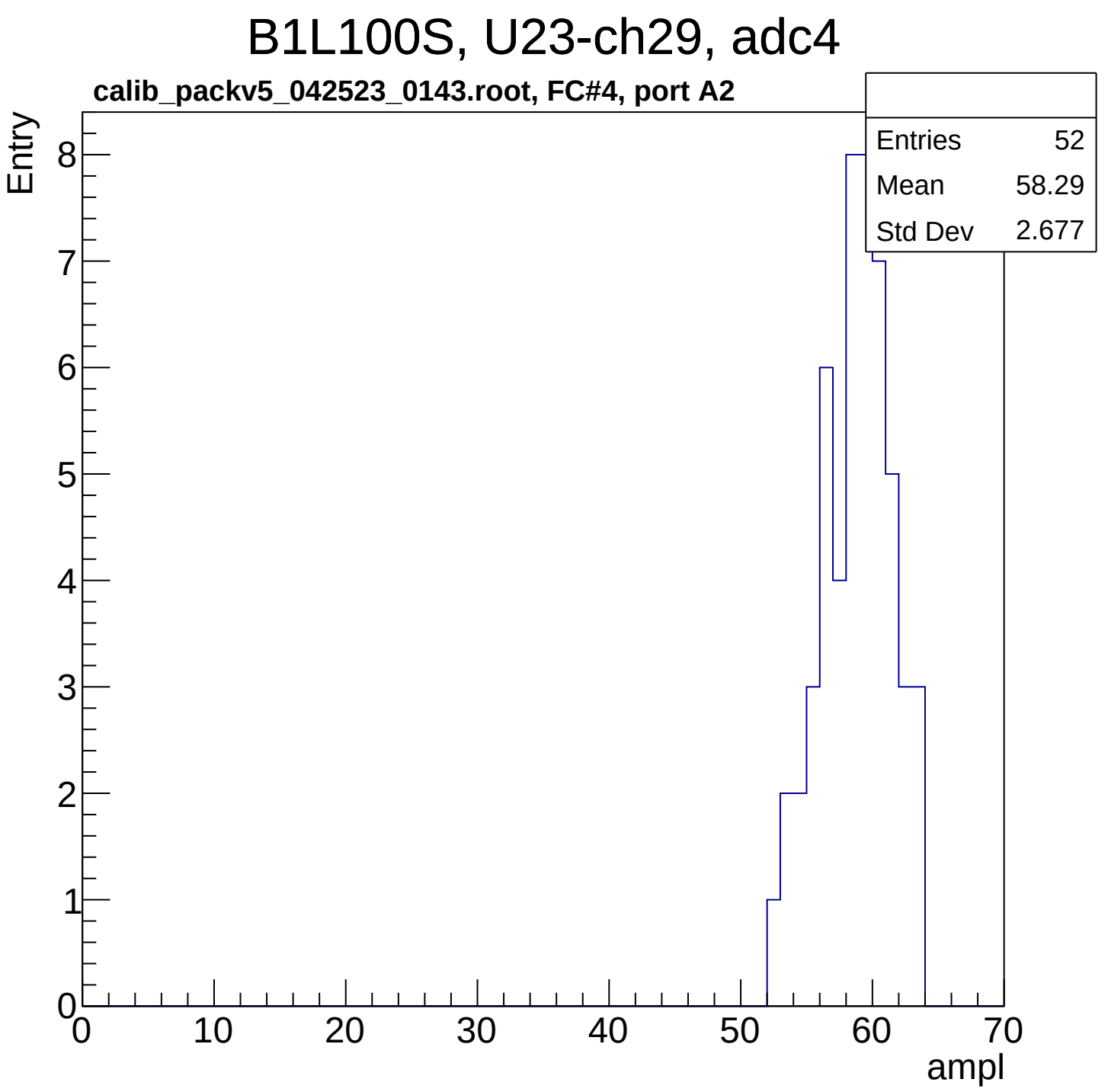
B1L100S, U23-ch29, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.29
Std Dev 2.677
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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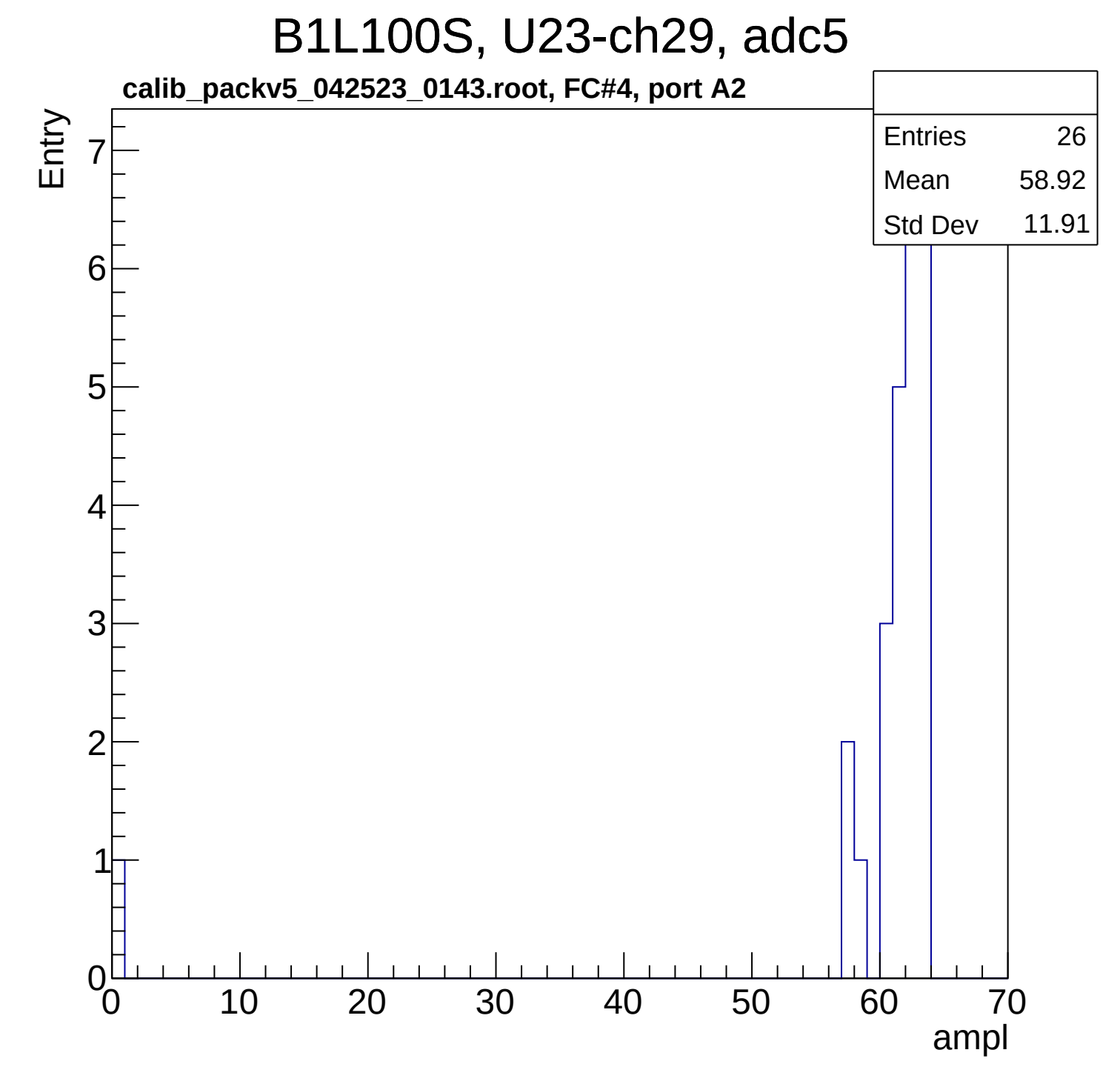
40
50
60
70


B1L100S, U23-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.92
Std Dev 11.91
Entry
ampl
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1
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3
4
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6
7
0
10
20
30
40
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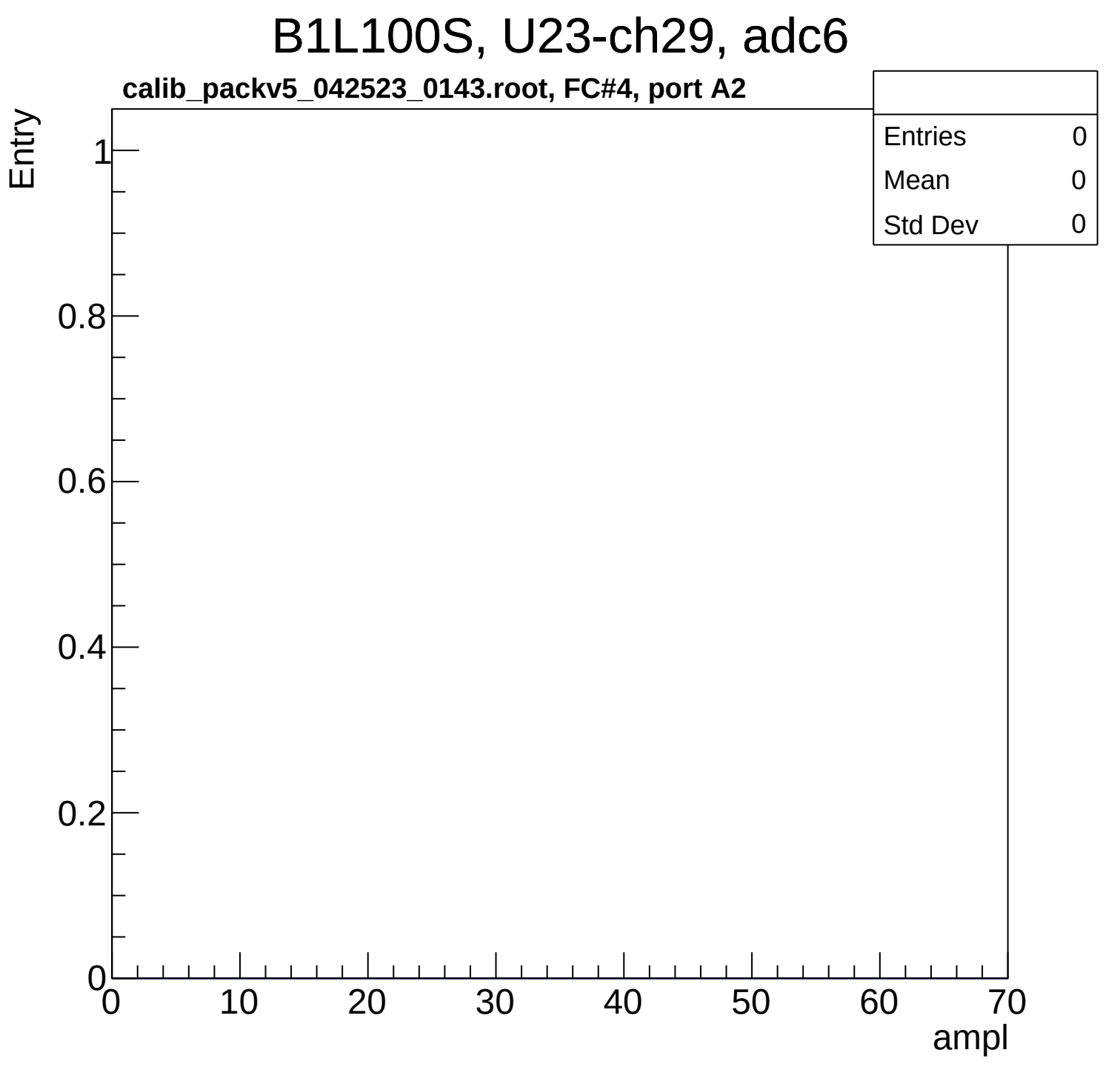
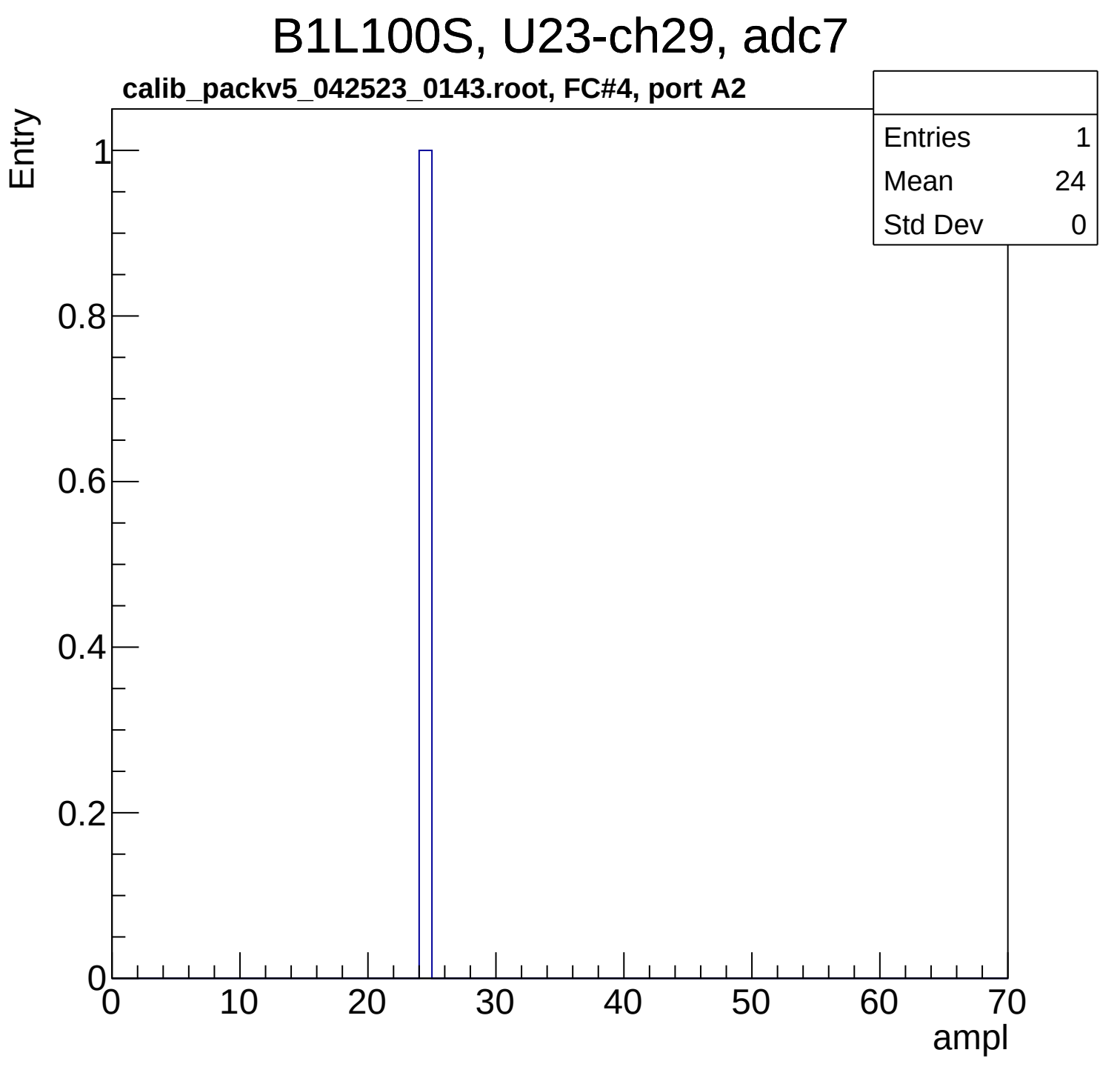
60
70


B1L100S, U23-ch29, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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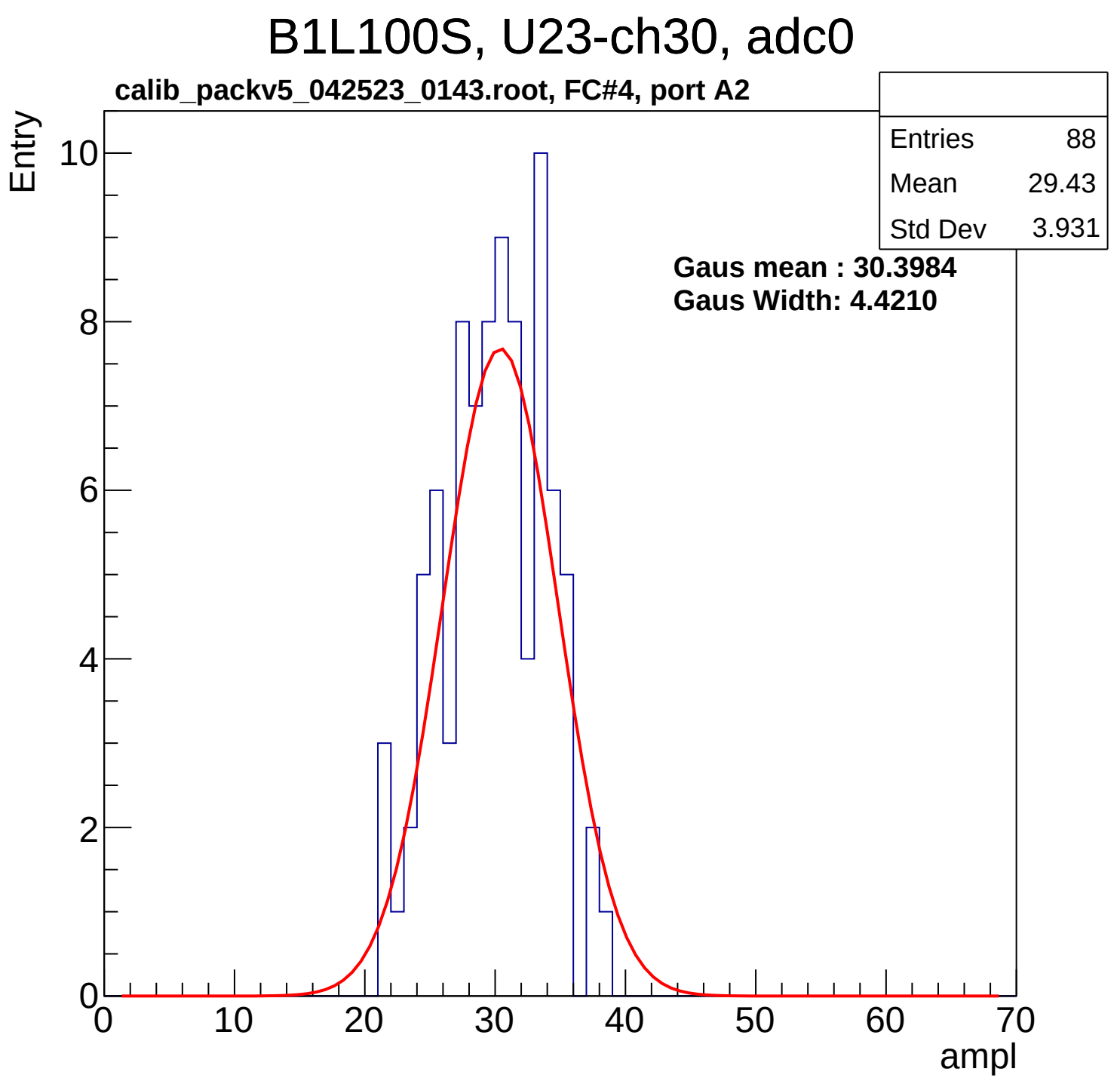
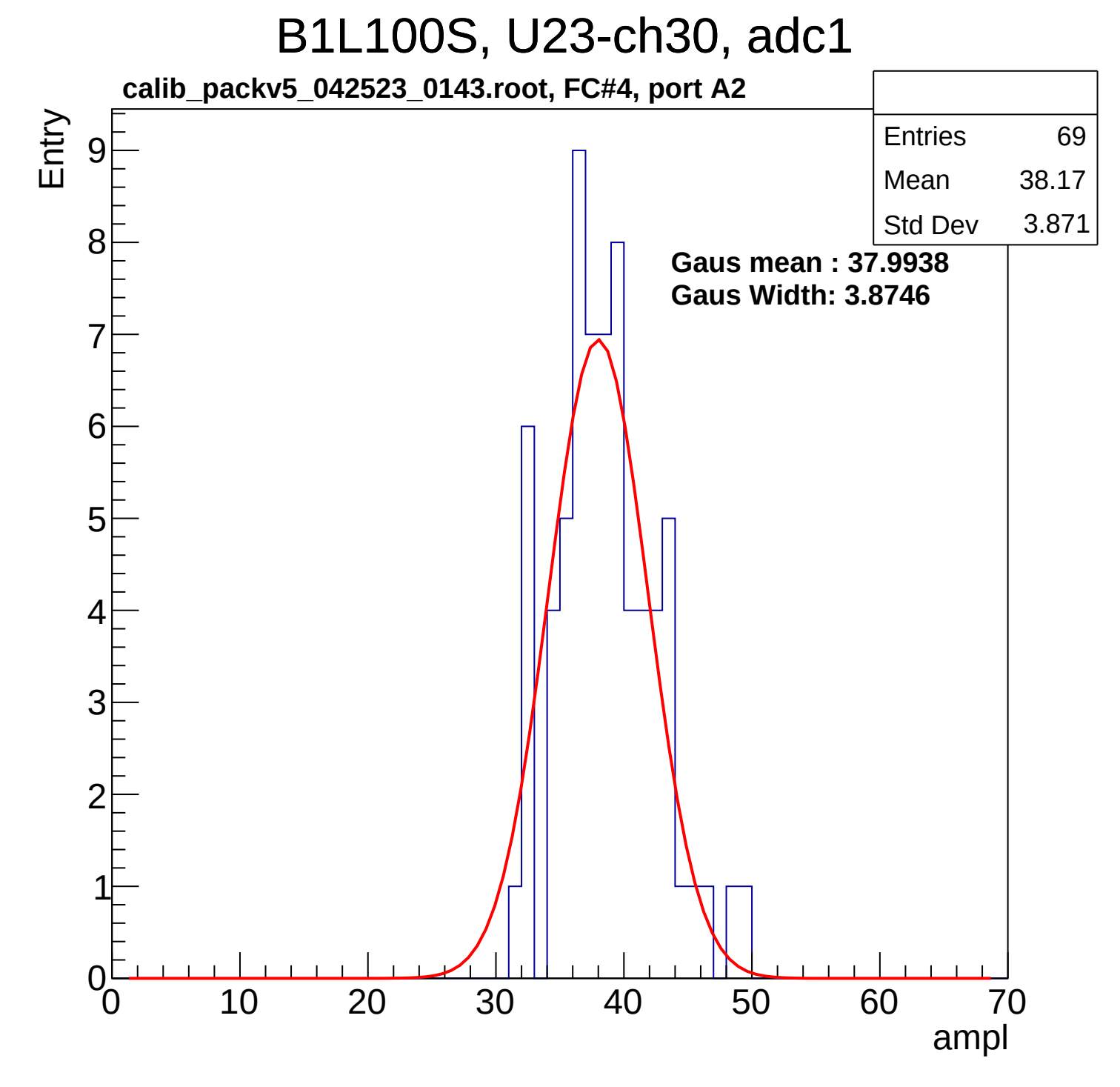
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 29.43
Std Dev 3.931
Gaus mean : 30.3984
Gaus Width: 4.4210
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 38.17
Std Dev 3.871
Gaus mean : 37.9938
Gaus Width: 3.8746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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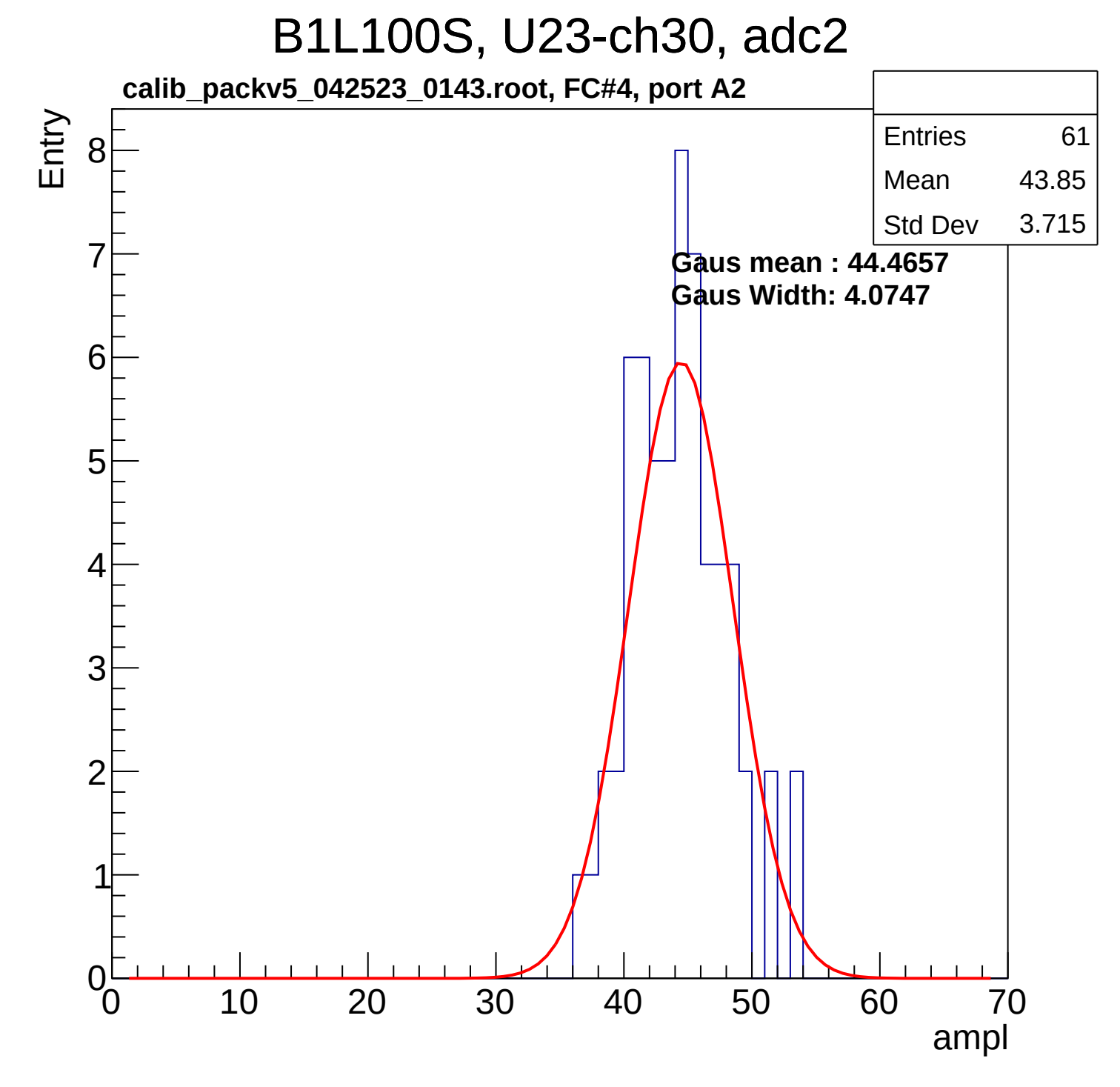
B1L100S, U23-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.85
Std Dev 3.715
Gaus mean : 44.4657
Gaus Width: 4.0747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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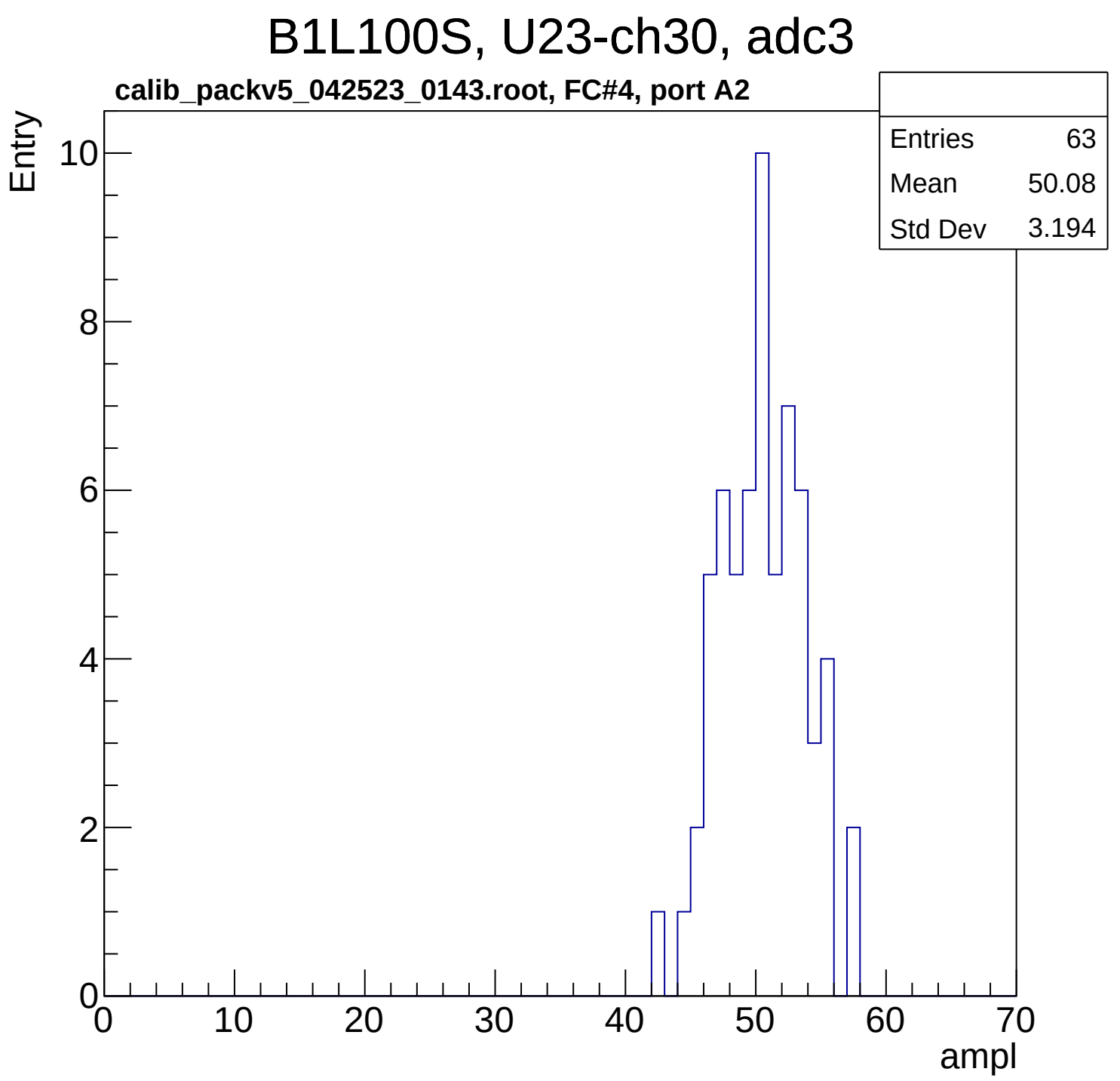
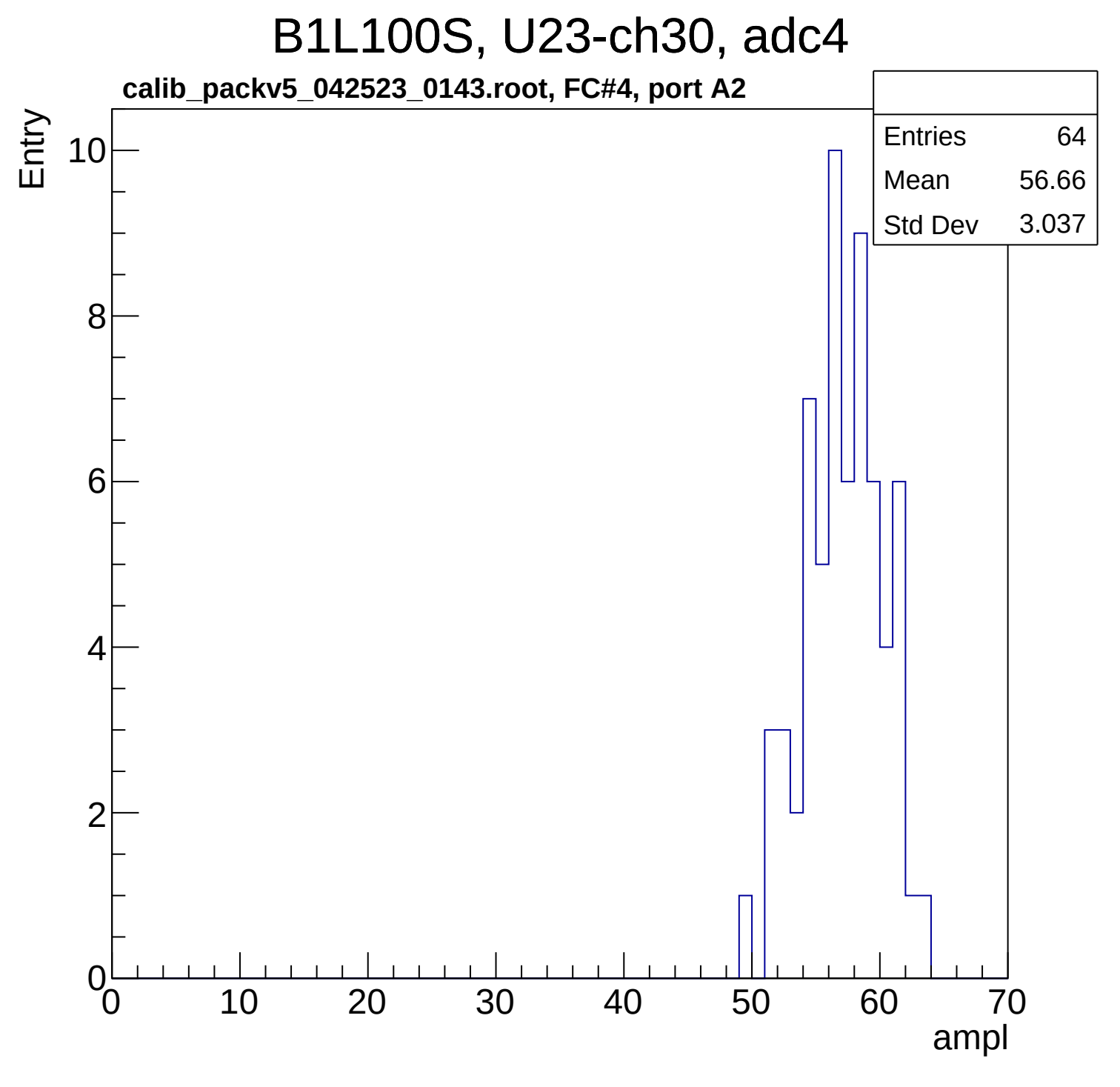
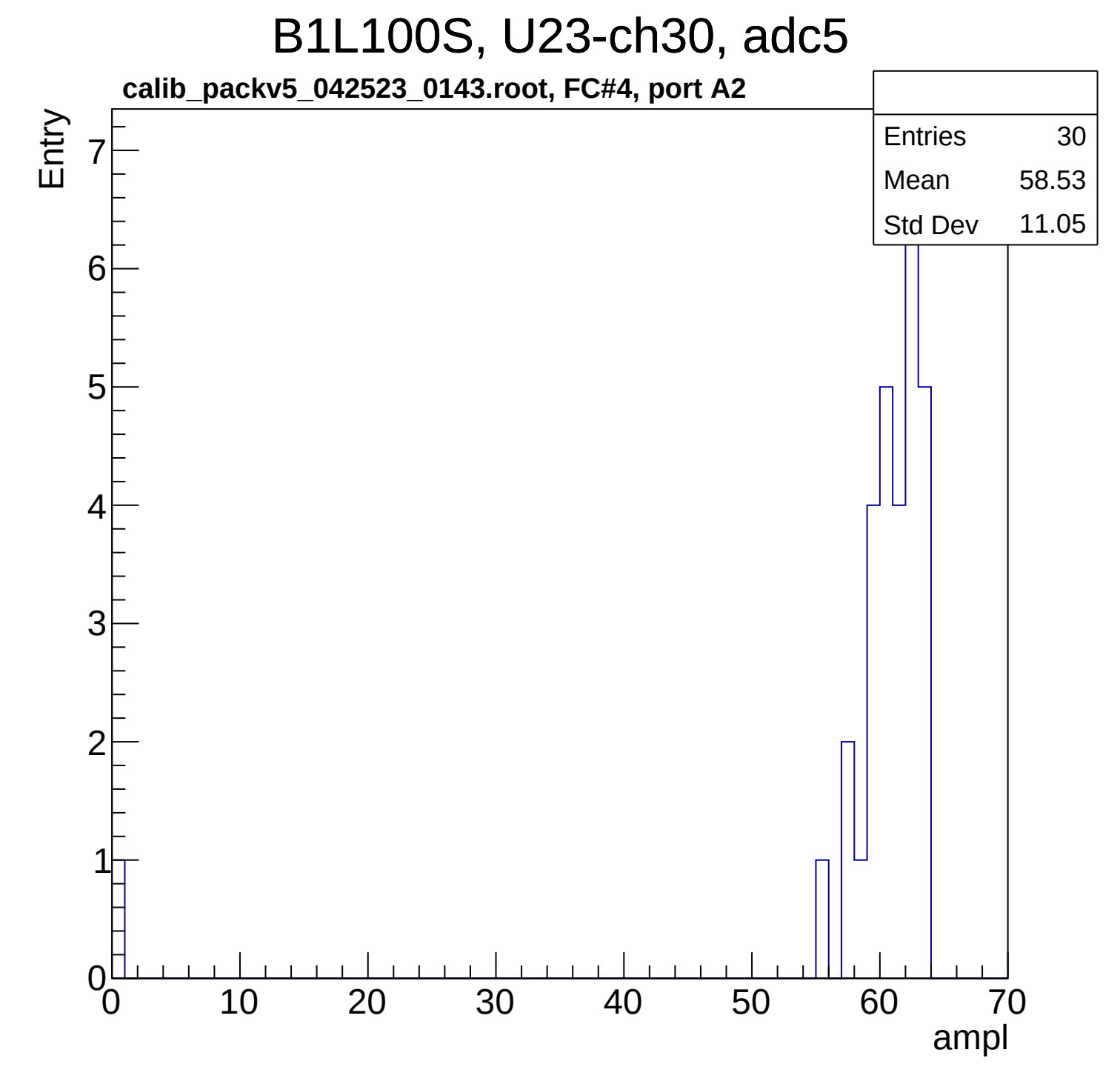

B1L100S, U23-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.08
Std Dev 3.194
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 56.66
Std Dev 3.037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.53
Std Dev 11.05
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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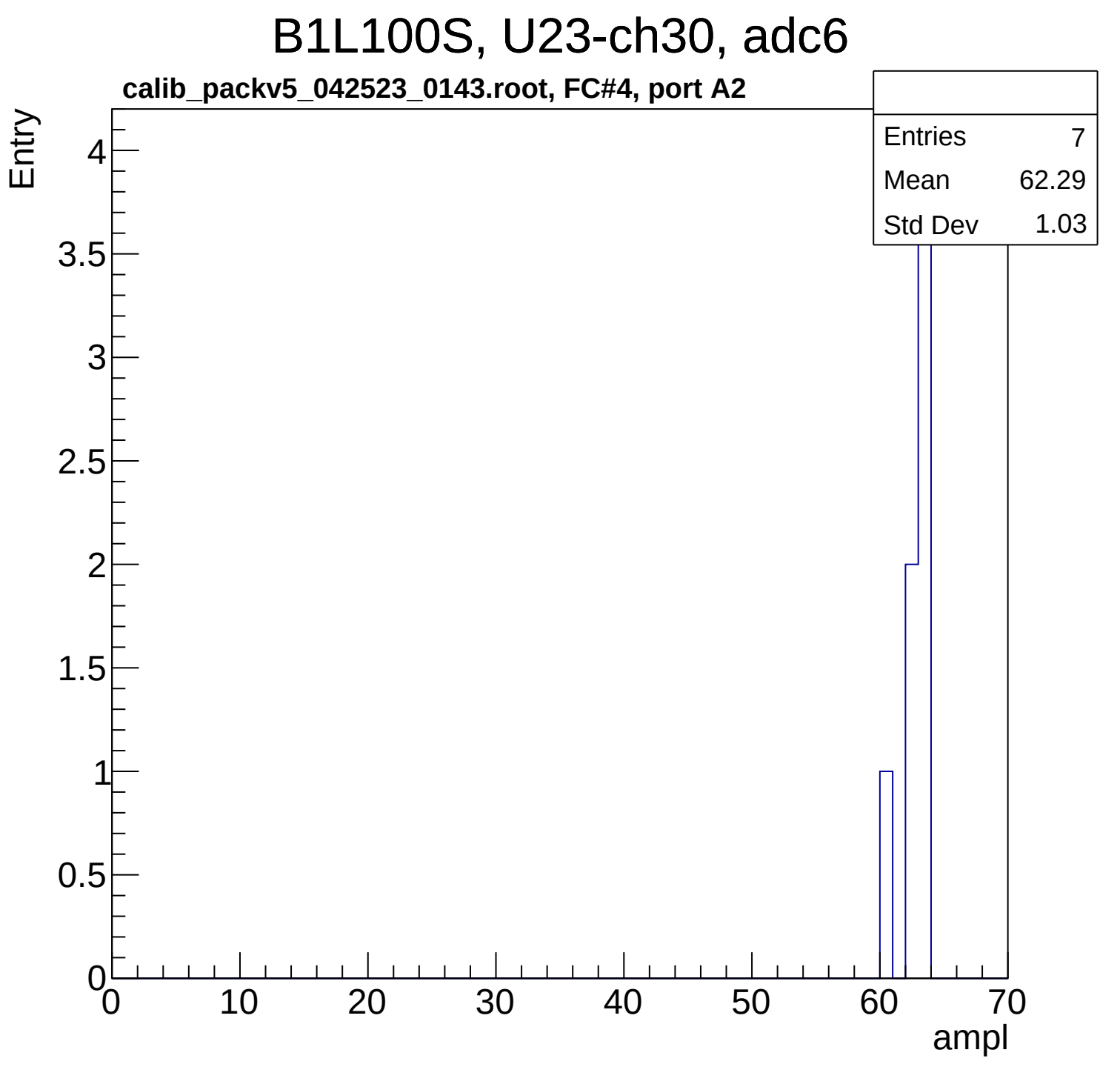
B1L100S, U23-ch30, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.29
Std Dev 1.03
Entry
ampl
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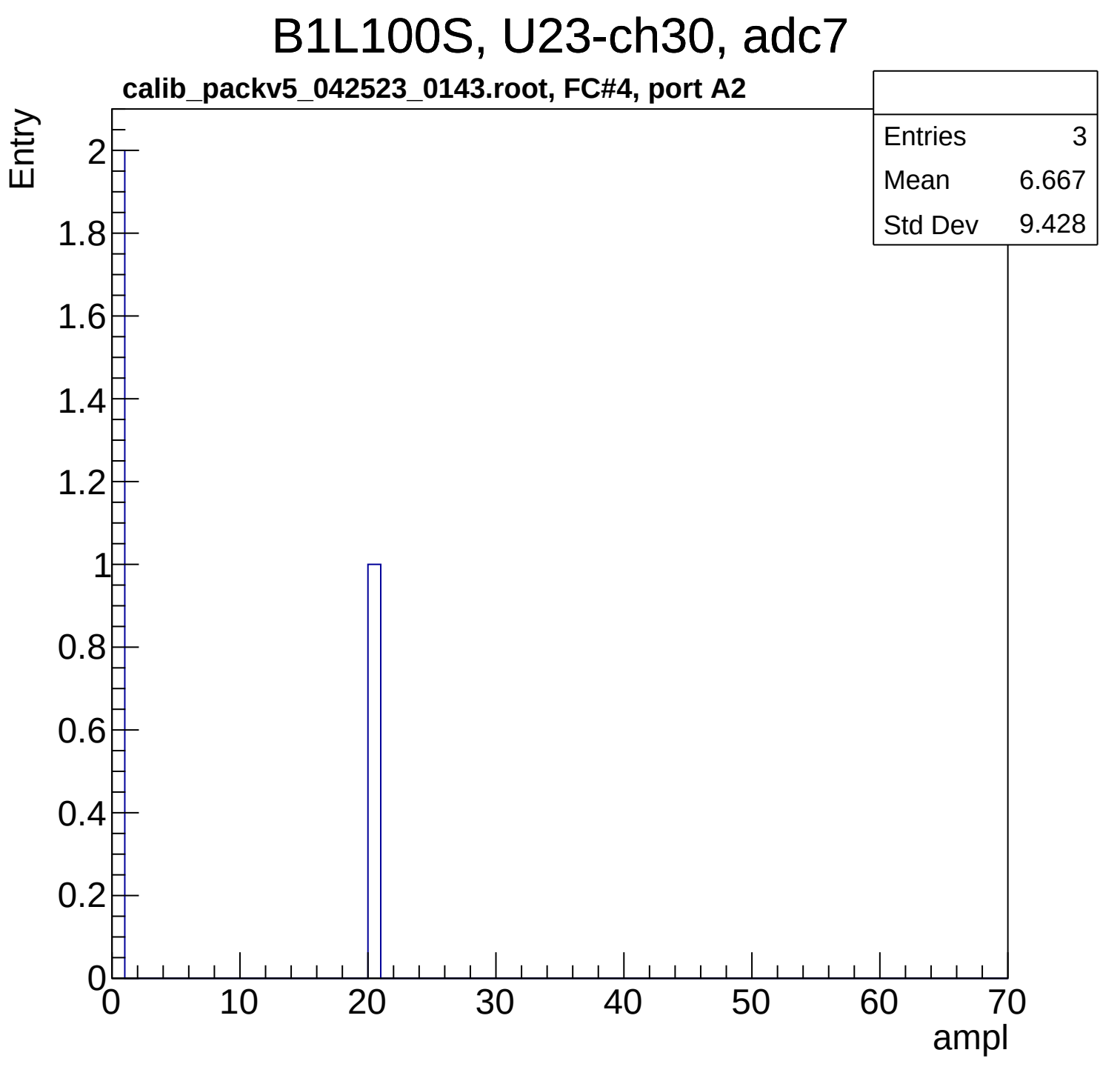
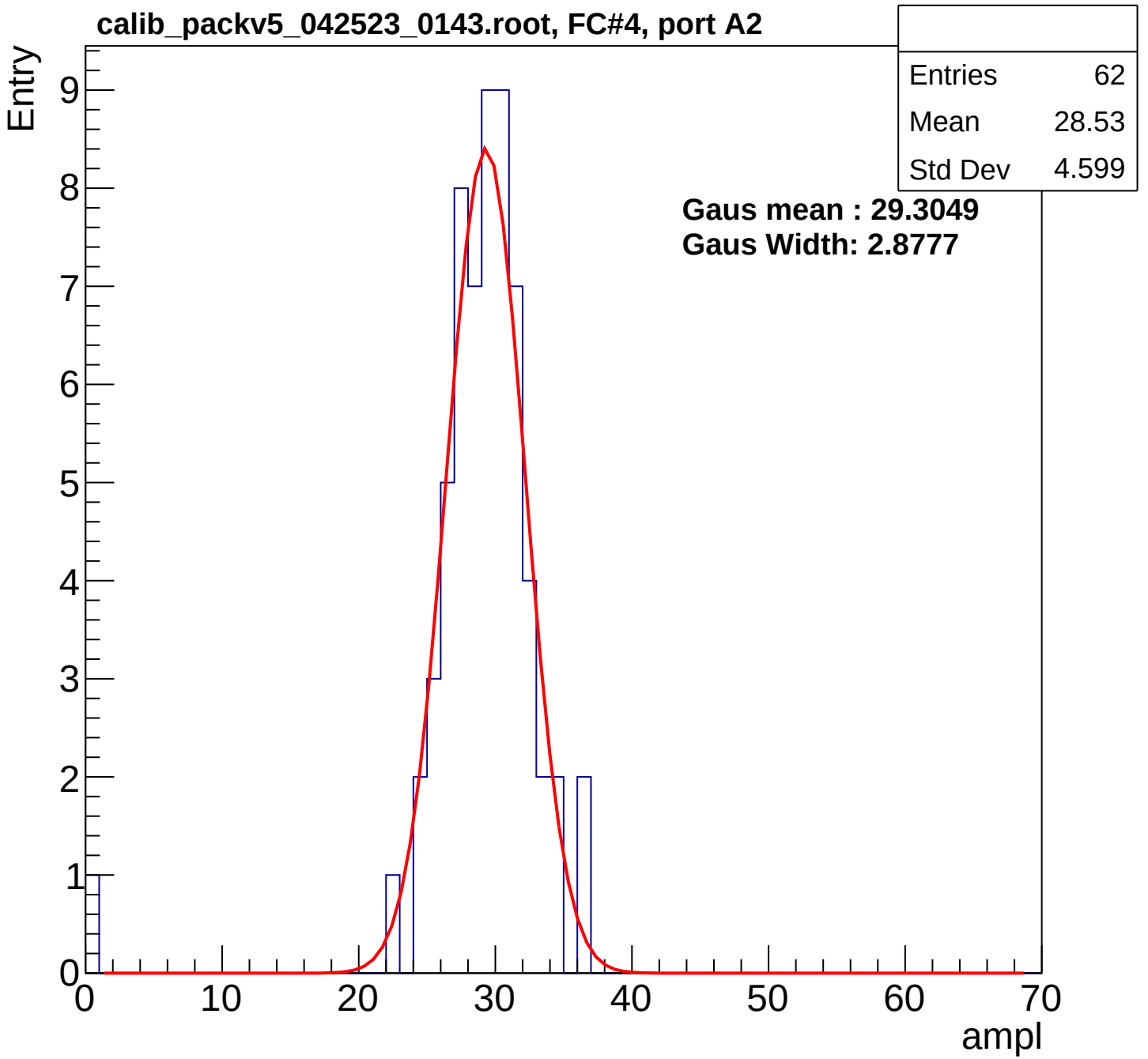
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U23-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.53
Std Dev 4.599
Gaus mean : 29.3049
Gaus Width: 2.8777
Entry
ampl
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2
3
4
5
6
7
8
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0
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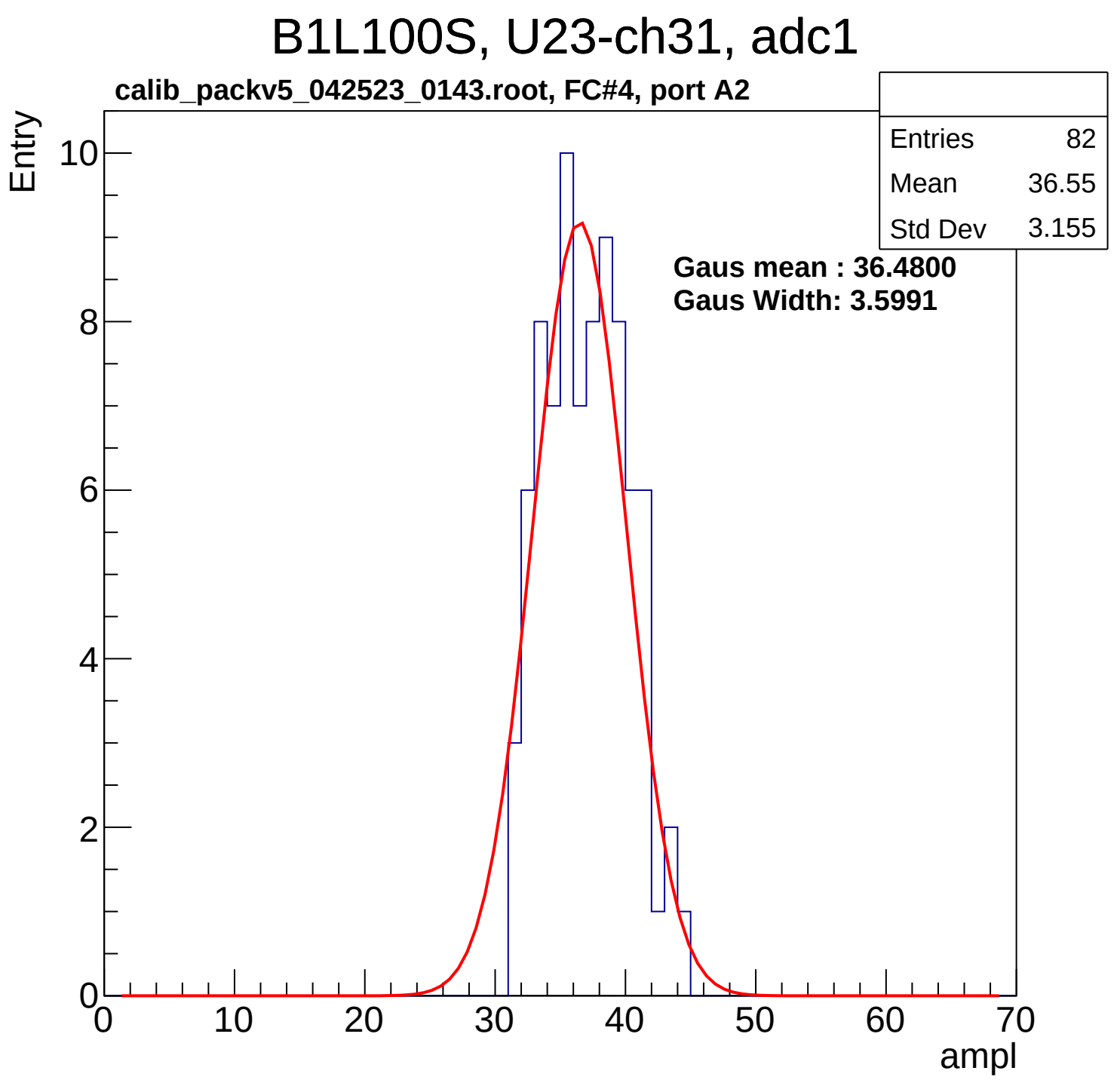
B1L100S, U23-ch31, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 36.55
Std Dev 3.155
Gaus mean : 36.4800
Gaus Width: 3.5991
Entry
ampl
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2
4
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8
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0
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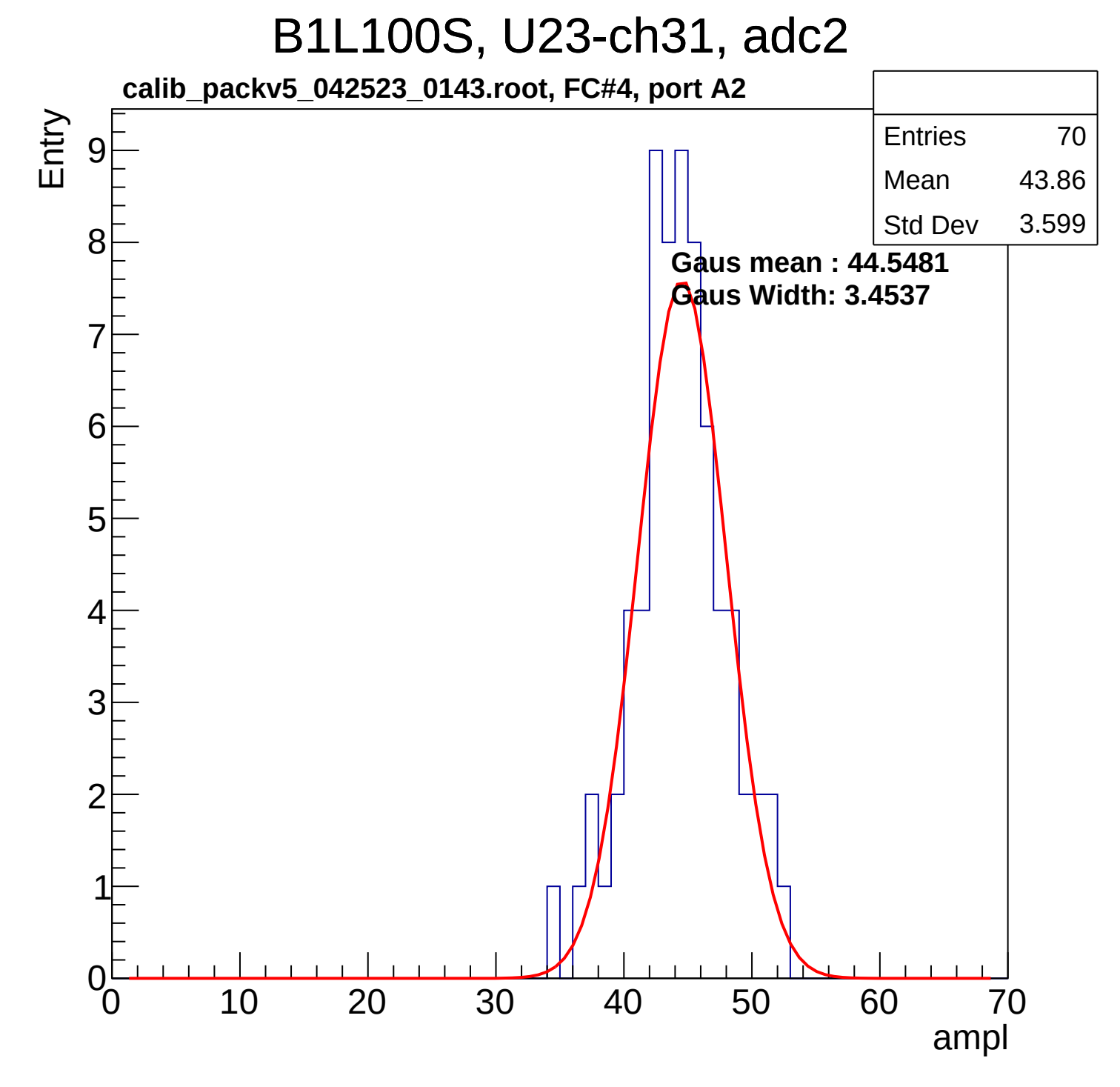
B1L100S, U23-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 43.86
Std Dev 3.599
Gaus mean : 44.5481
Gaus Width: 3.4537
Entry
ampl
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2
3
4
5
6
7
8
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0
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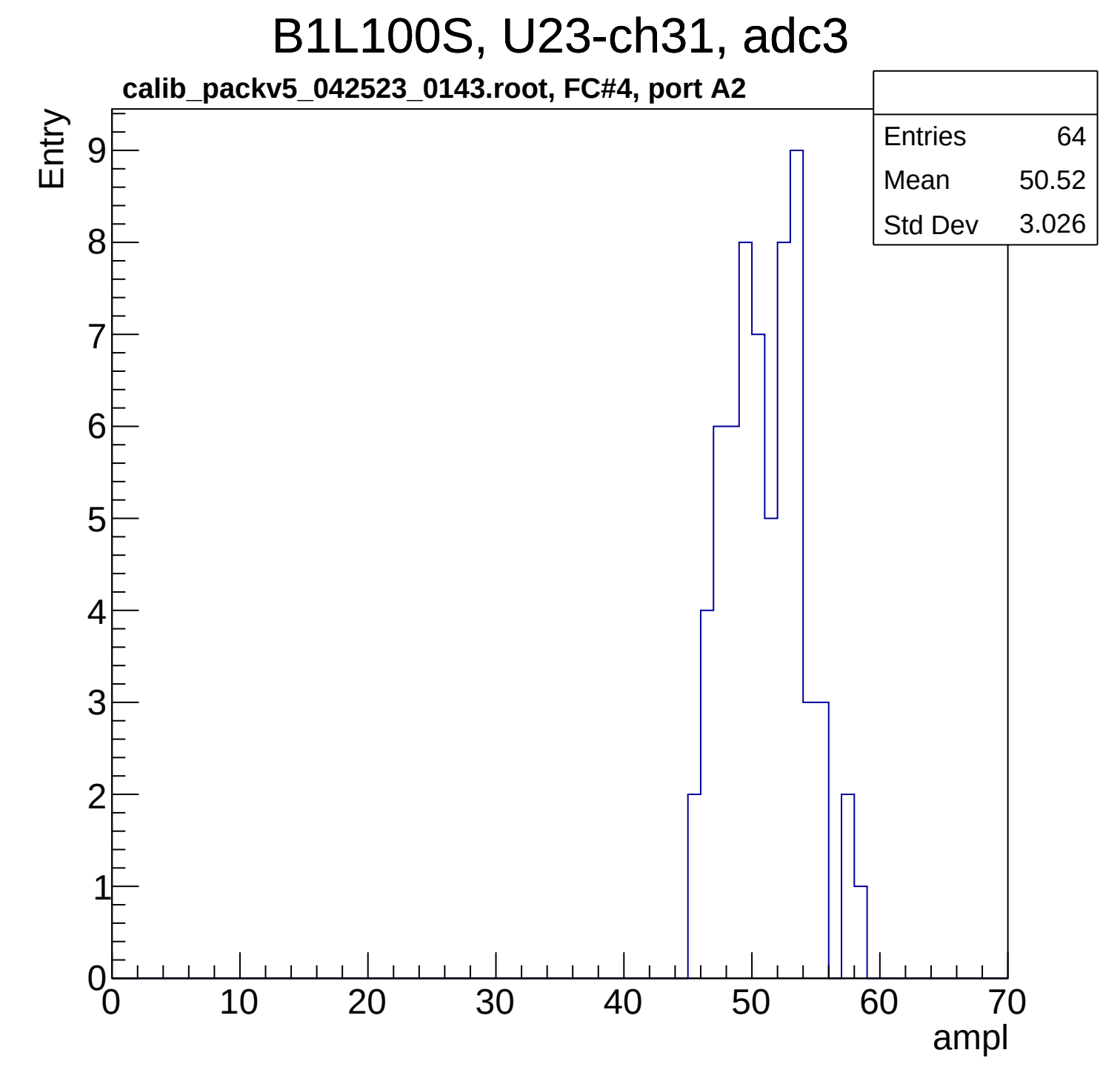
B1L100S, U23-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.52
Std Dev 3.026
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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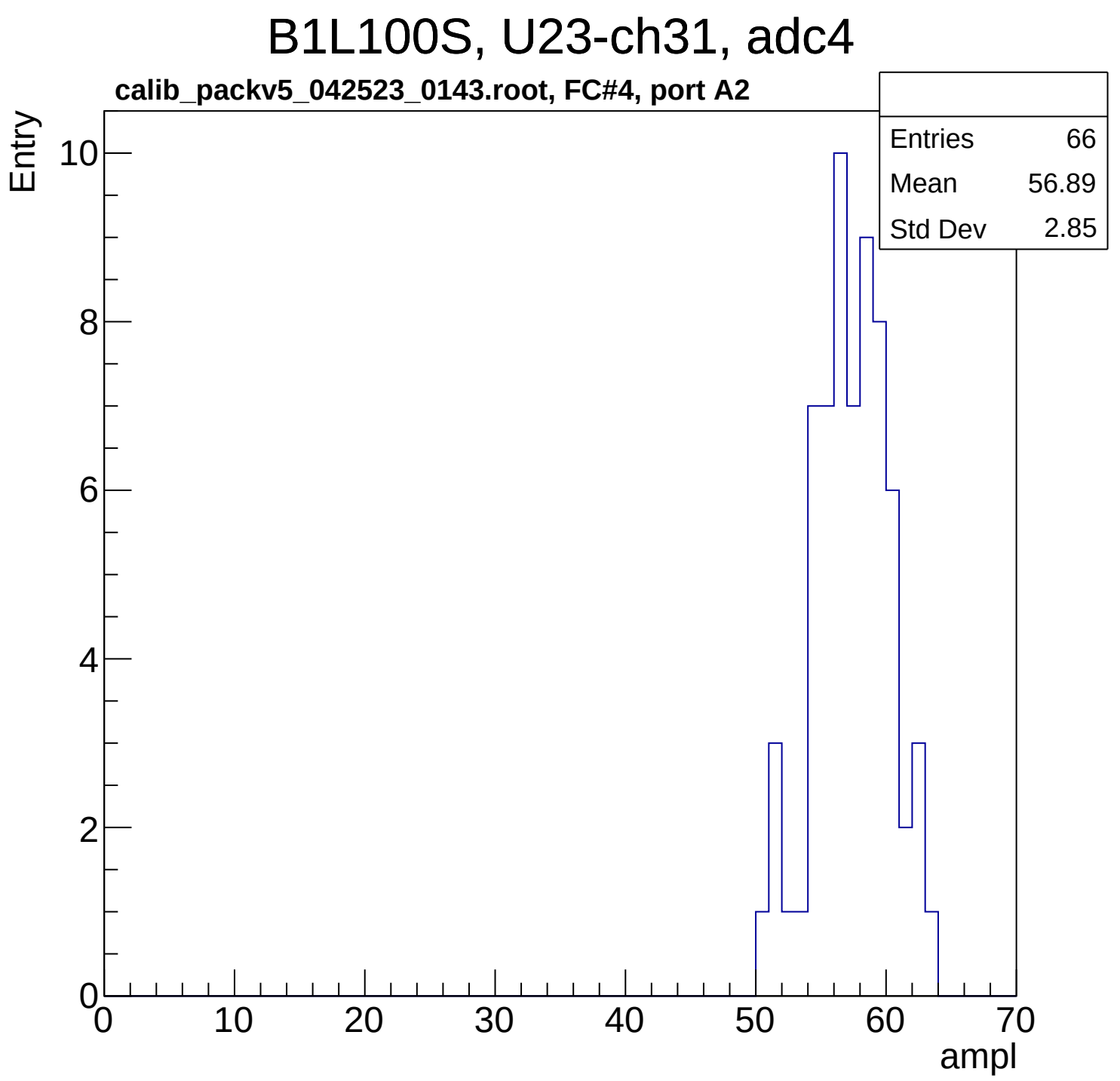
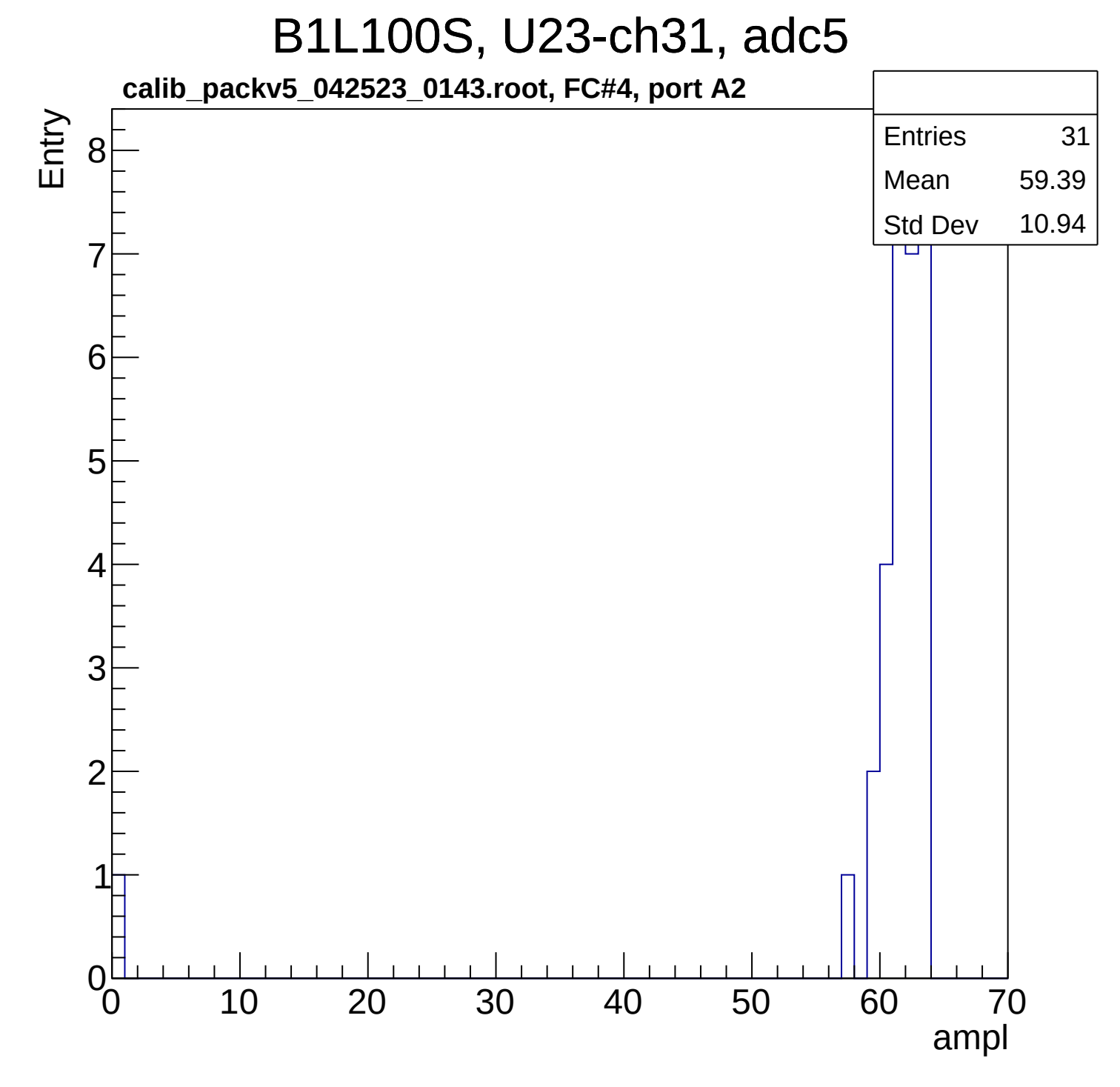

B1L100S, U23-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 56.89
Std Dev 2.85
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59.39
Std Dev 10.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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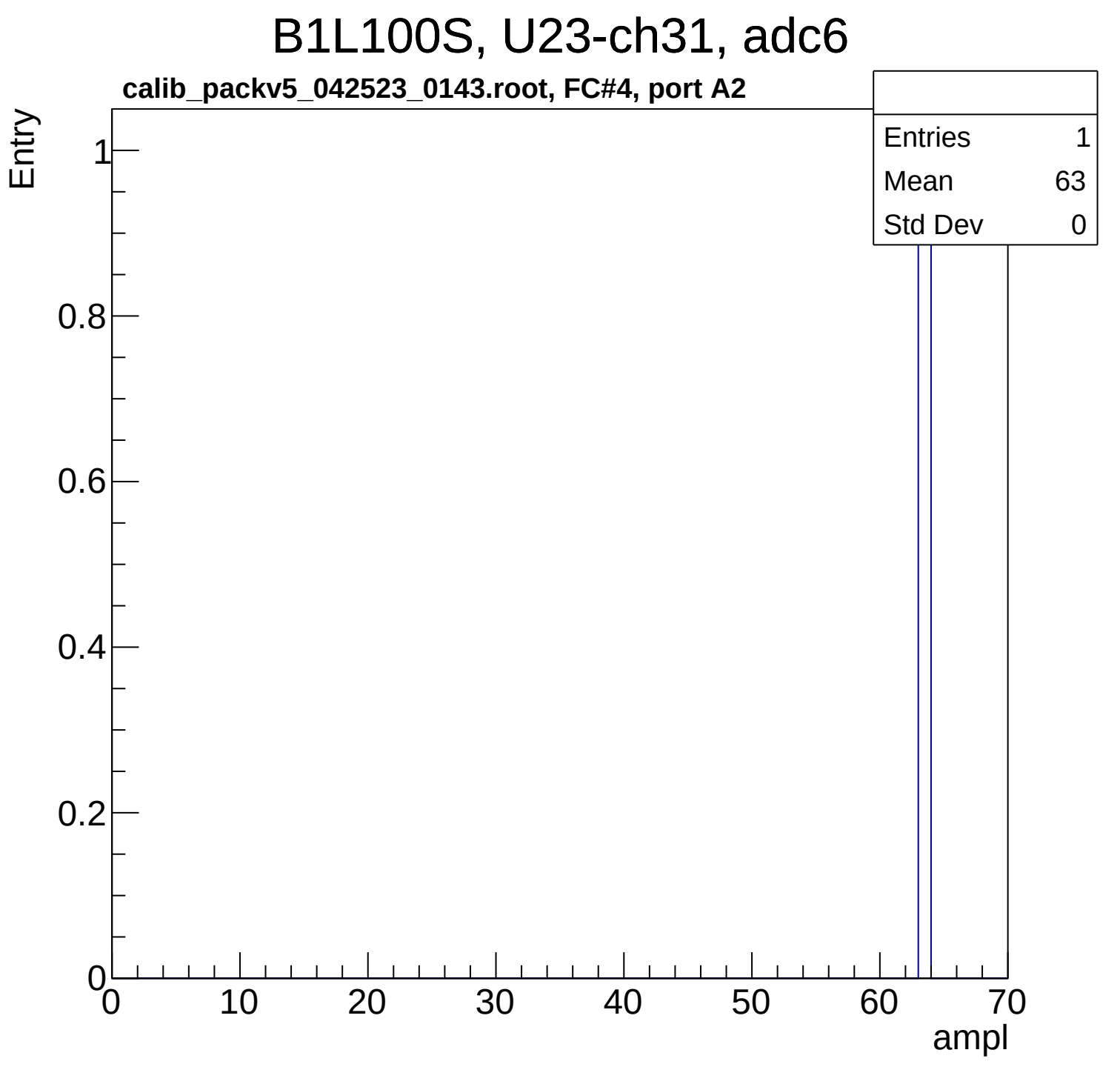
B1L100S, U23-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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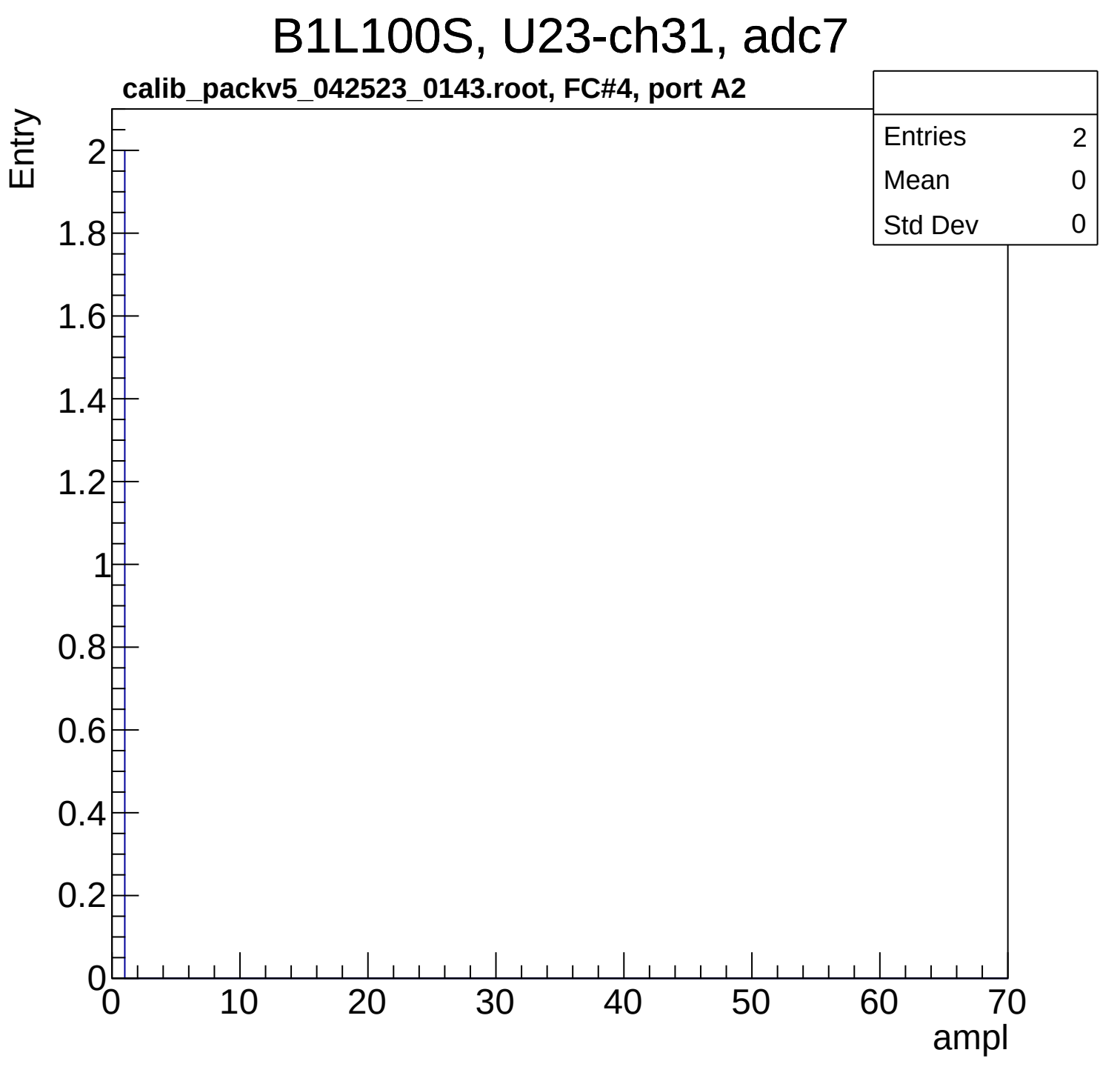
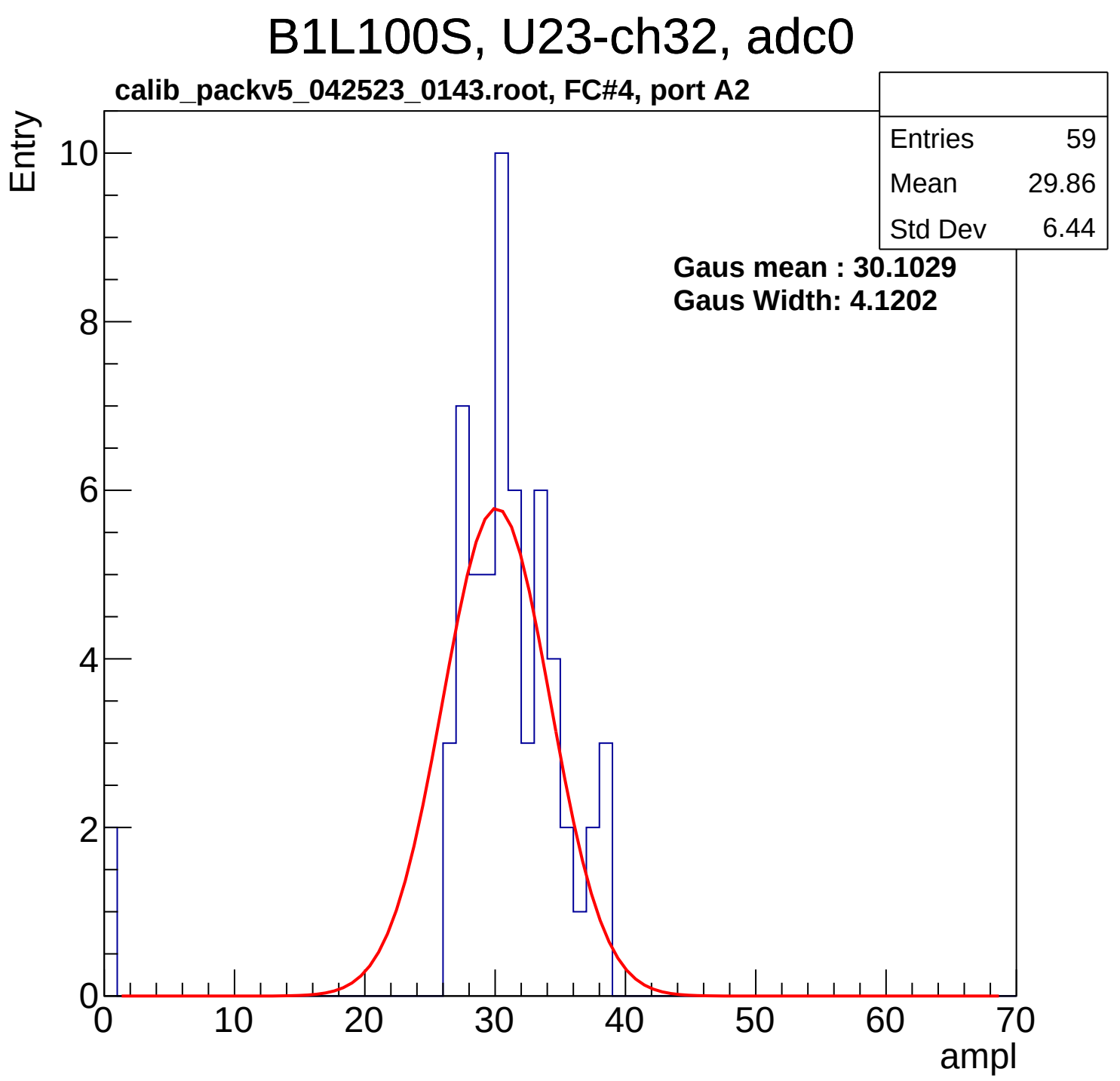
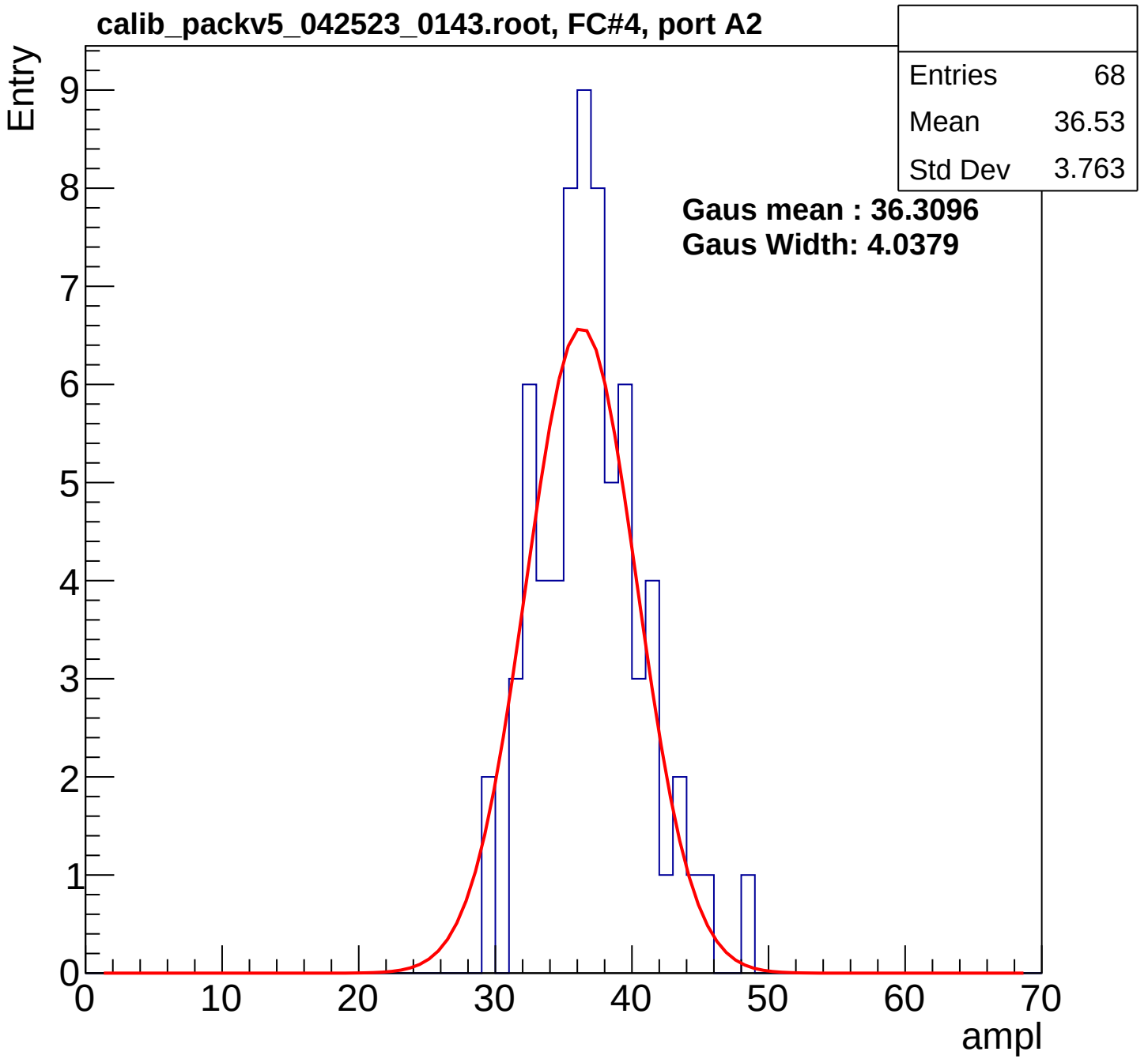
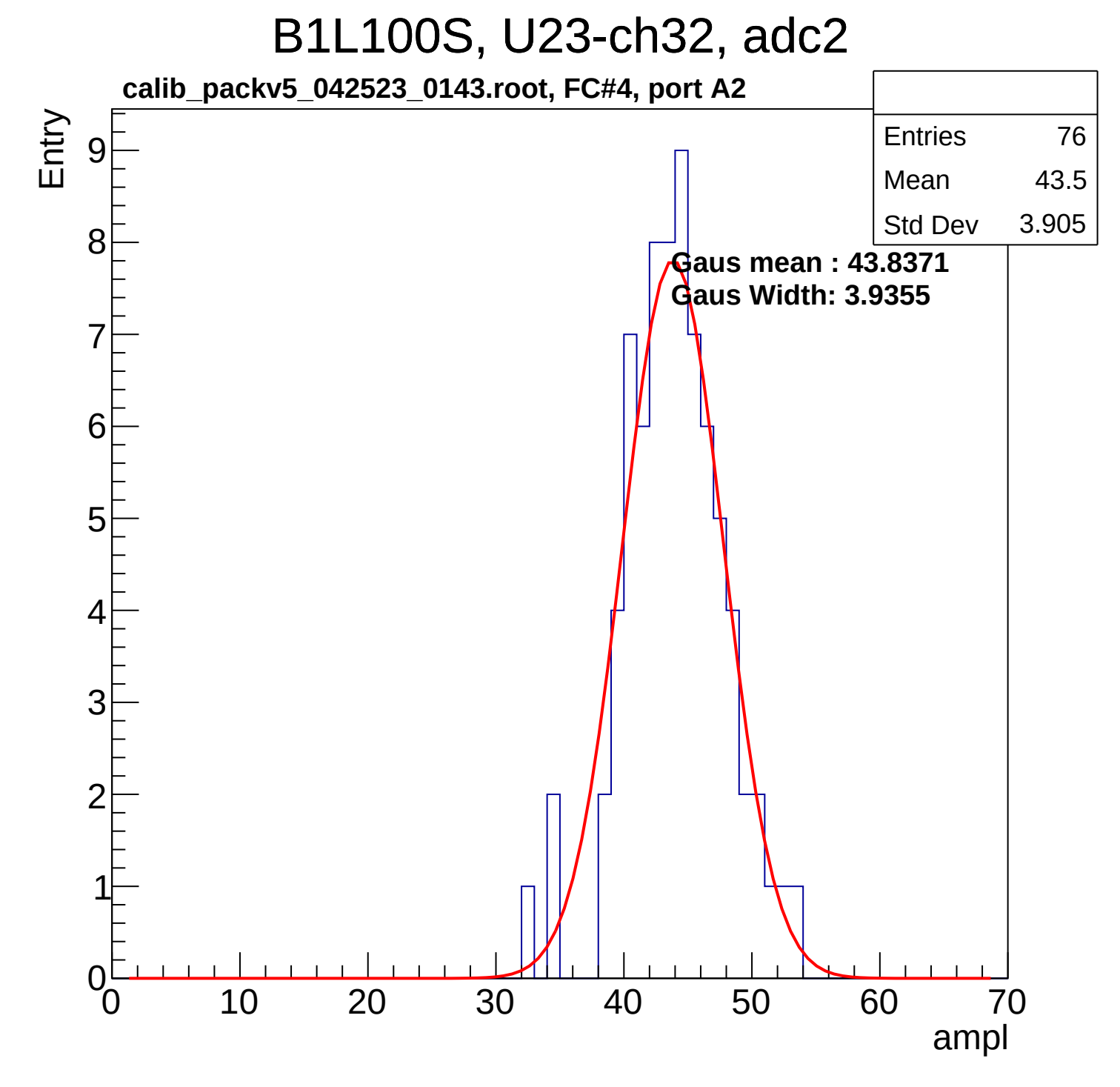
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 29.86
Std Dev 6.44
Gaus mean : 30.1029
Gaus Width: 4.1202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch32, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.53
Std Dev 3.763
Gaus mean : 36.3096
Gaus Width: 4.0379
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 43.5
Std Dev 3.905
Gaus mean : 43.8371
Gaus Width: 3.9355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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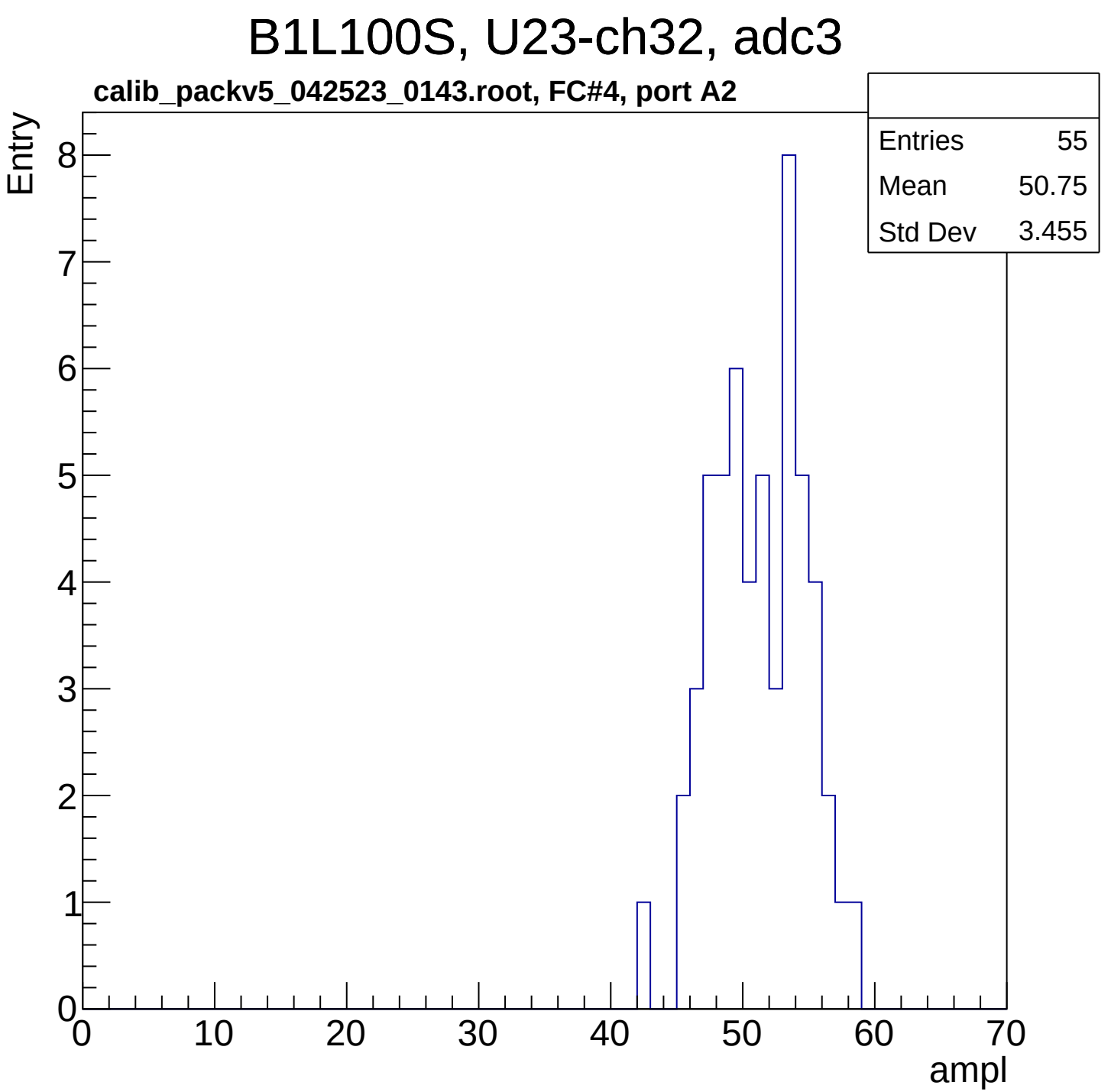
B1L100S, U23-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.75
Std Dev 3.455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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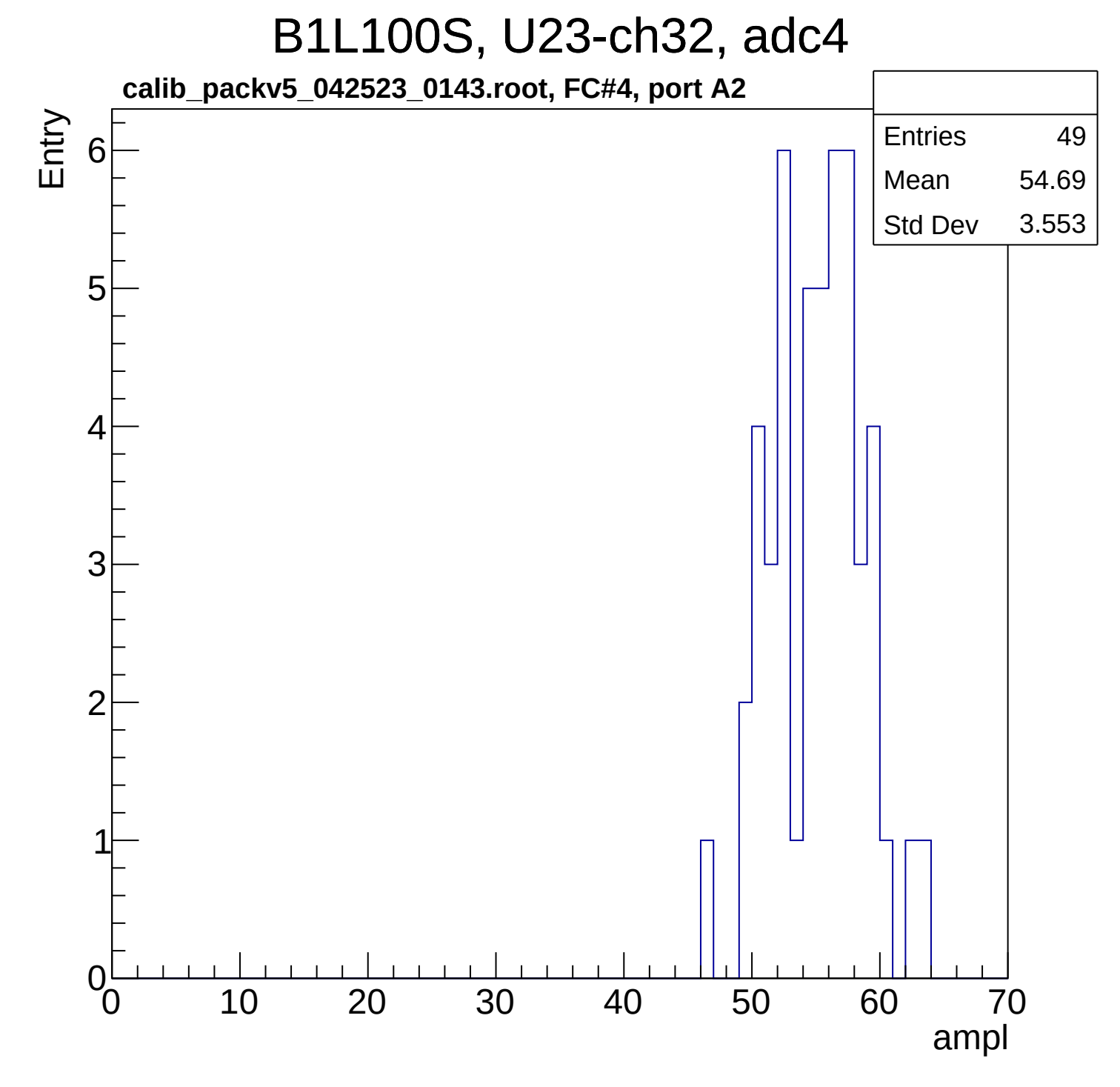
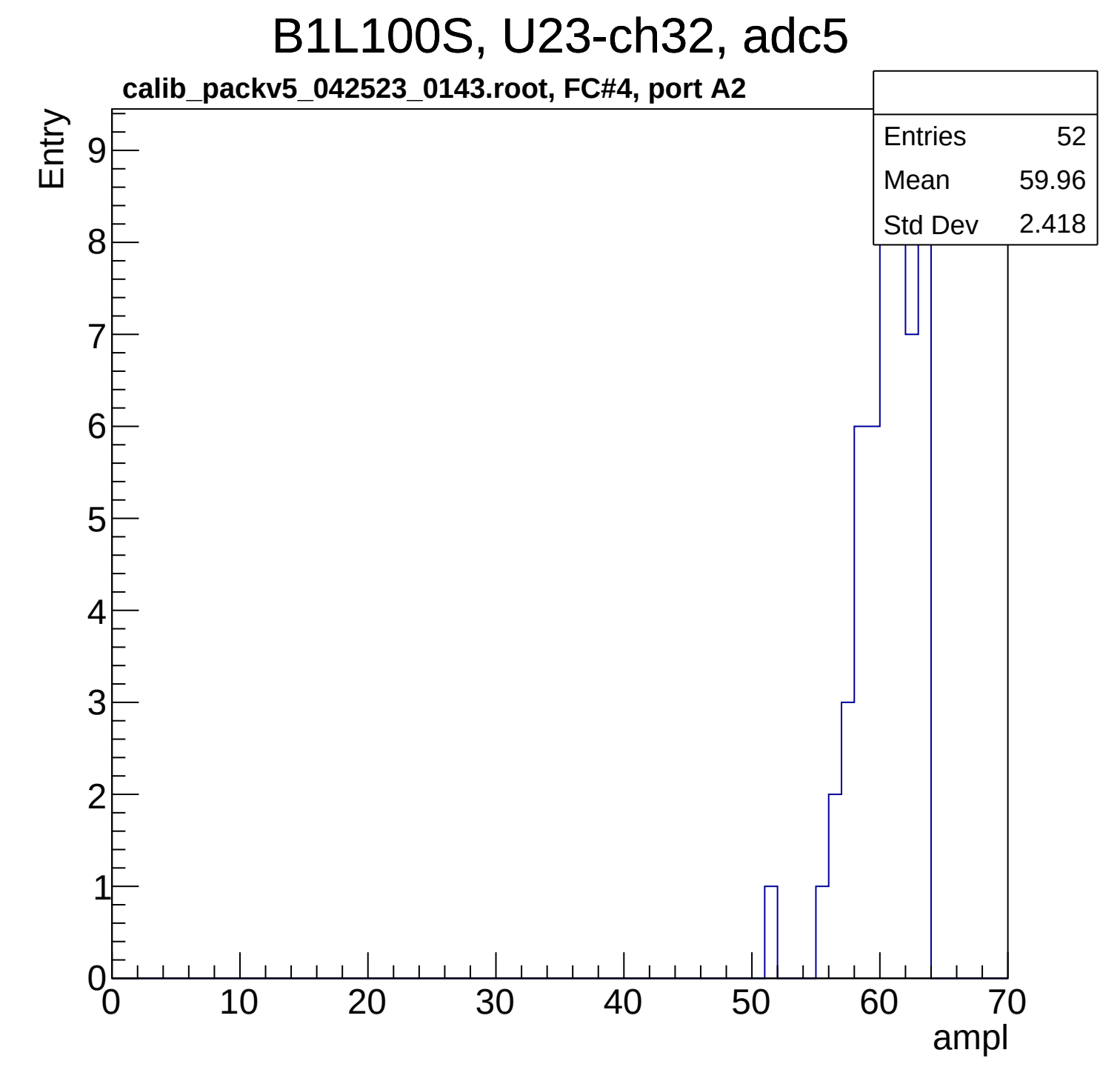

B1L100S, U23-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 54.69
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 59.96
Std Dev 2.418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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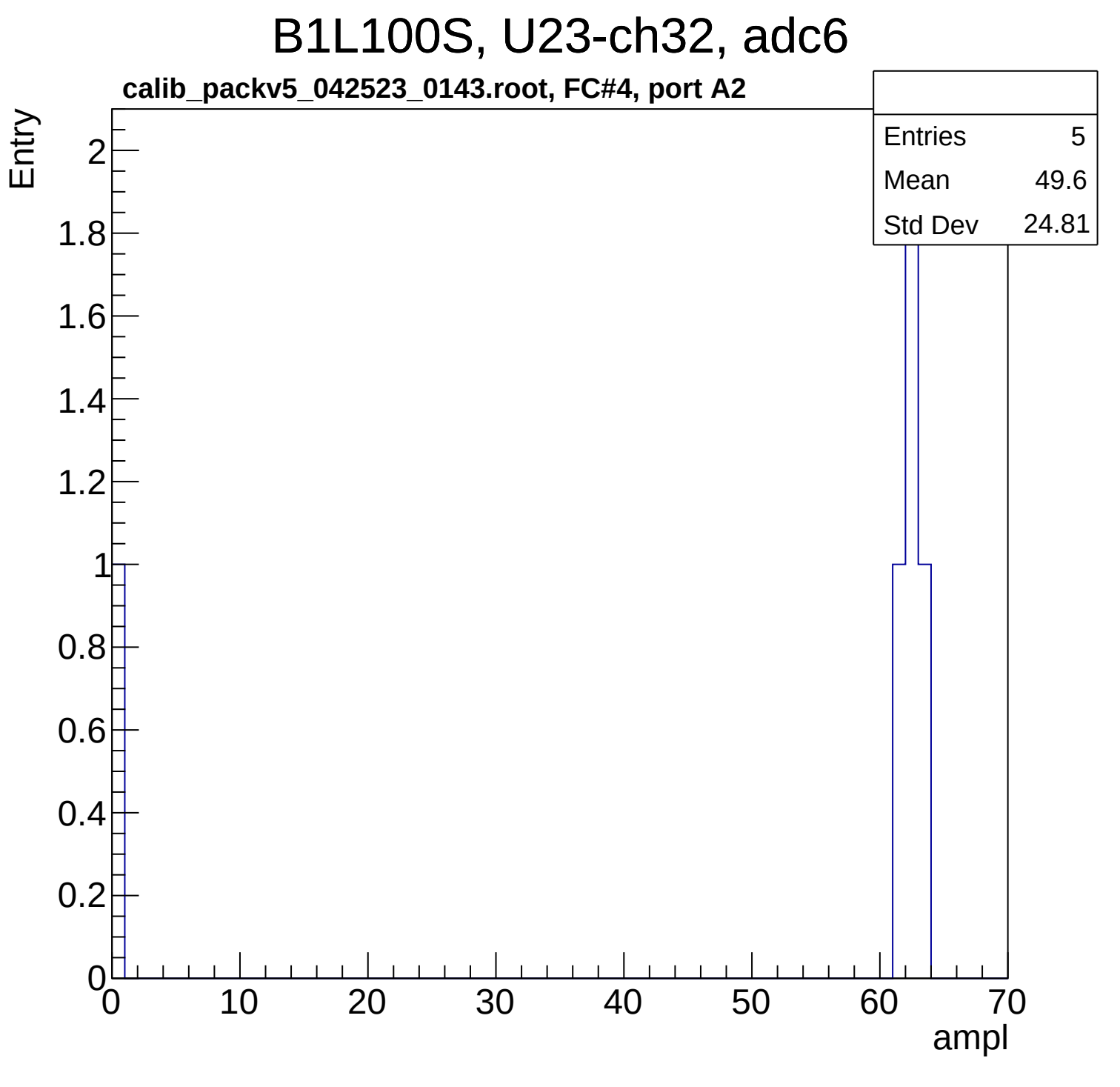
B1L100S, U23-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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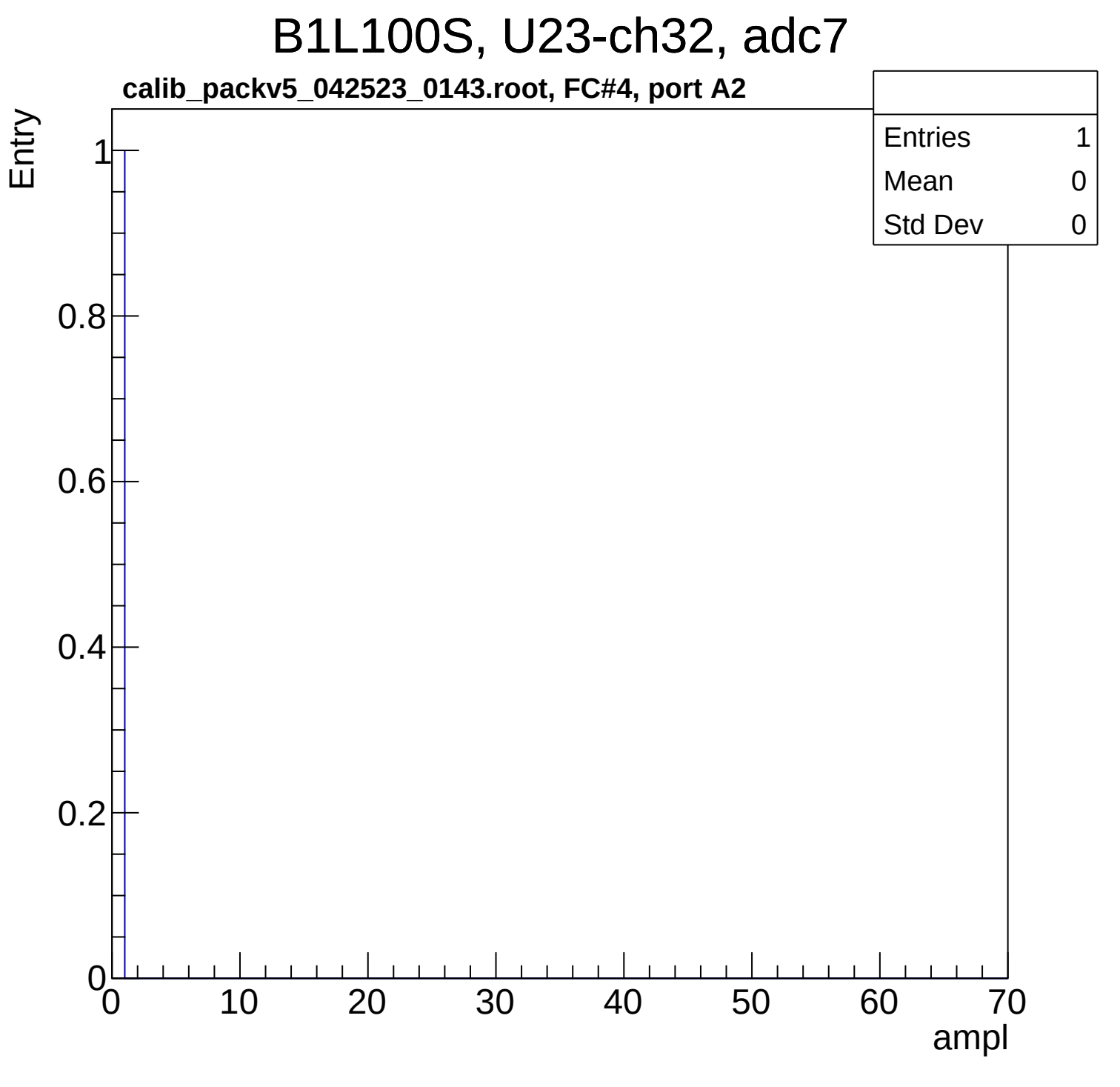
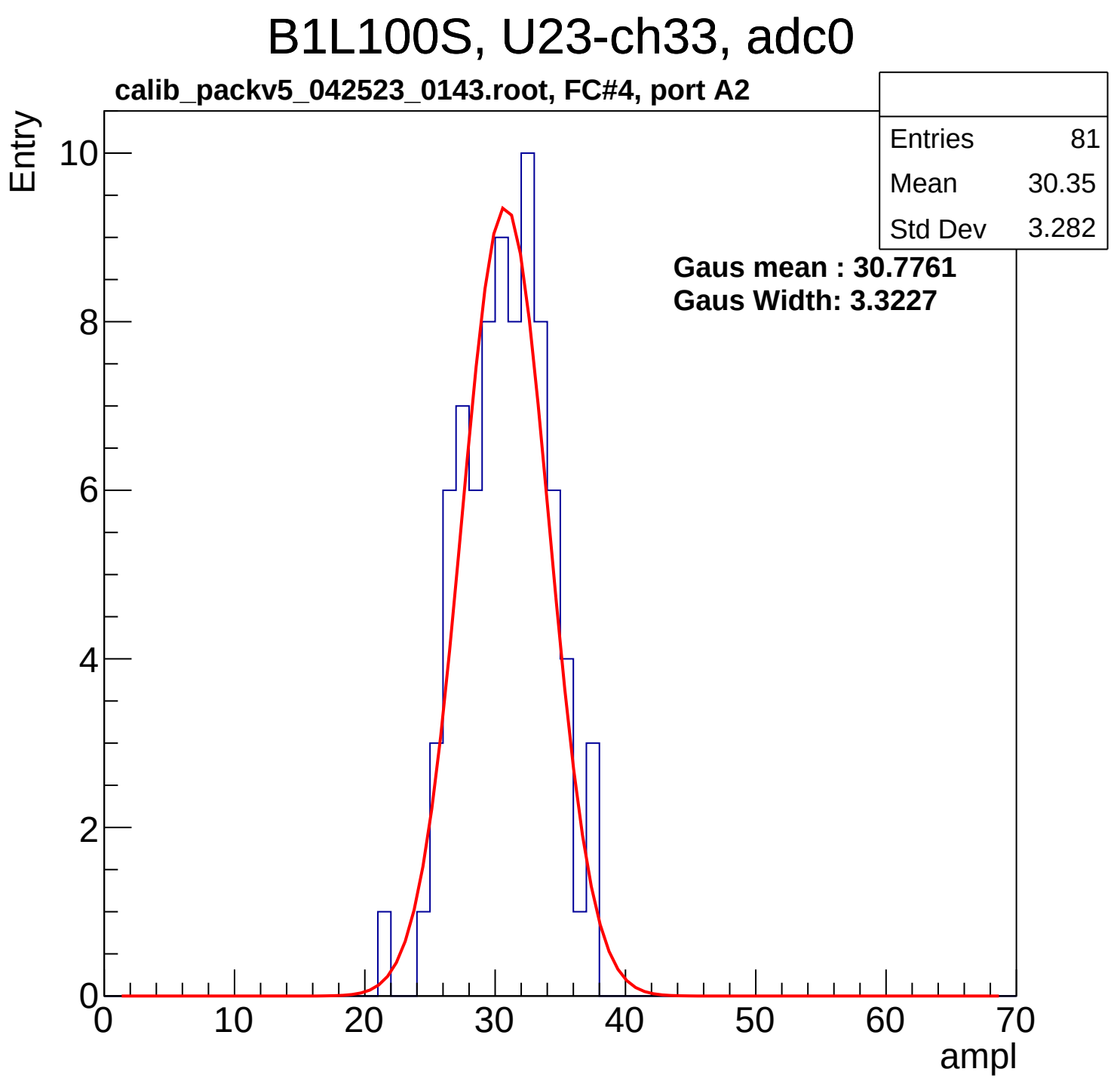
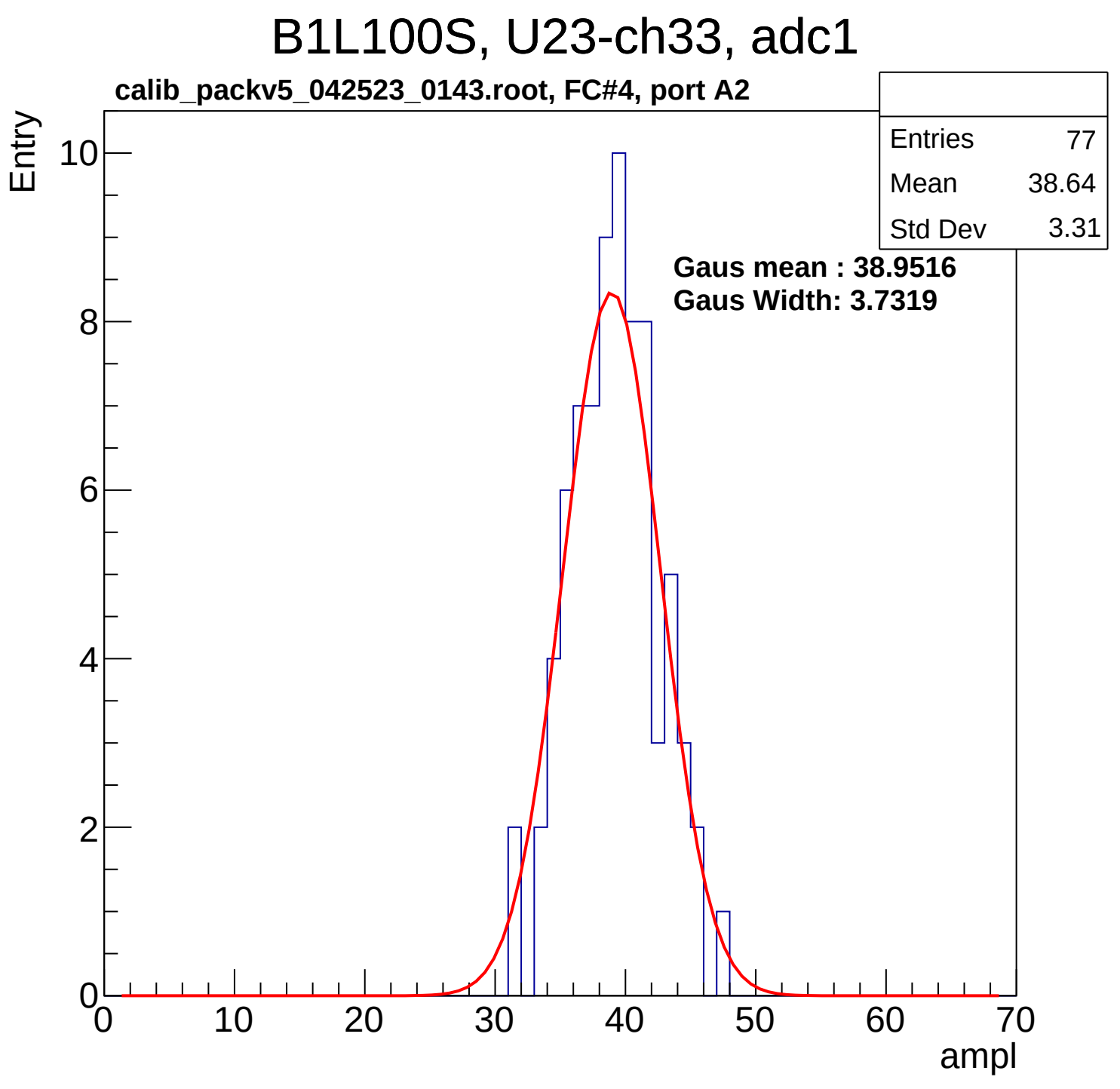
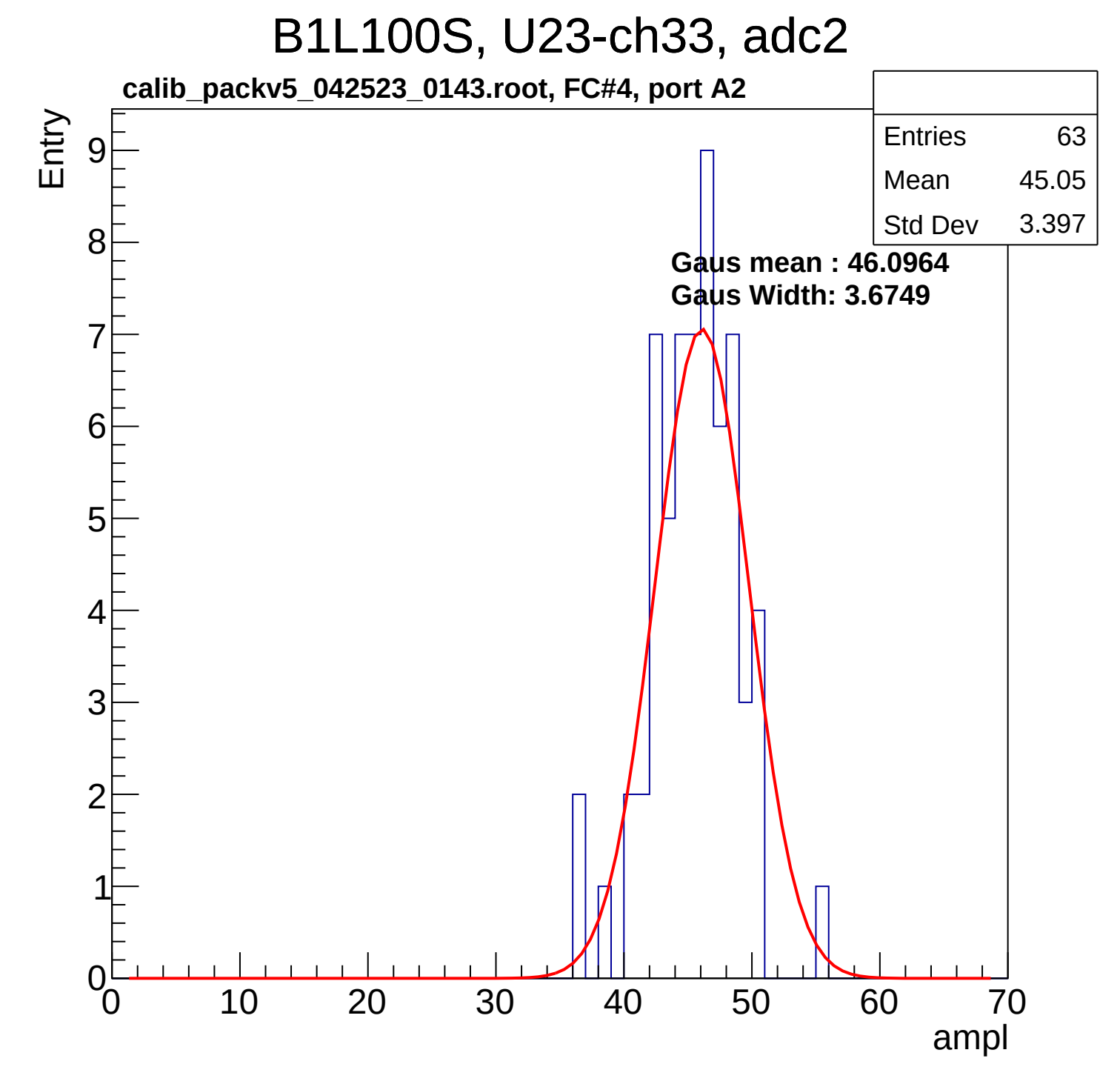

B1L100S, U23-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.35
Std Dev 3.282
Gaus mean : 30.7761
Gaus Width: 3.3227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch33, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 38.64
Std Dev 3.31
Gaus mean : 38.9516
Gaus Width: 3.7319
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 45.05
Std Dev 3.397
Gaus mean : 46.0964
Gaus Width: 3.6749
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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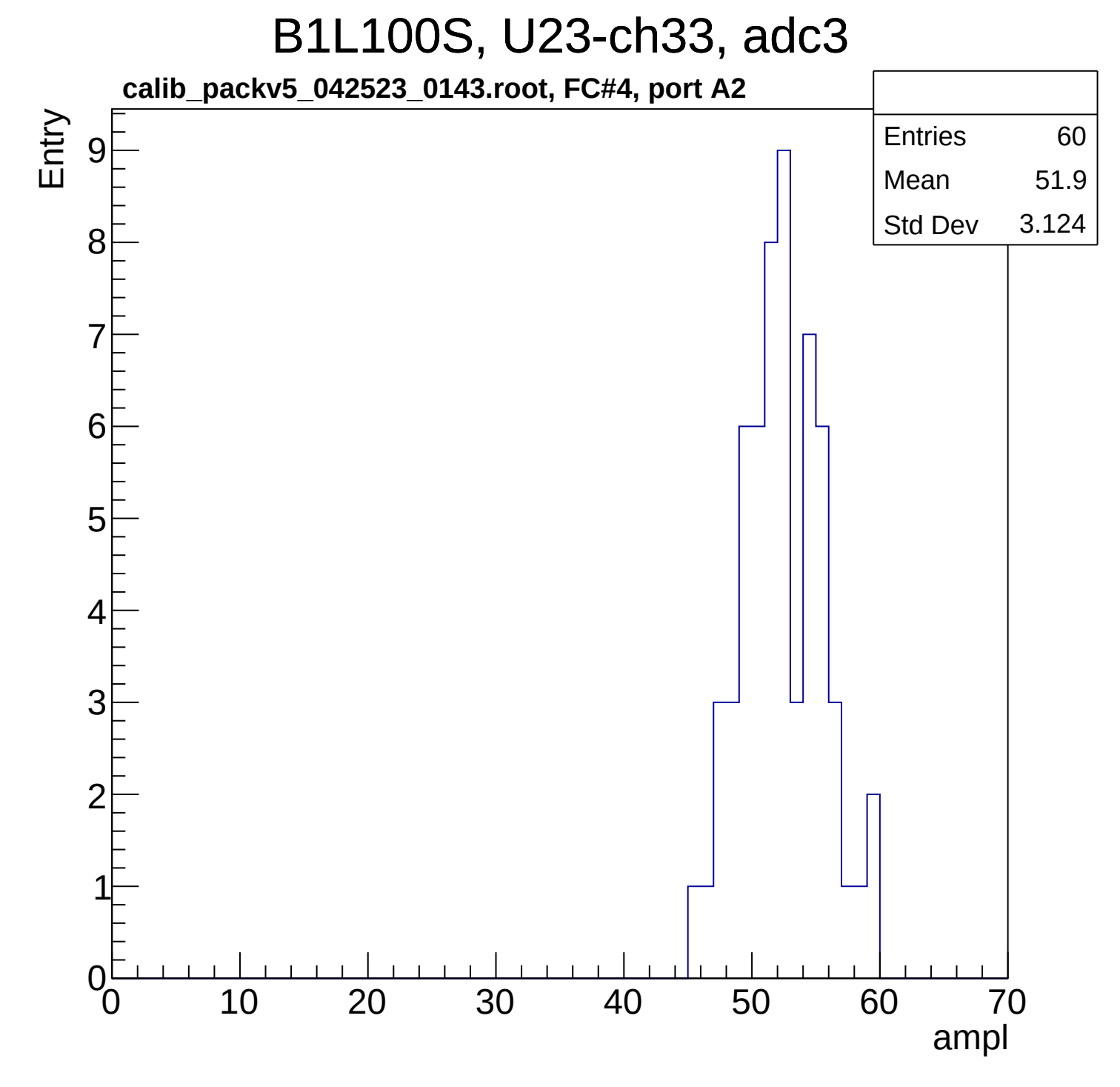
B1L100S, U23-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 51.9
Std Dev 3.124
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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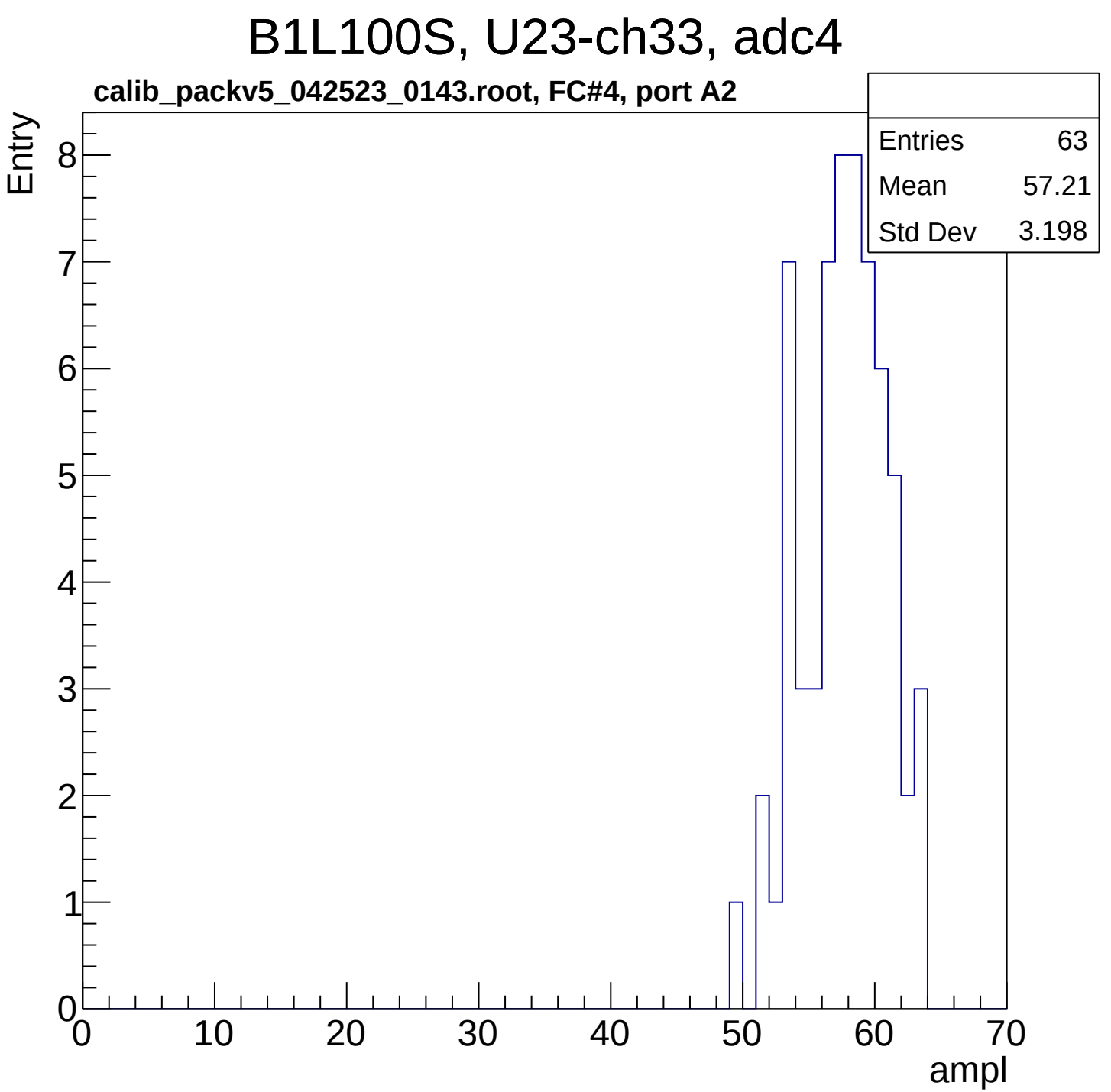
B1L100S, U23-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 57.21
Std Dev 3.198
Entry
ampl
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4
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0
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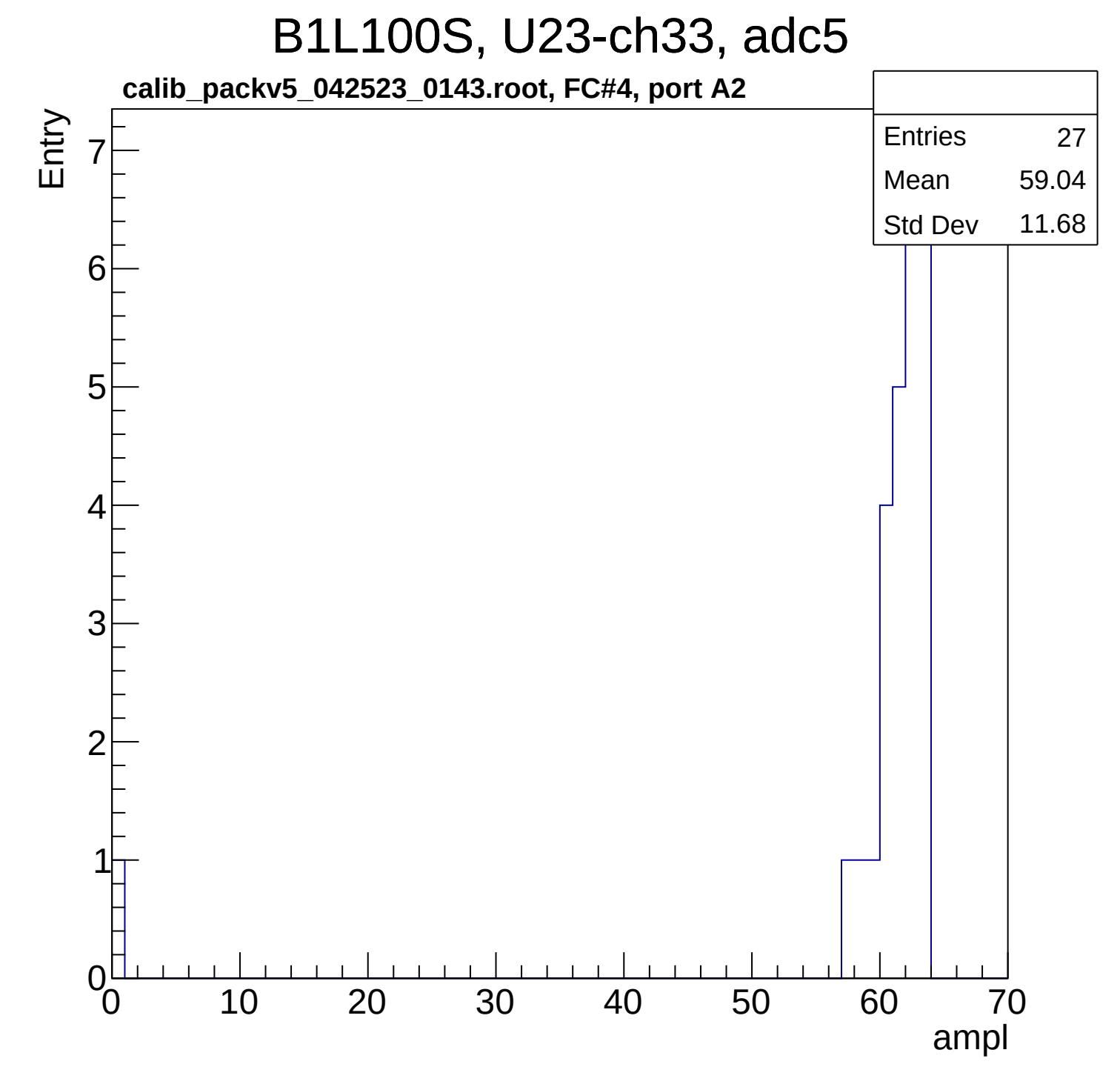
B1L100S, U23-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 59.04
Std Dev 11.68
Entry
ampl
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3
4
5
6
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0
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60
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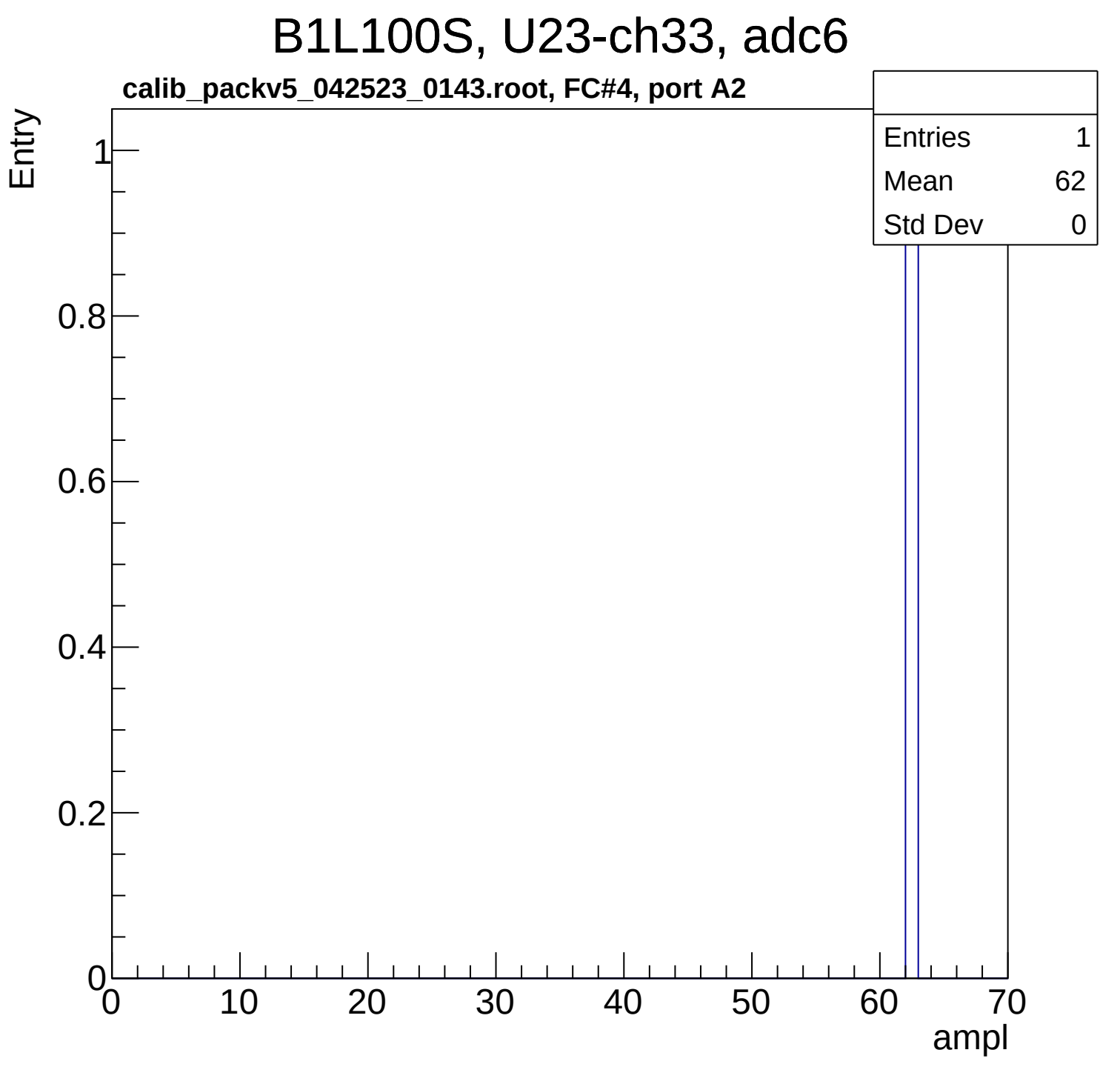
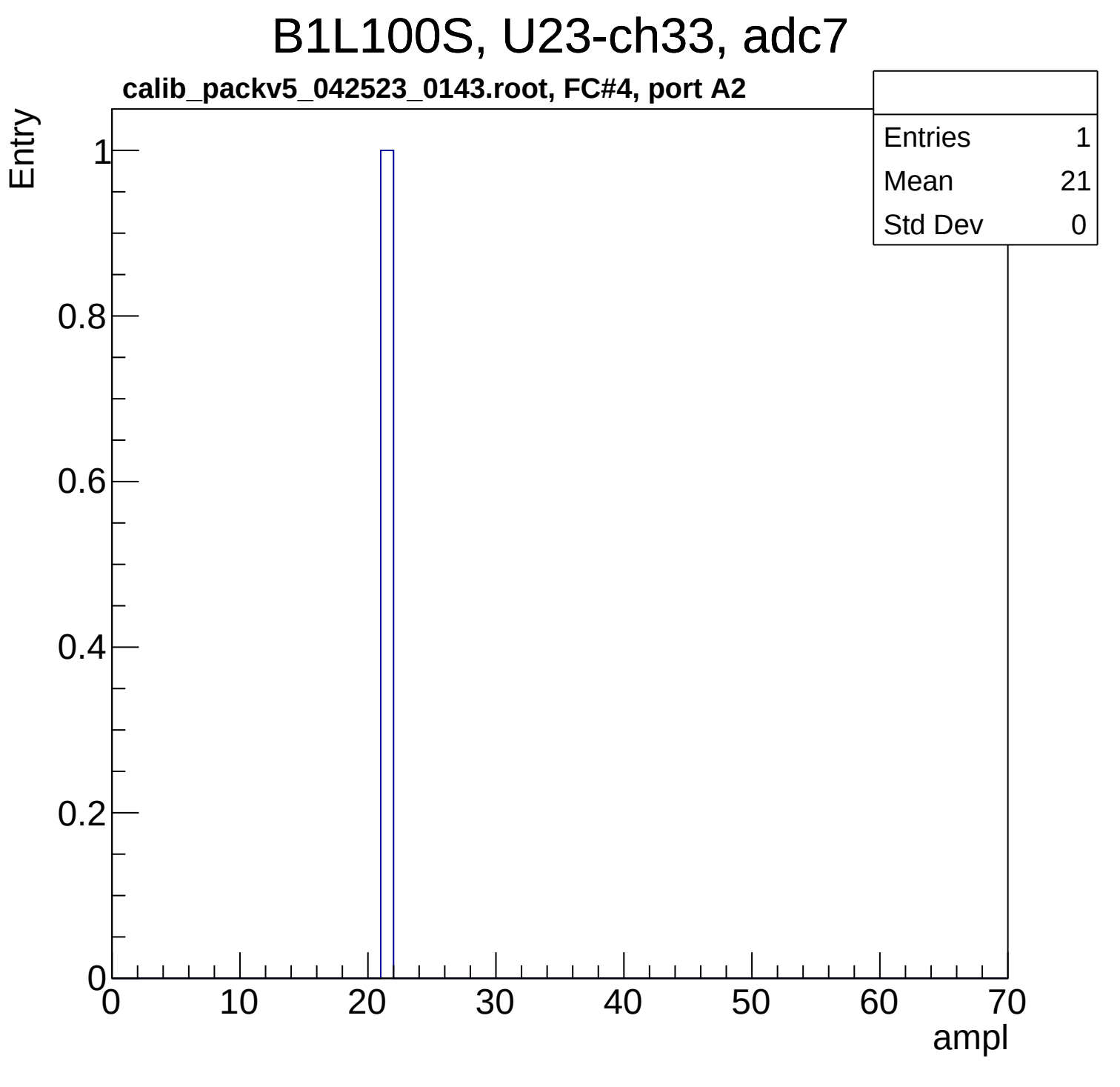

B1L100S, U23-ch33, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch33, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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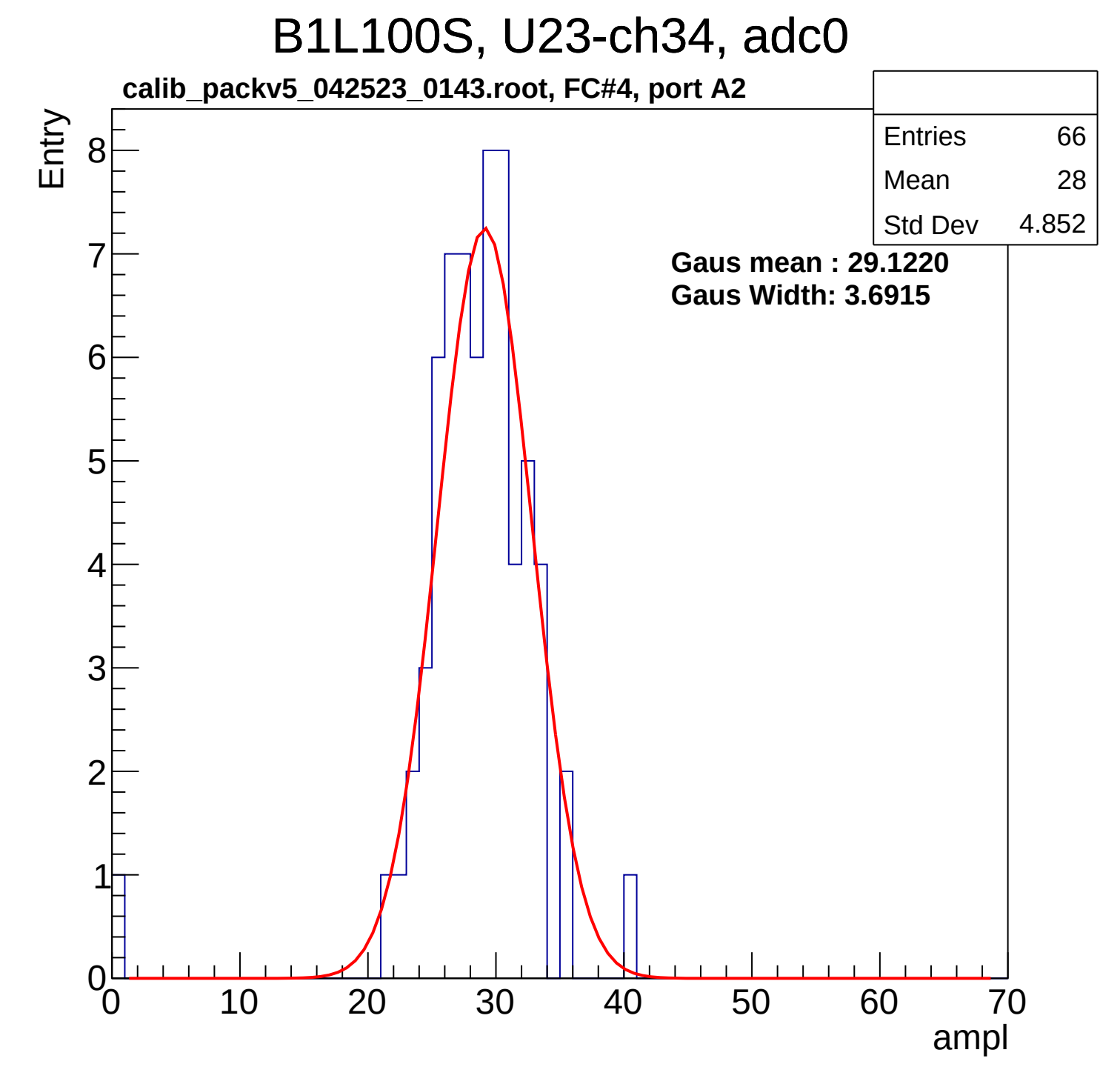
50
60
70
ampl


B1L100S, U23-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28
Std Dev 4.852
Gaus mean : 29.1220
Gaus Width: 3.6915
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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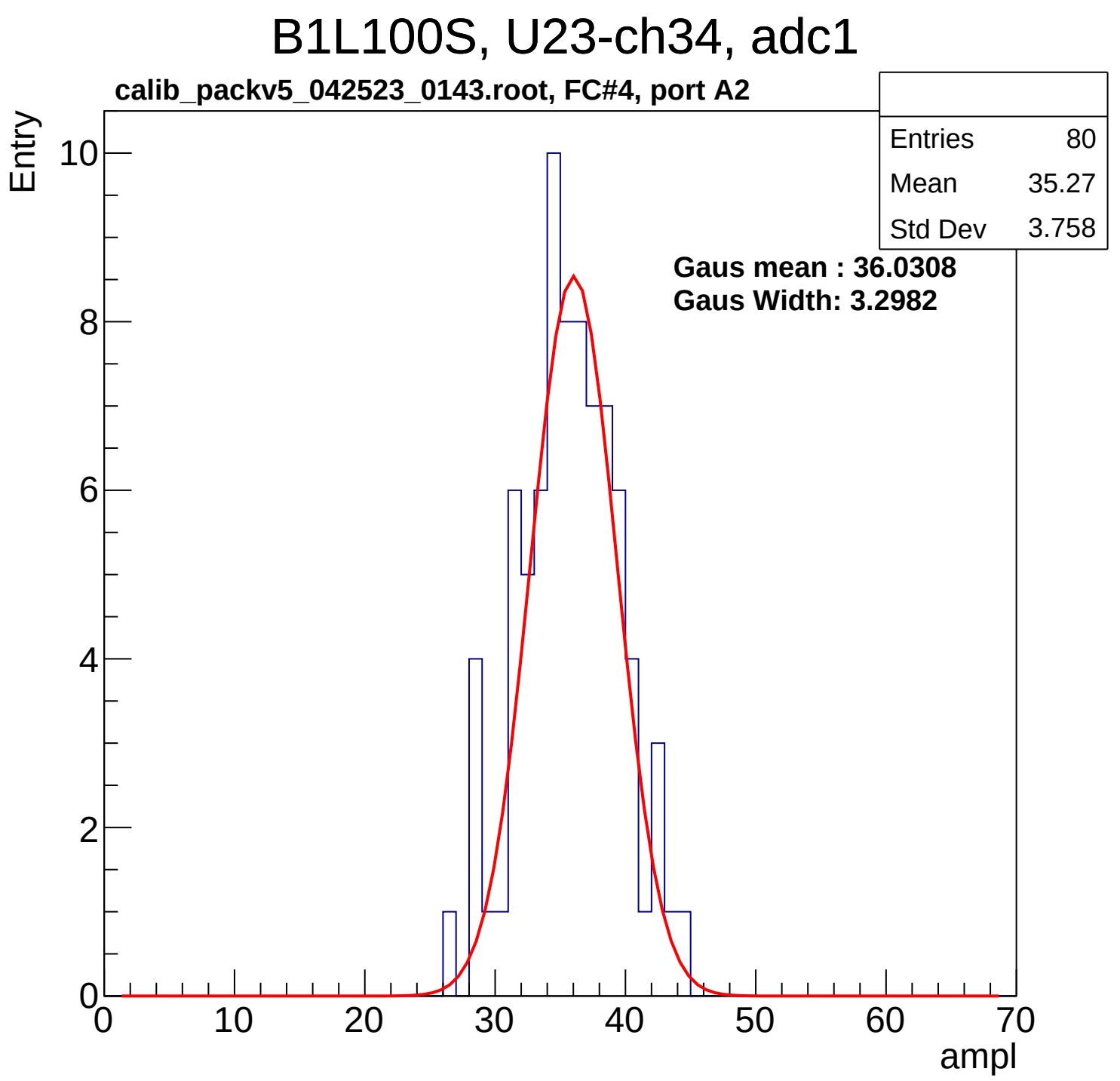
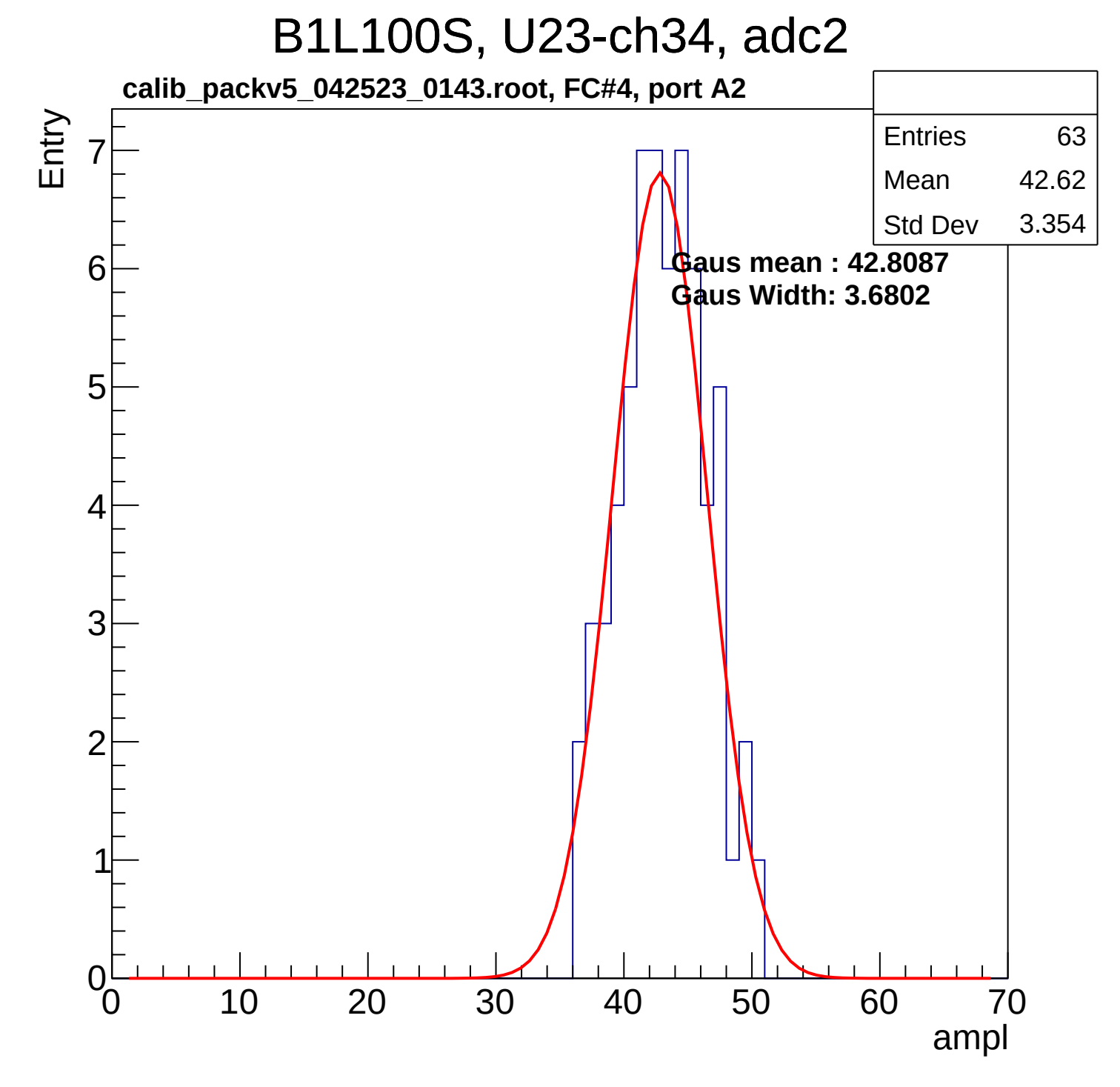
40
50
60
70


B1L100S, U23-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.27
Std Dev 3.758
Gaus mean : 36.0308
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.62
Std Dev 3.354
Gaus mean : 42.8087
Gaus Width: 3.6802
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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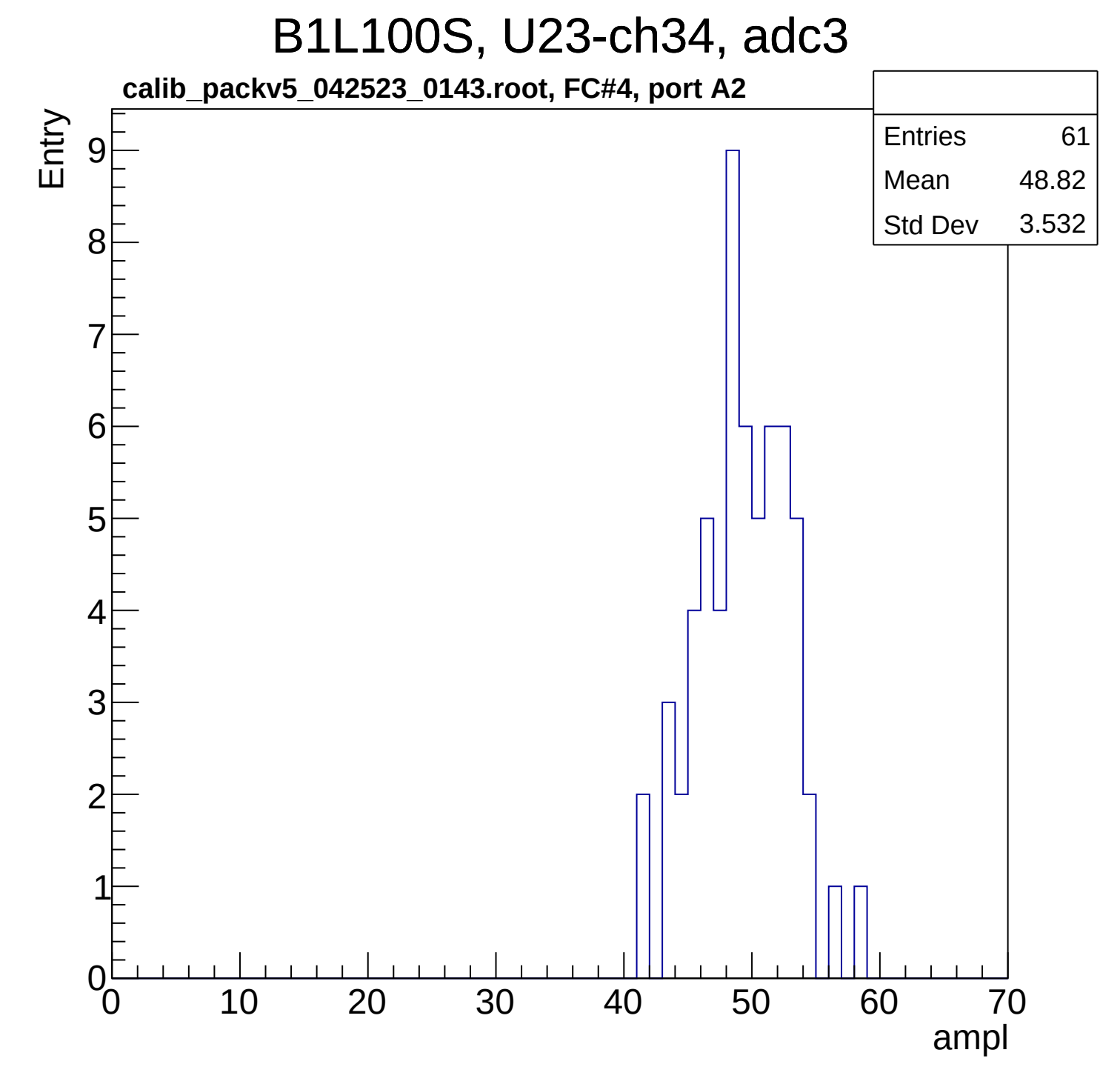
B1L100S, U23-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 48.82
Std Dev 3.532
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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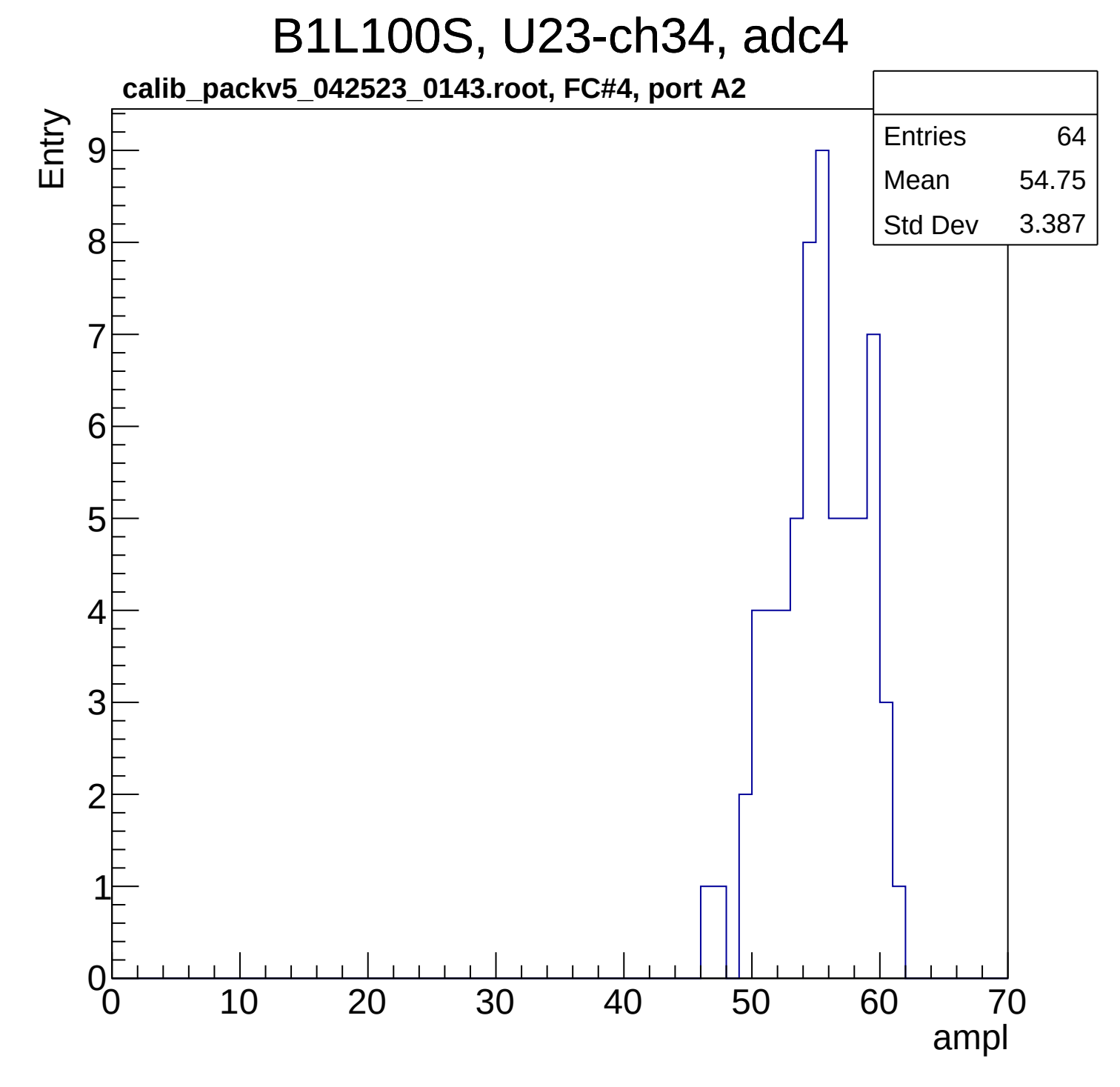
B1L100S, U23-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.75
Std Dev 3.387
Entry
ampl
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2
3
4
5
6
7
8
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0
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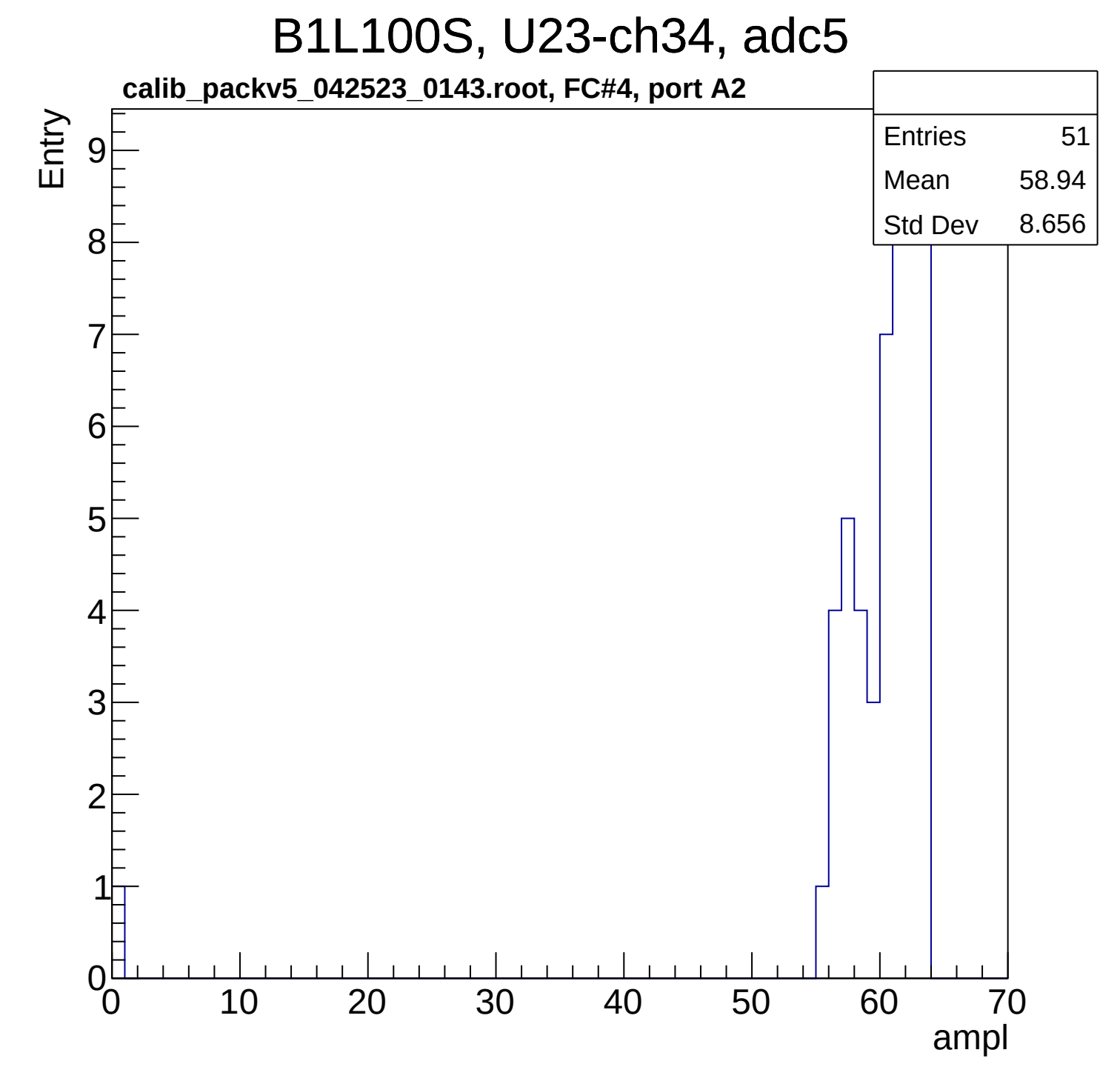
B1L100S, U23-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.94
Std Dev 8.656
Entry
ampl
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2
3
4
5
6
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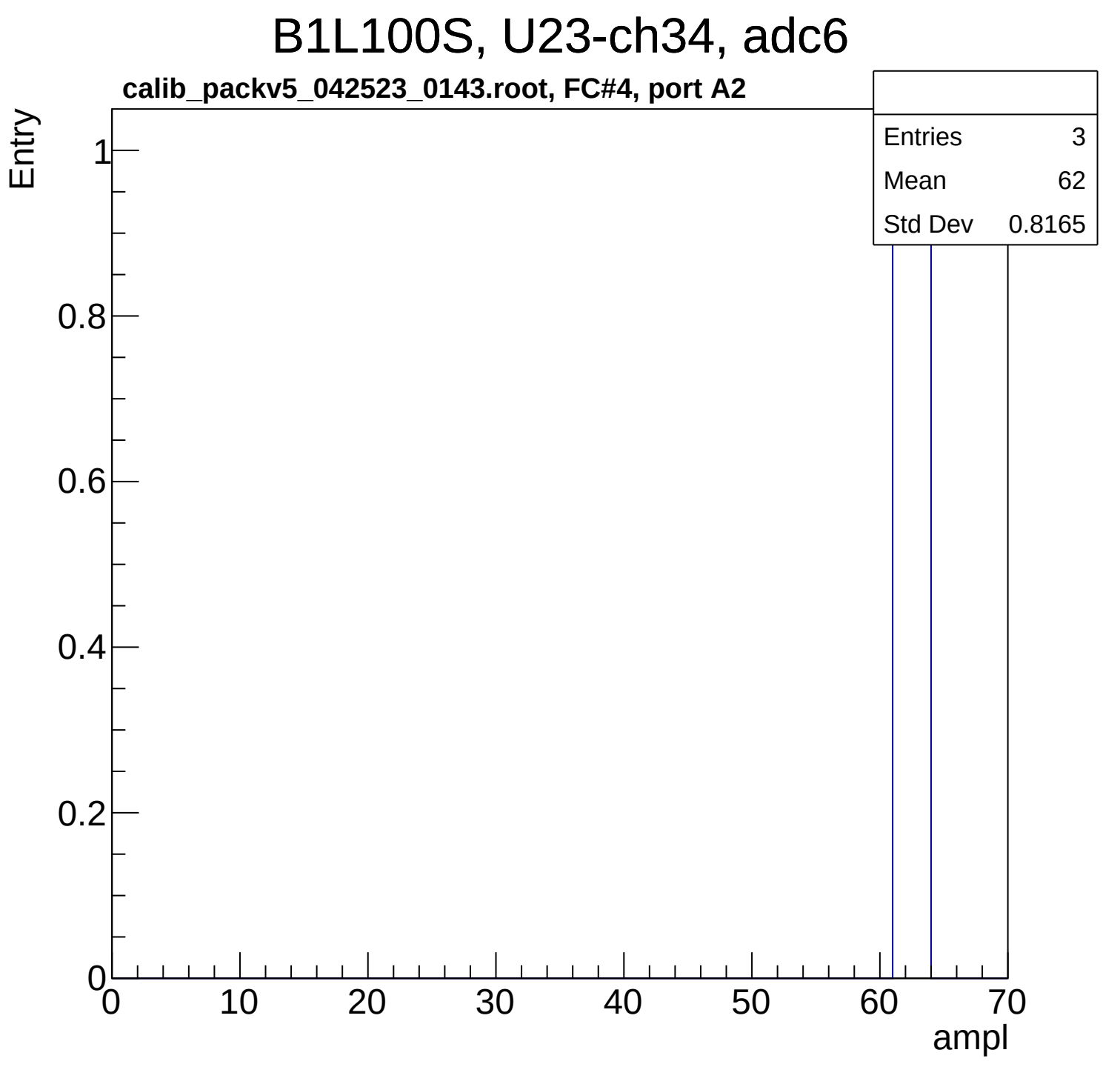
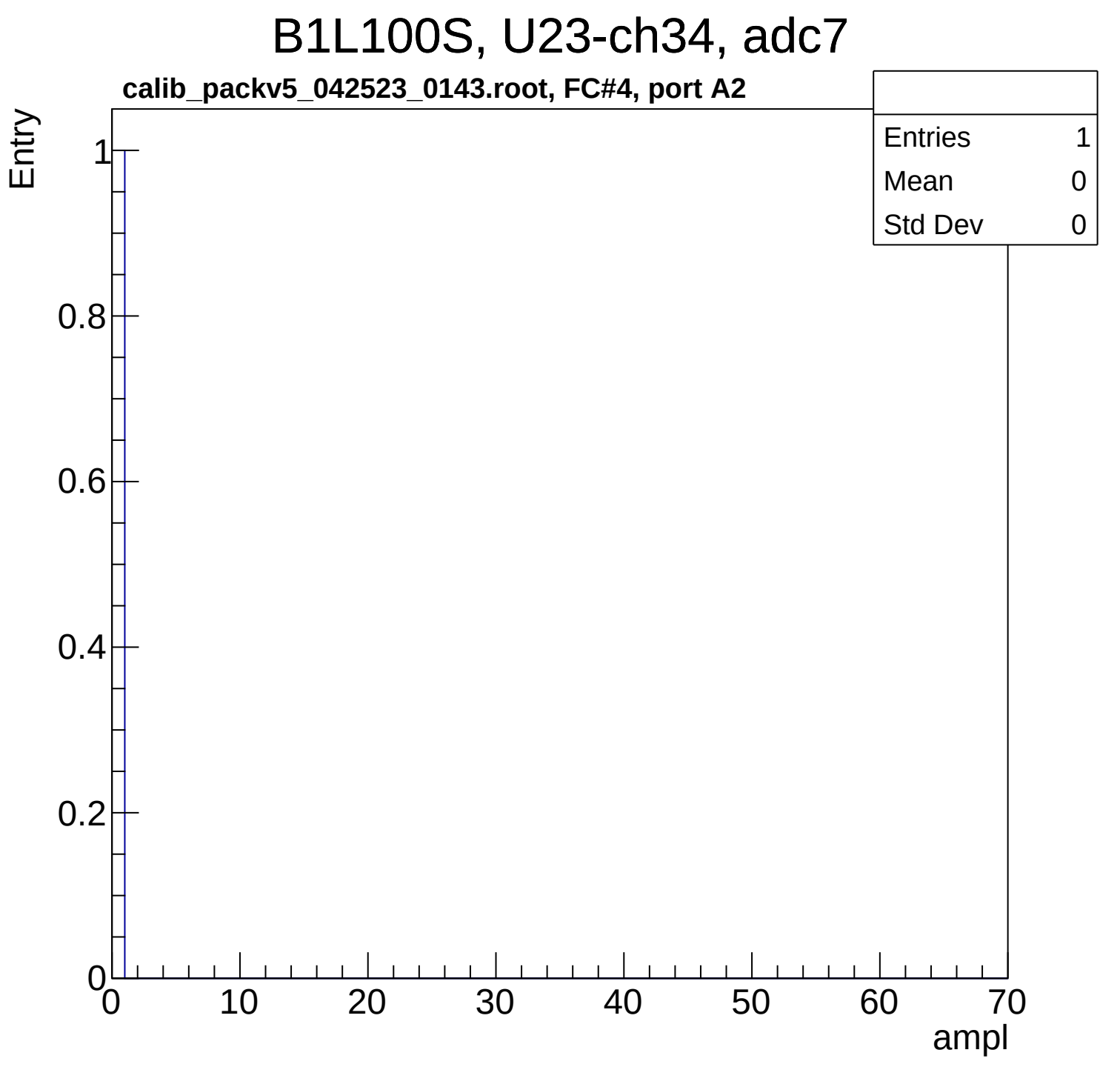

B1L100S, U23-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch34, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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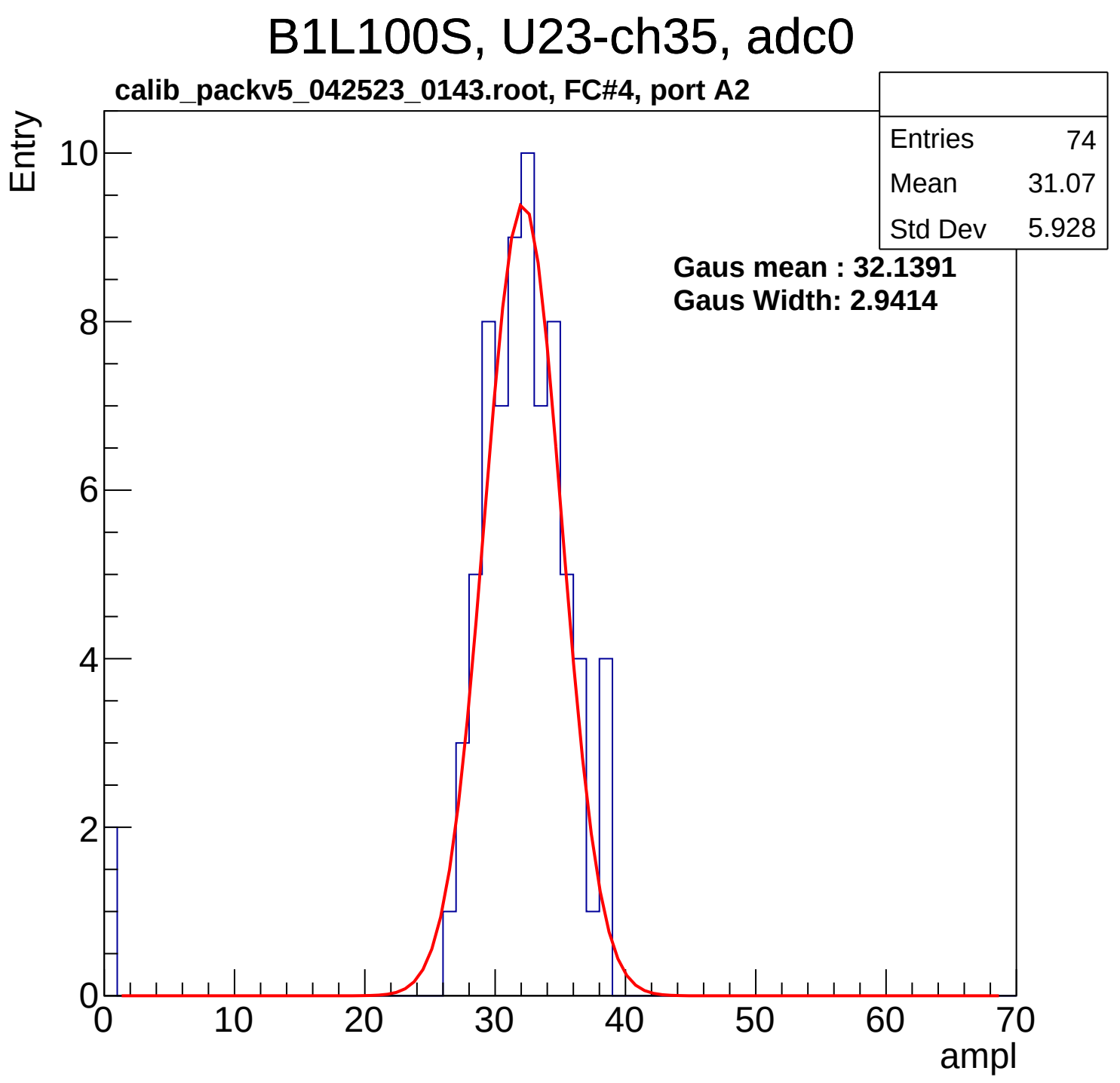
ampl


B1L100S, U23-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 31.07
Std Dev 5.928
Gaus mean : 32.1391
Gaus Width: 2.9414
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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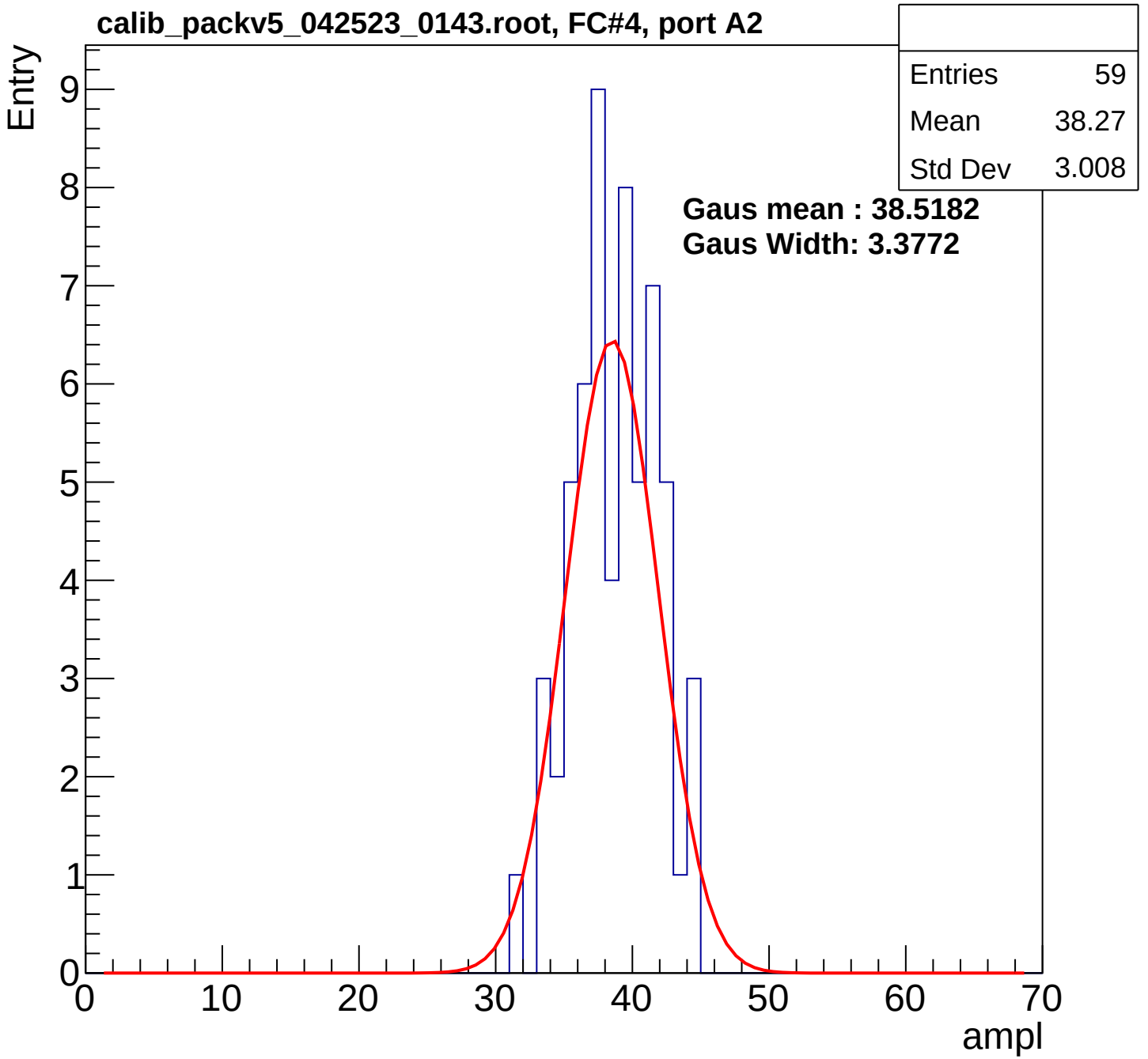
B1L100S, U23-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 38.27
Std Dev 3.008
Gaus mean : 38.5182
Gaus Width: 3.3772
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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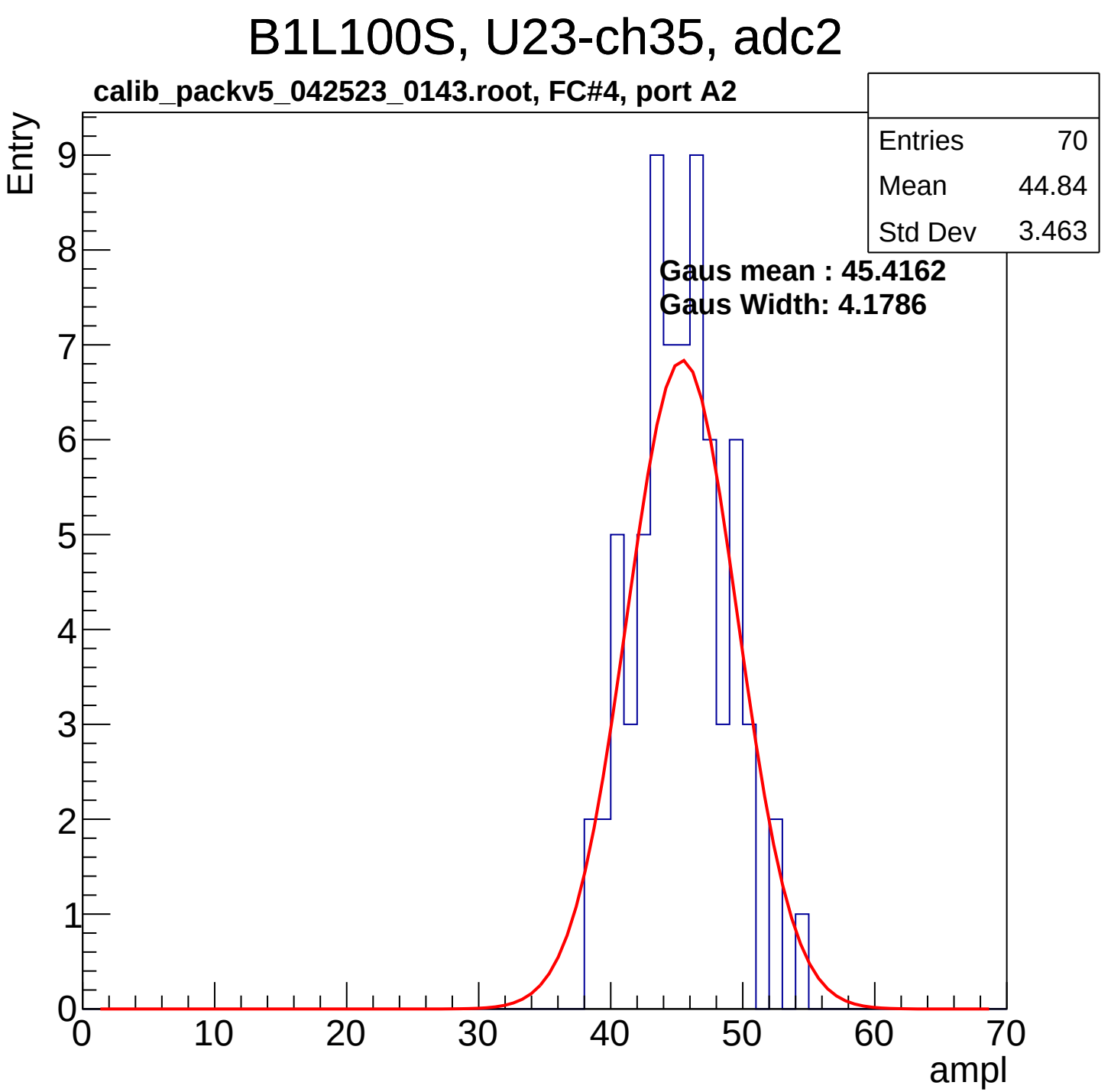
B1L100S, U23-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.84
Std Dev 3.463
Gaus mean : 45.4162
Gaus Width: 4.1786
Entry
ampl
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2
3
4
5
6
7
8
9
0
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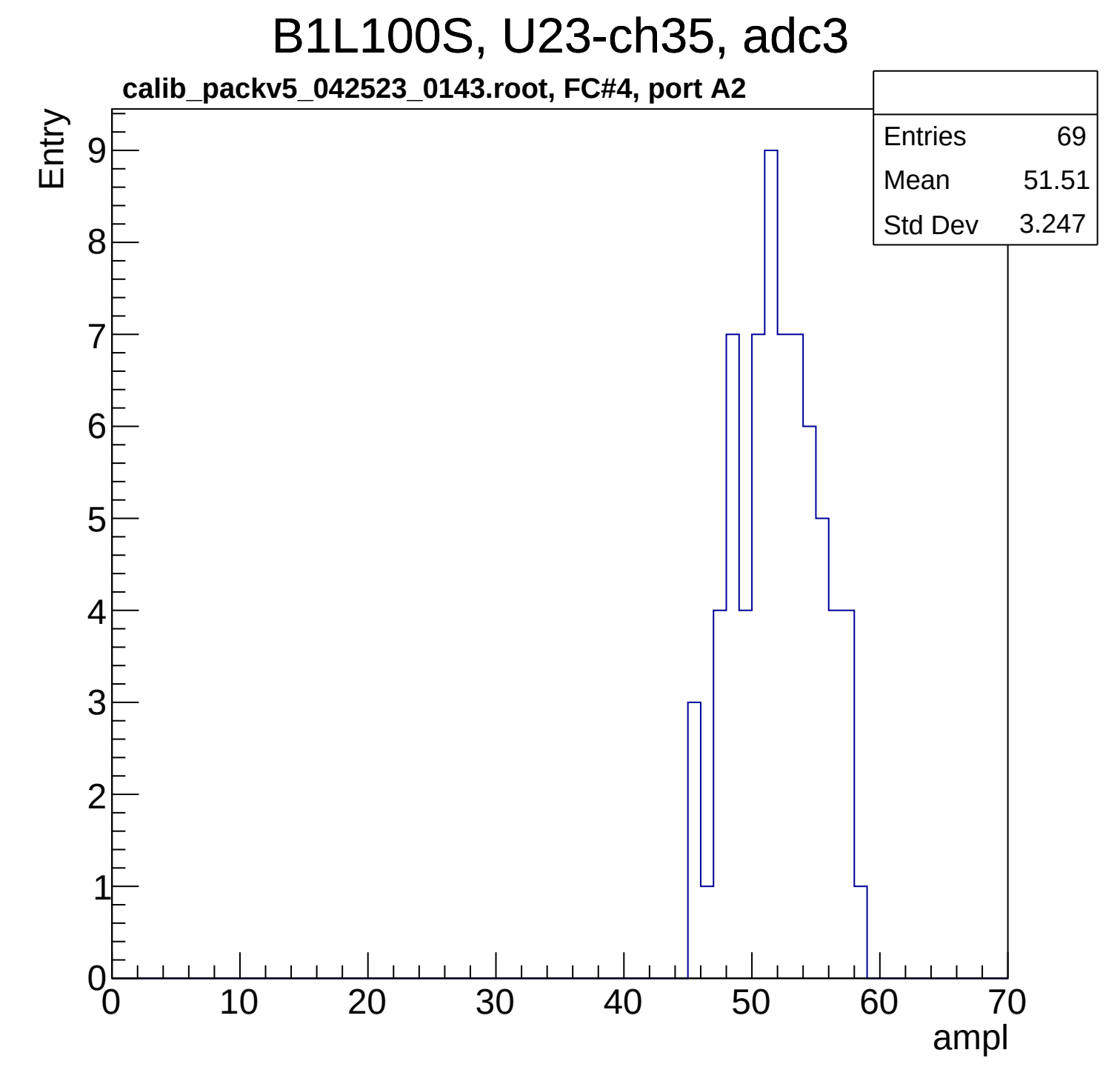
B1L100S, U23-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.51
Std Dev 3.247
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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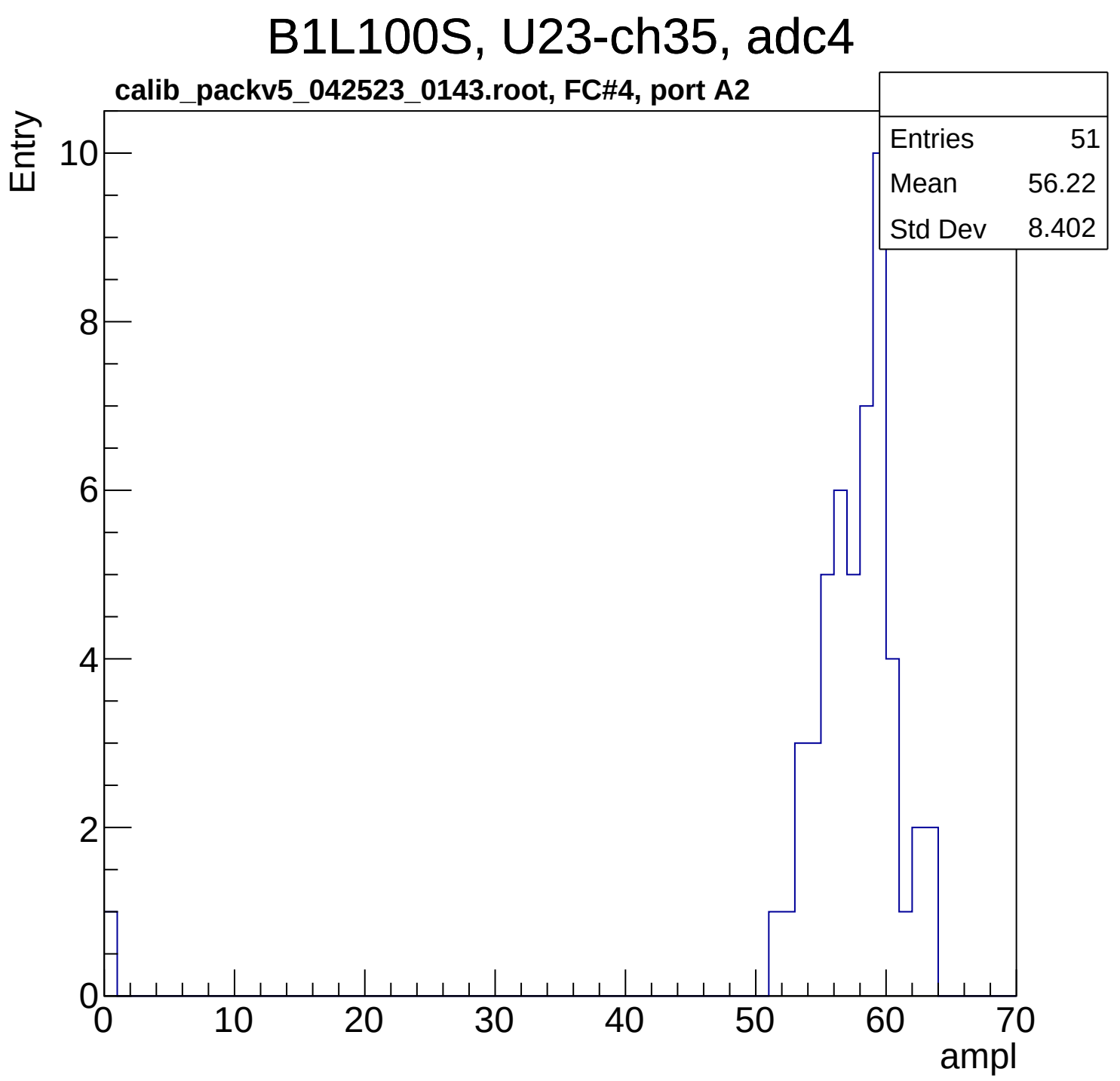
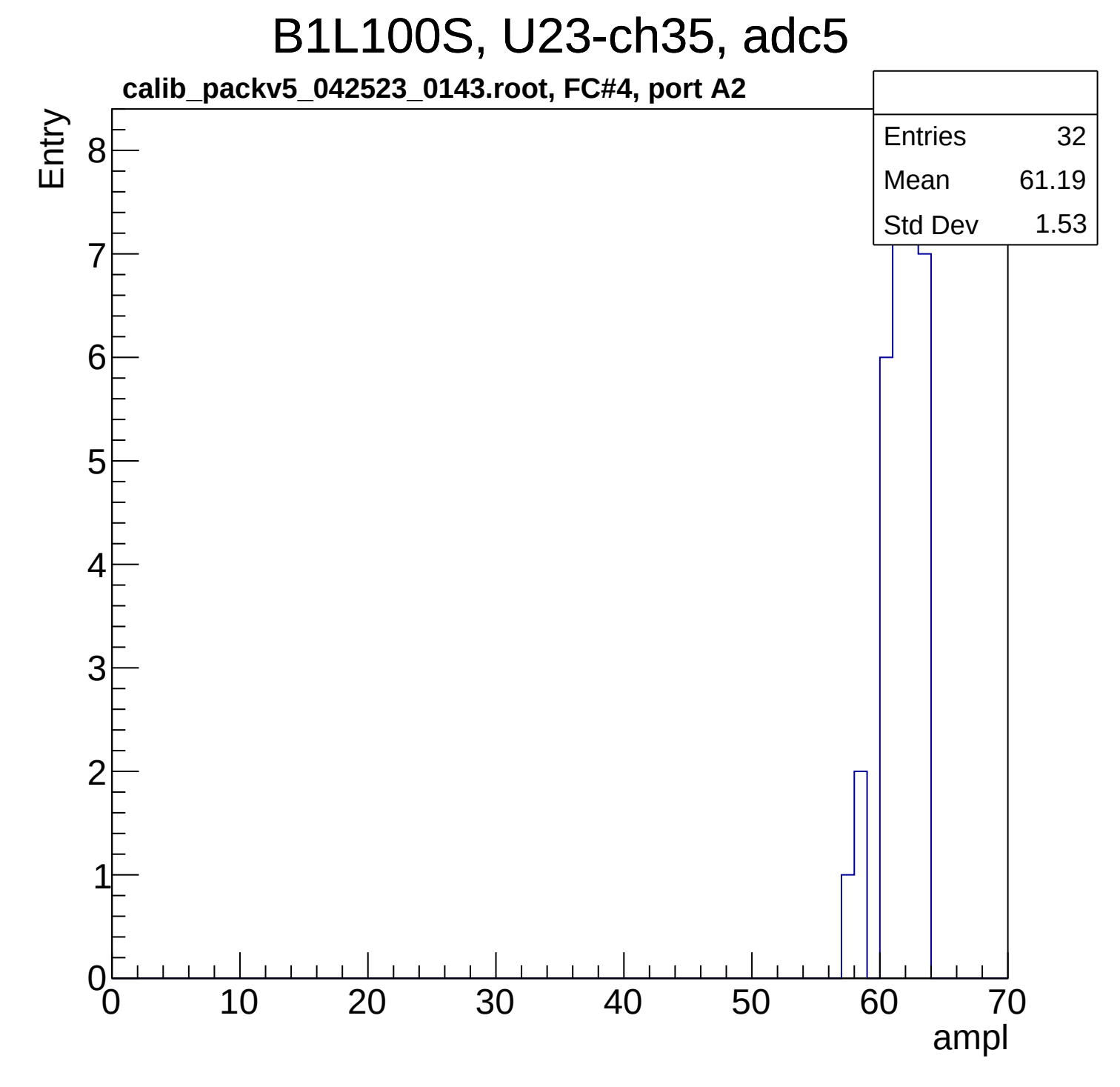

B1L100S, U23-ch35, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.22
Std Dev 8.402
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 61.19
Std Dev 1.53
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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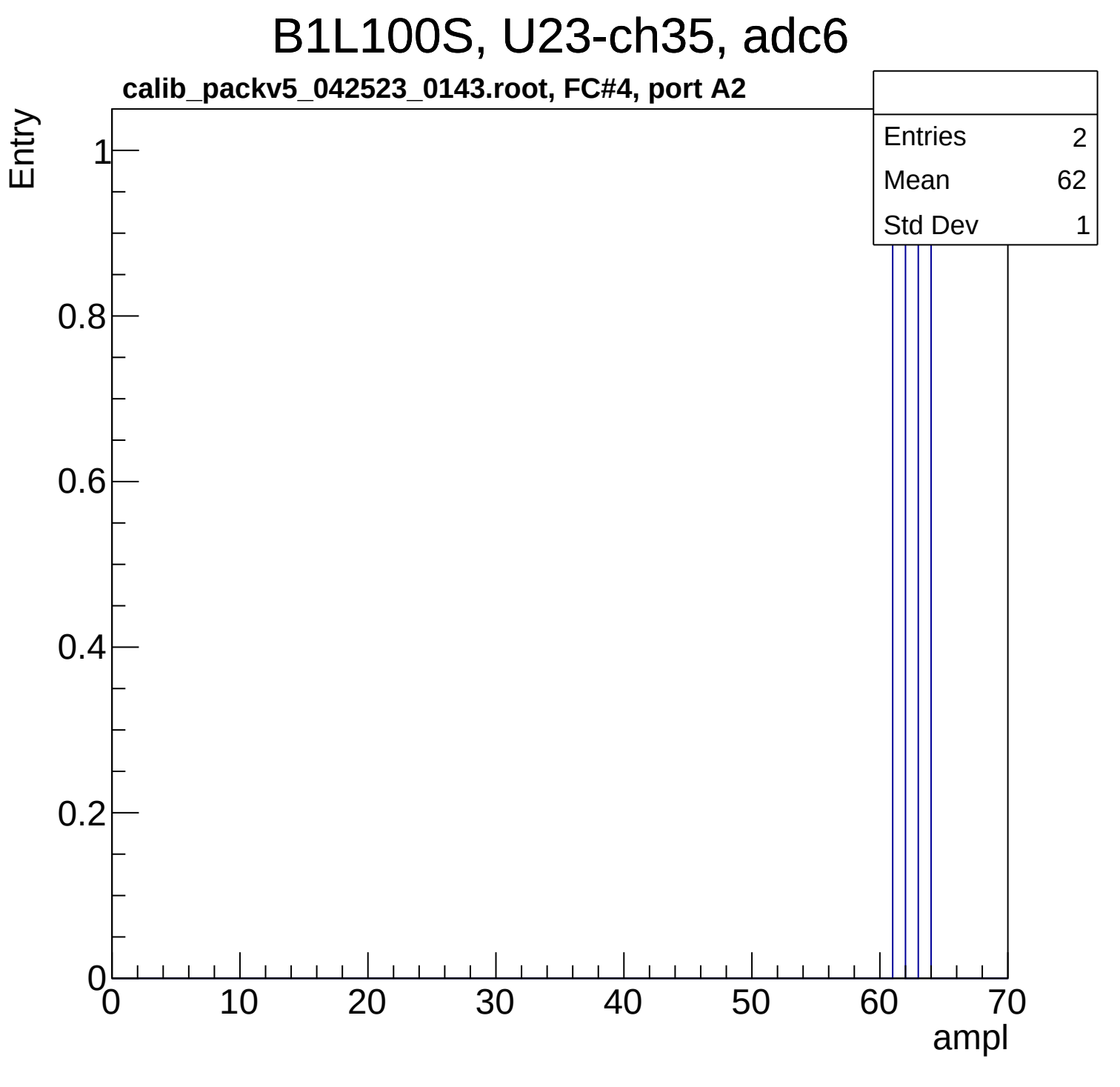
B1L100S, U23-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
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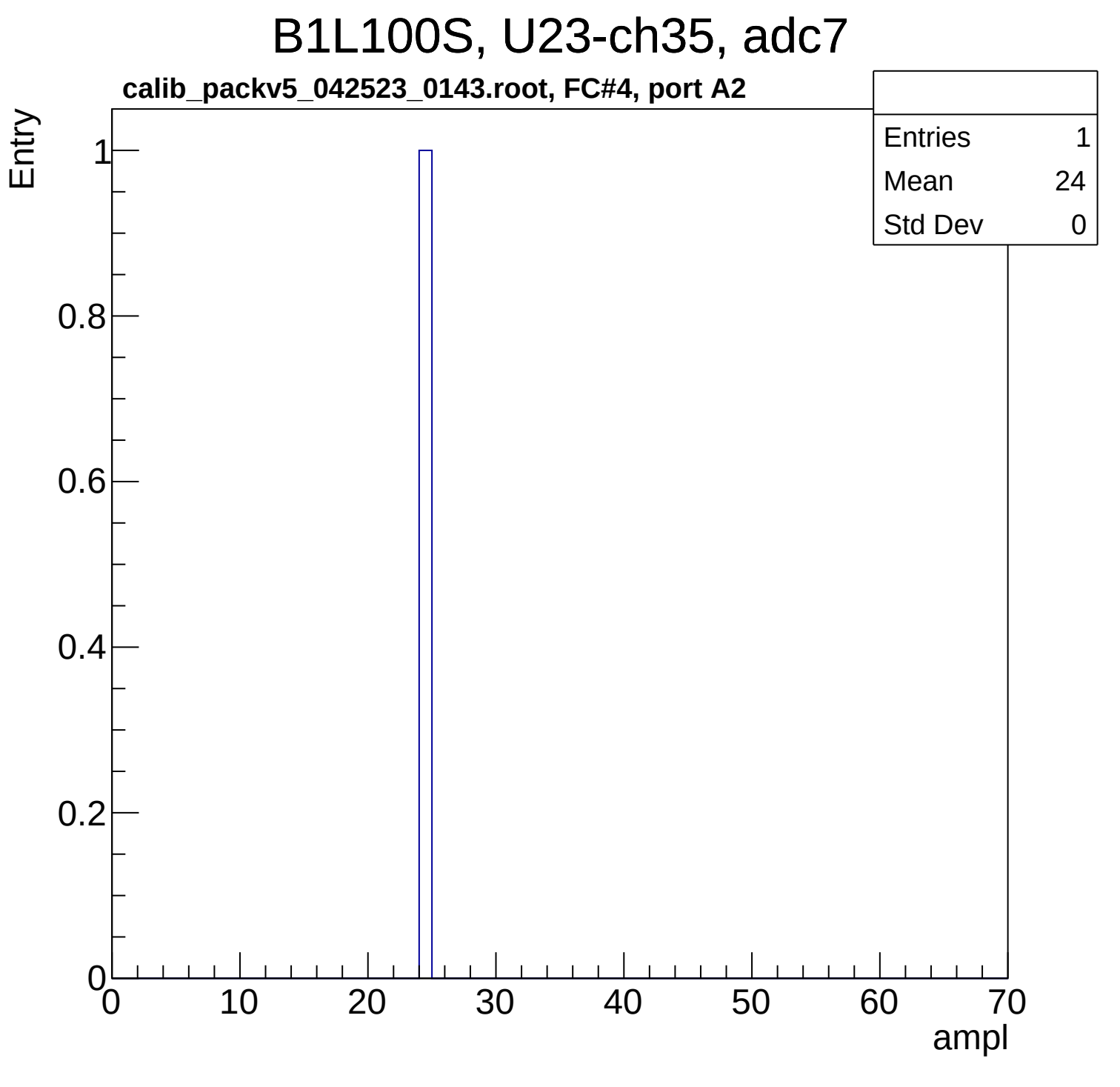
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch35, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
ampl
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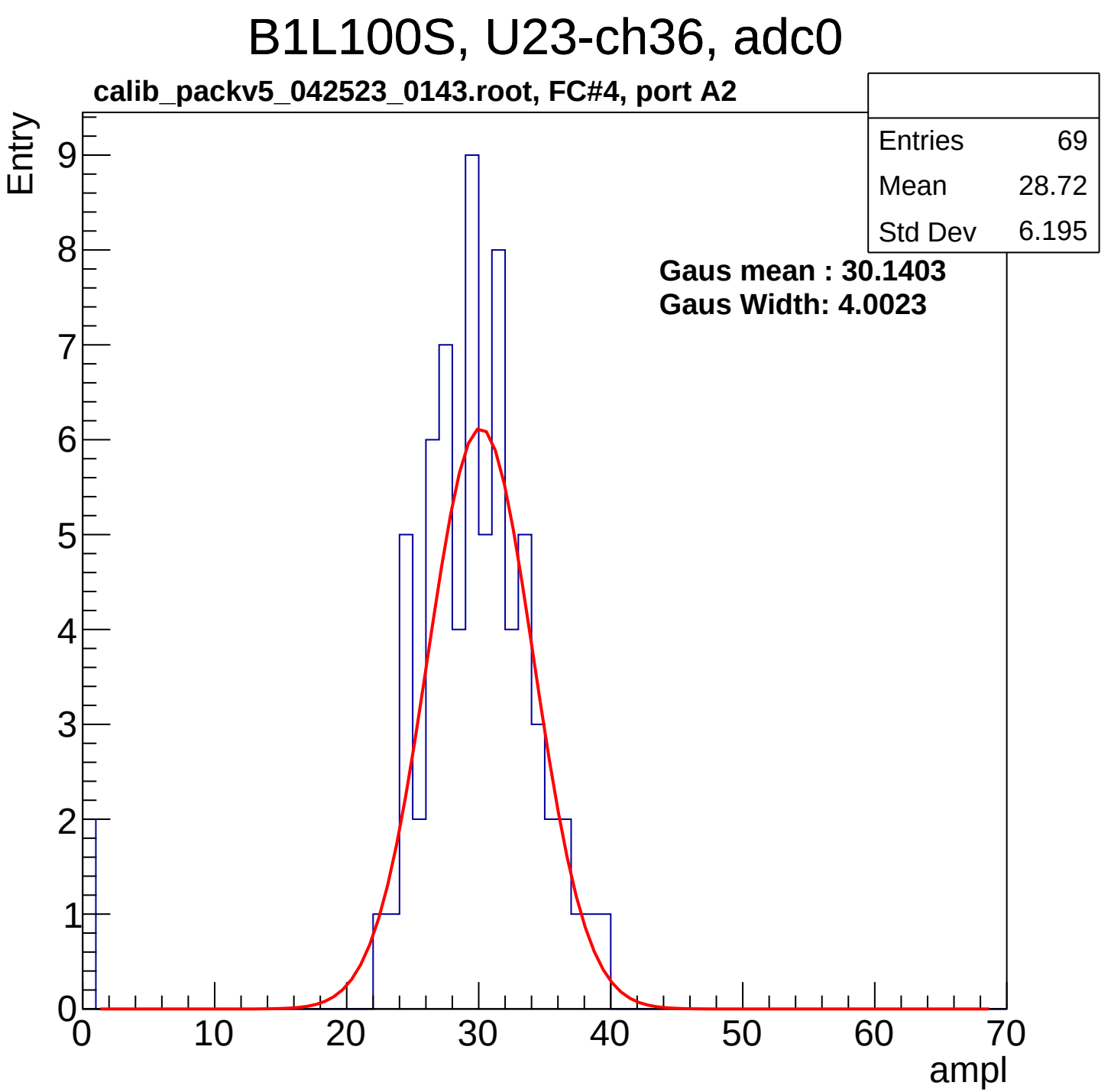
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U23-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.72
Std Dev 6.195
Gaus mean : 30.1403
Gaus Width: 4.0023
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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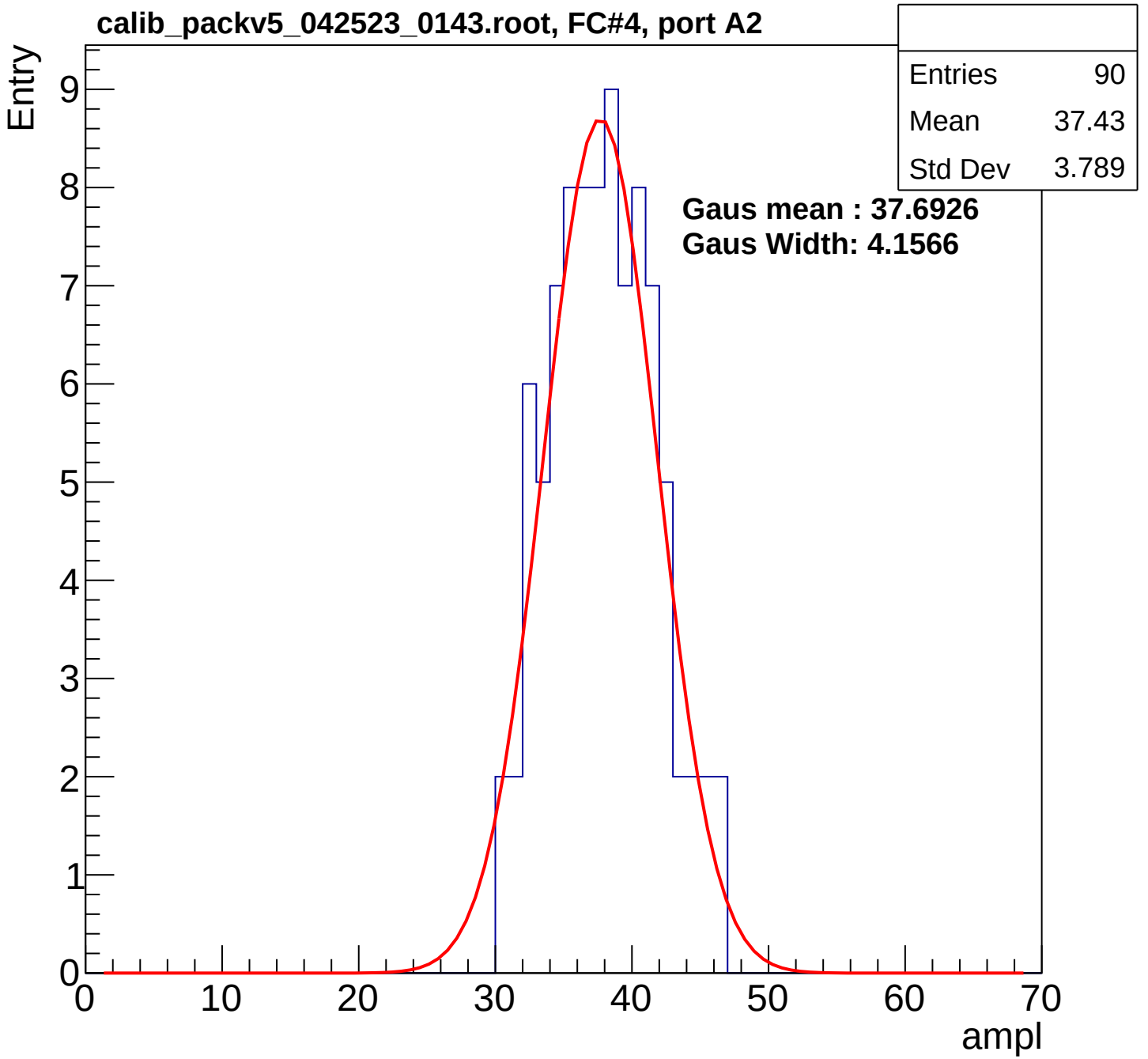
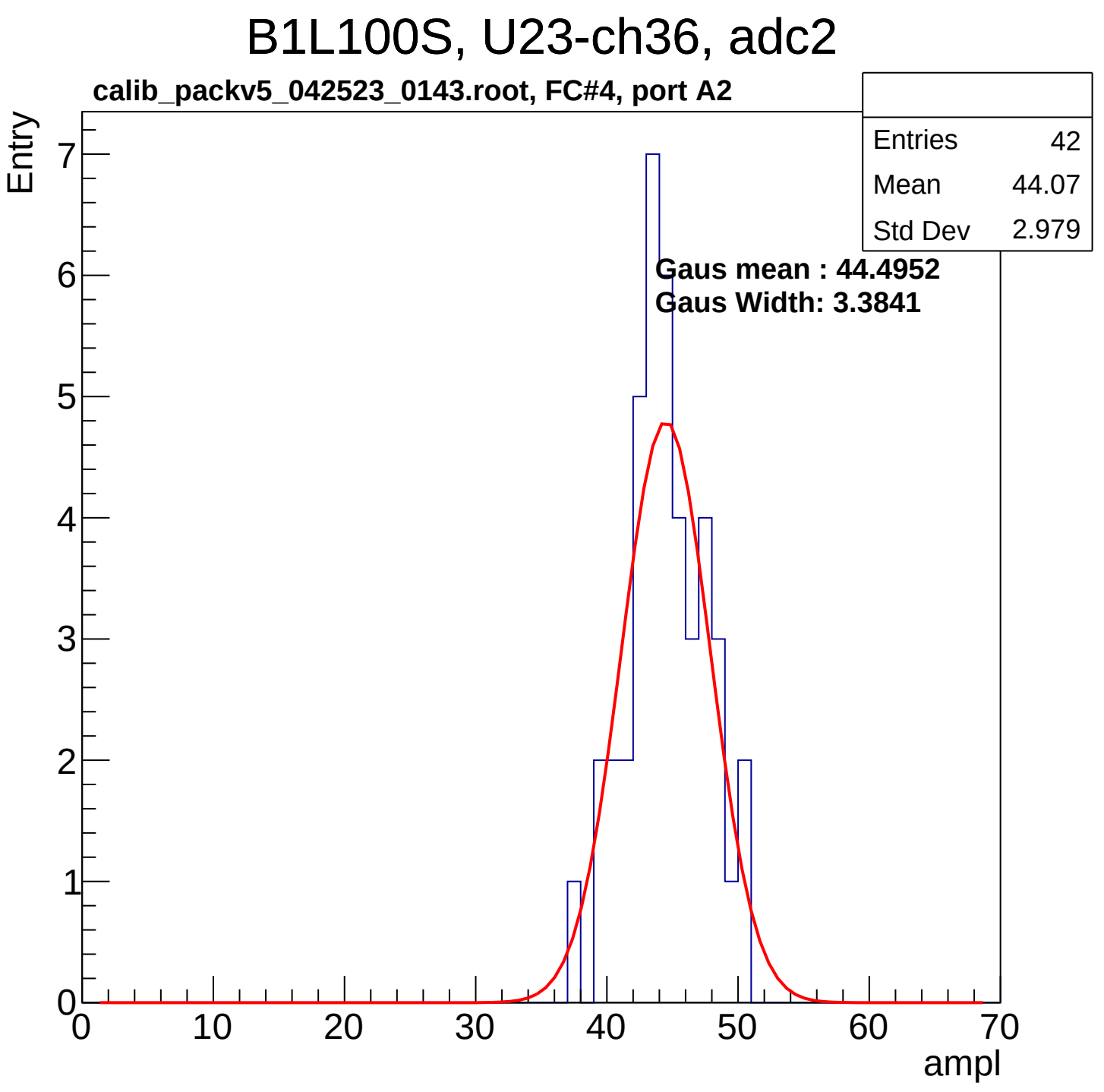

B1L100S, U23-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 37.43
Std Dev 3.789
Gaus mean : 37.6926
Gaus Width: 4.1566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 44.07
Std Dev 2.979
Gaus mean : 44.4952
Gaus Width: 3.3841
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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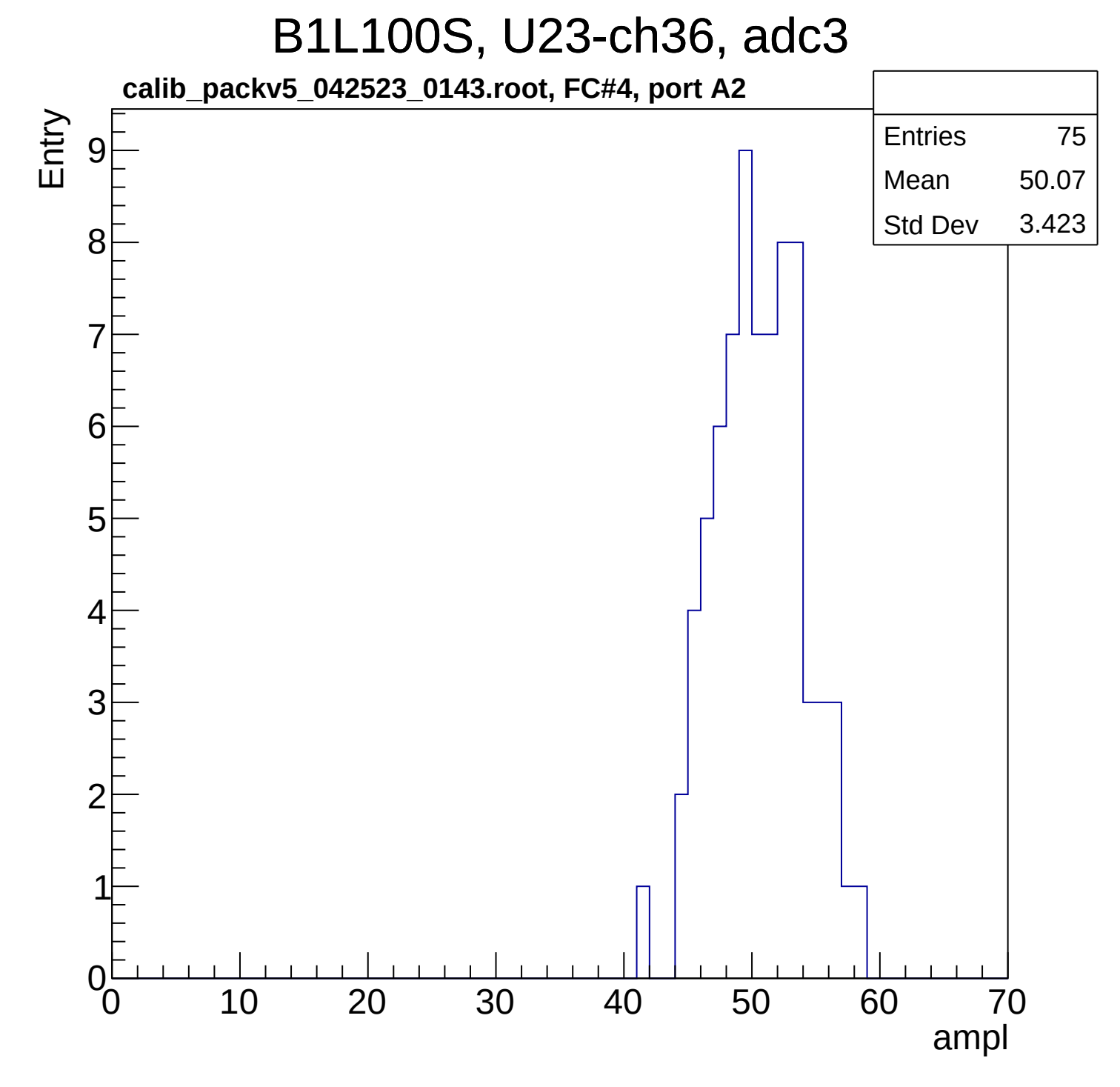
B1L100S, U23-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.07
Std Dev 3.423
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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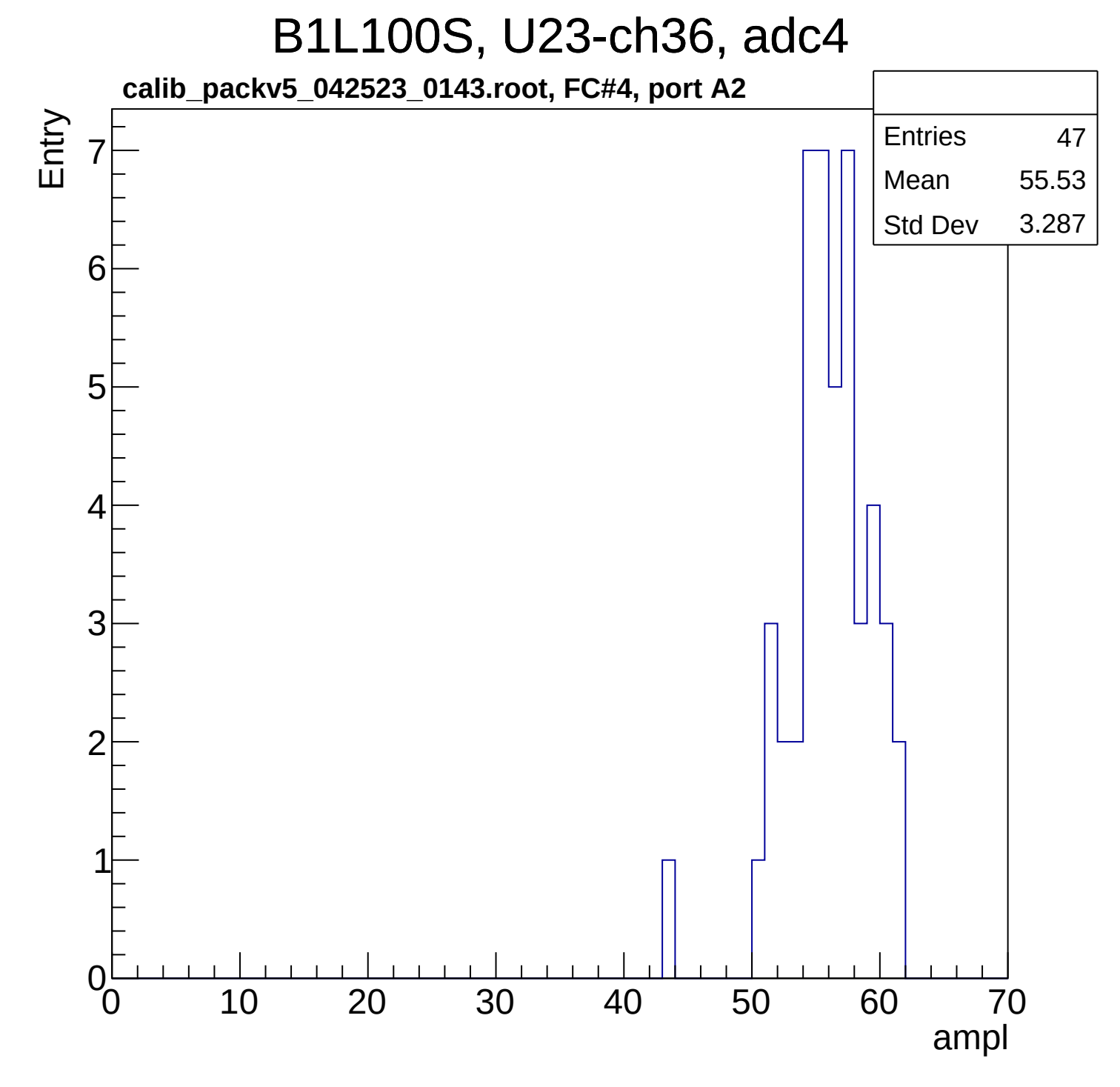
B1L100S, U23-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.53
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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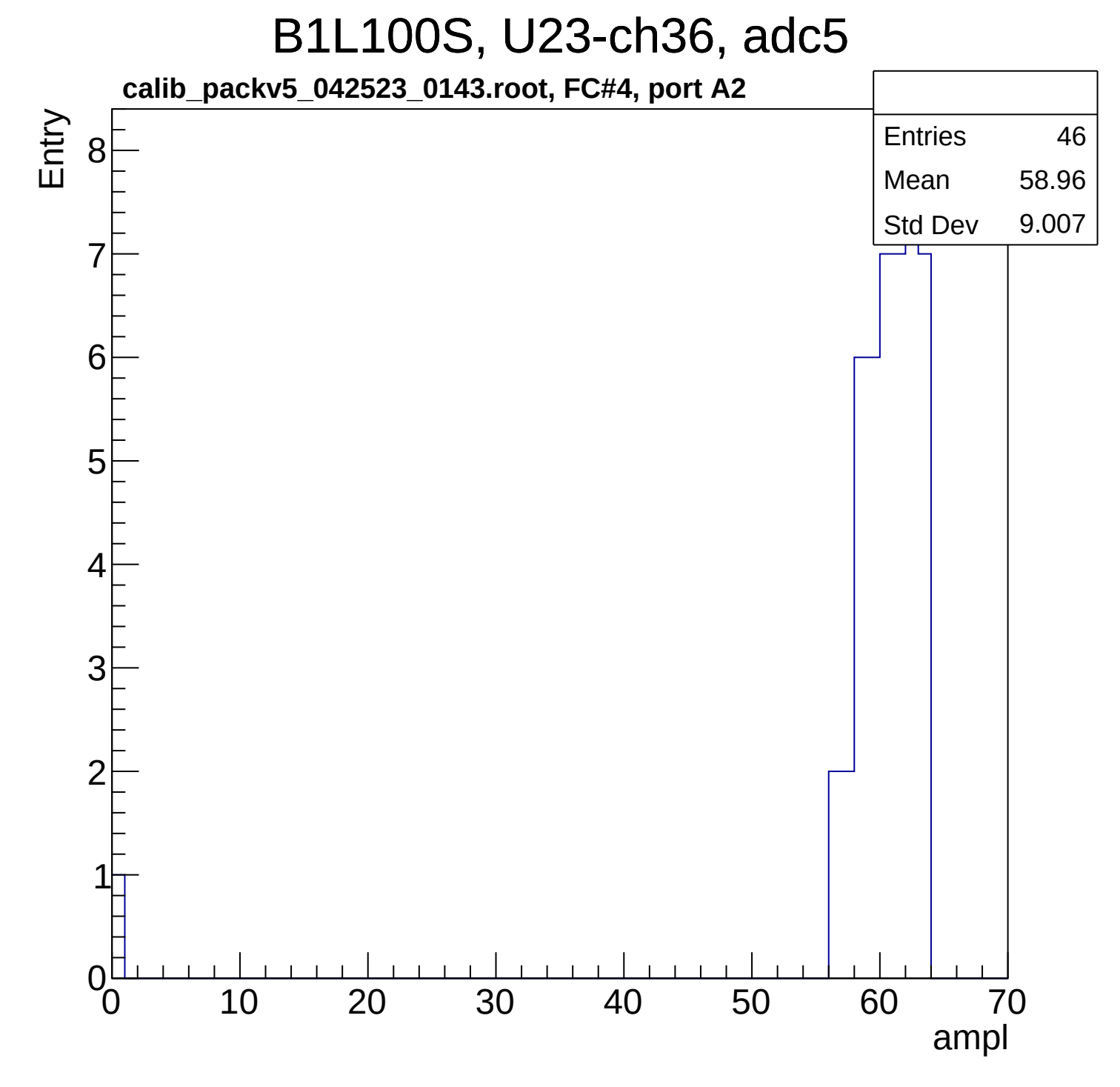
B1L100S, U23-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.96
Std Dev 9.007
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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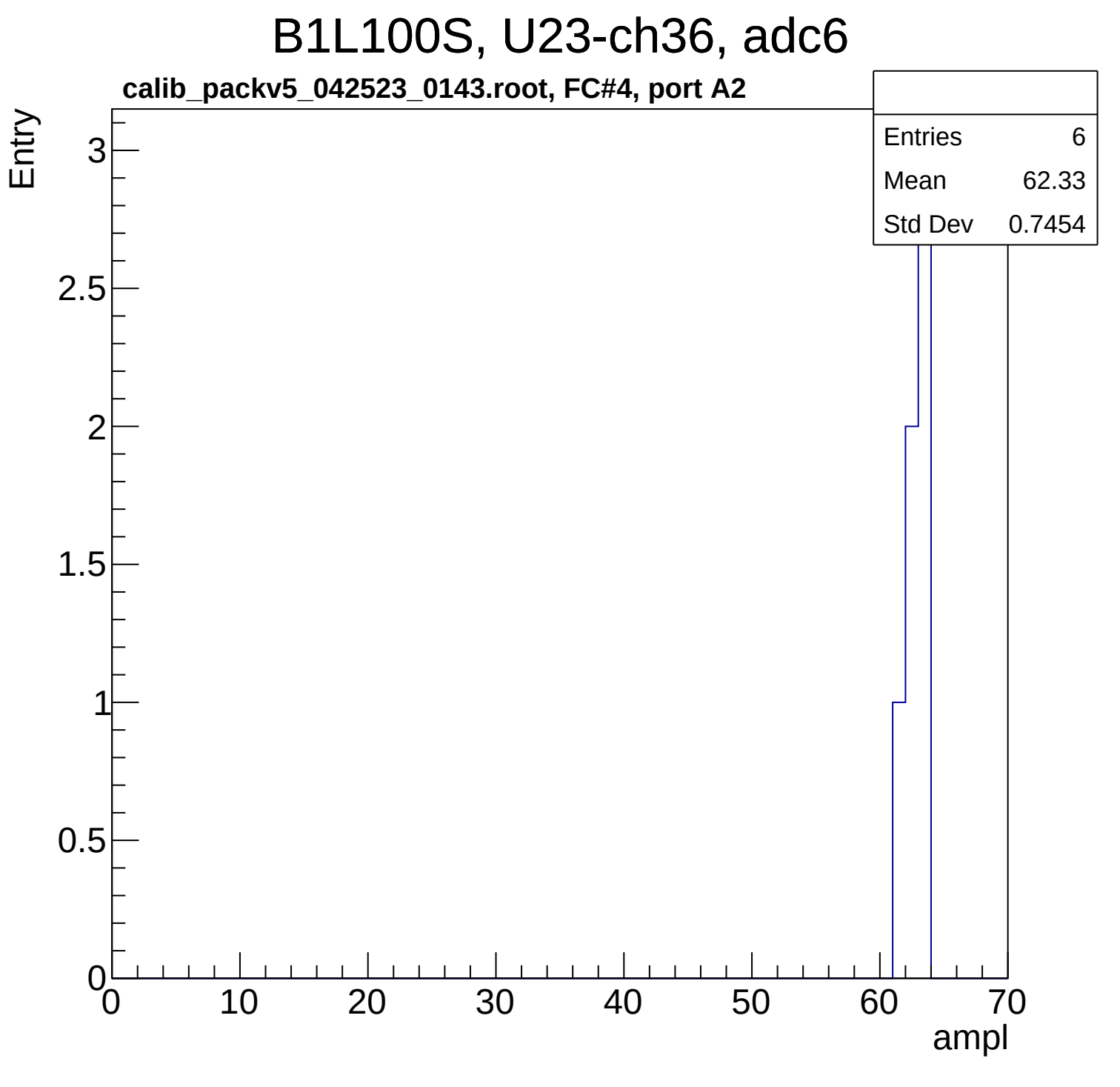
B1L100S, U23-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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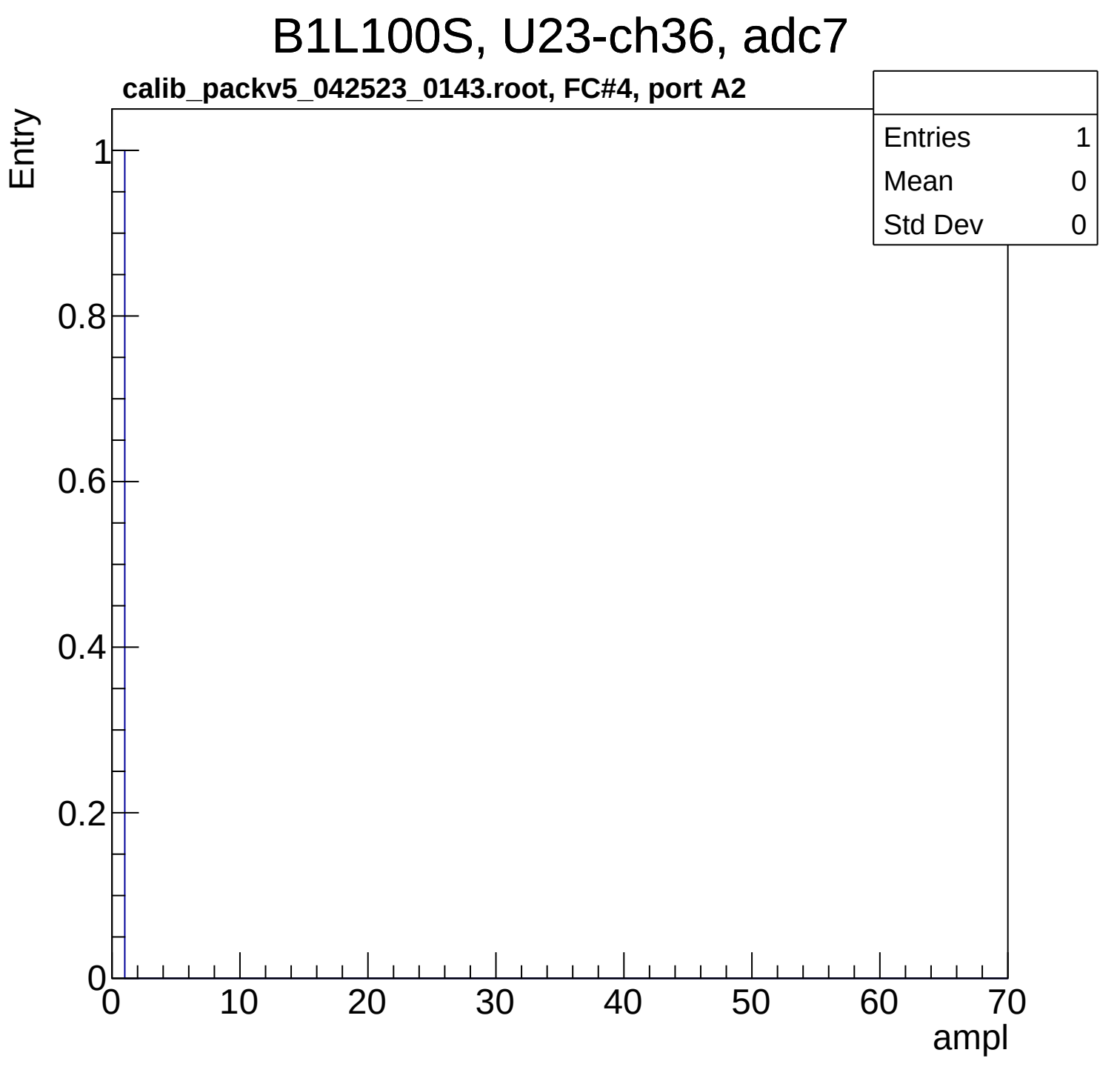
40
50
60
70


B1L100S, U23-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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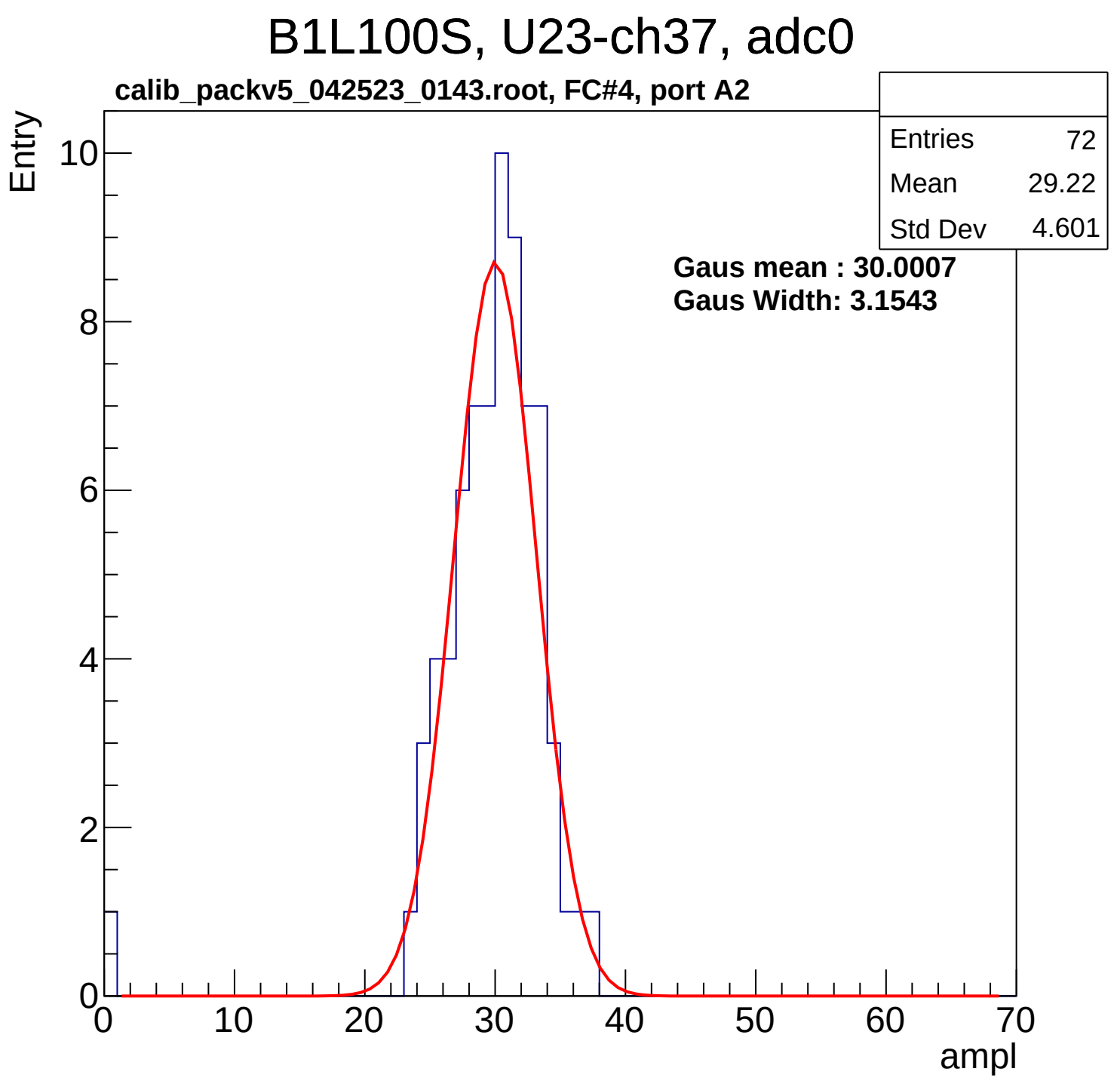
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.22
Std Dev 4.601
Gaus mean : 30.0007
Gaus Width: 3.1543
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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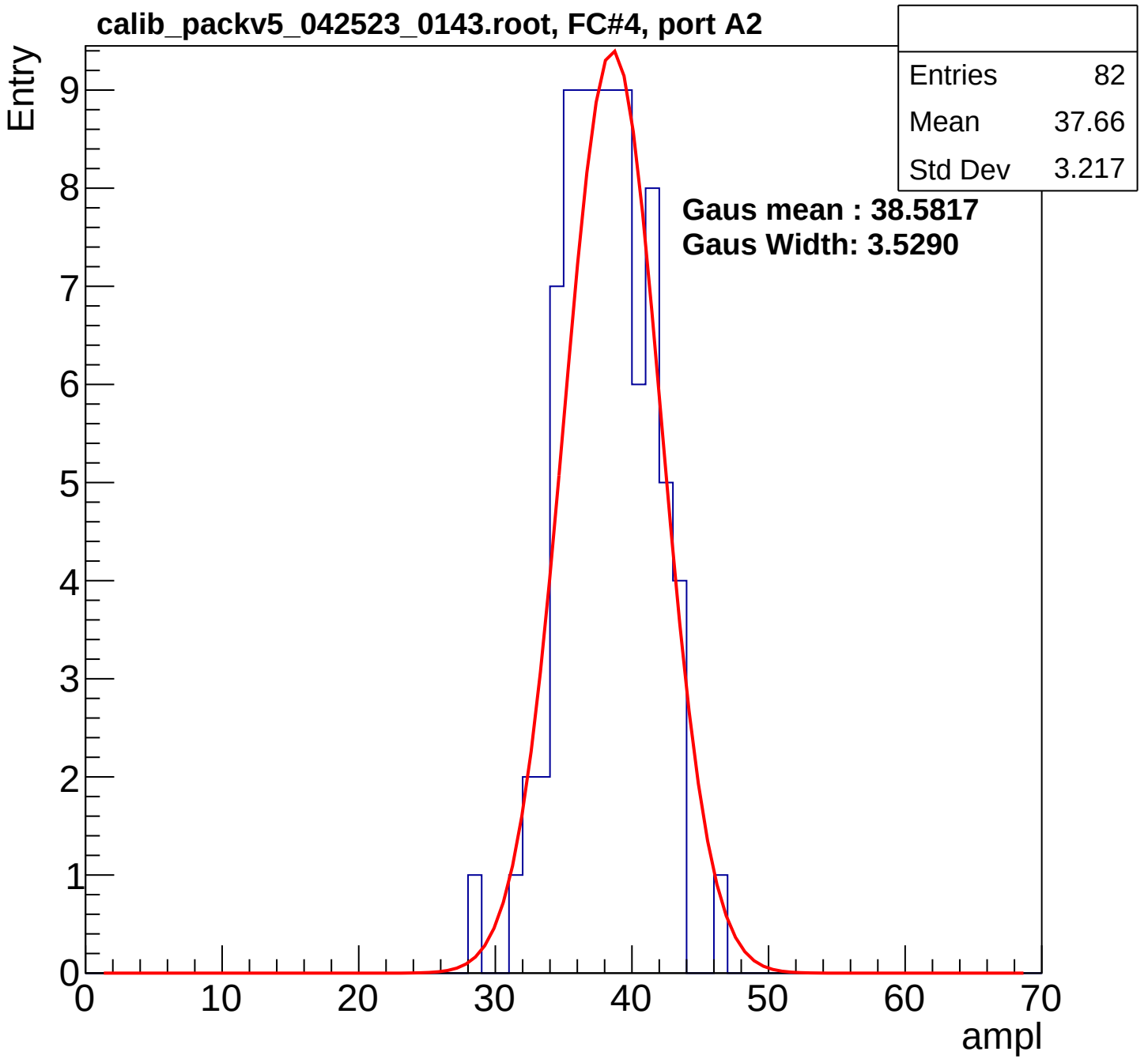
B1L100S, U23-ch37, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 37.66
Std Dev 3.217
Gaus mean : 38.5817
Gaus Width: 3.5290
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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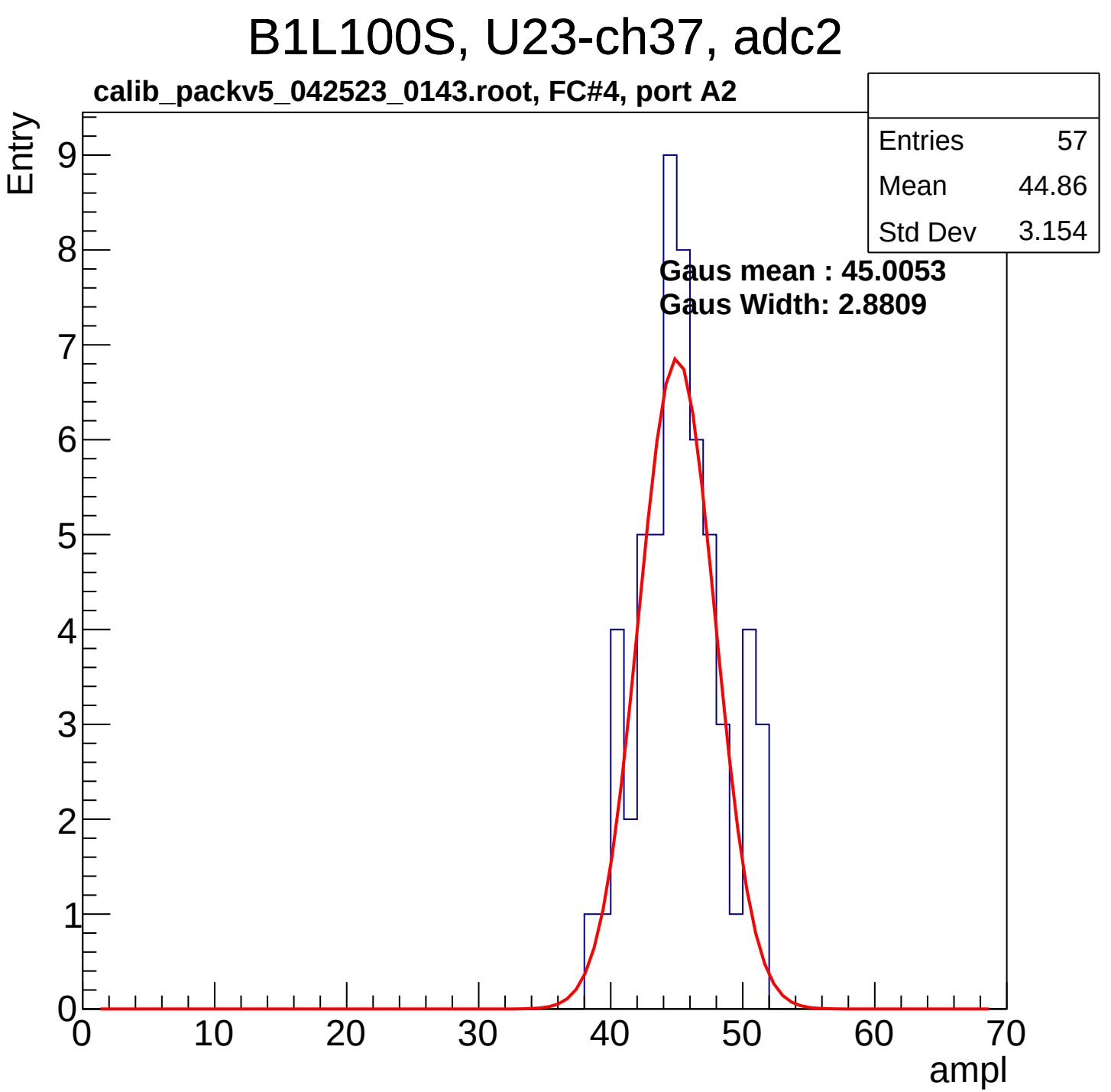
B1L100S, U23-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.86
Std Dev 3.154
Gaus mean : 45.0053
Gaus Width: 2.8809
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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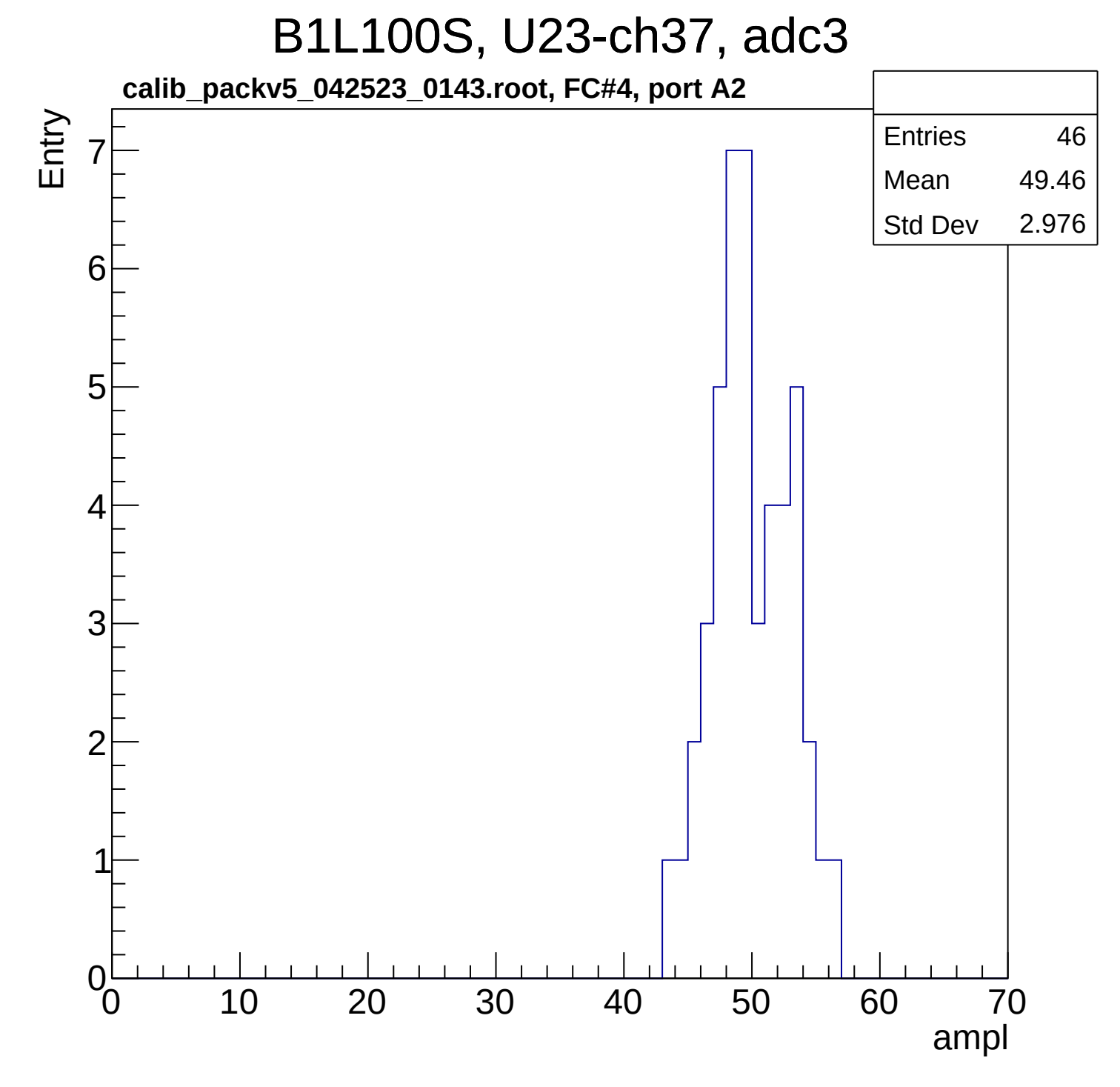
B1L100S, U23-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 49.46
Std Dev 2.976
Entry
ampl
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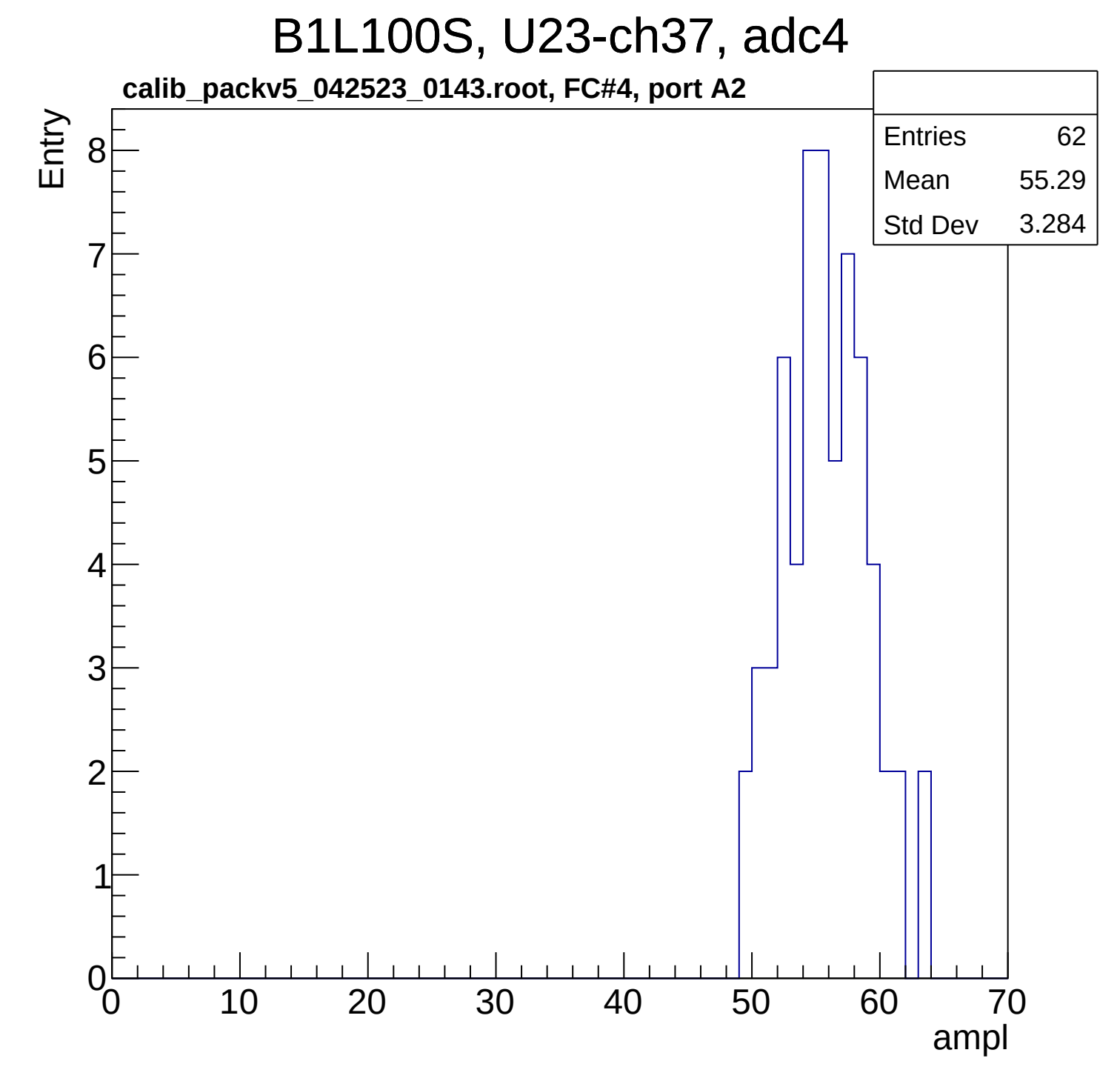
B1L100S, U23-ch37, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.29
Std Dev 3.284
Entry
ampl
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3
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5
6
7
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0
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60
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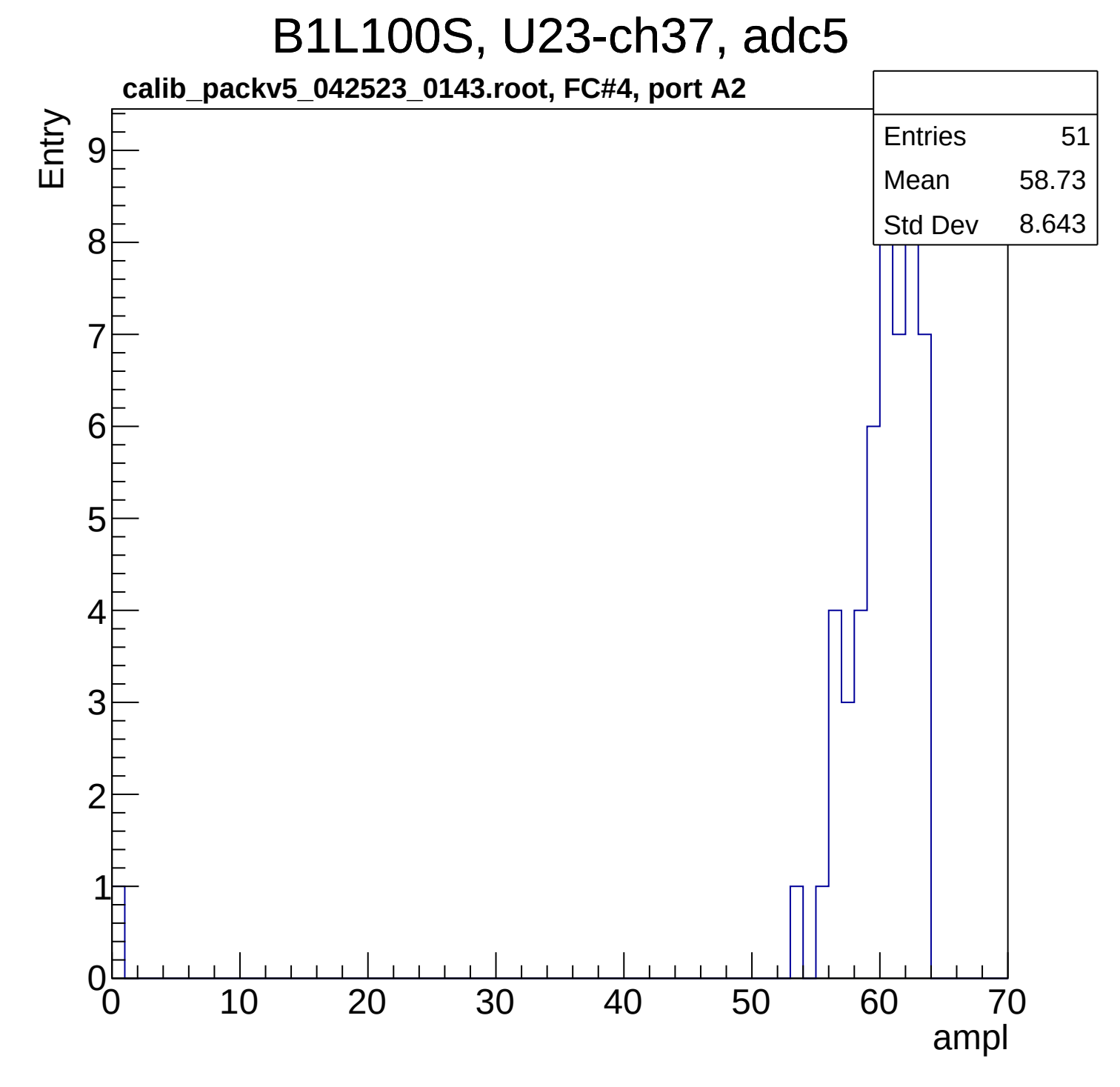
B1L100S, U23-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.73
Std Dev 8.643
Entry
ampl
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3
4
5
6
7
8
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0
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20
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60
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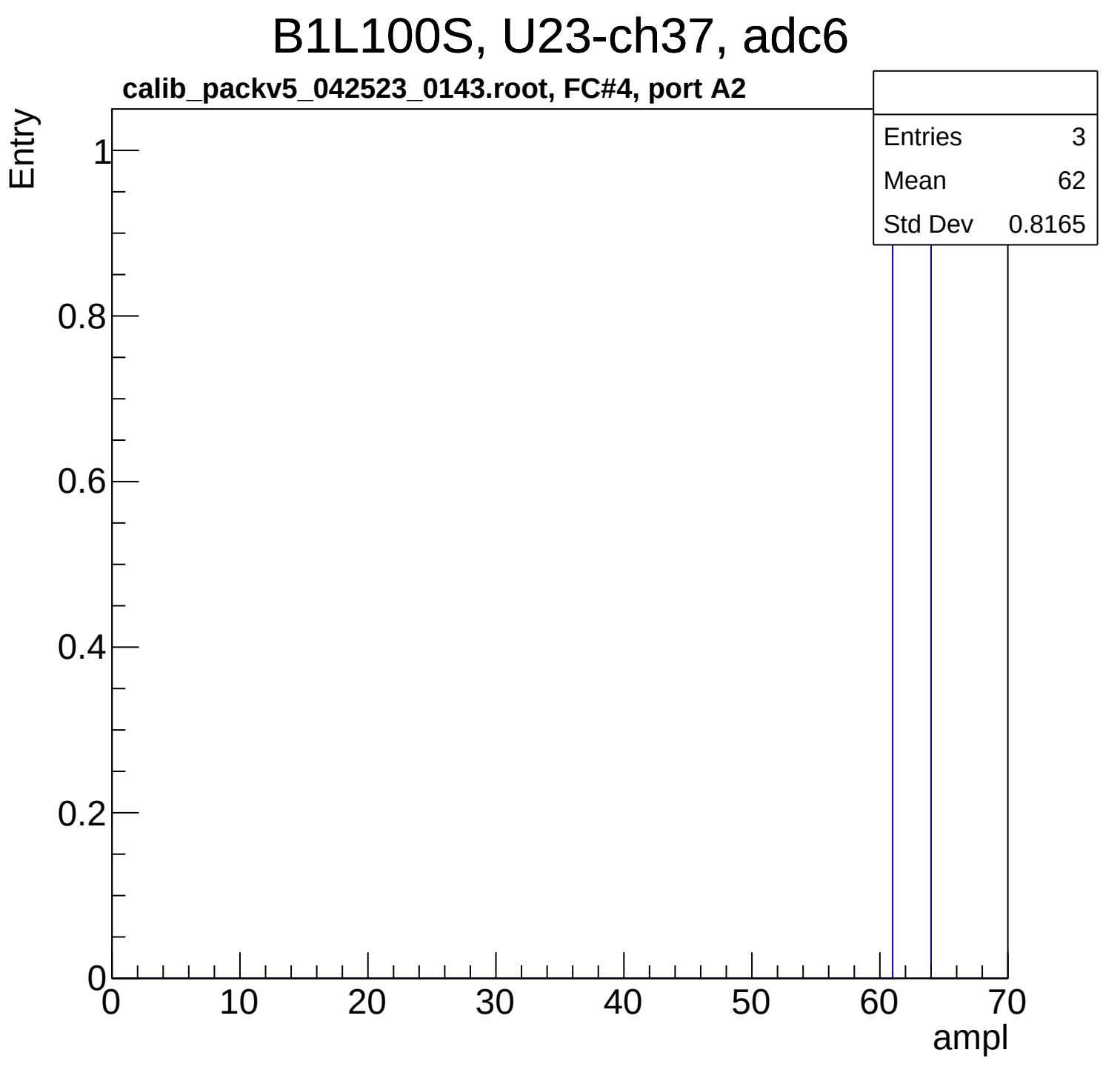
B1L100S, U23-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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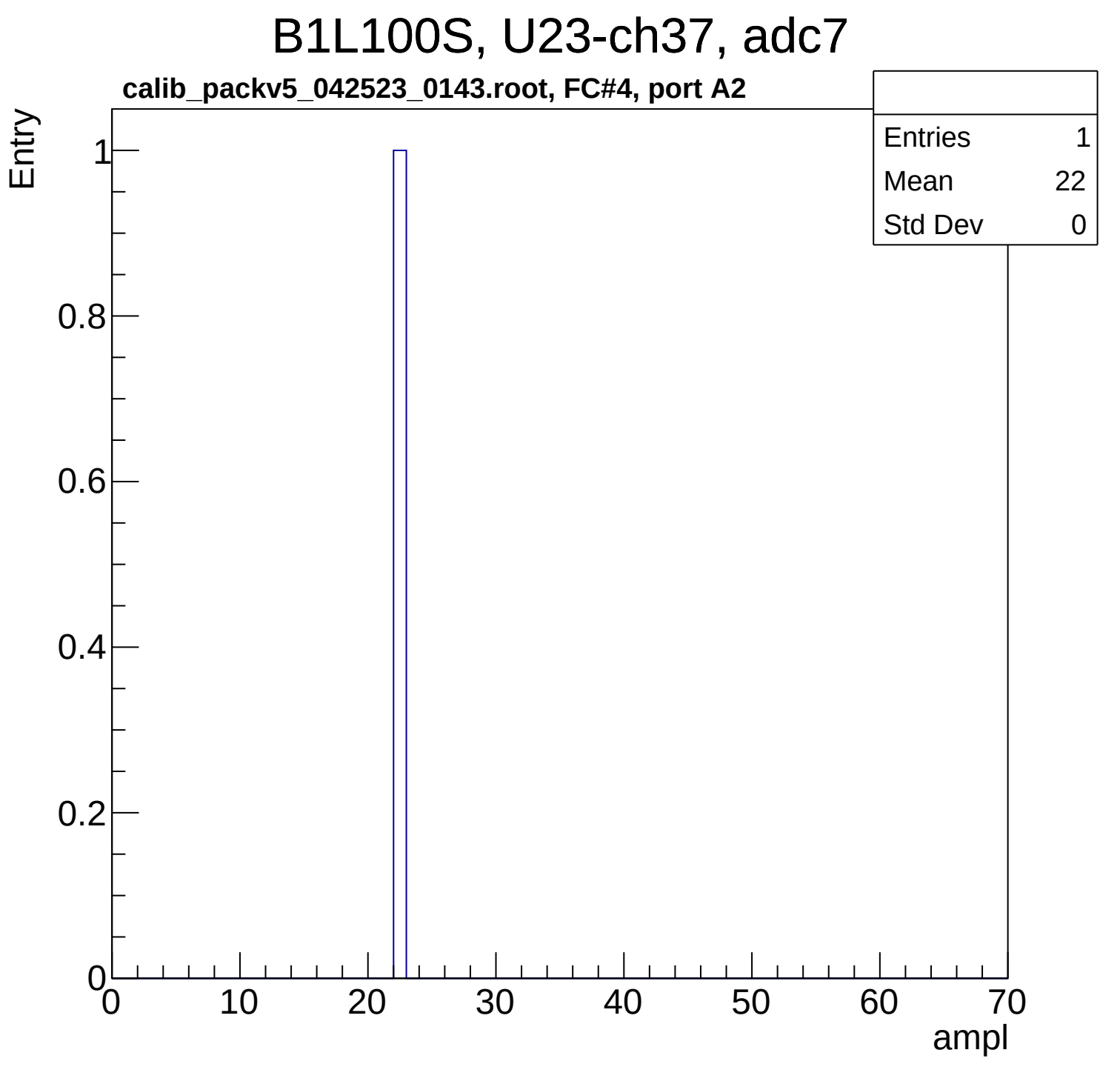
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
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10
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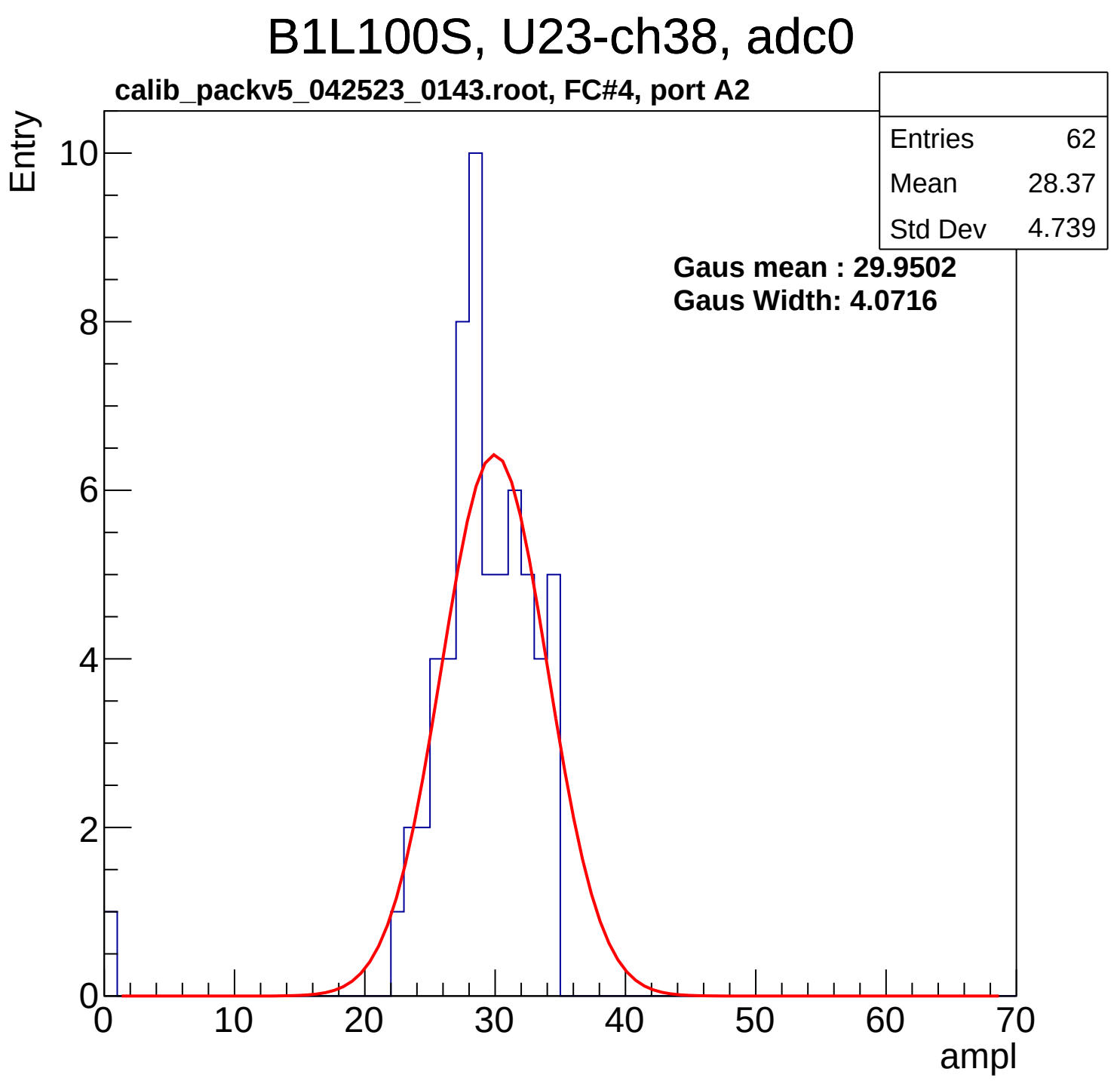
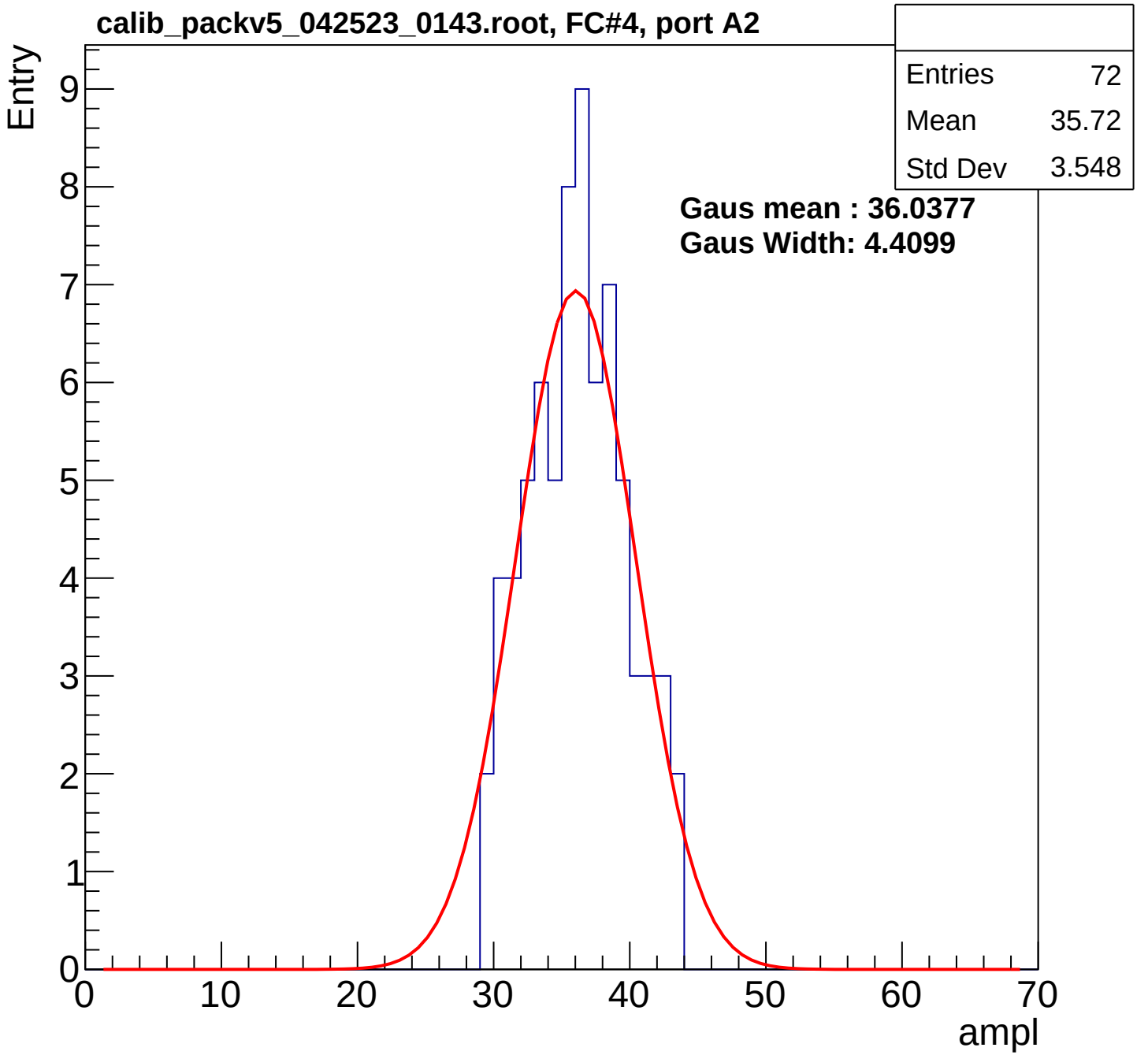
40
50
60
70
ampl


B1L100S, U23-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.37
Std Dev 4.739
Gaus mean : 29.9502
Gaus Width: 4.0716
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 35.72
Std Dev 3.548
Gaus mean : 36.0377
Gaus Width: 4.4099
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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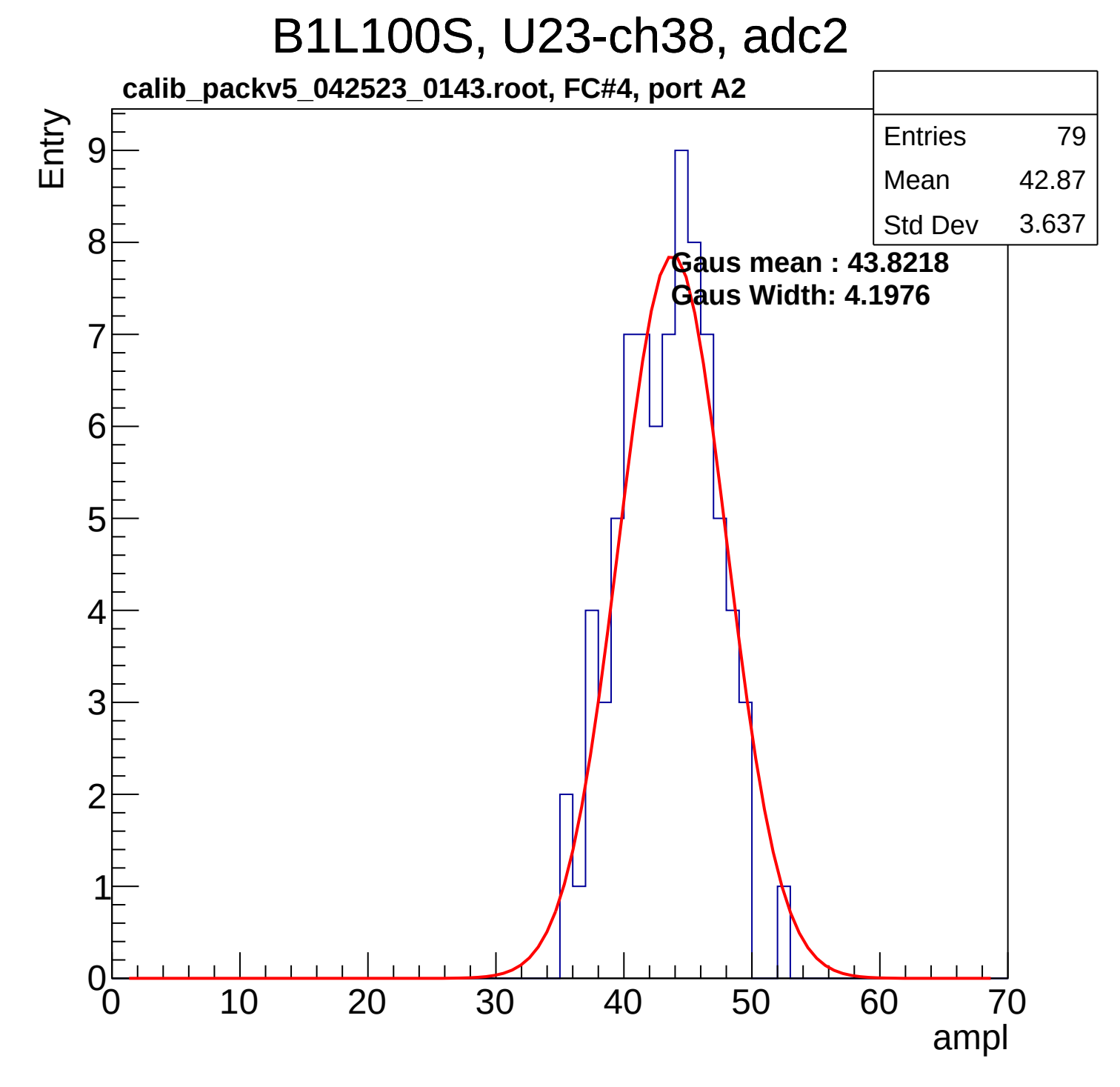
B1L100S, U23-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 42.87
Std Dev 3.637
Gaus mean : 43.8218
Gaus Width: 4.1976
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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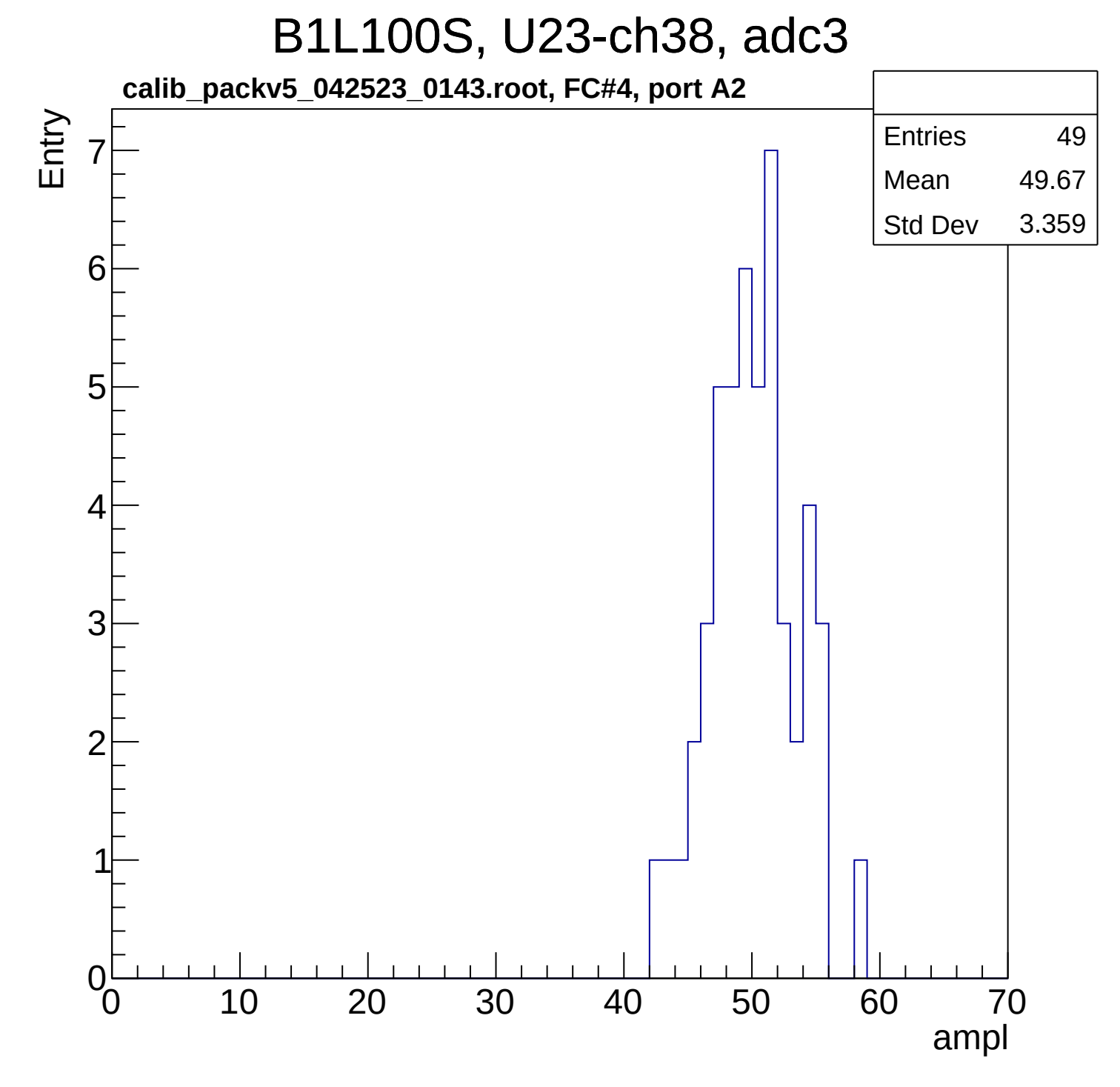
B1L100S, U23-ch38, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 49.67
Std Dev 3.359
Entry
ampl
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3
4
5
6
7
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60
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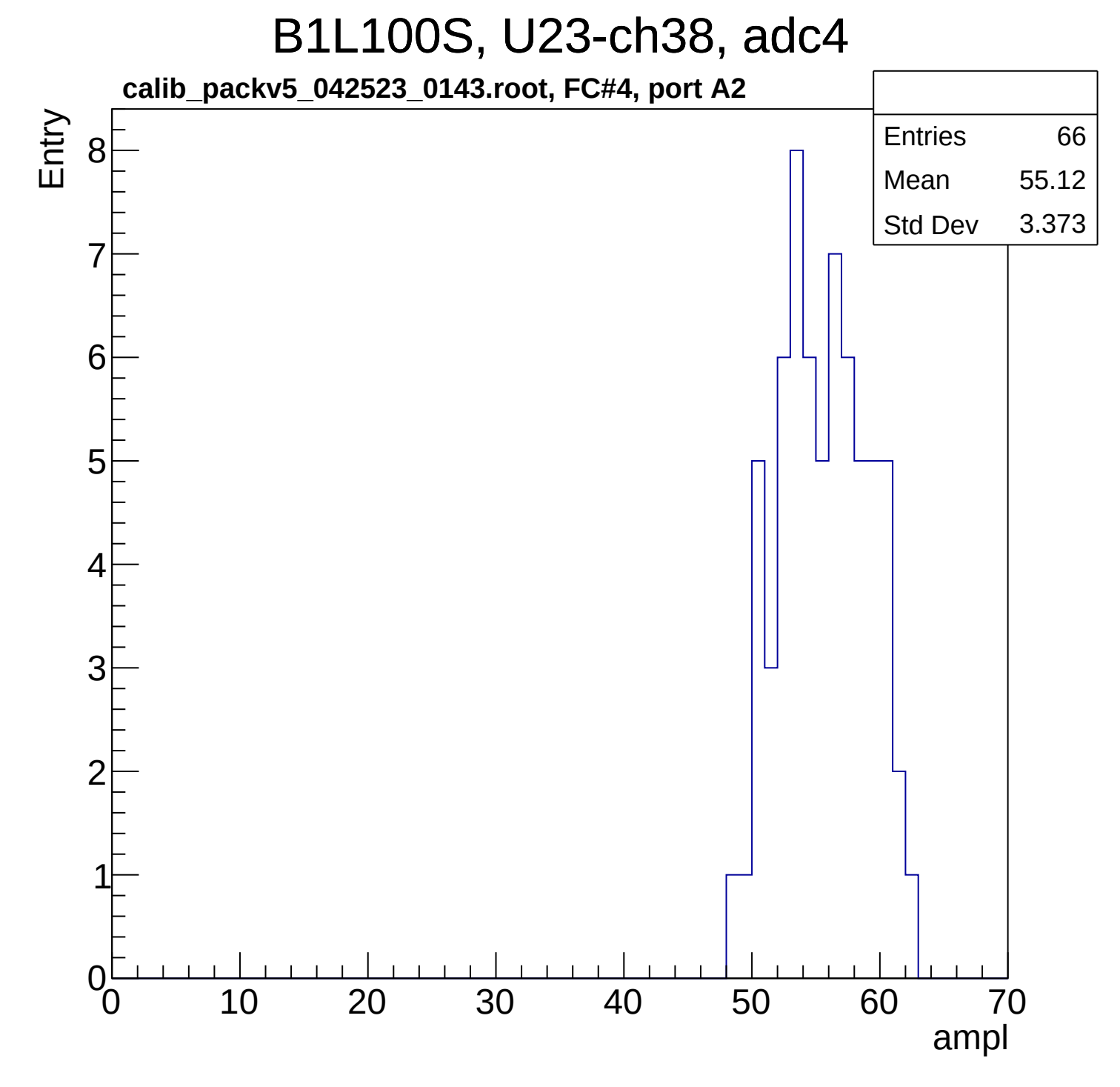
B1L100S, U23-ch38, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.12
Std Dev 3.373
Entry
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4
5
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7
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20
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50
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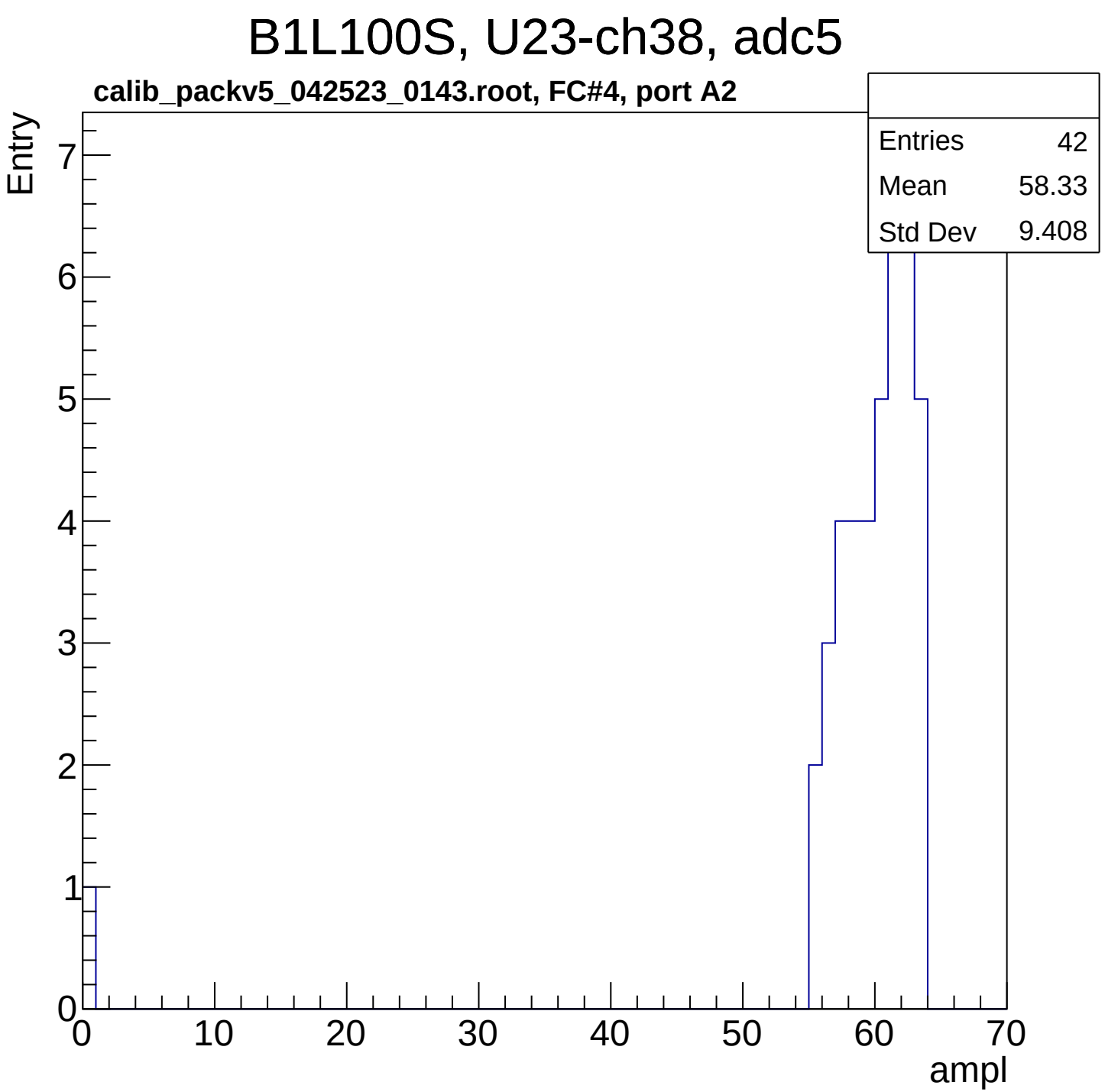
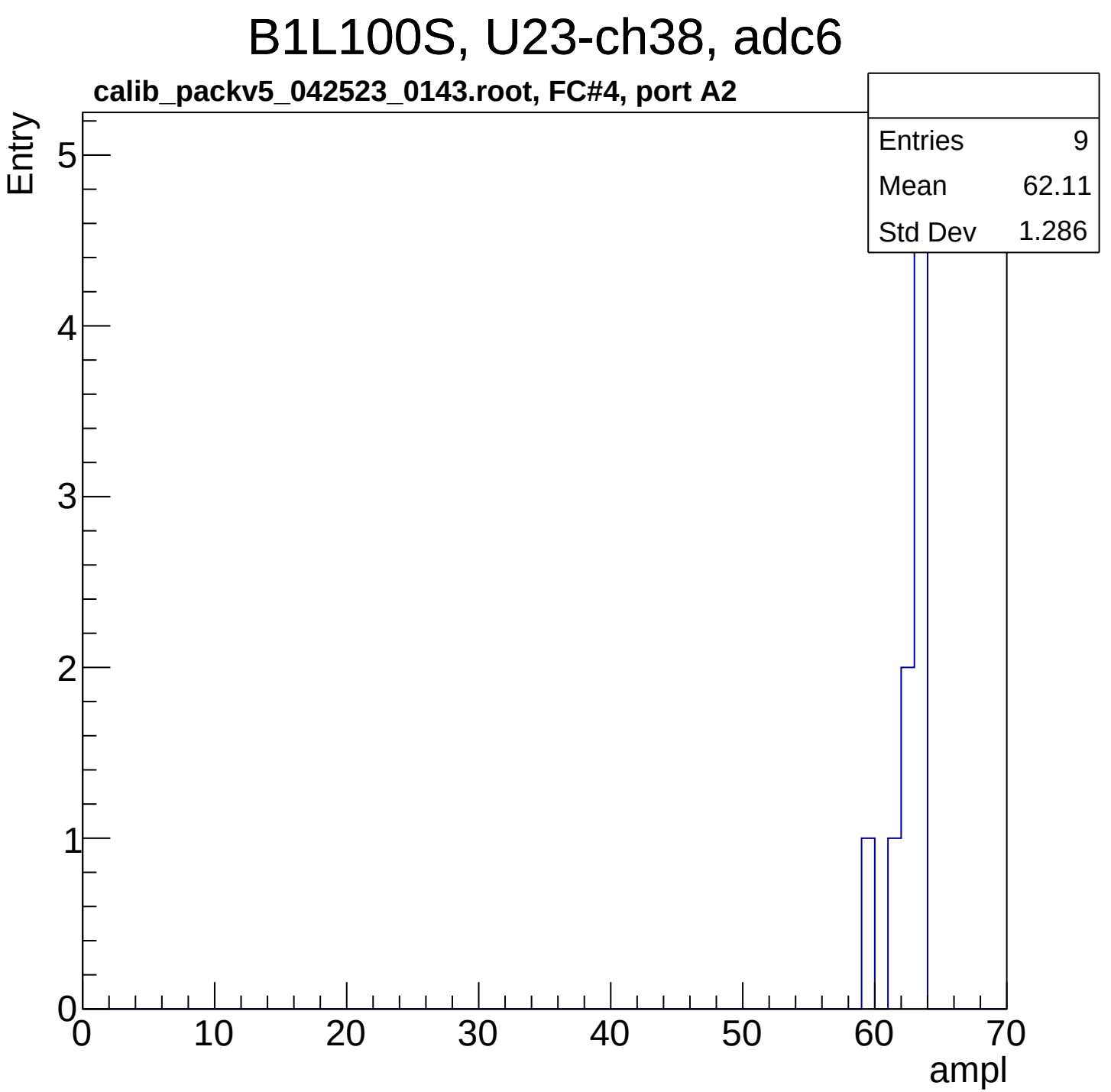
70
ampl


B1L100S, U23-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.33
Std Dev 9.408
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch38, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62.11
Std Dev 1.286
Entry
ampl
0
1
2
3
4
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0
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40
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60
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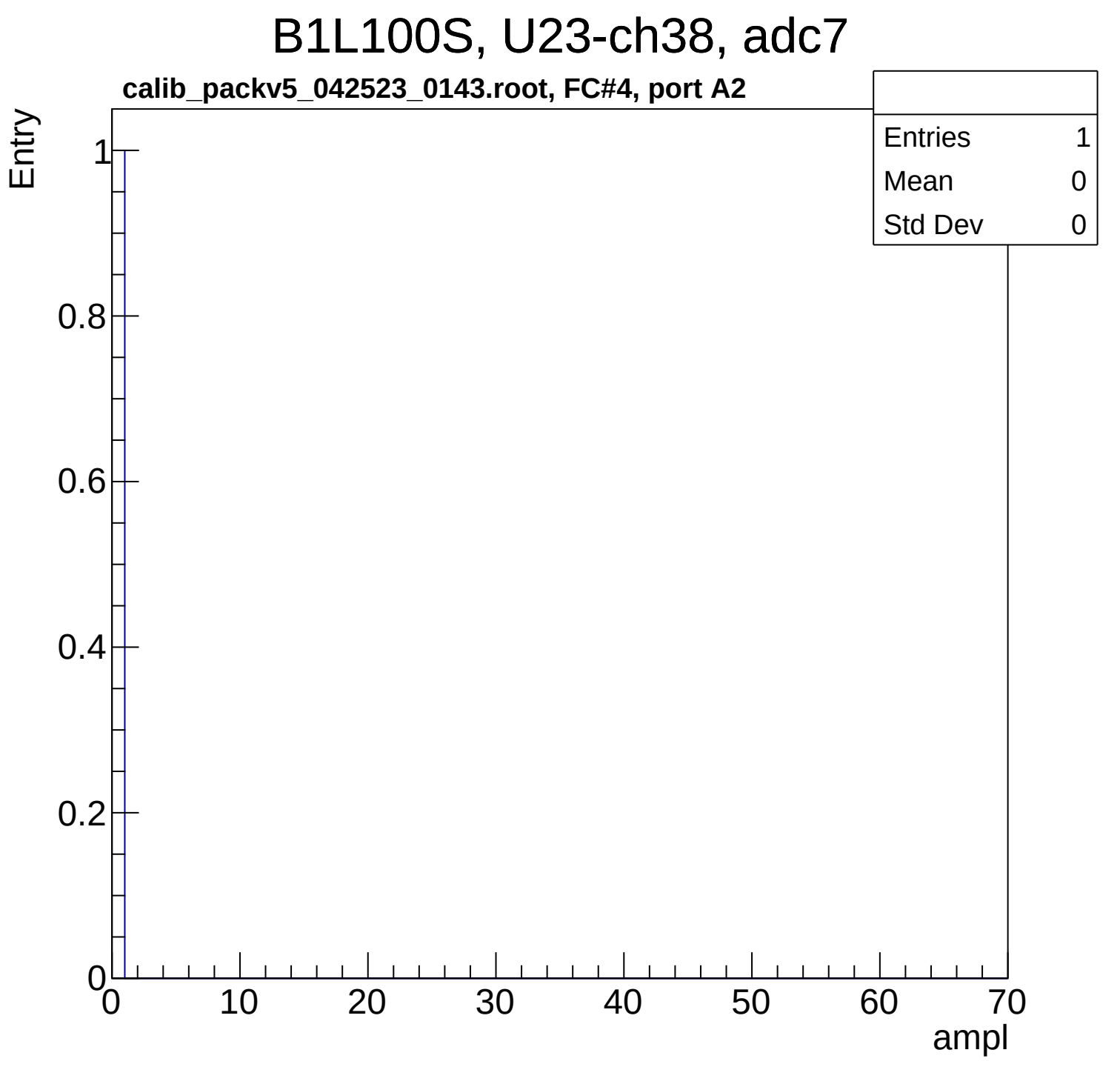
B1L100S, U23-ch38, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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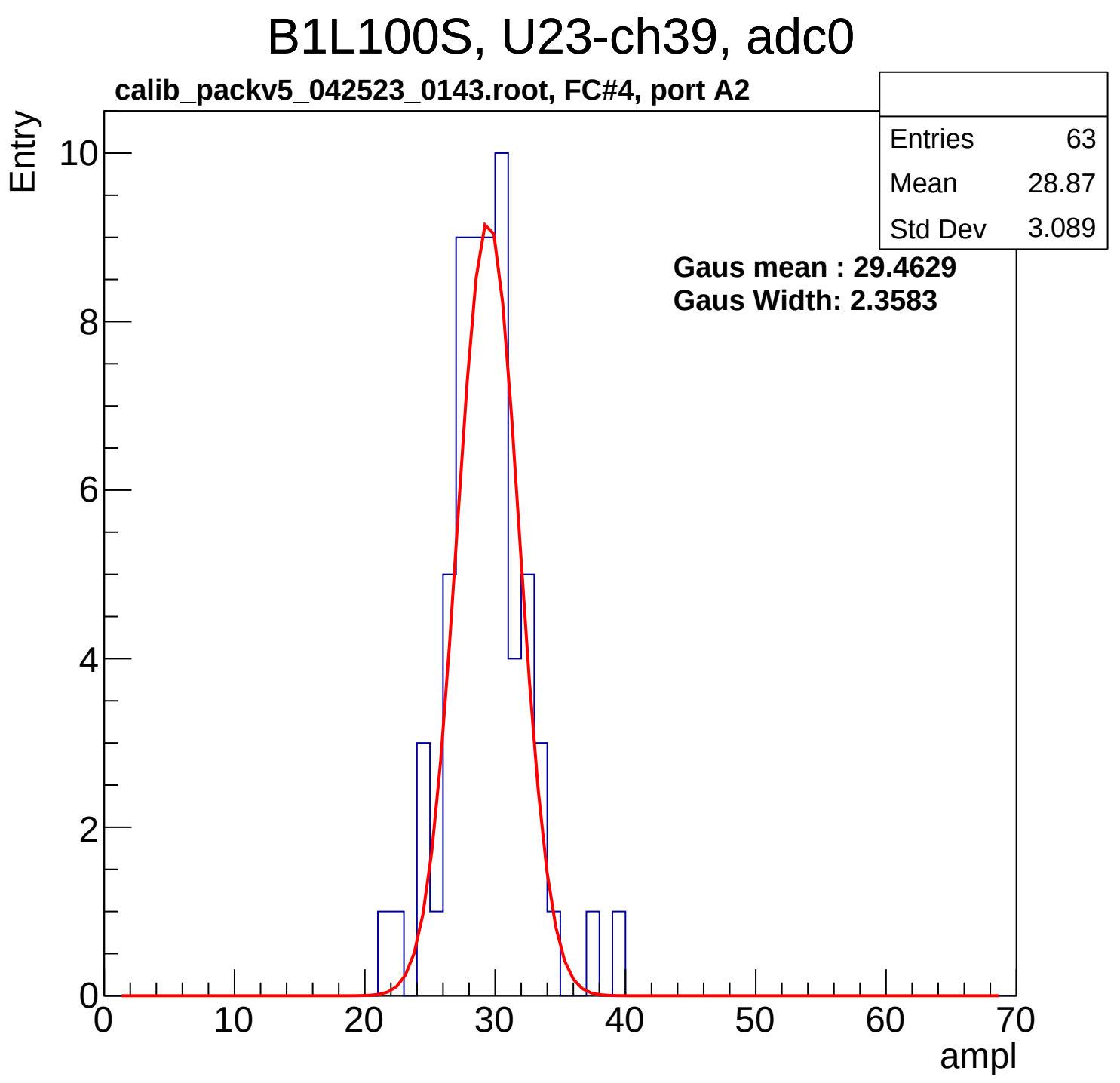
40
50
60
70
ampl


B1L100S, U23-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.87
Std Dev 3.089
Gaus mean : 29.4629
Gaus Width: 2.3583
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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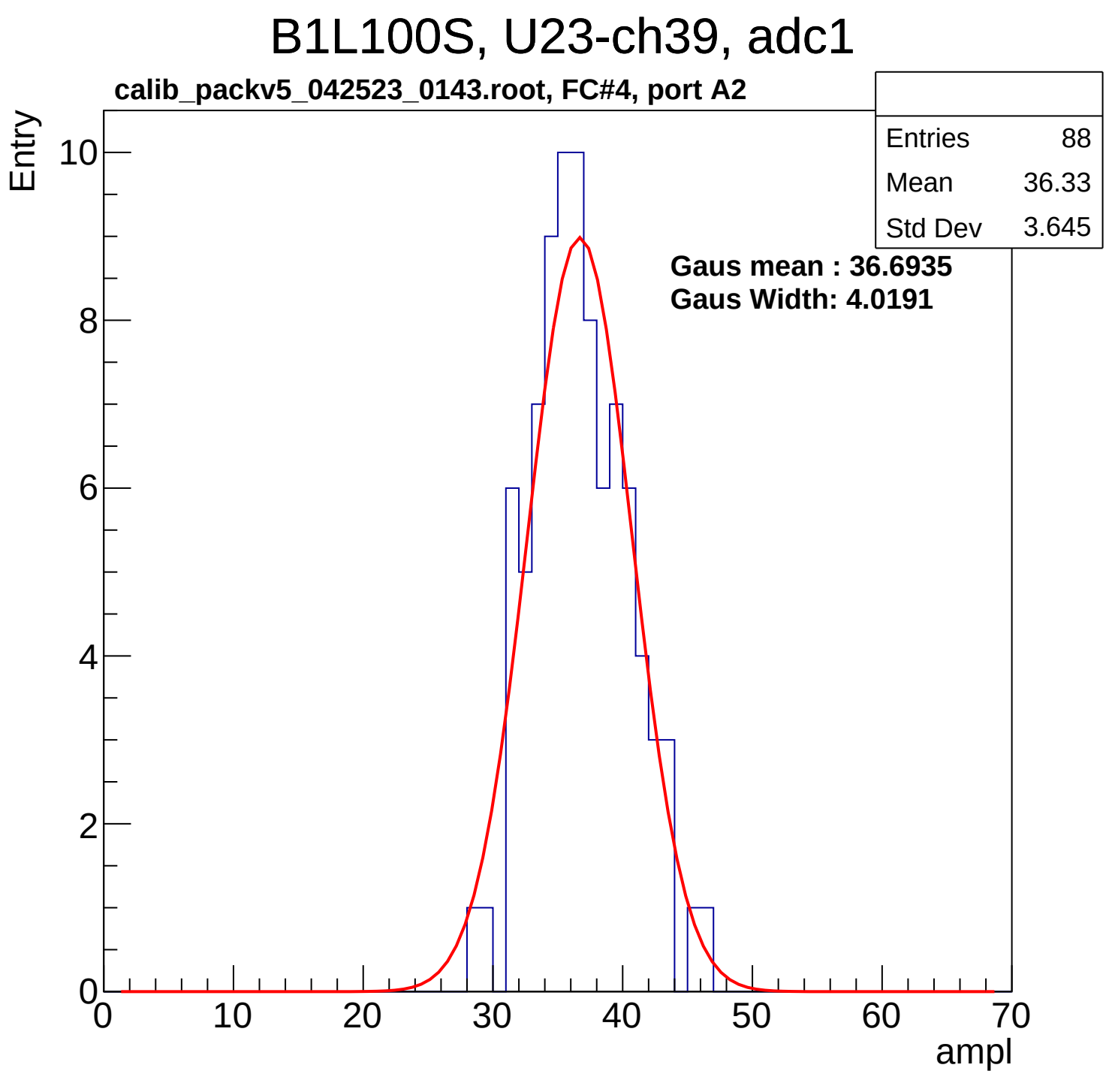
B1L100S, U23-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 36.33
Std Dev 3.645
Gaus mean : 36.6935
Gaus Width: 4.0191
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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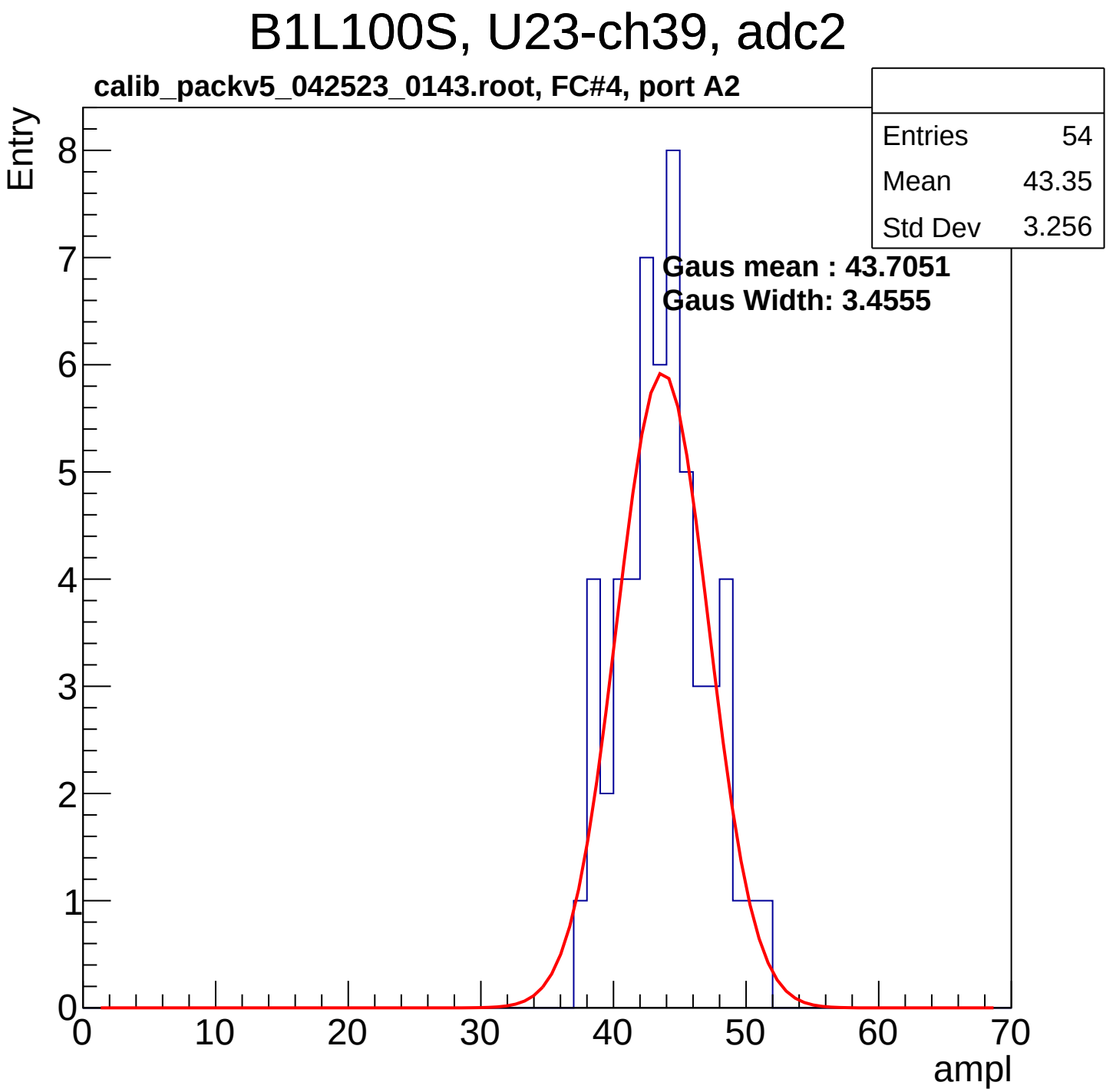
B1L100S, U23-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 43.35
Std Dev 3.256
Gaus mean : 43.7051
Gaus Width: 3.4555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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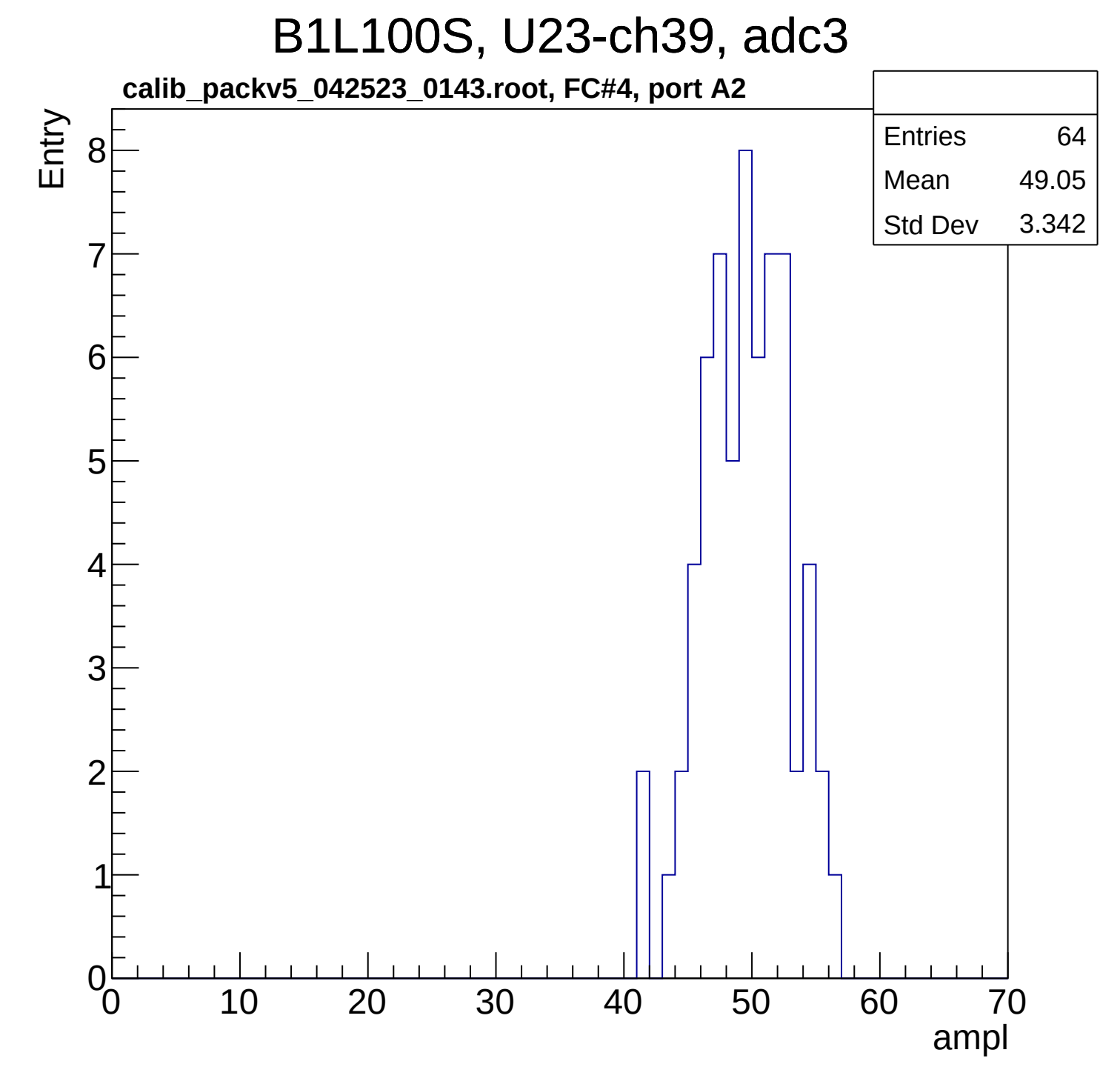
B1L100S, U23-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.05
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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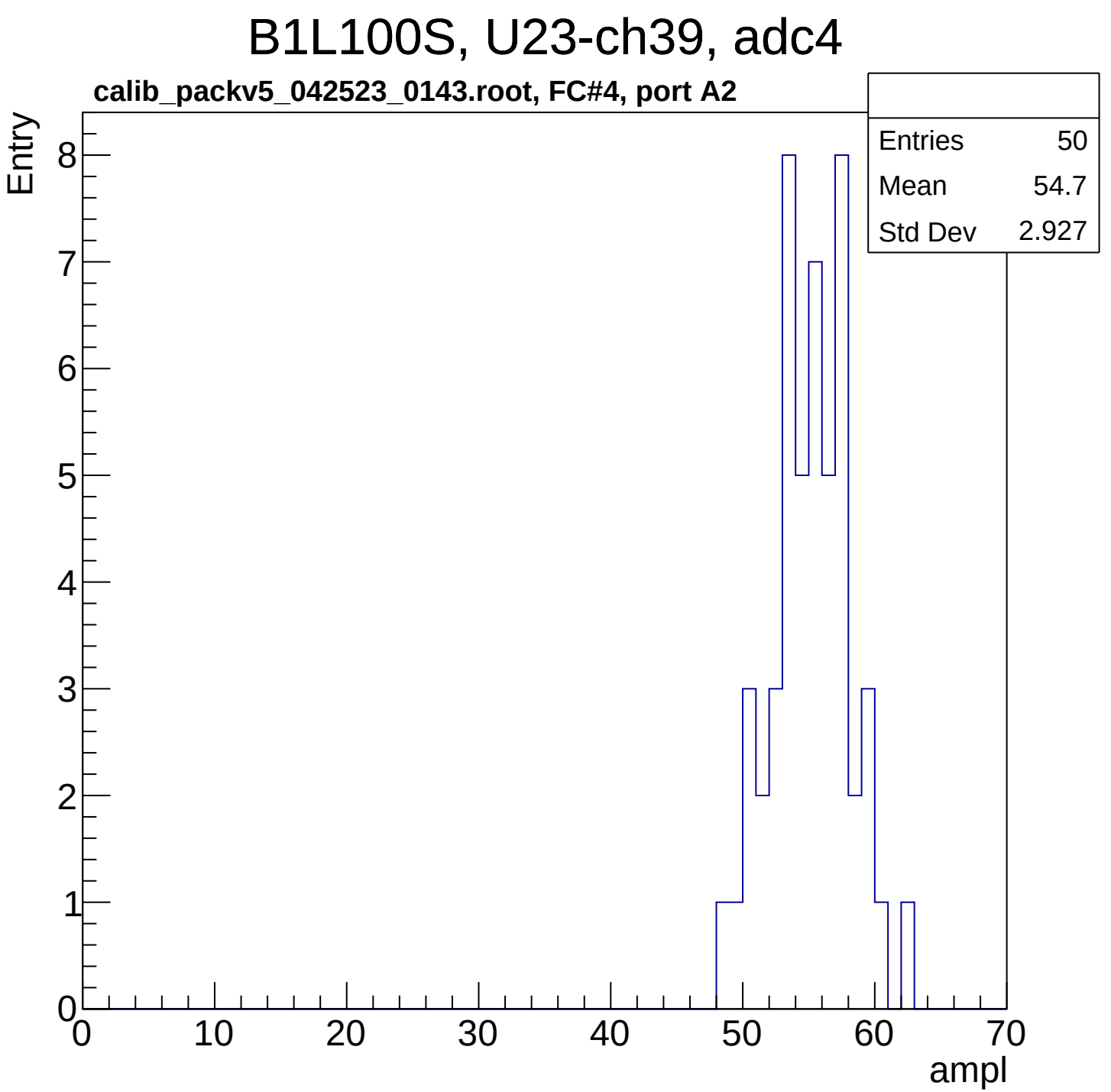
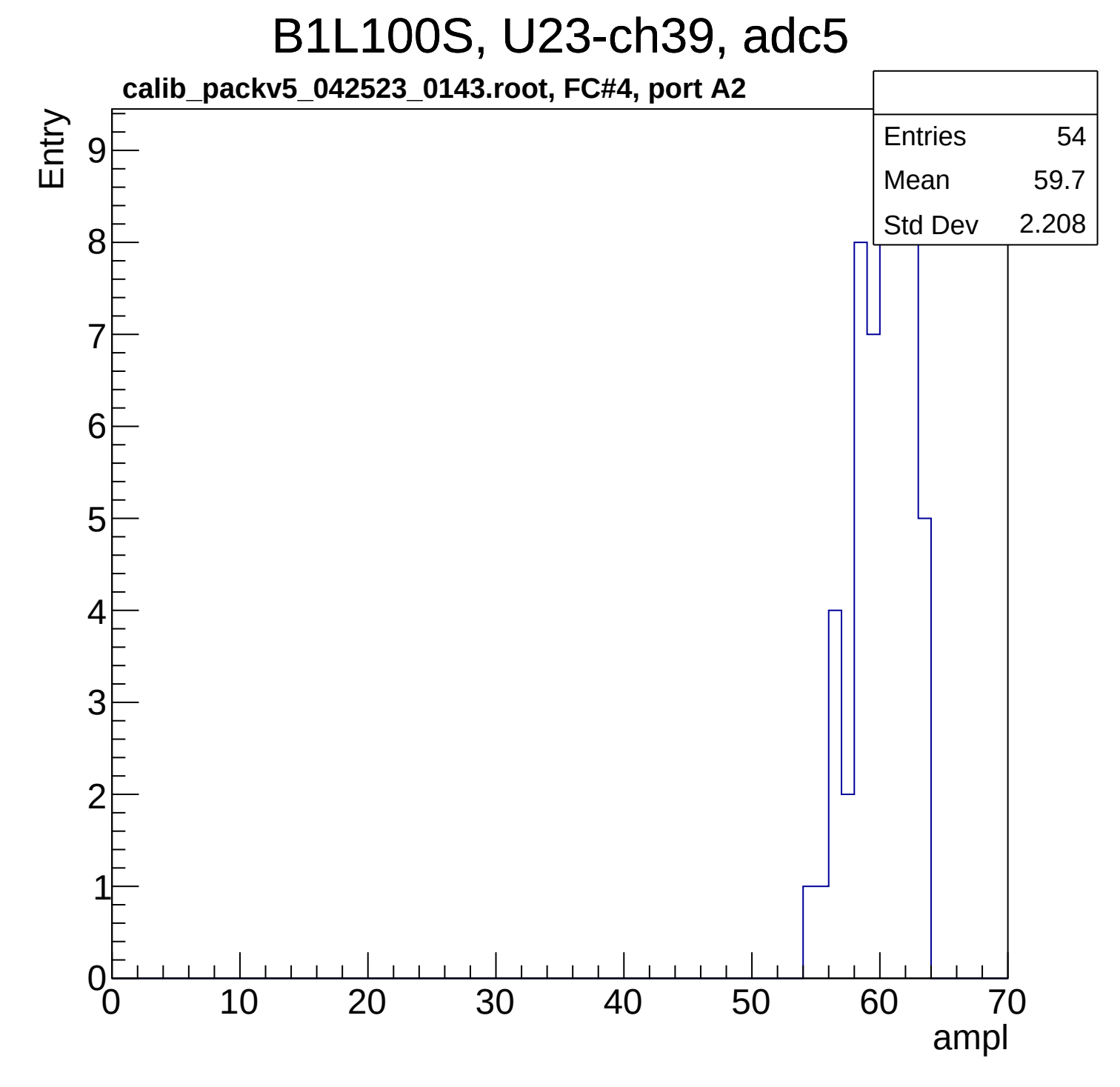
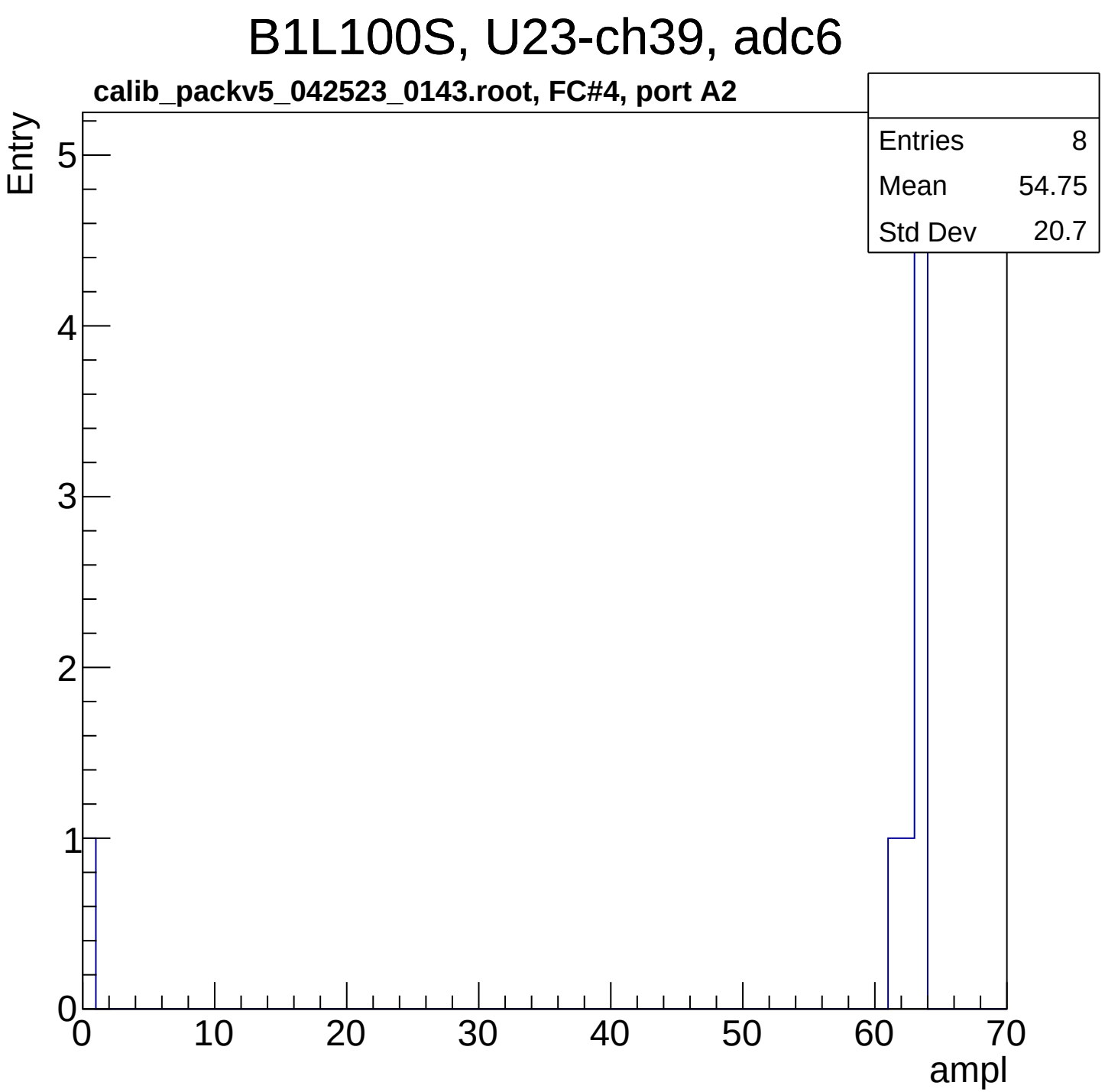
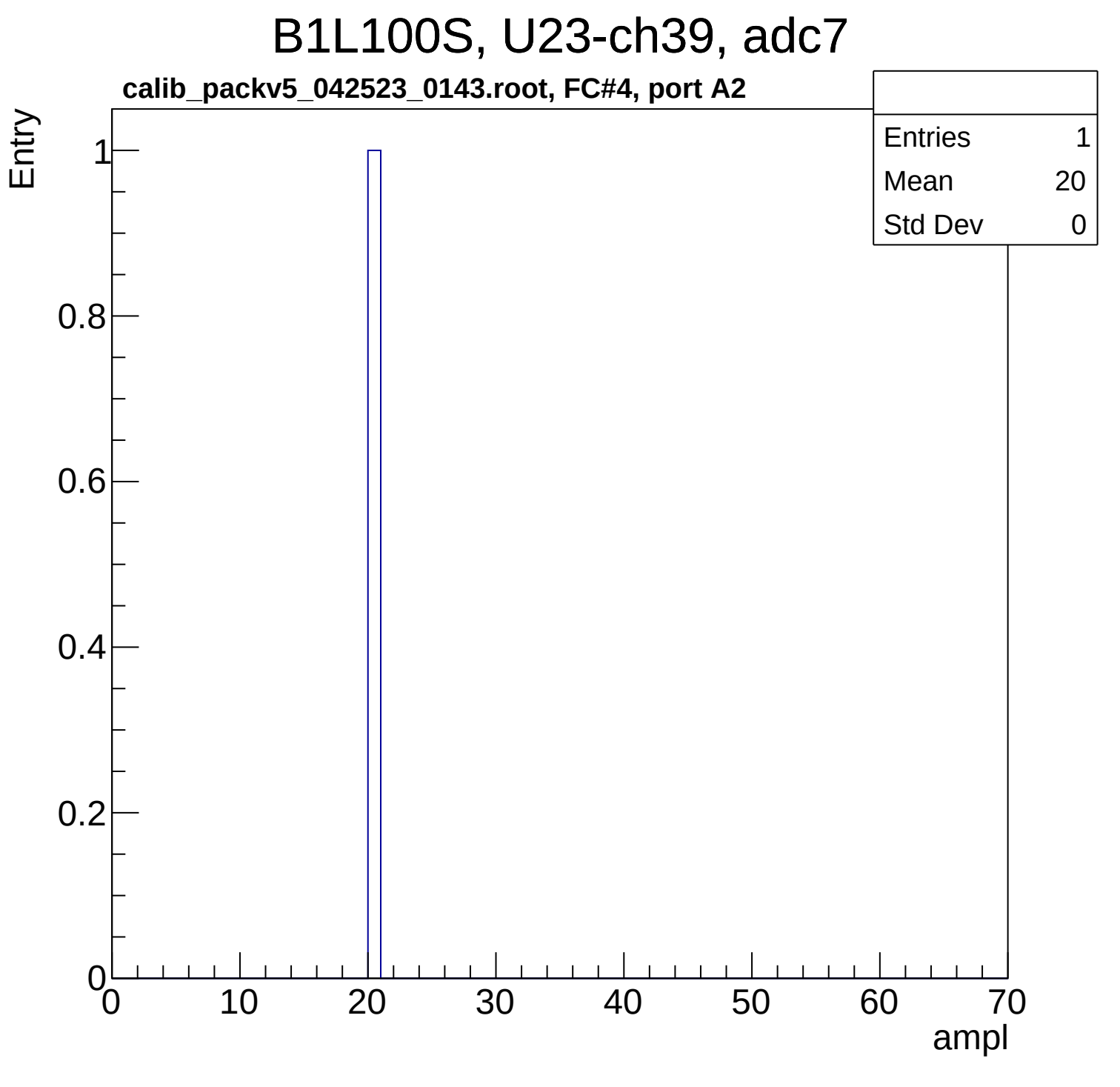

B1L100S, U23-ch39, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.7
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 59.7
Std Dev 2.208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54.75
Std Dev 20.7
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
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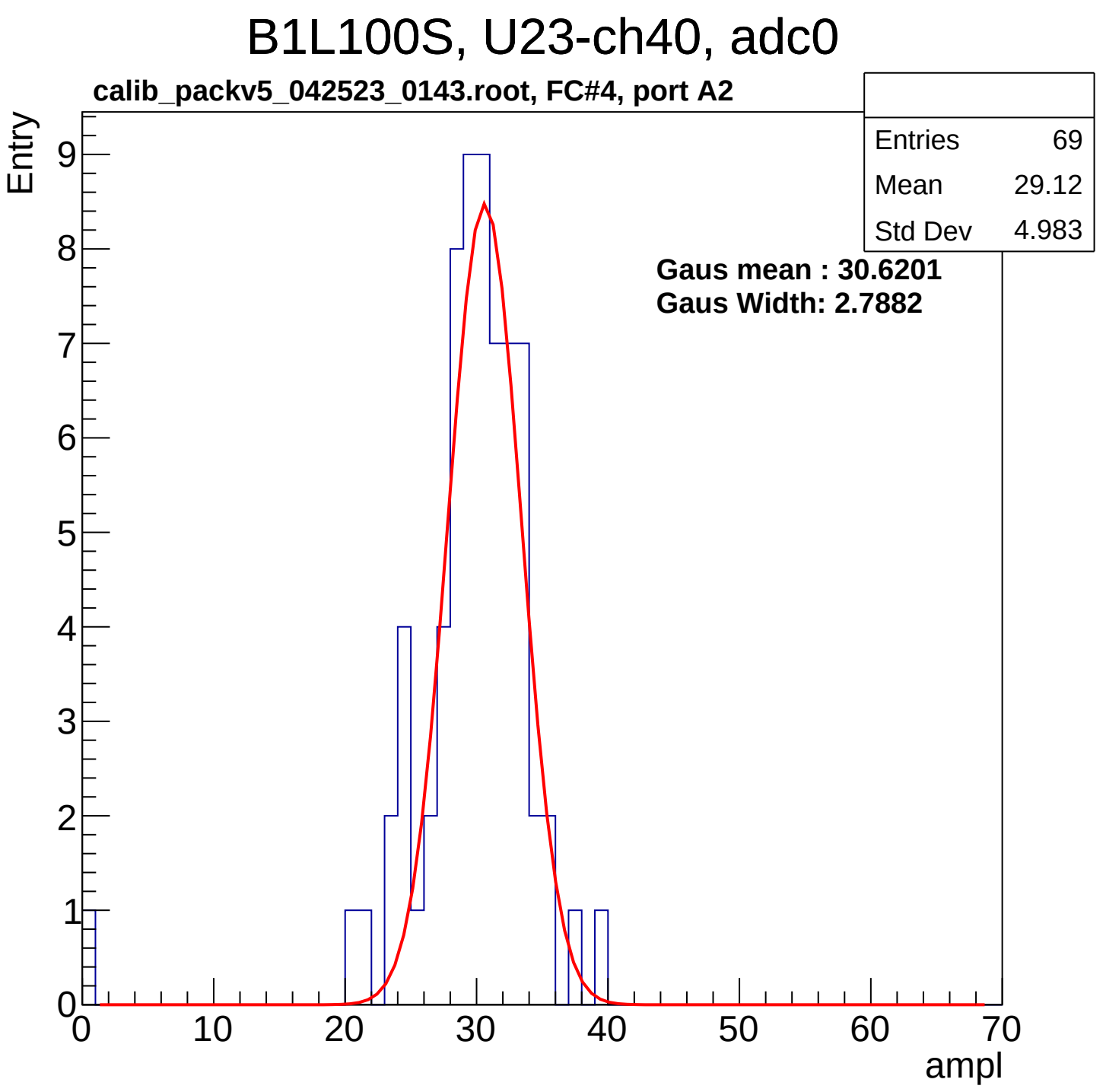
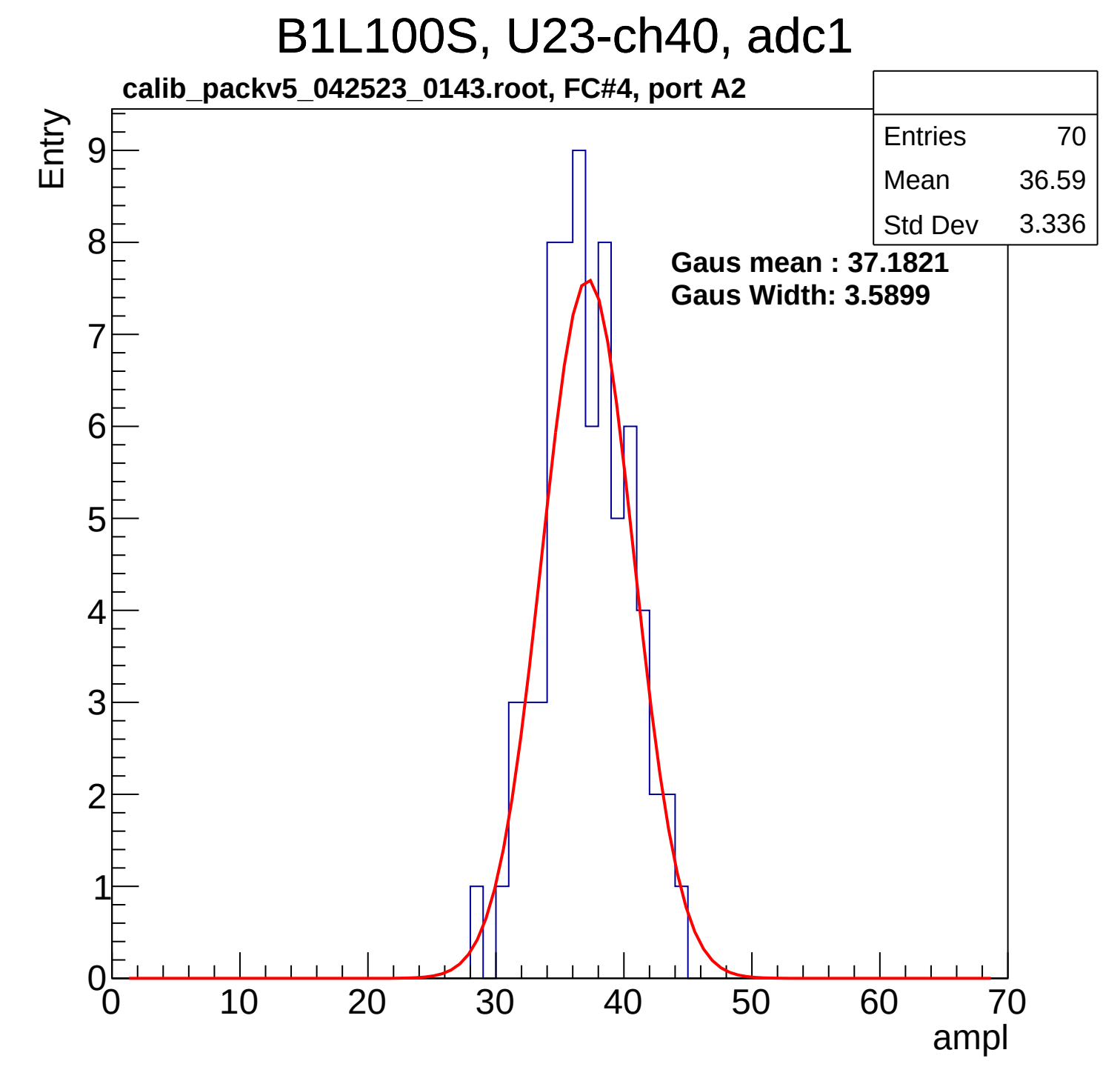
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.12
Std Dev 4.983
Gaus mean : 30.6201
Gaus Width: 2.7882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.59
Std Dev 3.336
Gaus mean : 37.1821
Gaus Width: 3.5899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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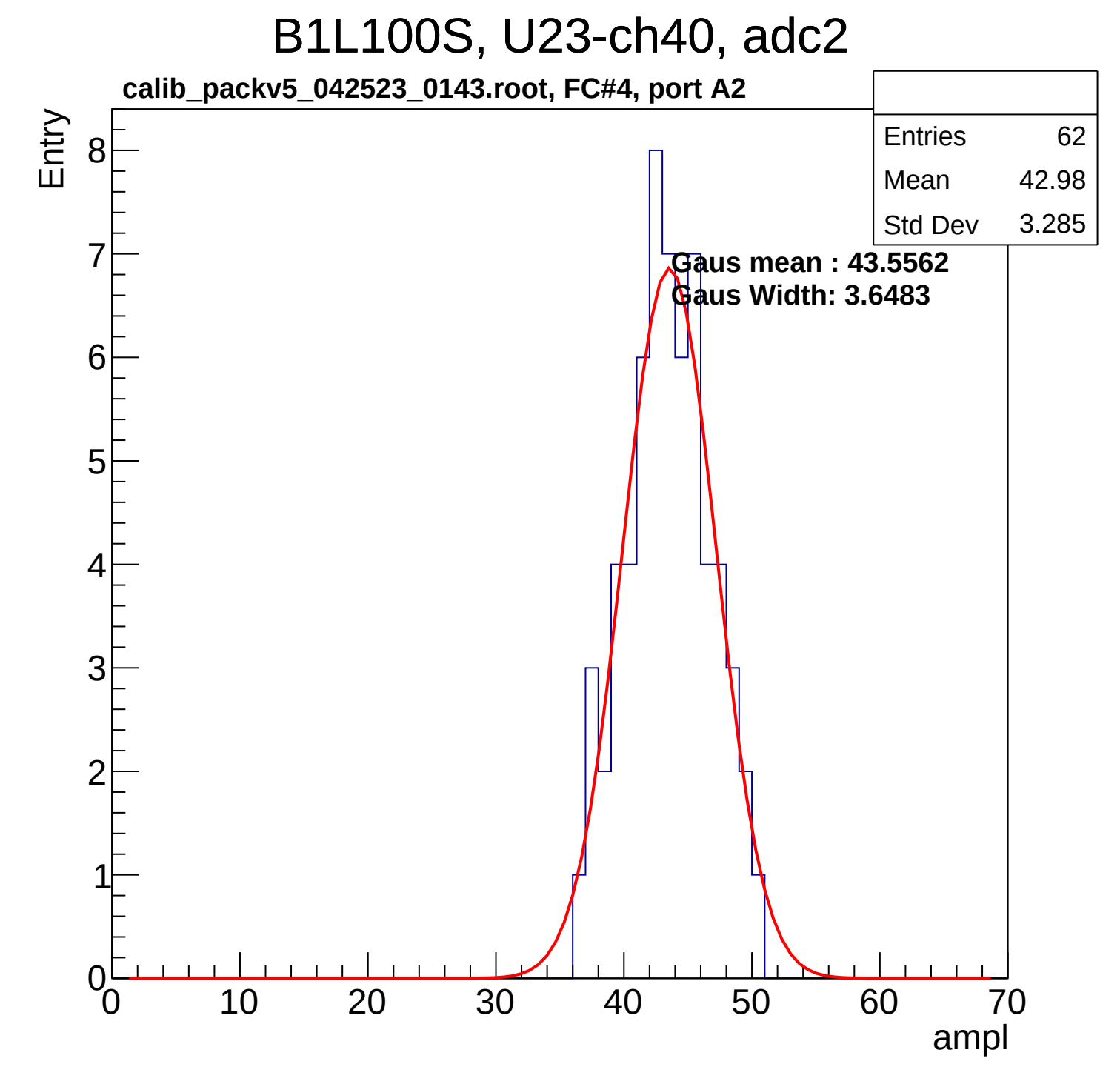
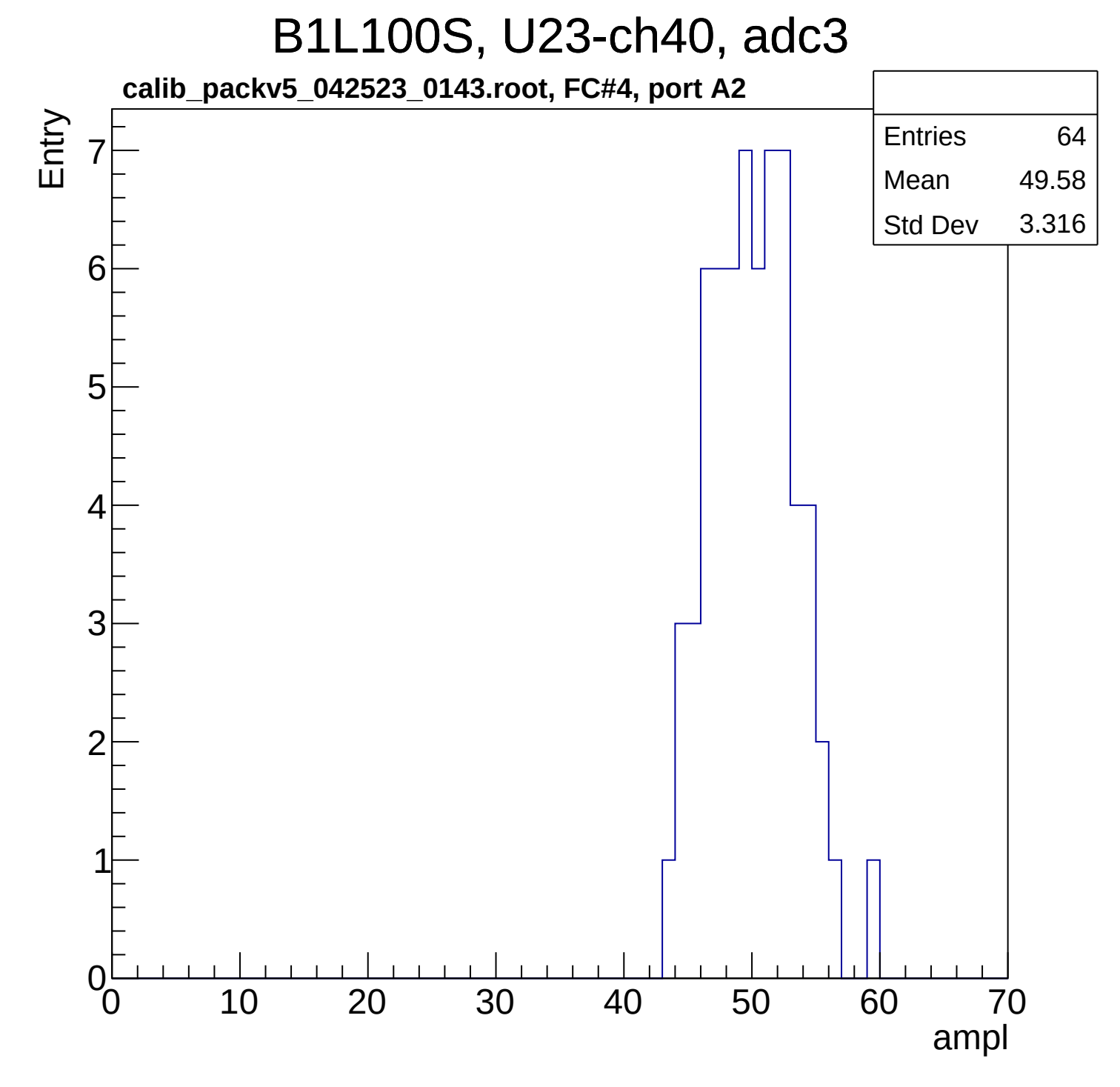

B1L100S, U23-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.98
Std Dev 3.285
Gaus mean : 43.5562
Gaus Width: 3.6483
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch40, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.58
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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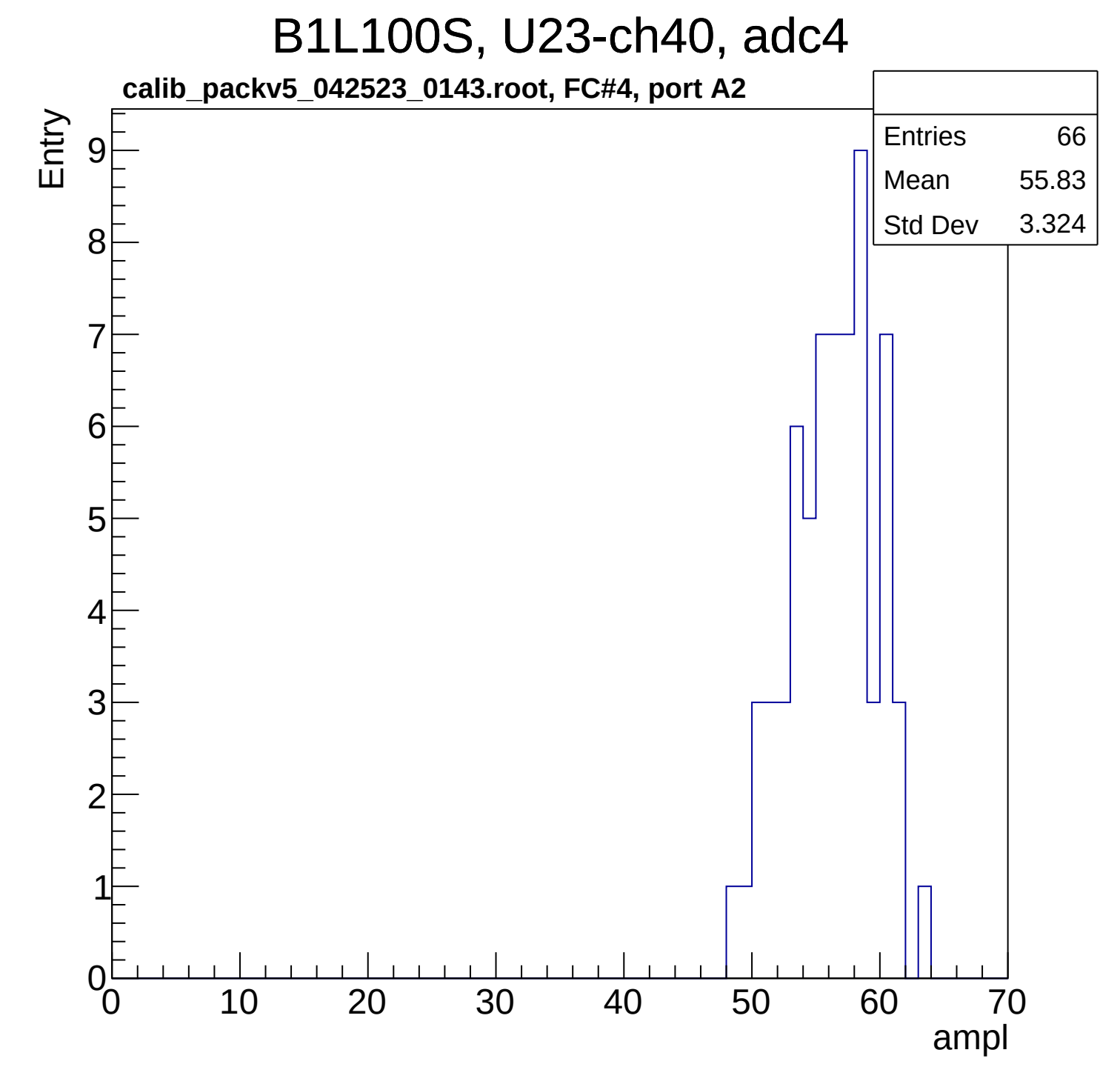
B1L100S, U23-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.83
Std Dev 3.324
Entry
ampl
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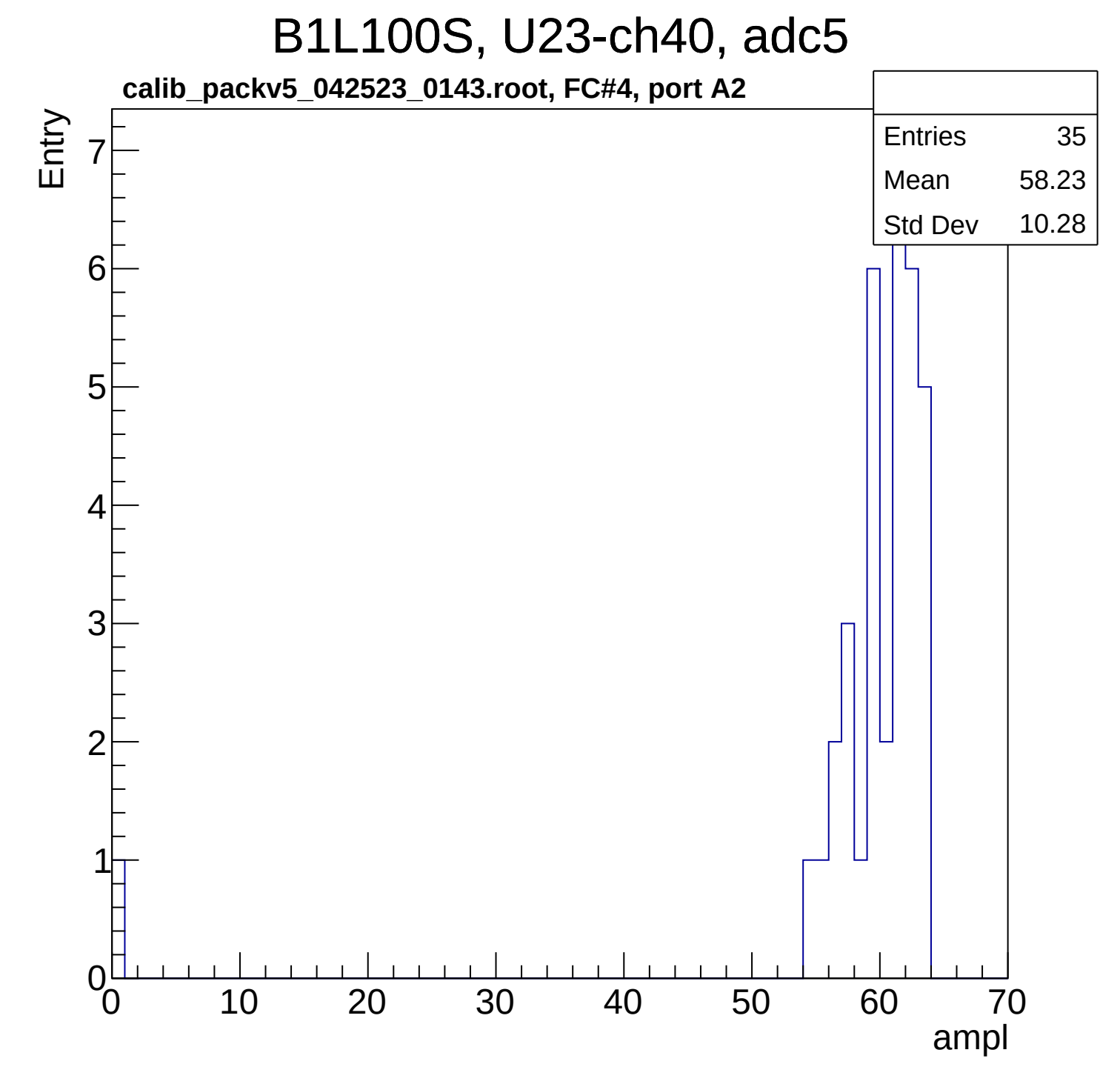
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.23
Std Dev 10.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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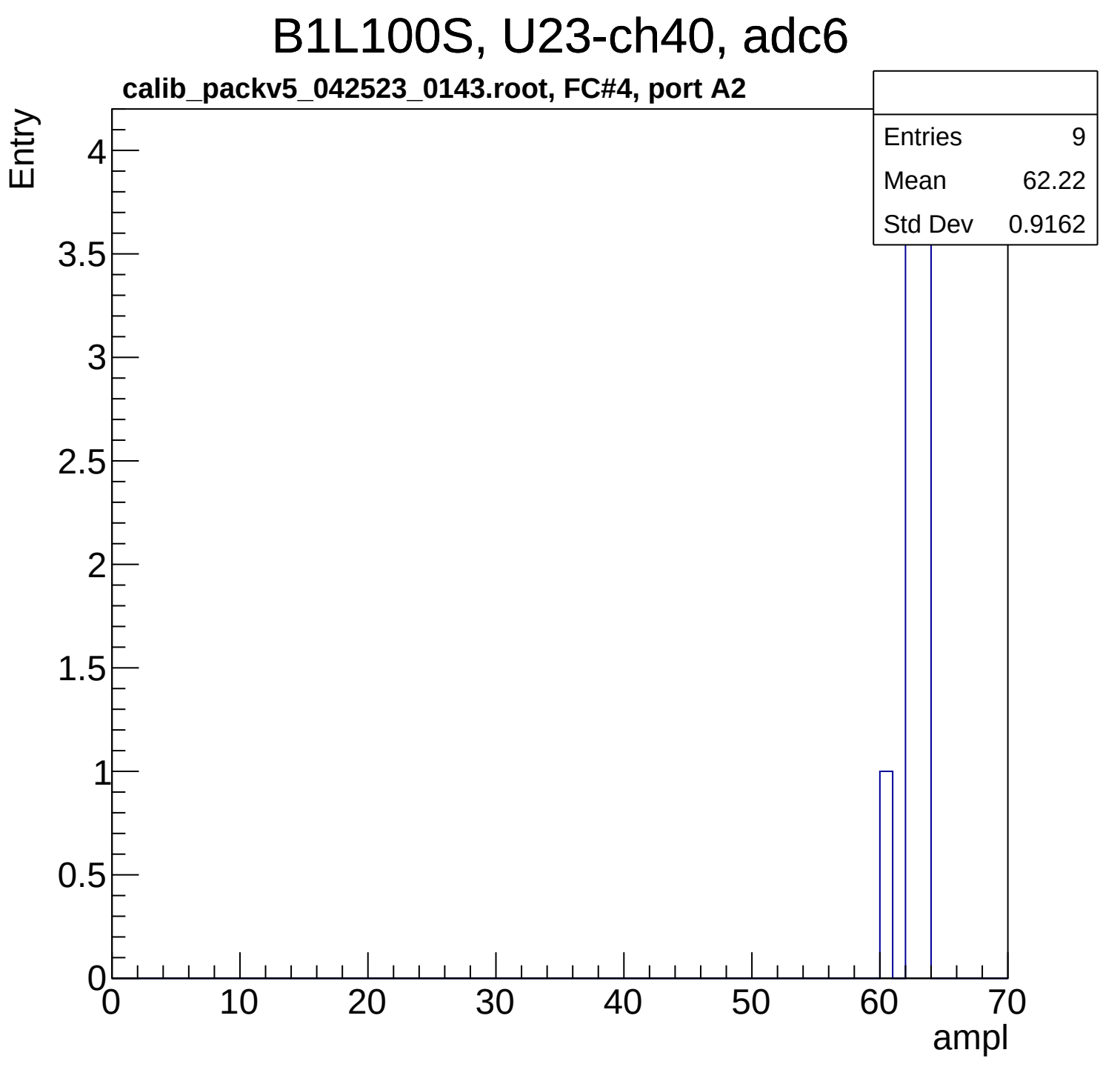
B1L100S, U23-ch40, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62.22
Std Dev 0.9162
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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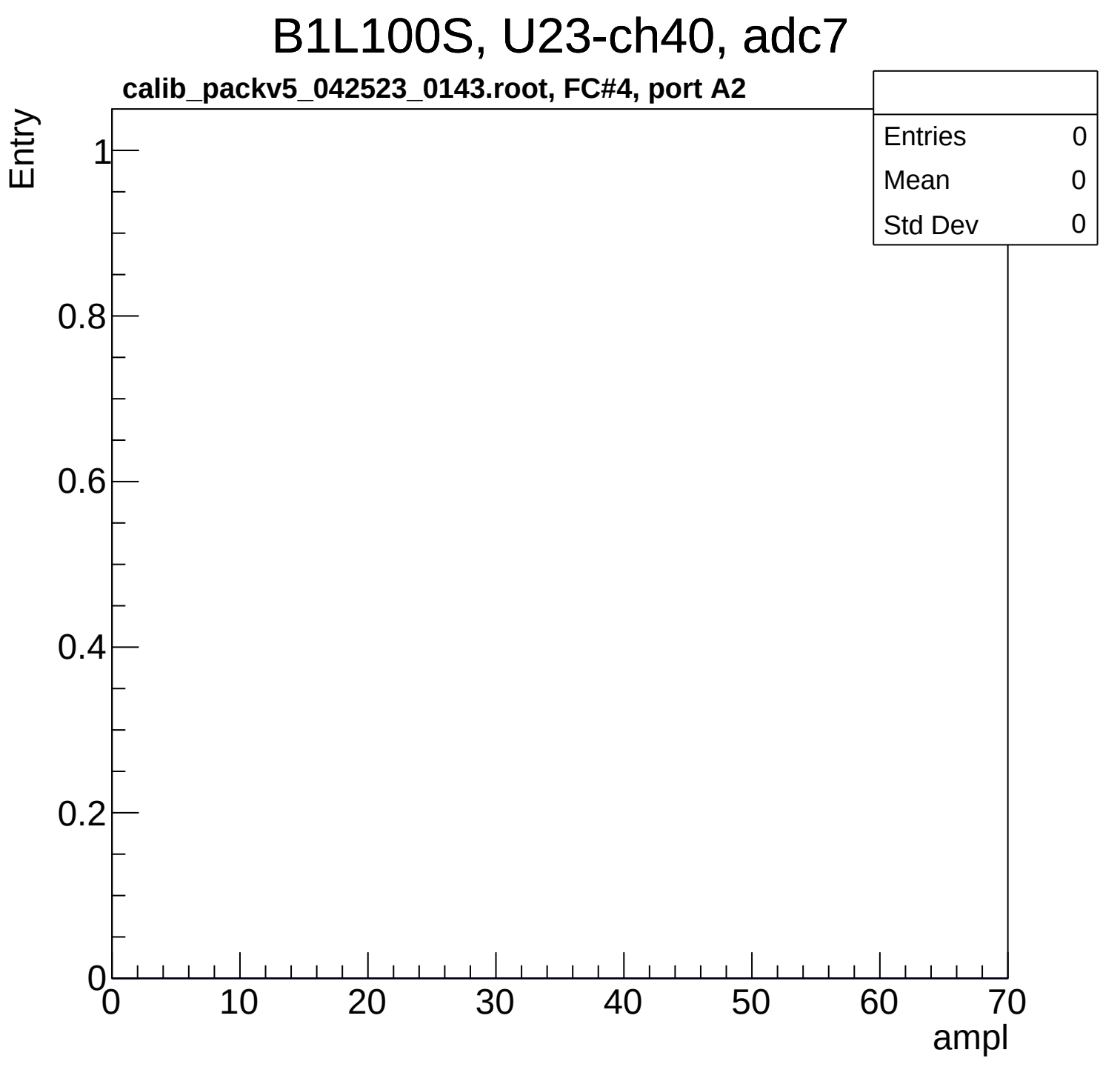
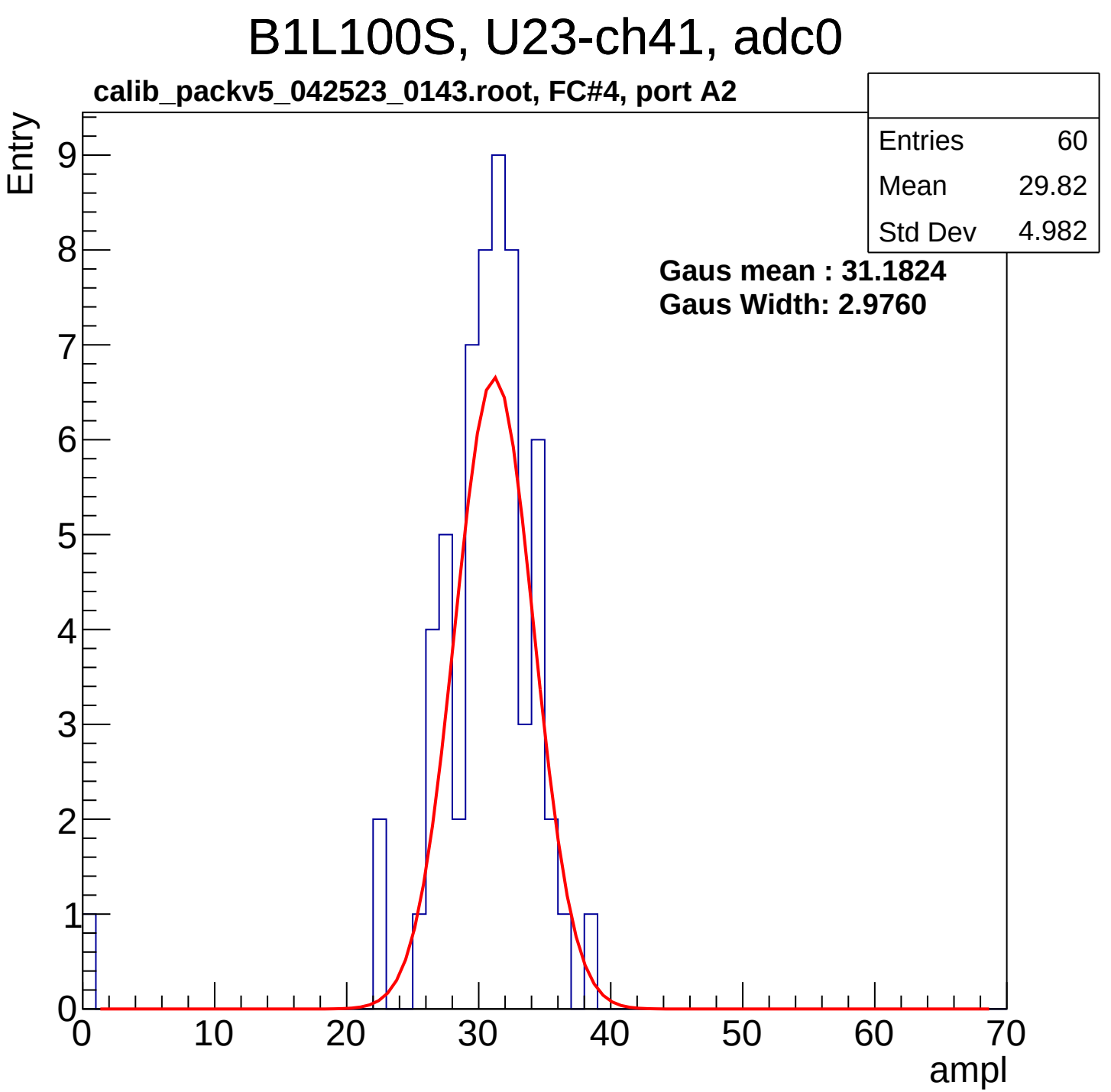
40
50
60
70


B1L100S, U23-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch41, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.82
Std Dev 4.982
Gaus mean : 31.1824
Gaus Width: 2.9760
Entry
ampl
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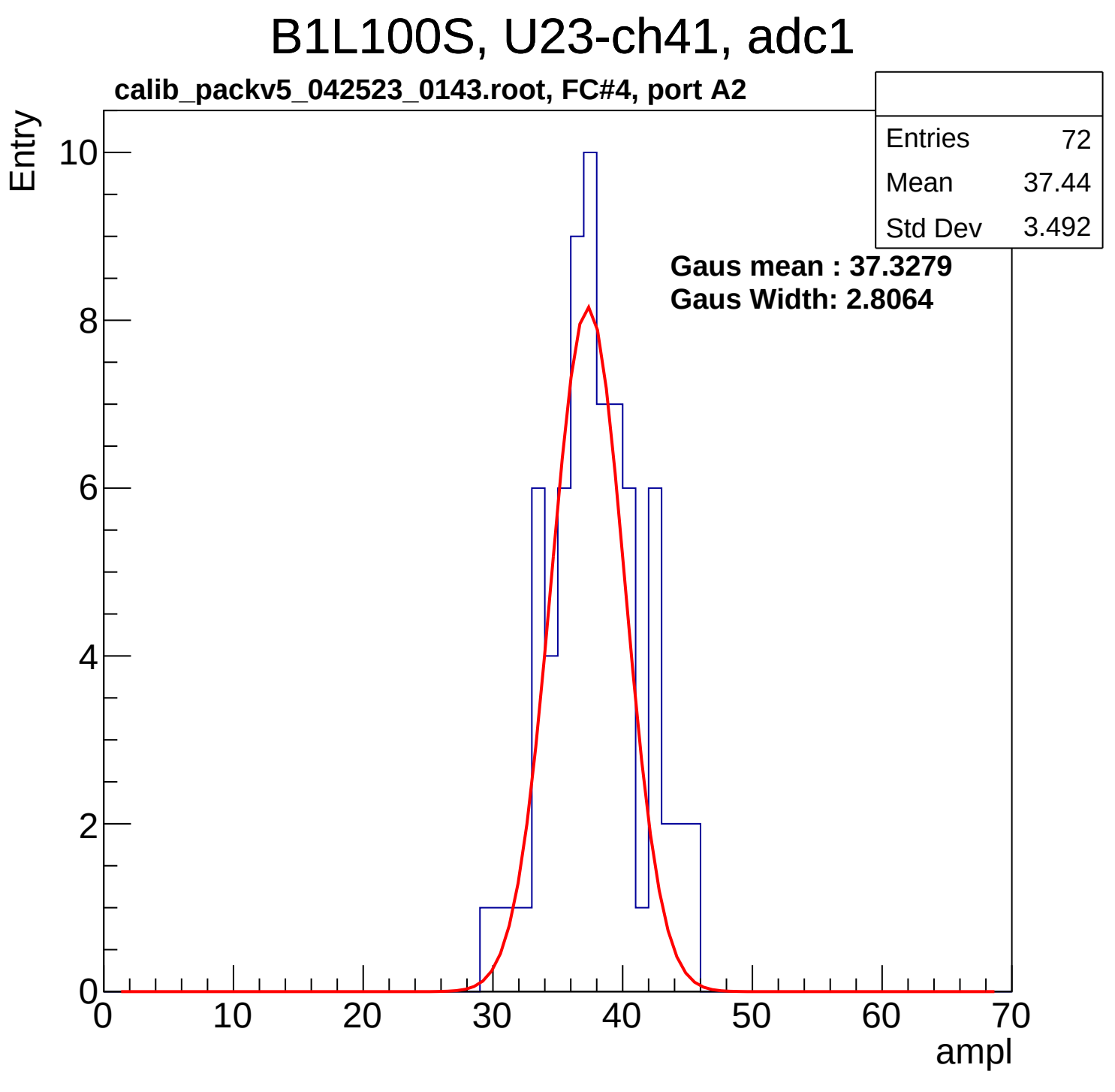
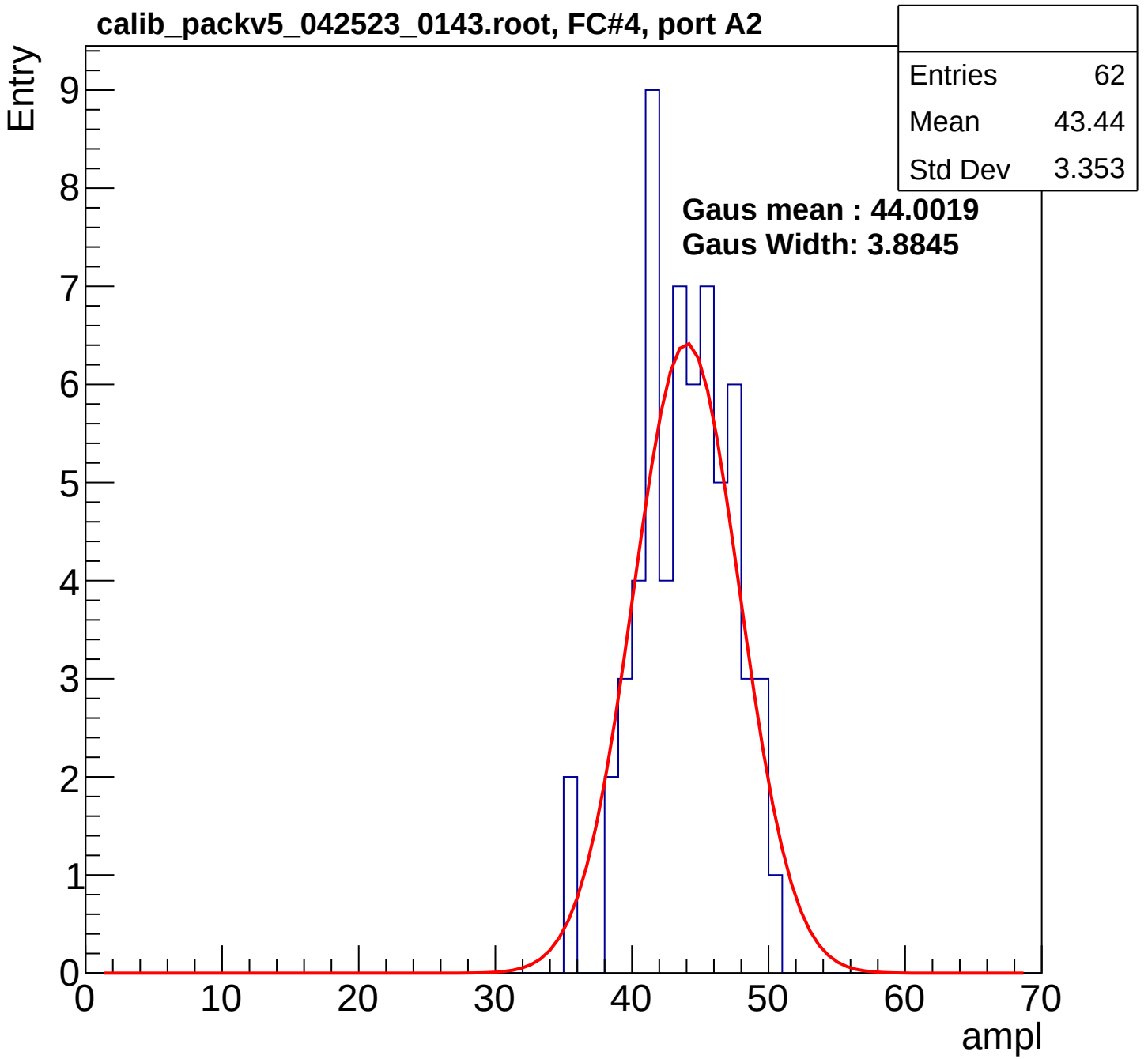
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.44
Std Dev 3.492
Gaus mean : 37.3279
Gaus Width: 2.8064
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.44
Std Dev 3.353
Gaus mean : 44.0019
Gaus Width: 3.8845
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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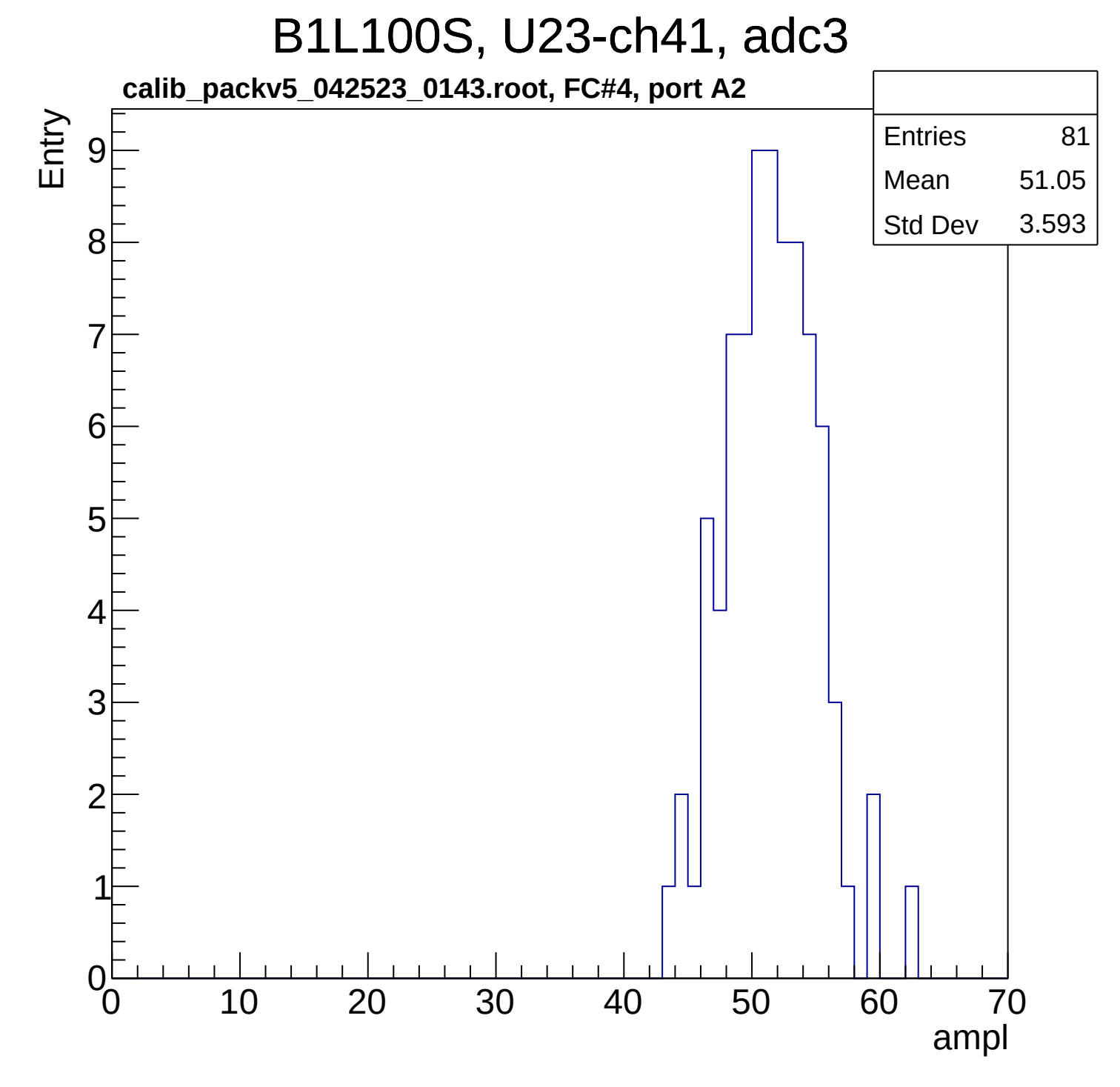
B1L100S, U23-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 51.05
Std Dev 3.593
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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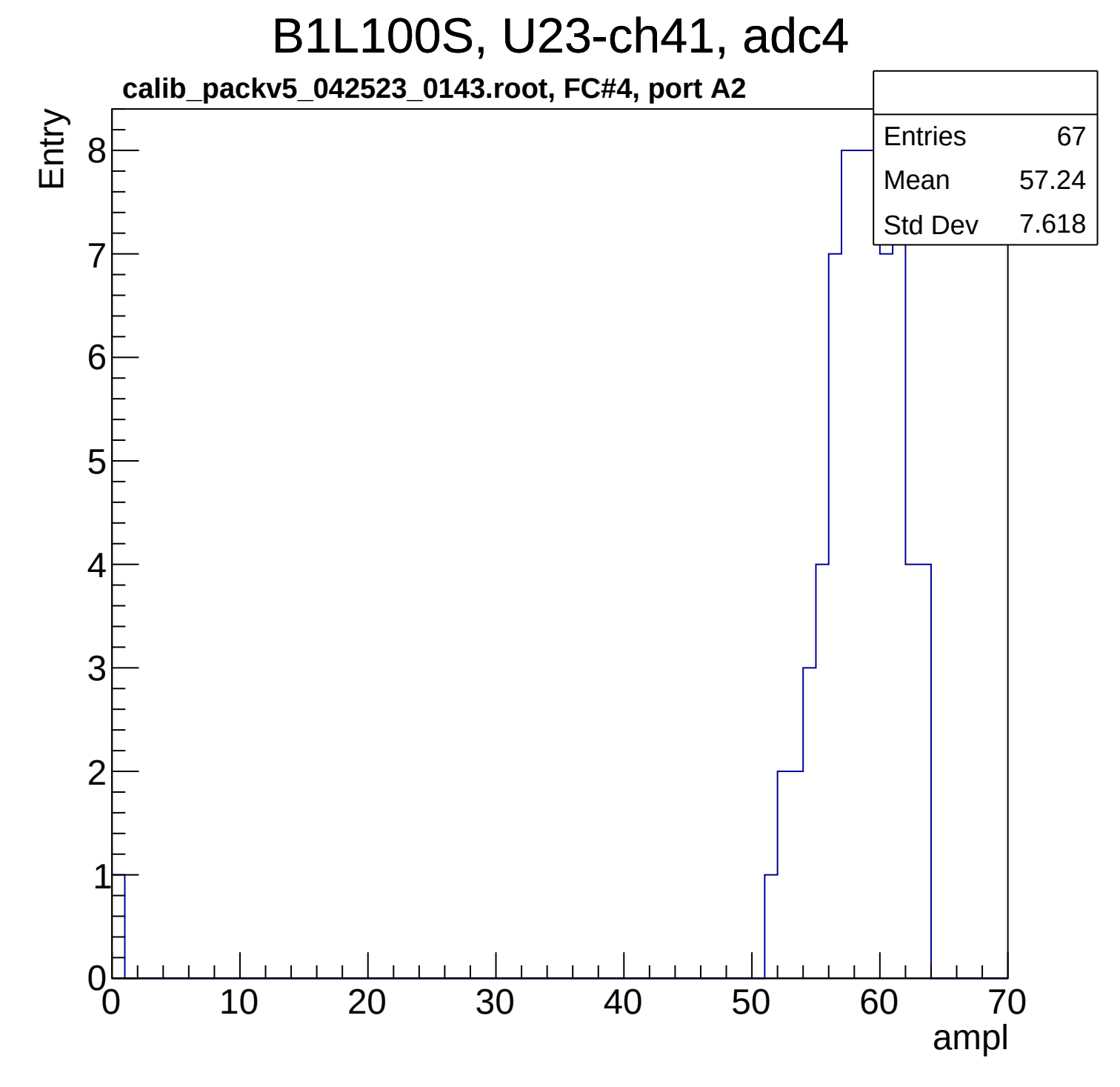
B1L100S, U23-ch41, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 57.24
Std Dev 7.618
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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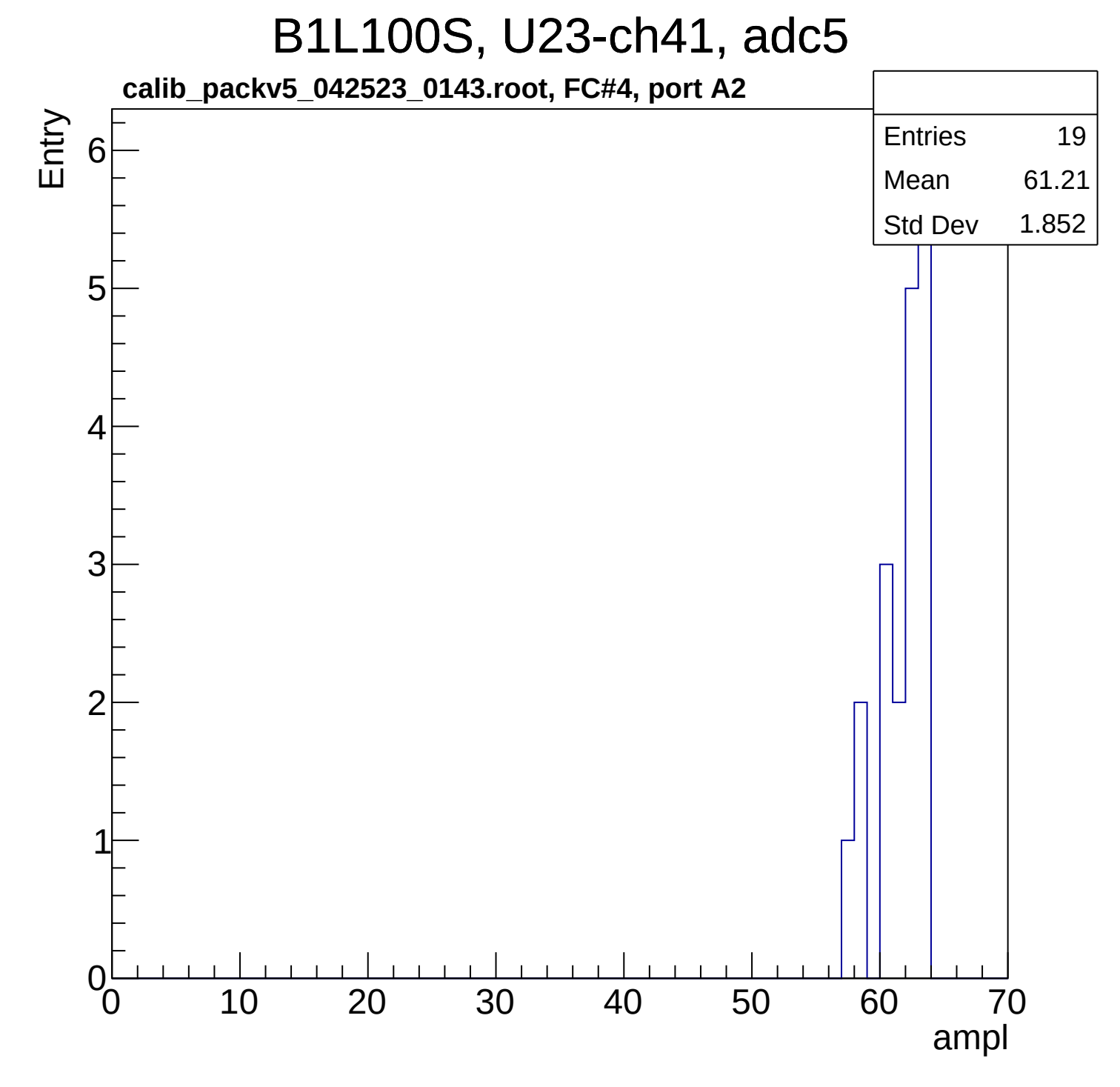
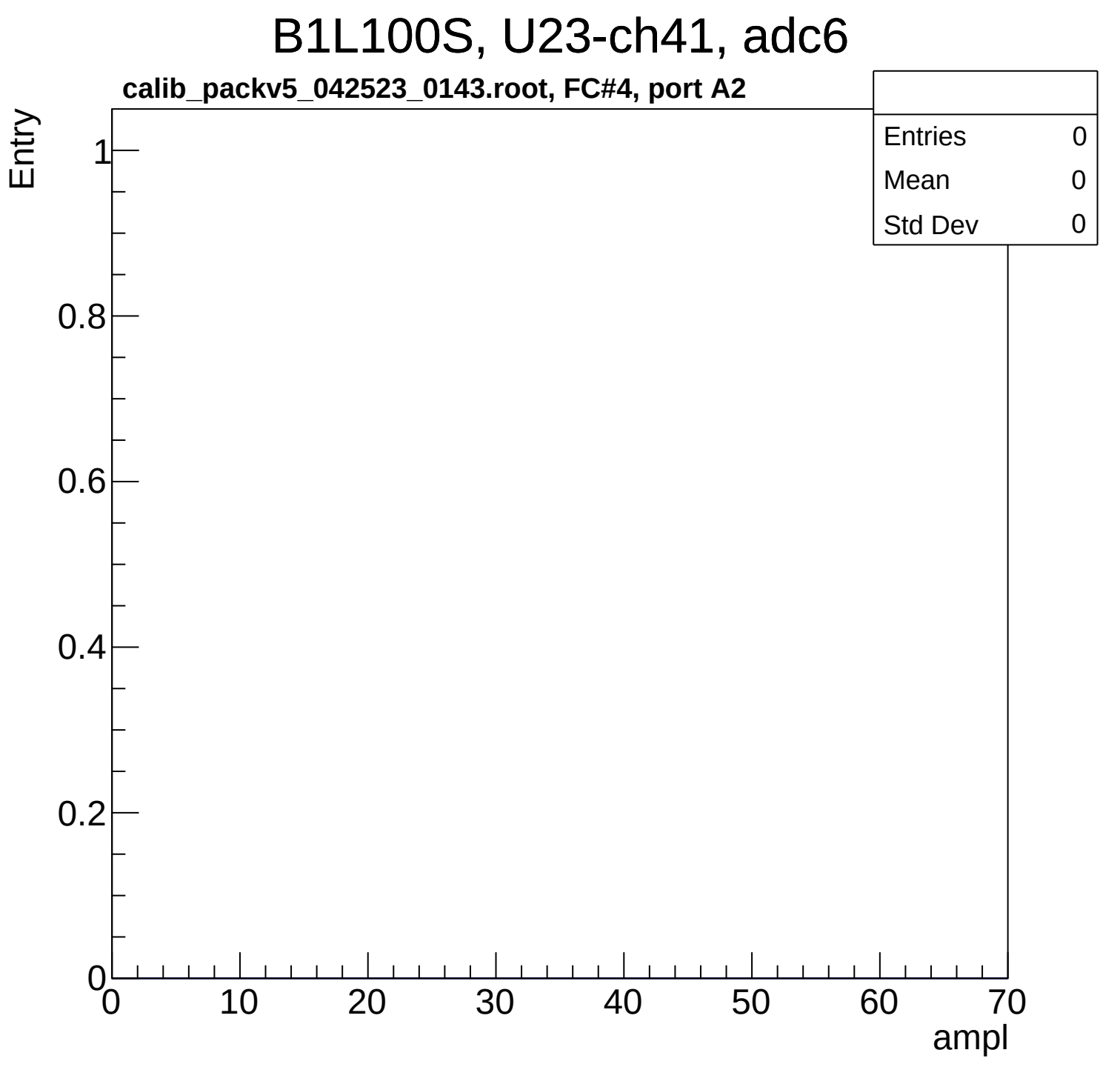
60
70


B1L100S, U23-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 19
Mean 61.21
Std Dev 1.852
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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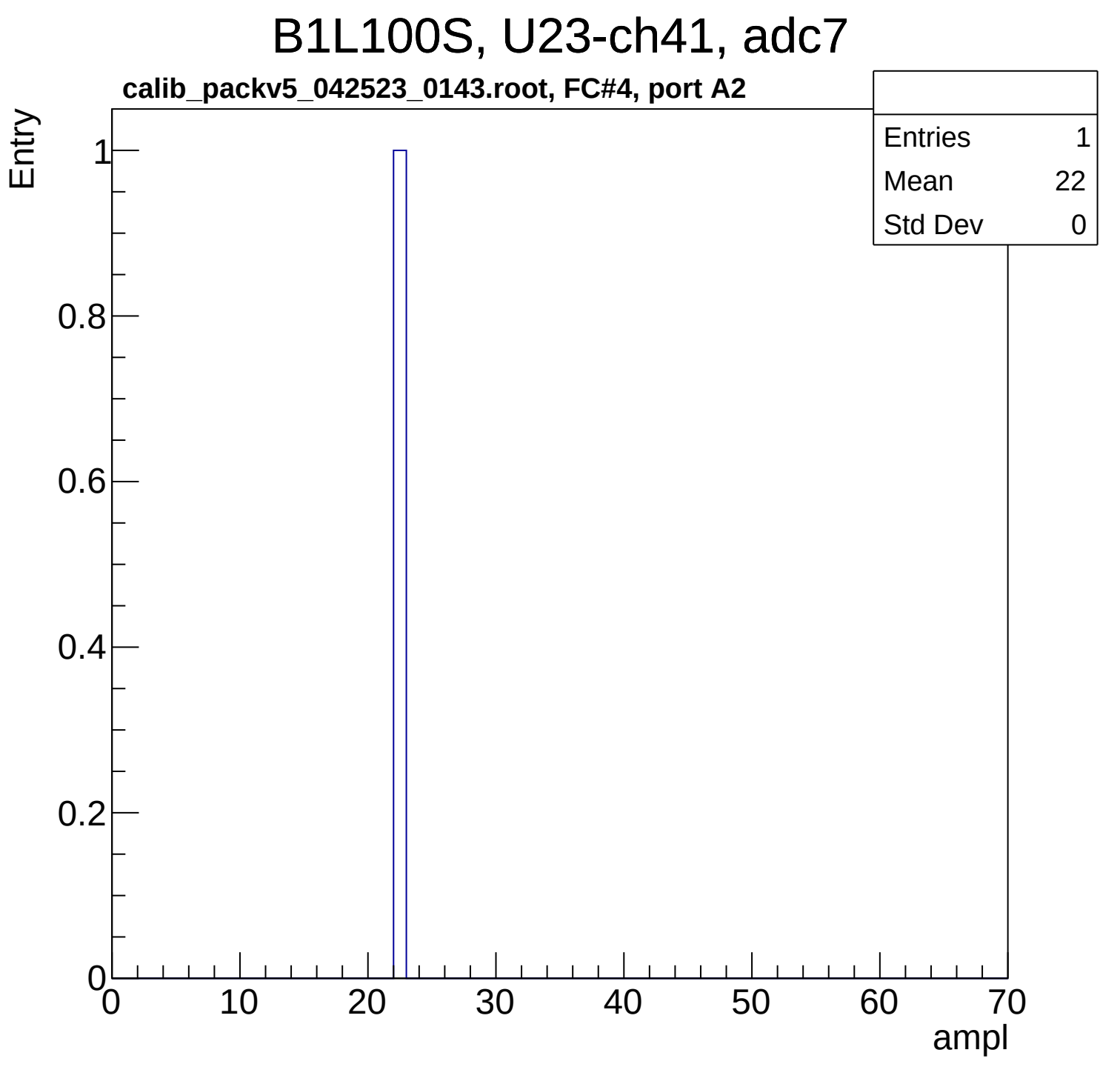
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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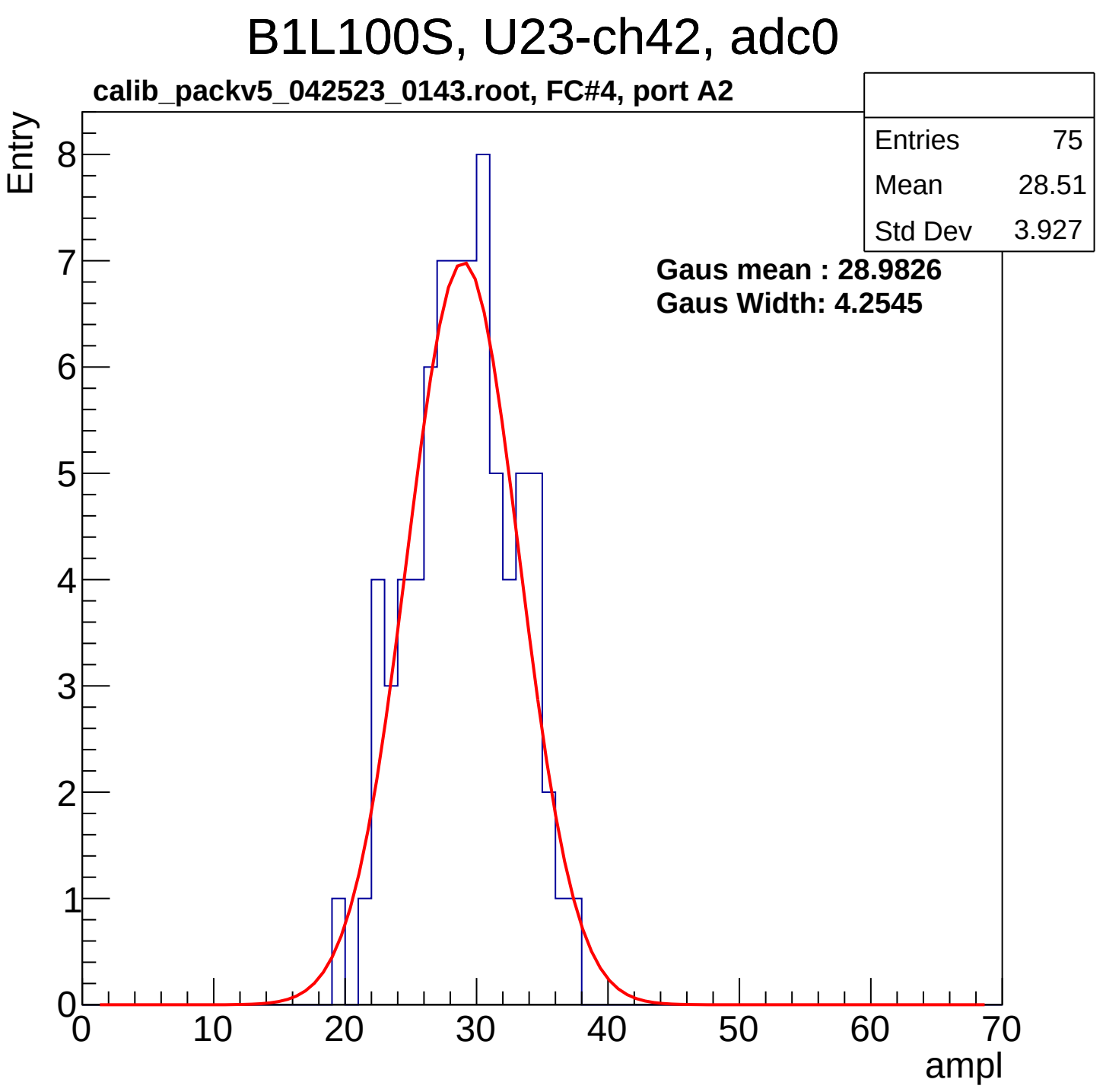
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.51
Std Dev 3.927
Gaus mean : 28.9826
Gaus Width: 4.2545
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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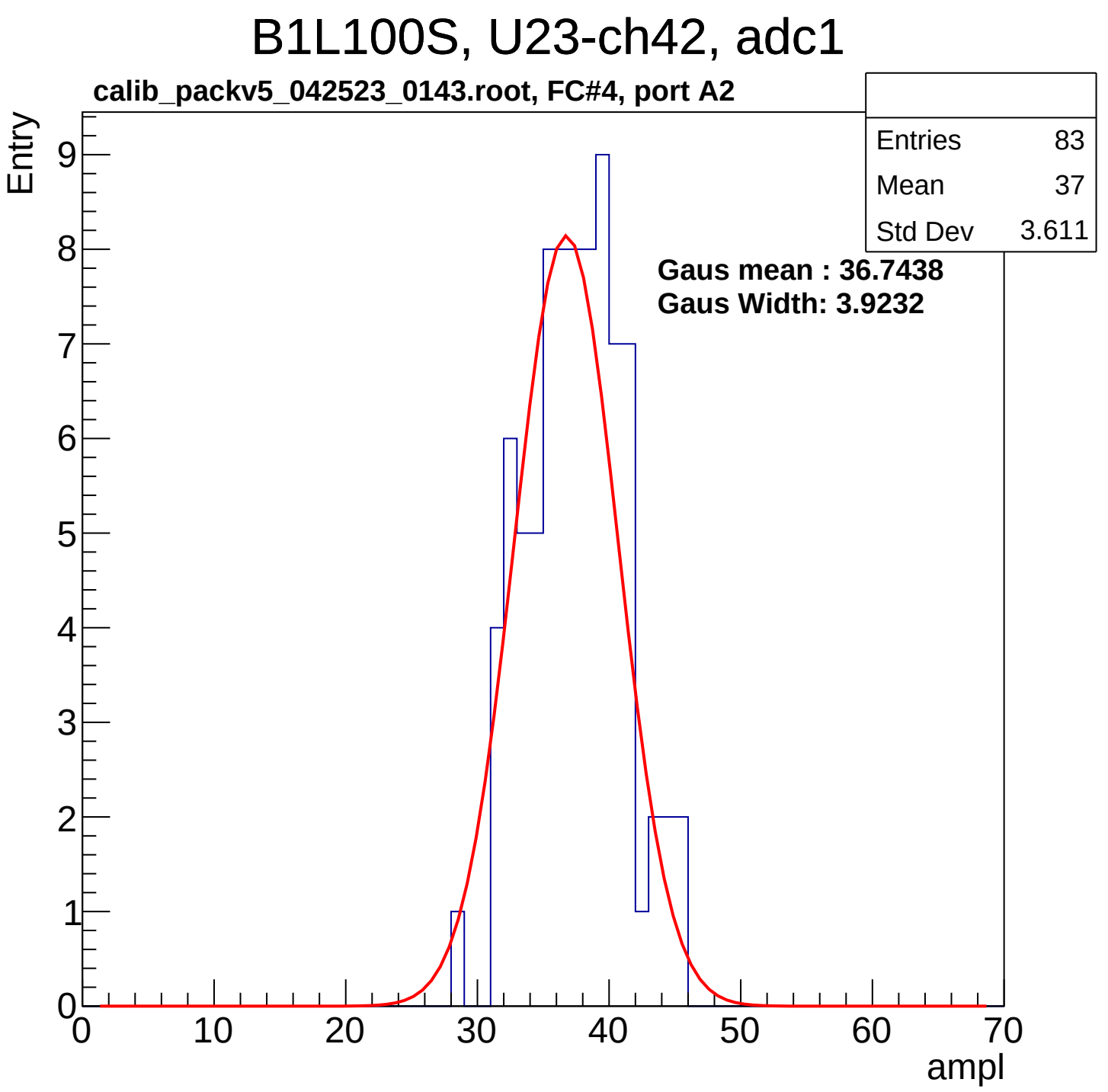
B1L100S, U23-ch42, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 37
Std Dev 3.611
Gaus mean : 36.7438
Gaus Width: 3.9232
Entry
ampl
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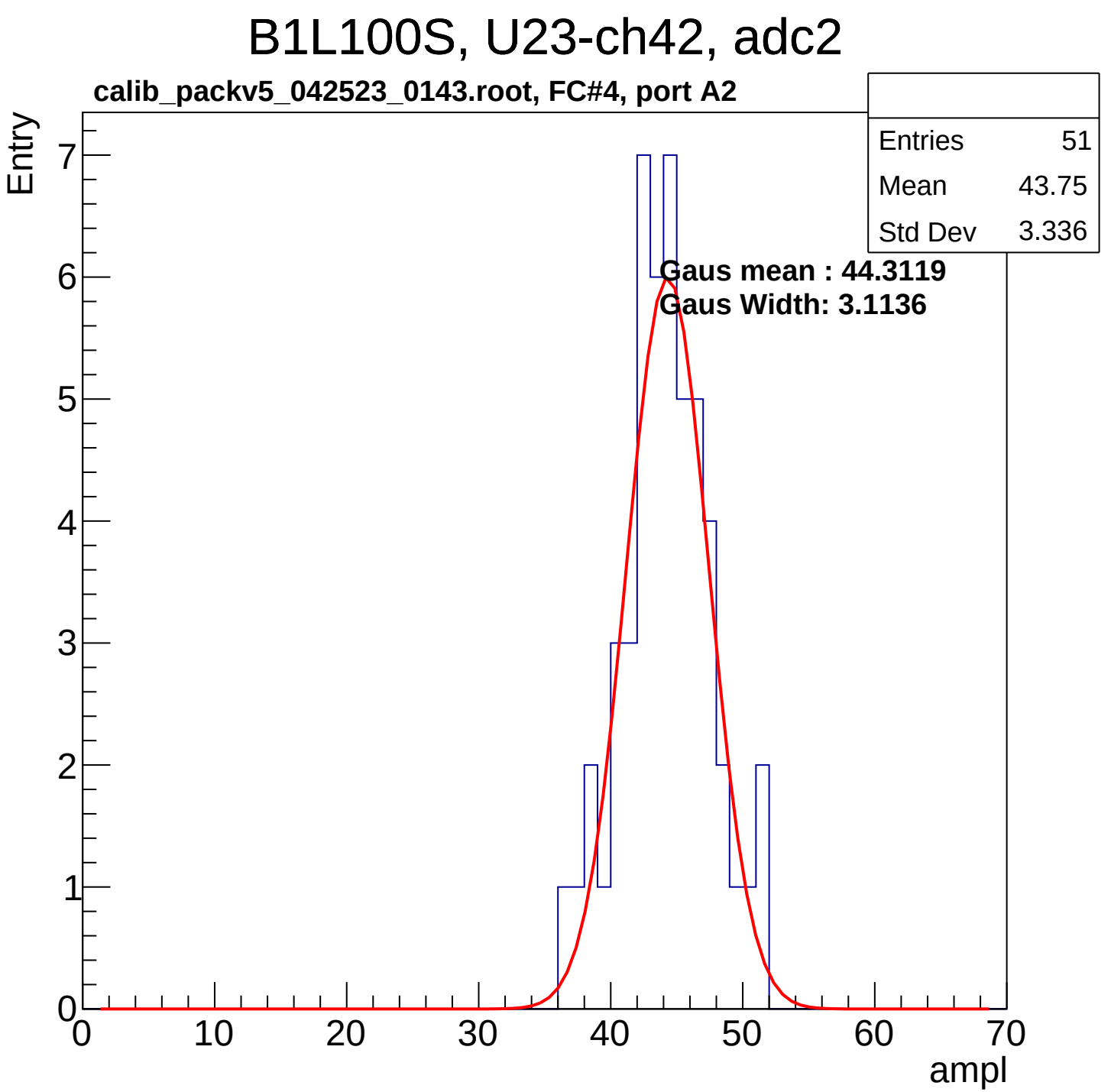
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 43.75
Std Dev 3.336
Gaus mean : 44.3119
Gaus Width: 3.1136
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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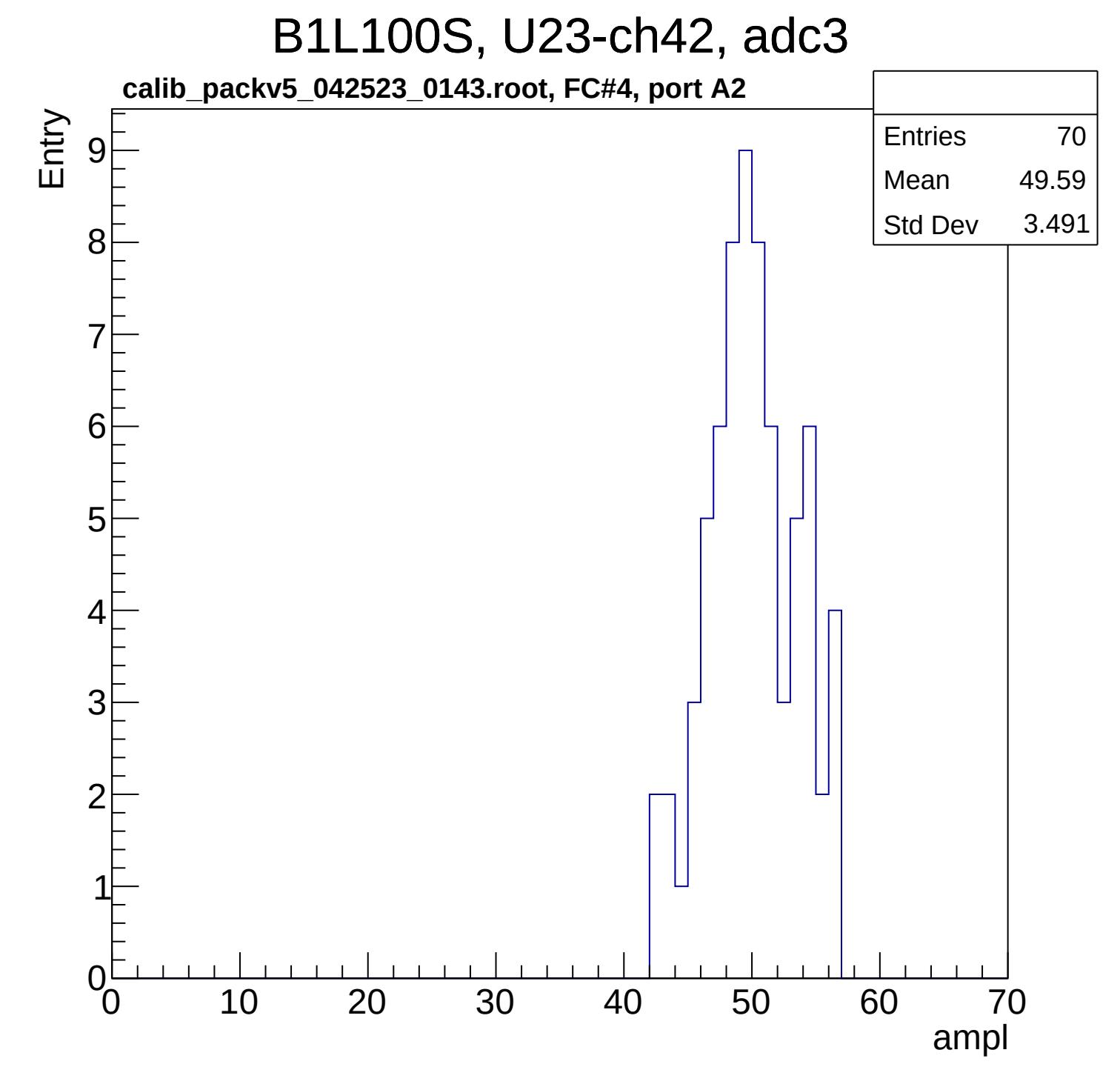
B1L100S, U23-ch42, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.59
Std Dev 3.491
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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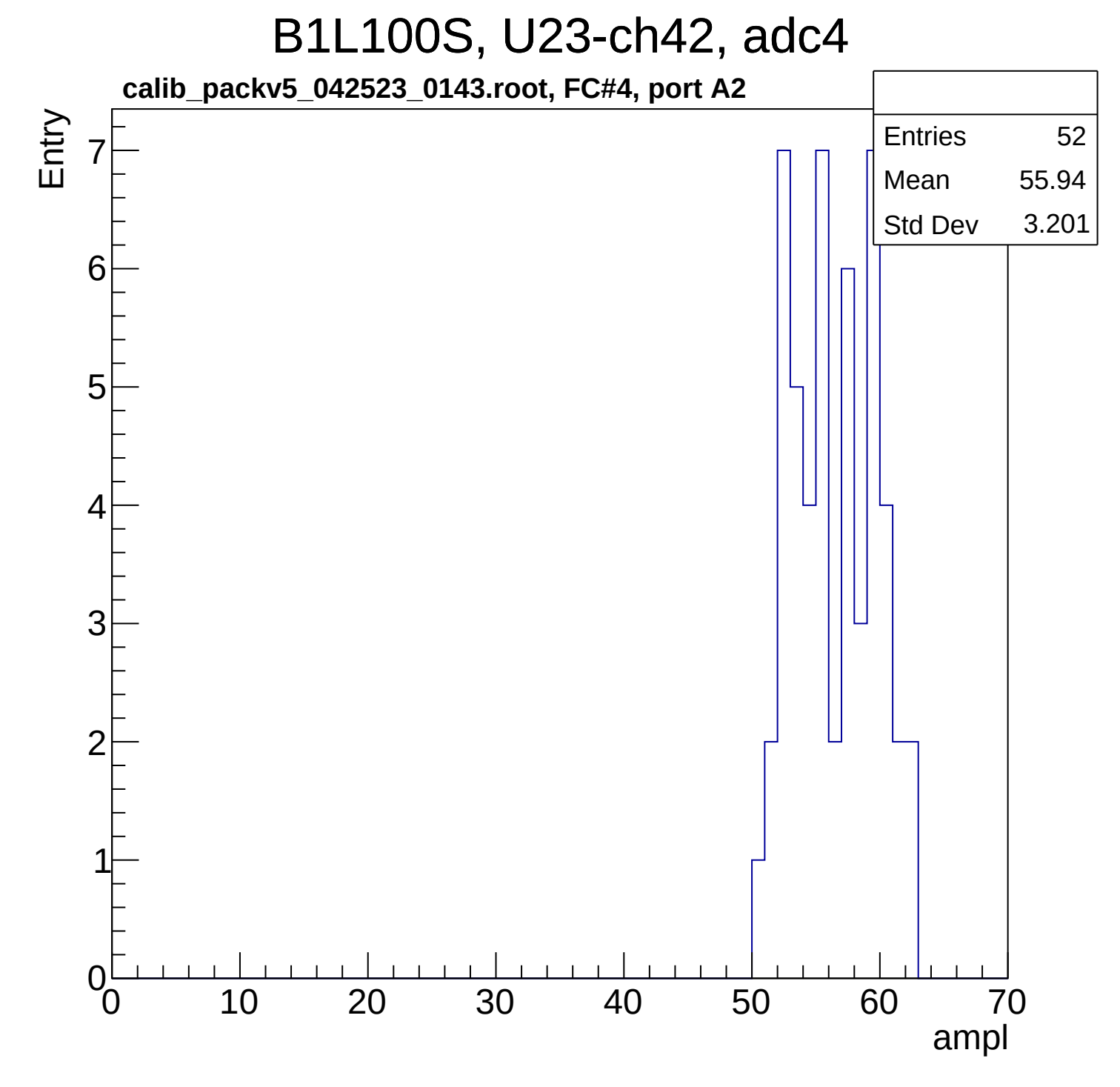
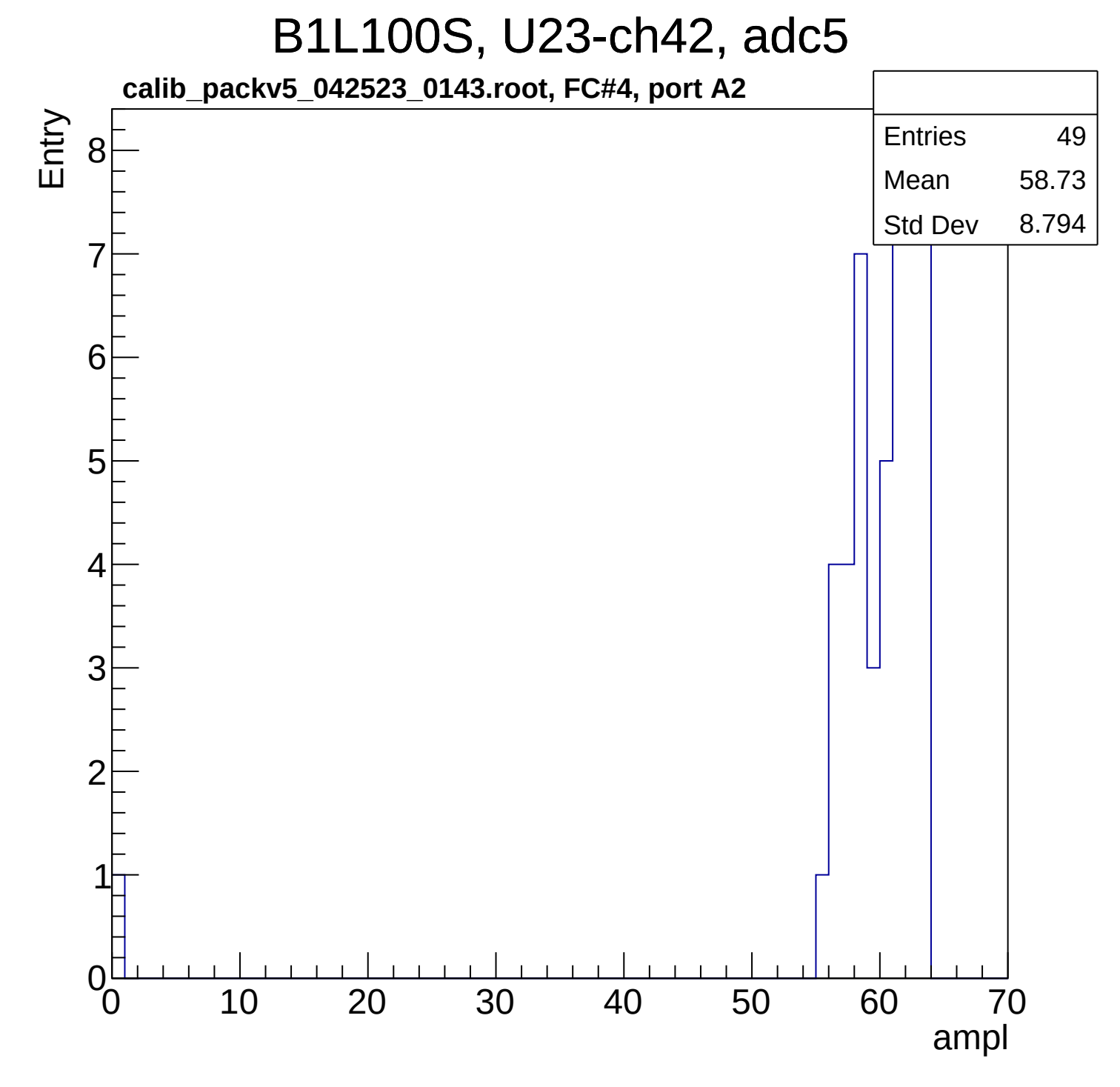

B1L100S, U23-ch42, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.94
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.73
Std Dev 8.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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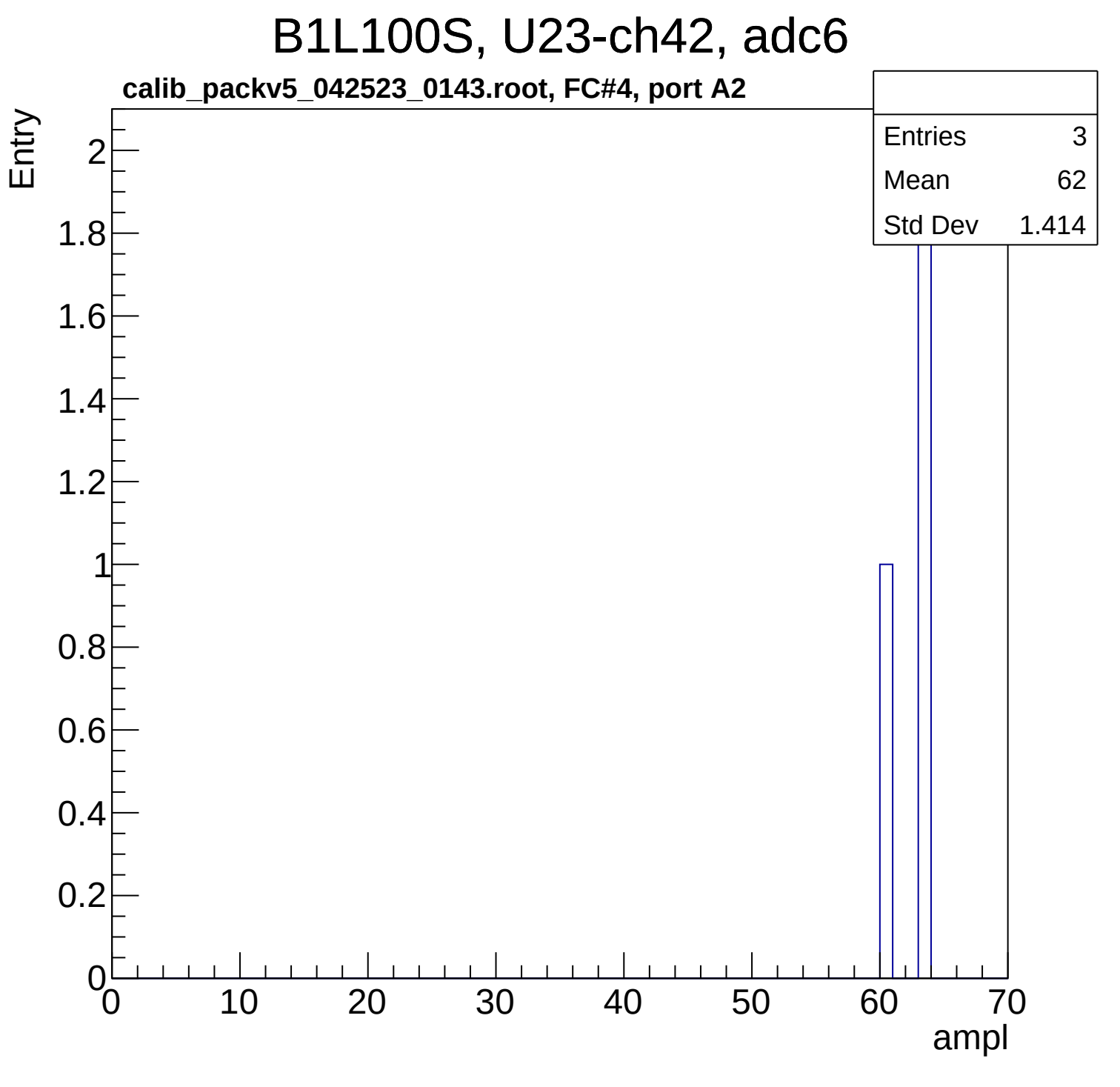
B1L100S, U23-ch42, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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40
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60
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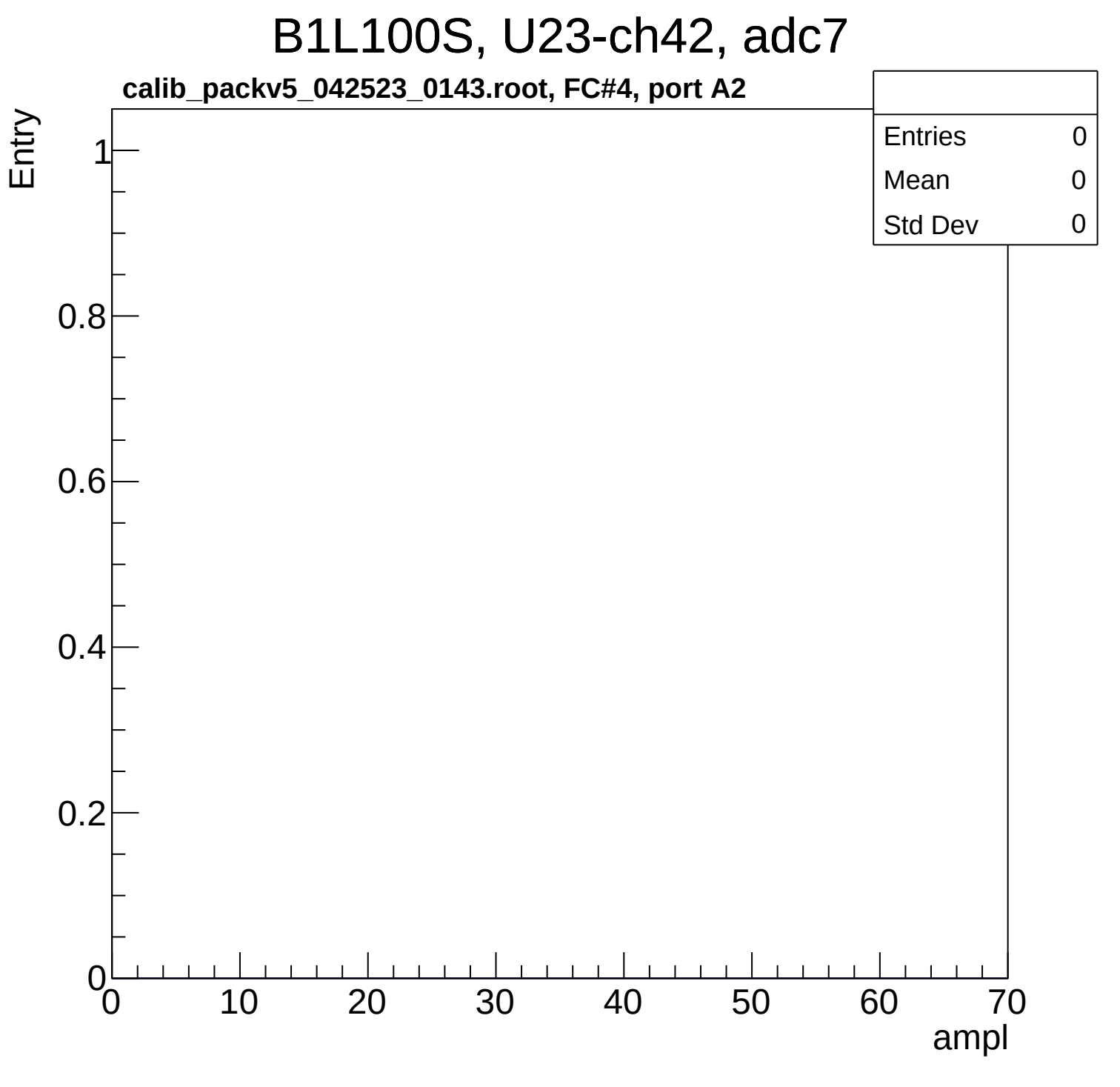
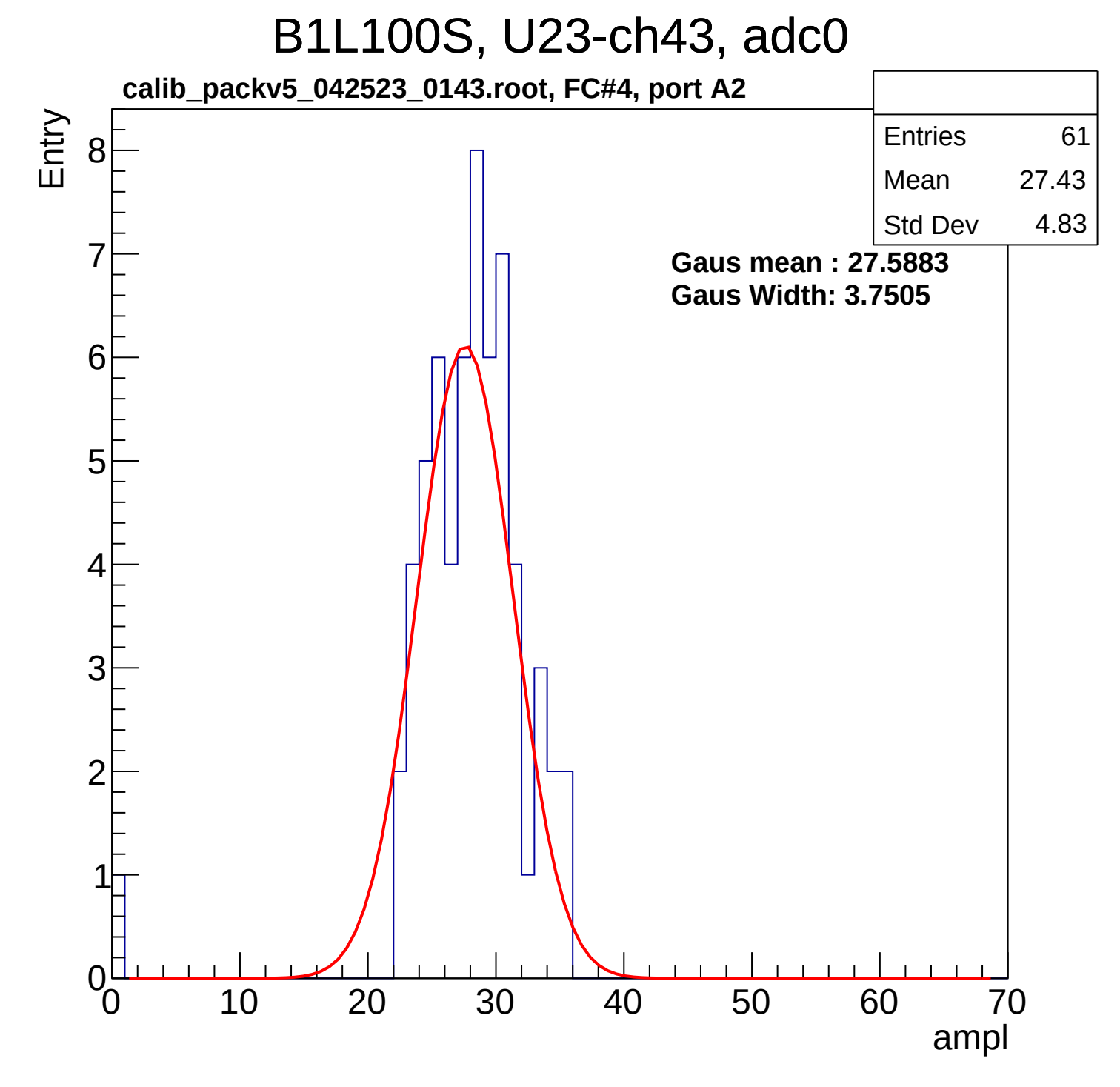
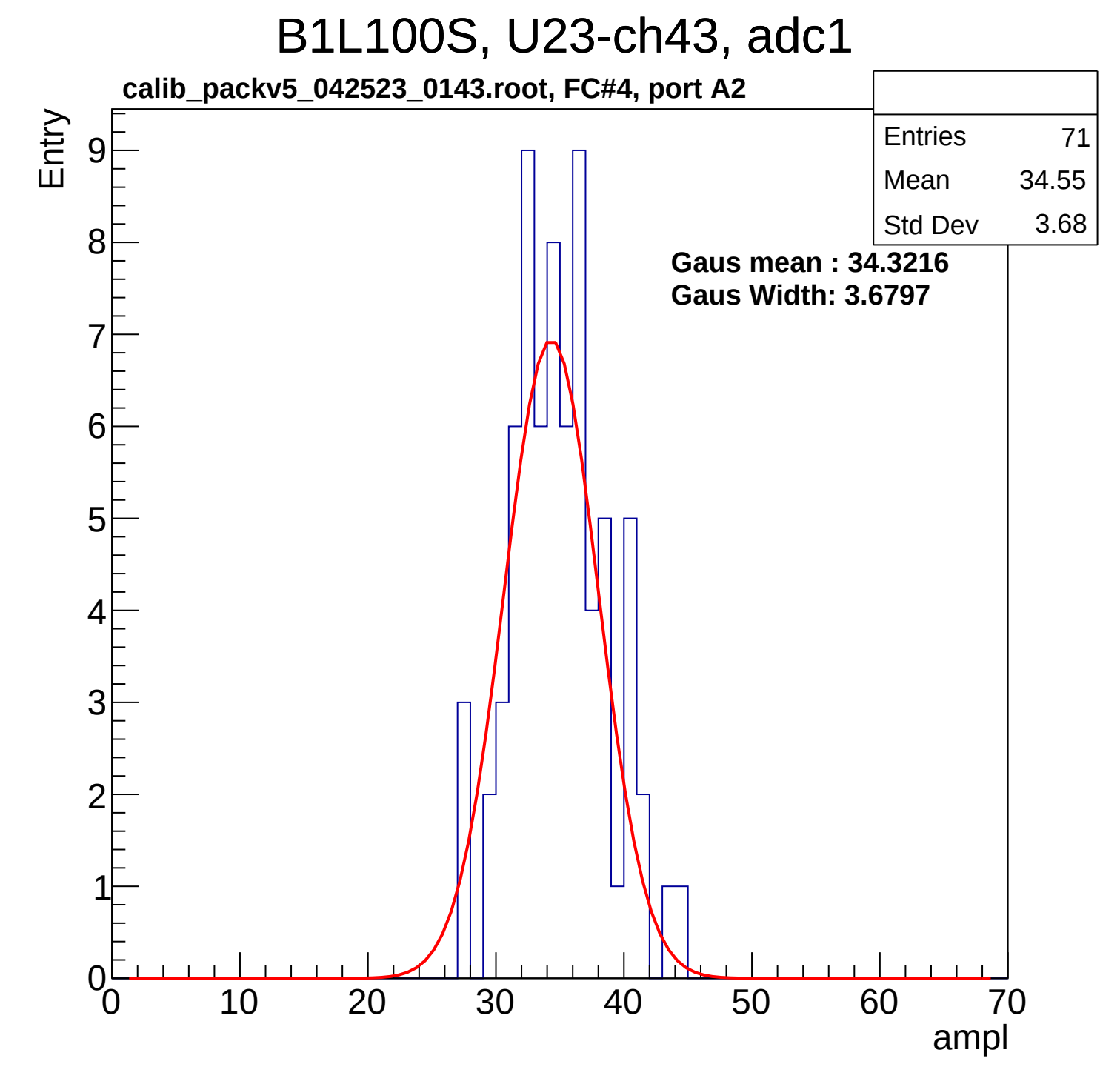

B1L100S, U23-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch43, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 27.43
Std Dev 4.83
Gaus mean : 27.5883
Gaus Width: 3.7505
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 34.55
Std Dev 3.68
Gaus mean : 34.3216
Gaus Width: 3.6797
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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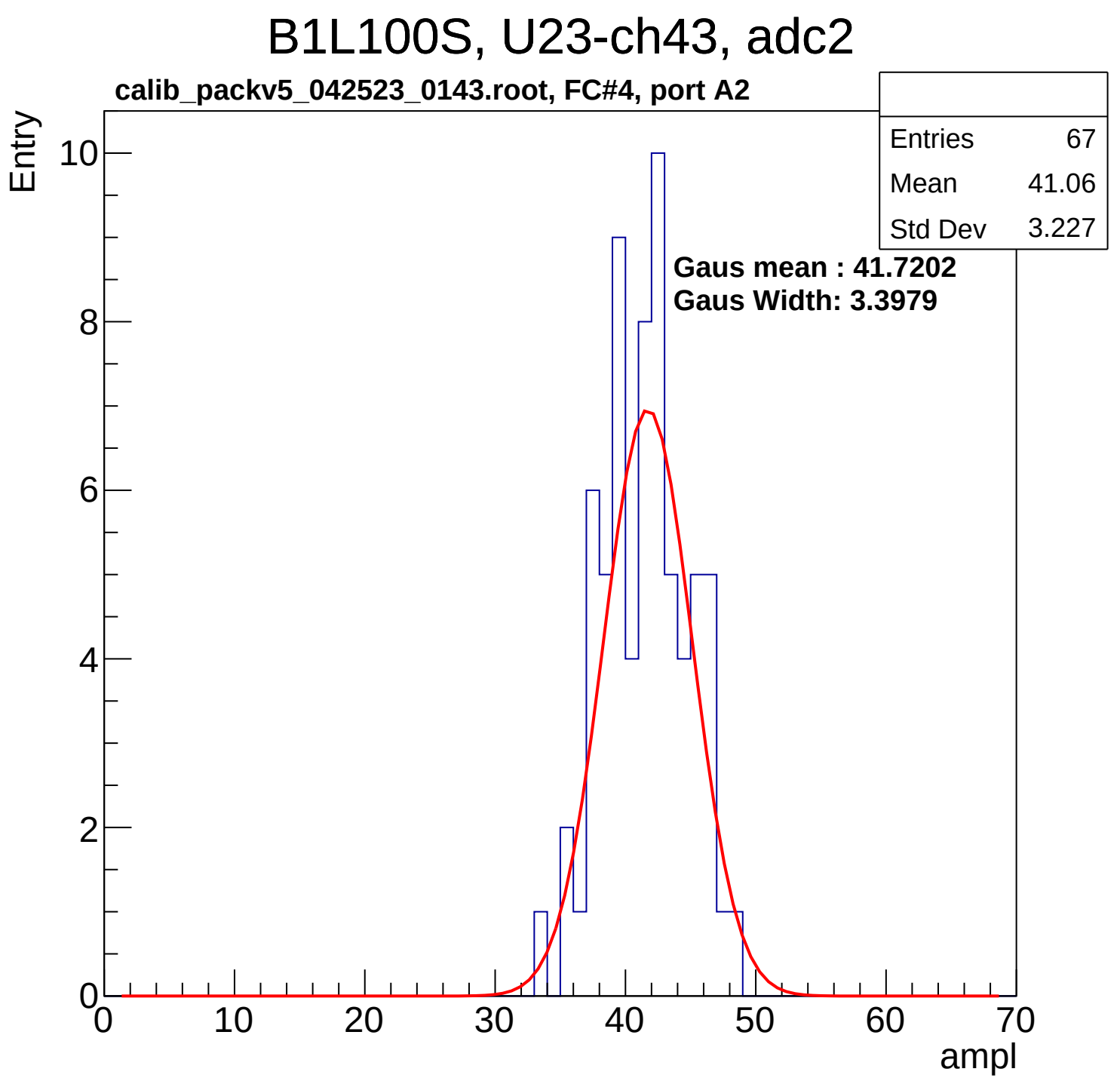
B1L100S, U23-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 41.06
Std Dev 3.227
Gaus mean : 41.7202
Gaus Width: 3.3979
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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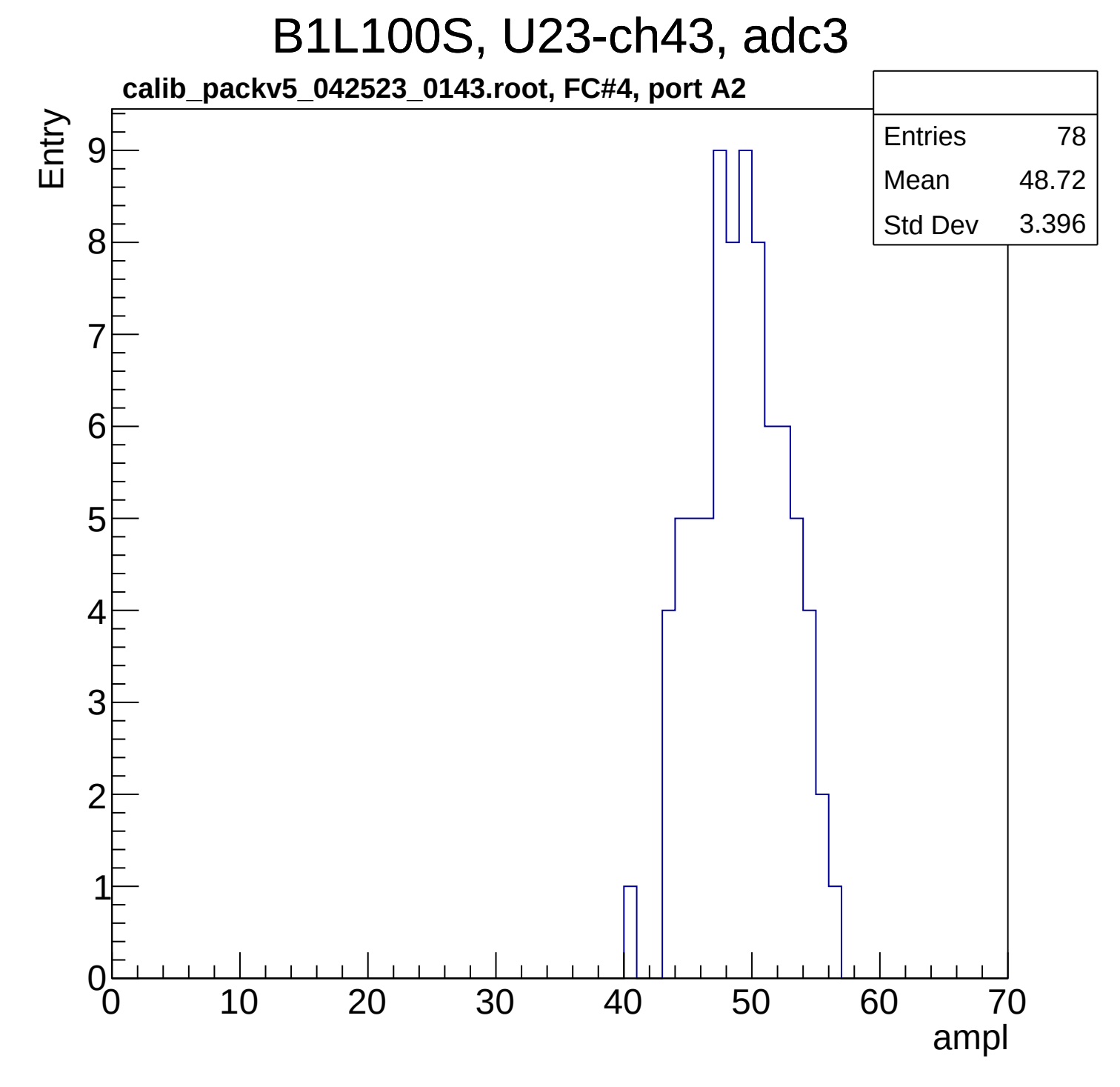
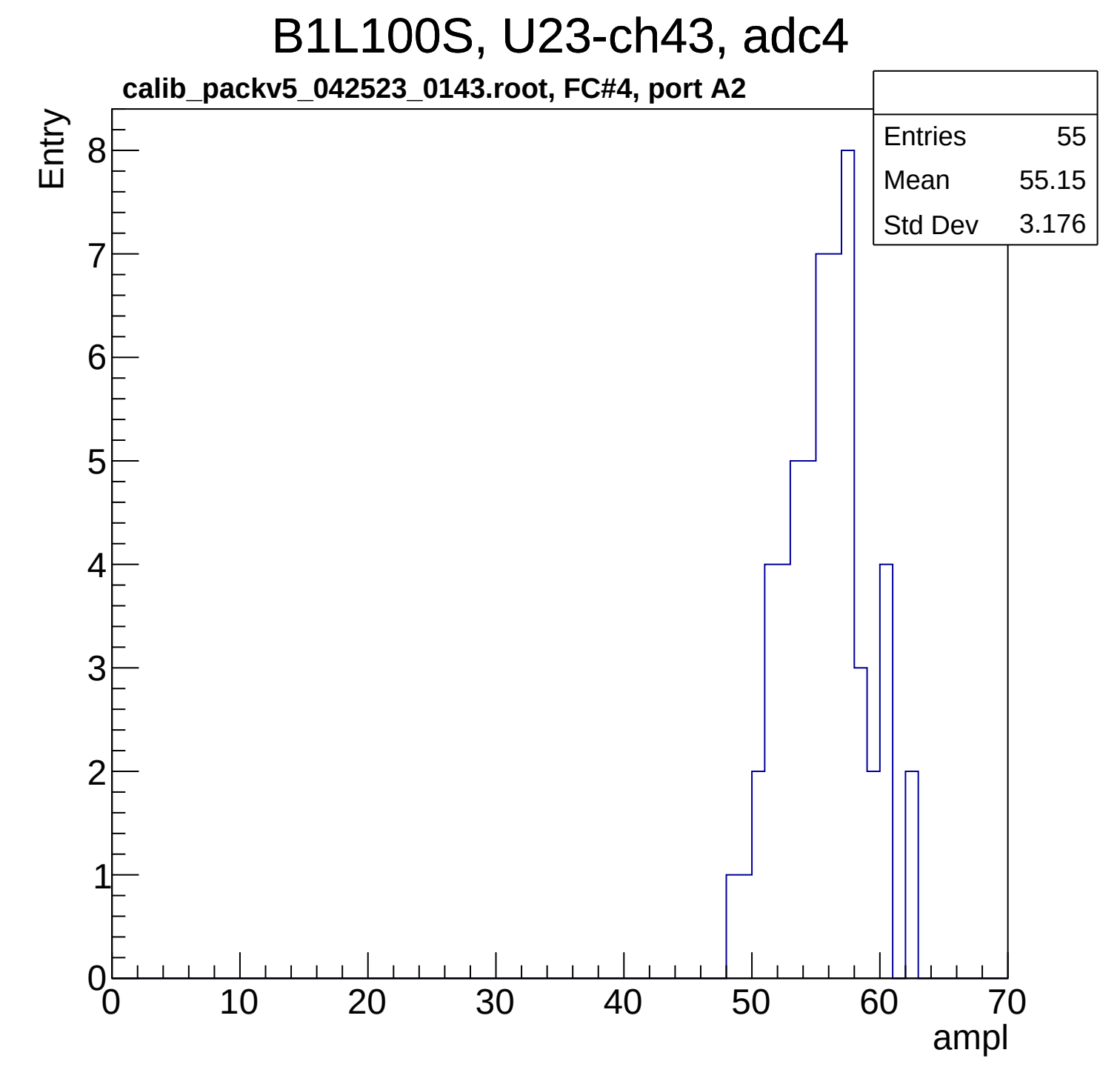

B1L100S, U23-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 48.72
Std Dev 3.396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.15
Std Dev 3.176
Entry
ampl
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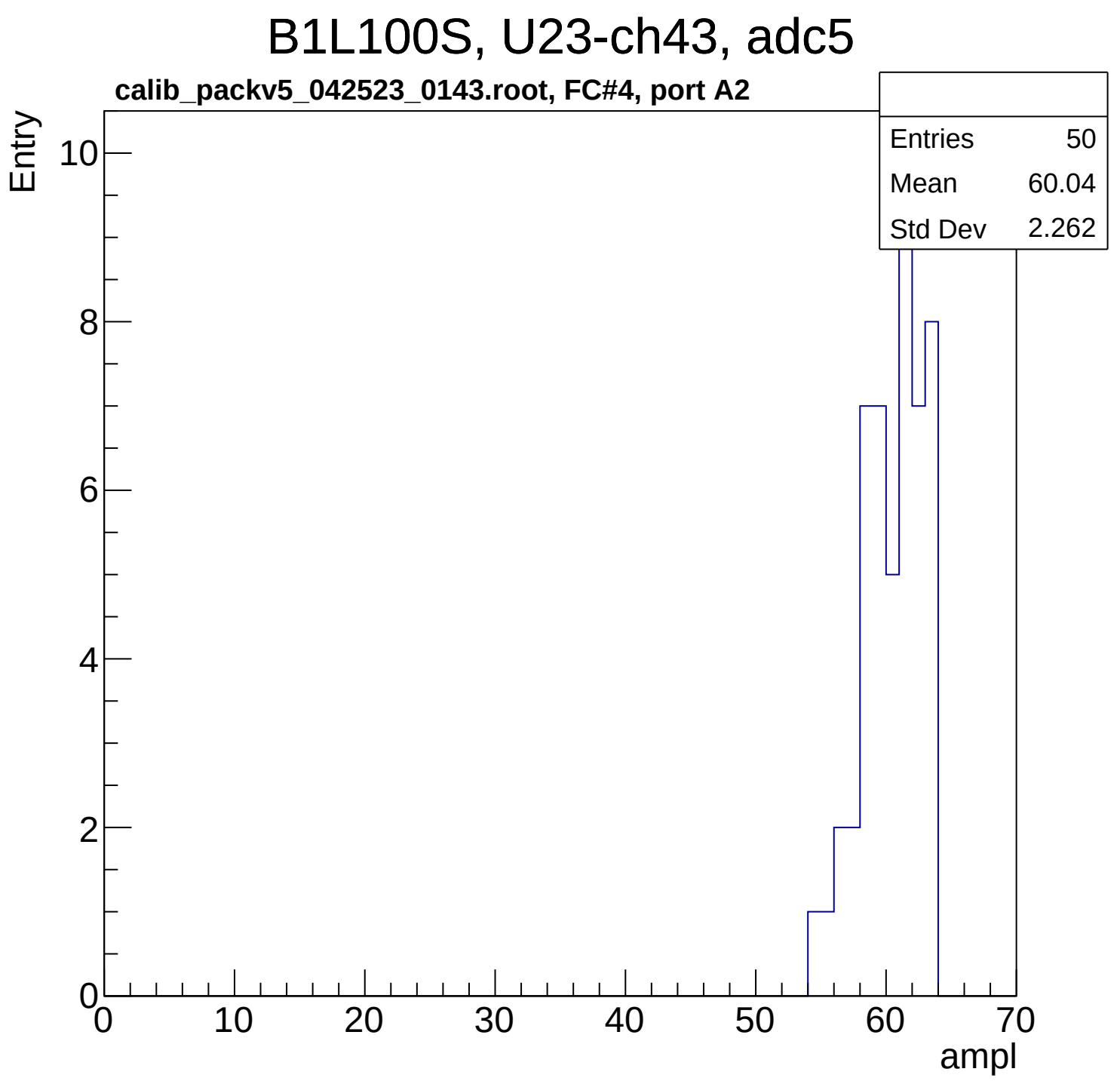
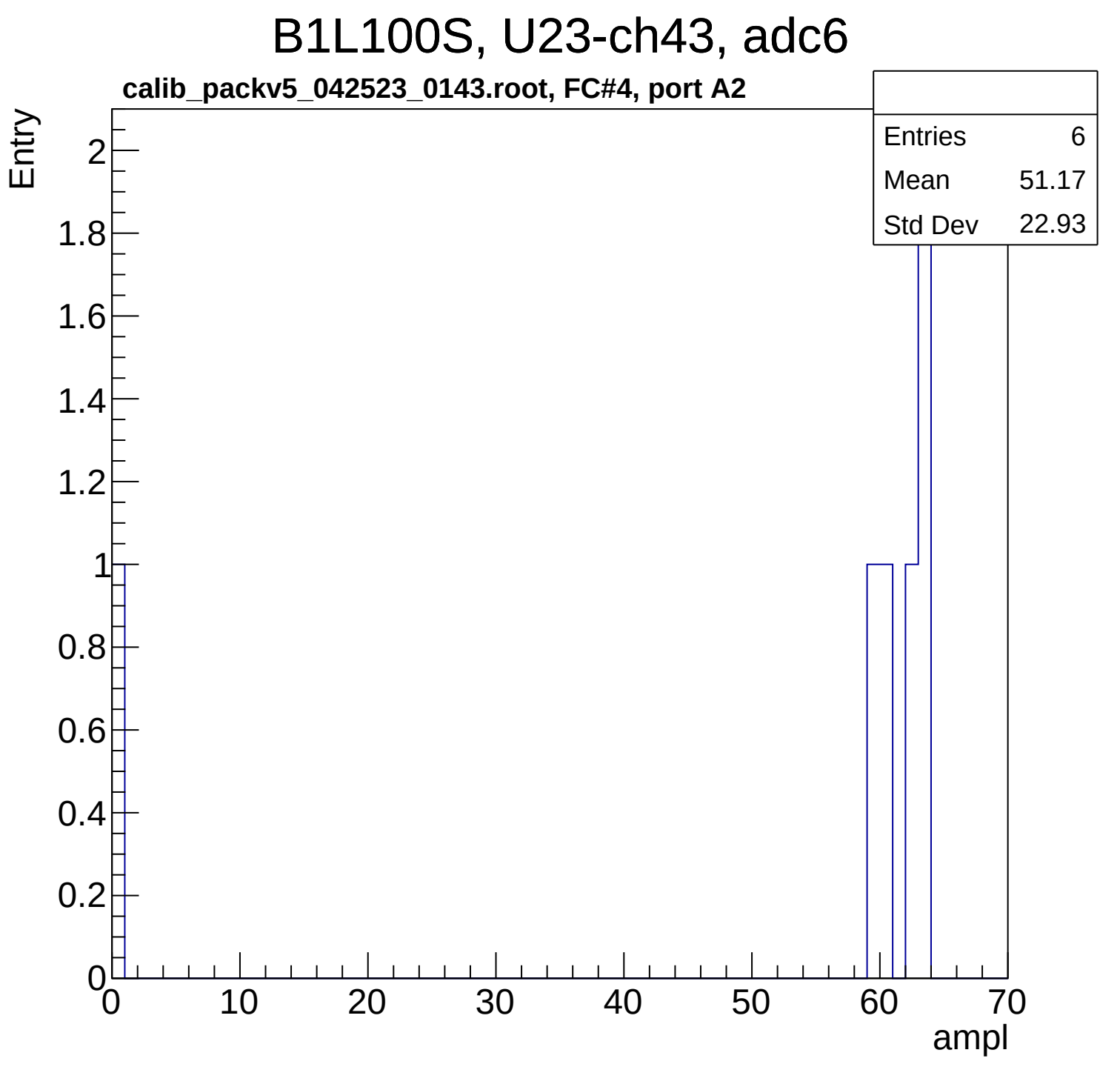
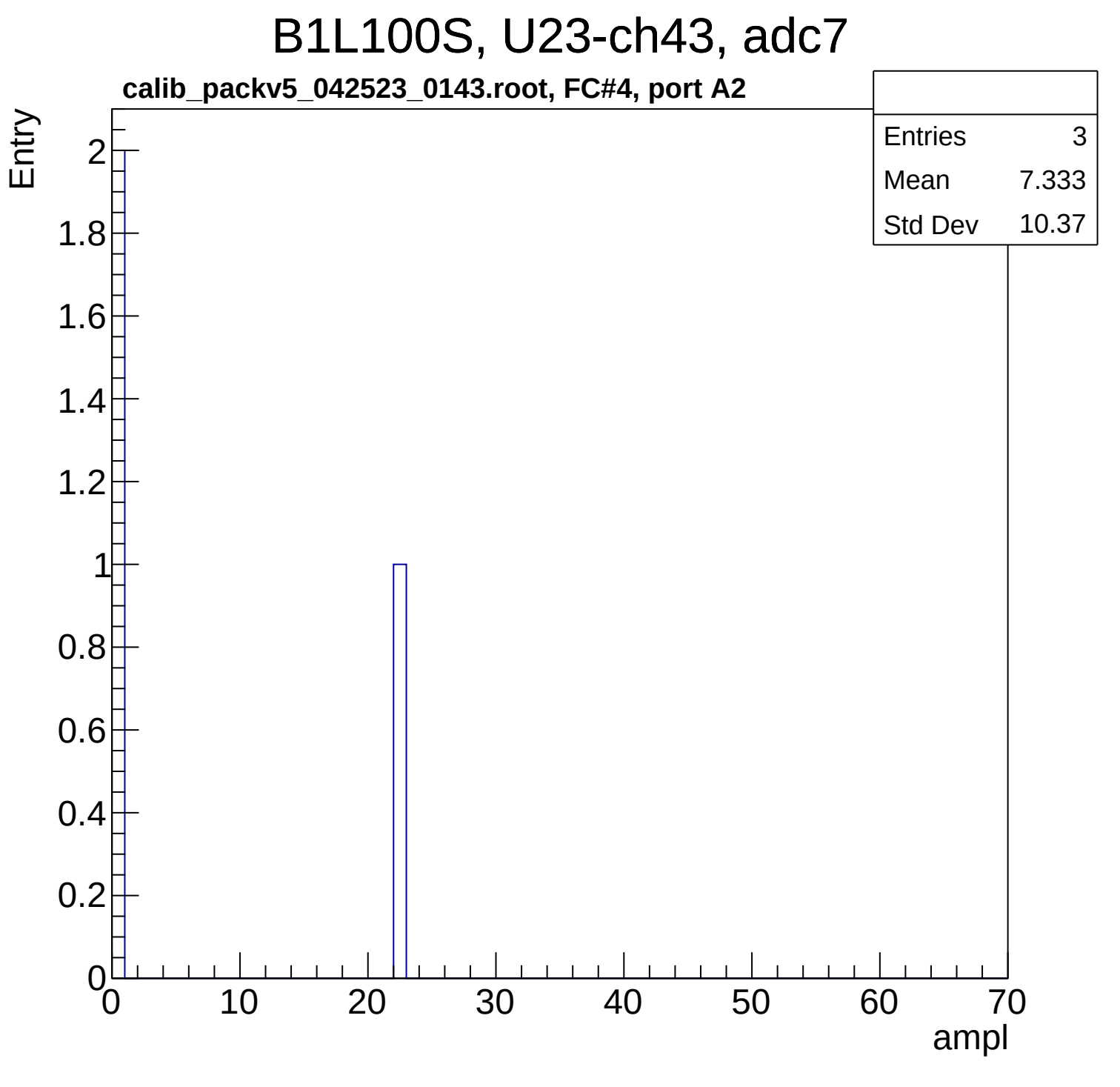
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch43, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 60.04
Std Dev 2.262
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch43, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.17
Std Dev 22.93
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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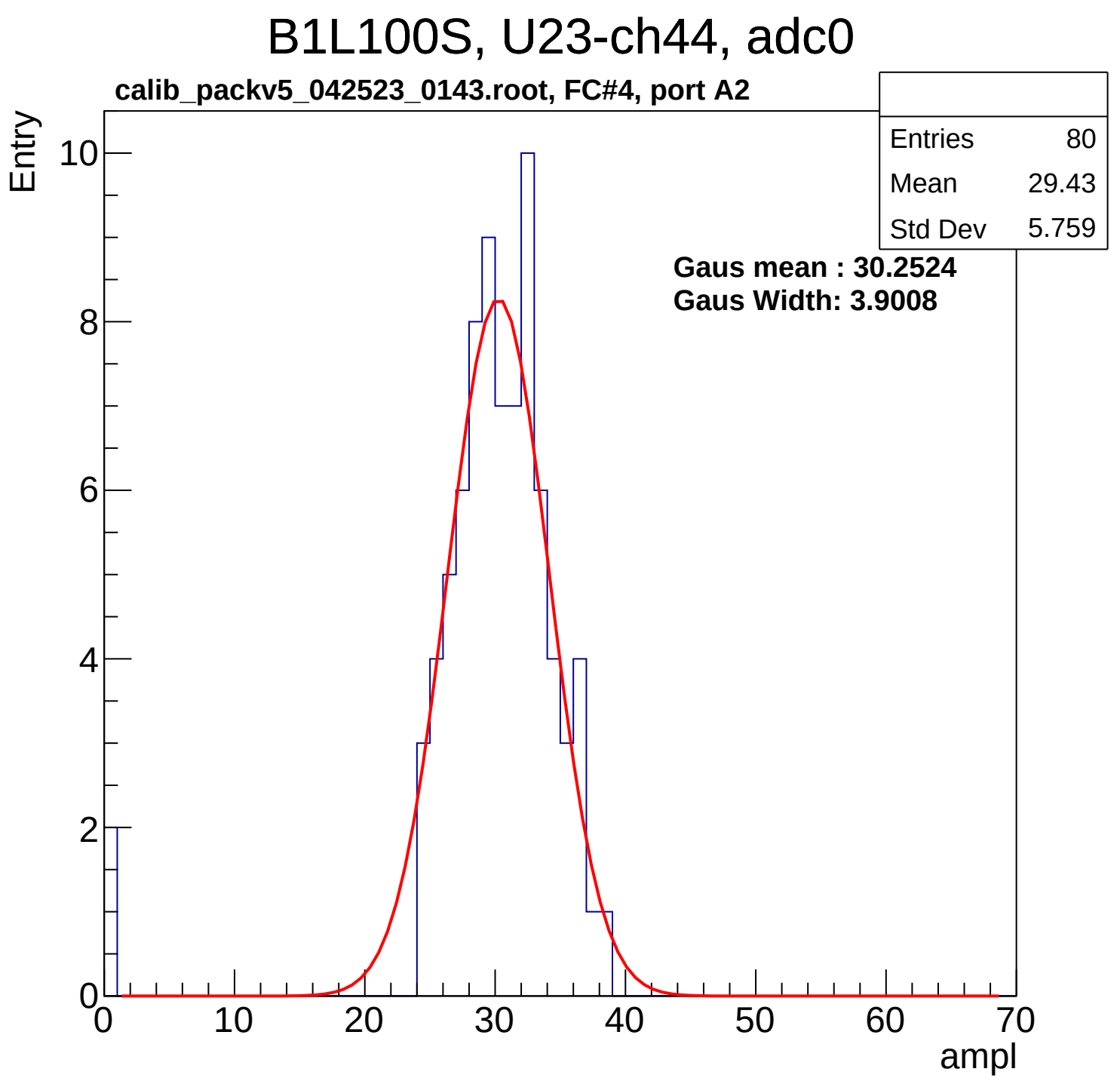
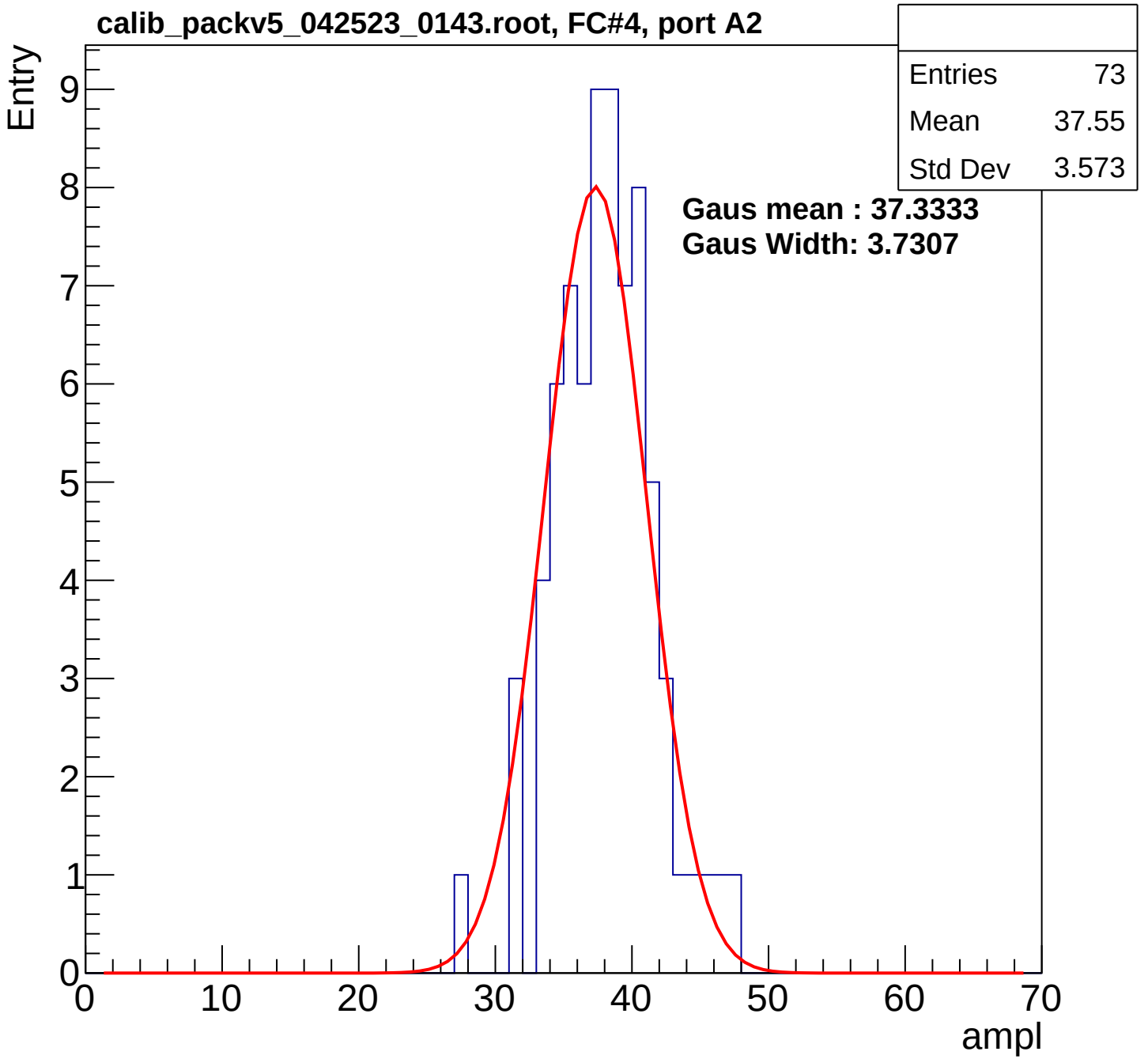
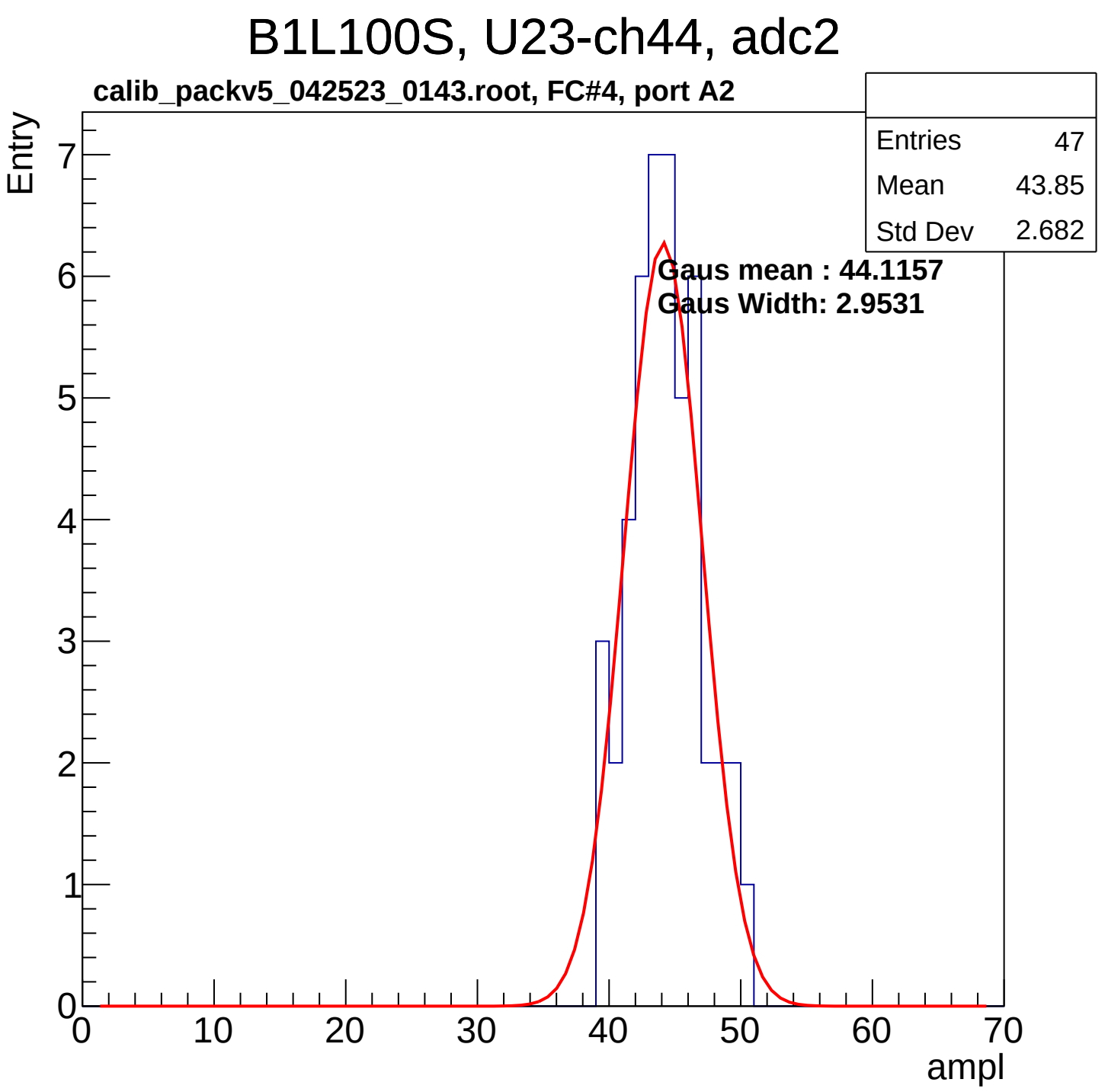
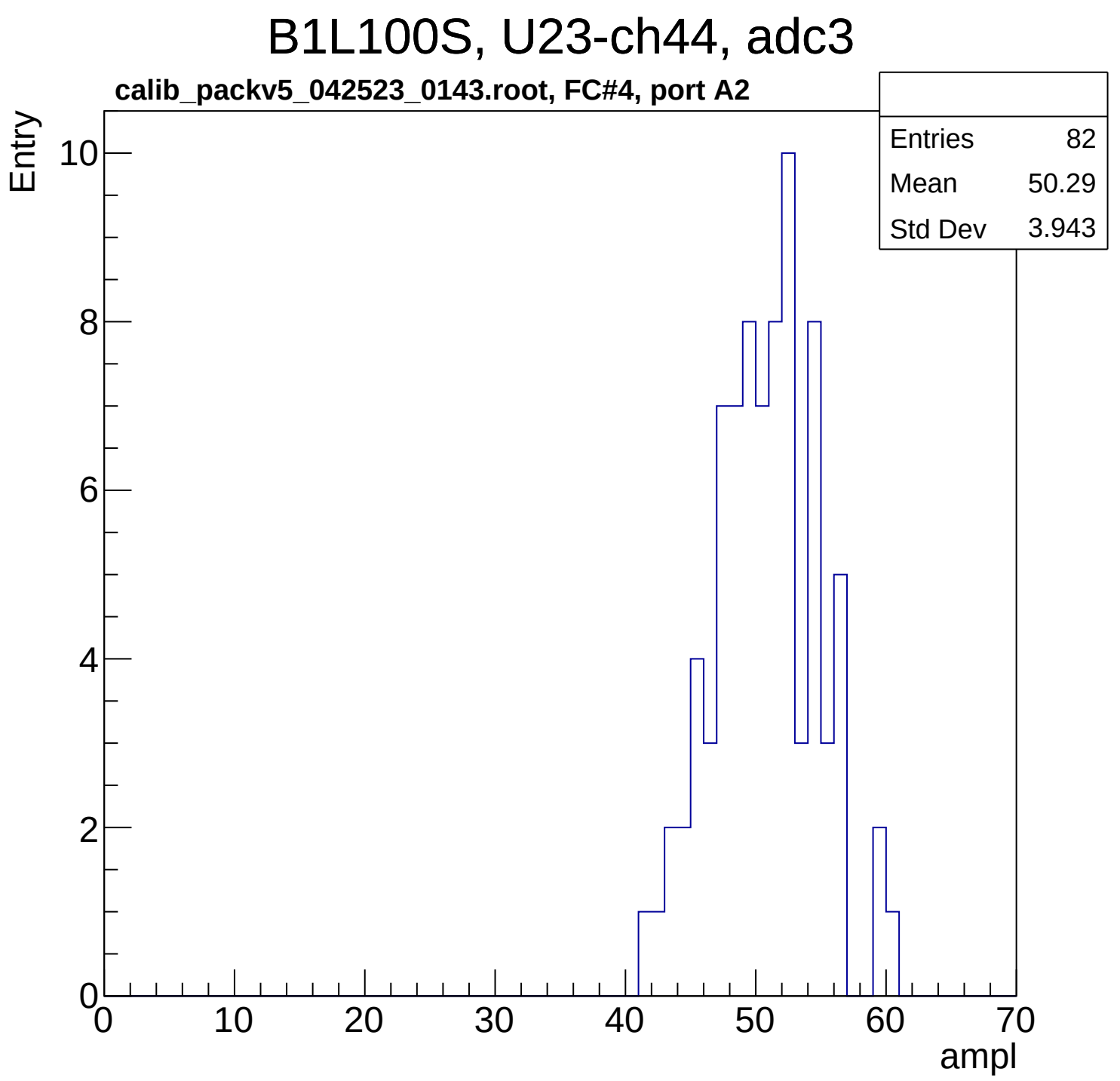
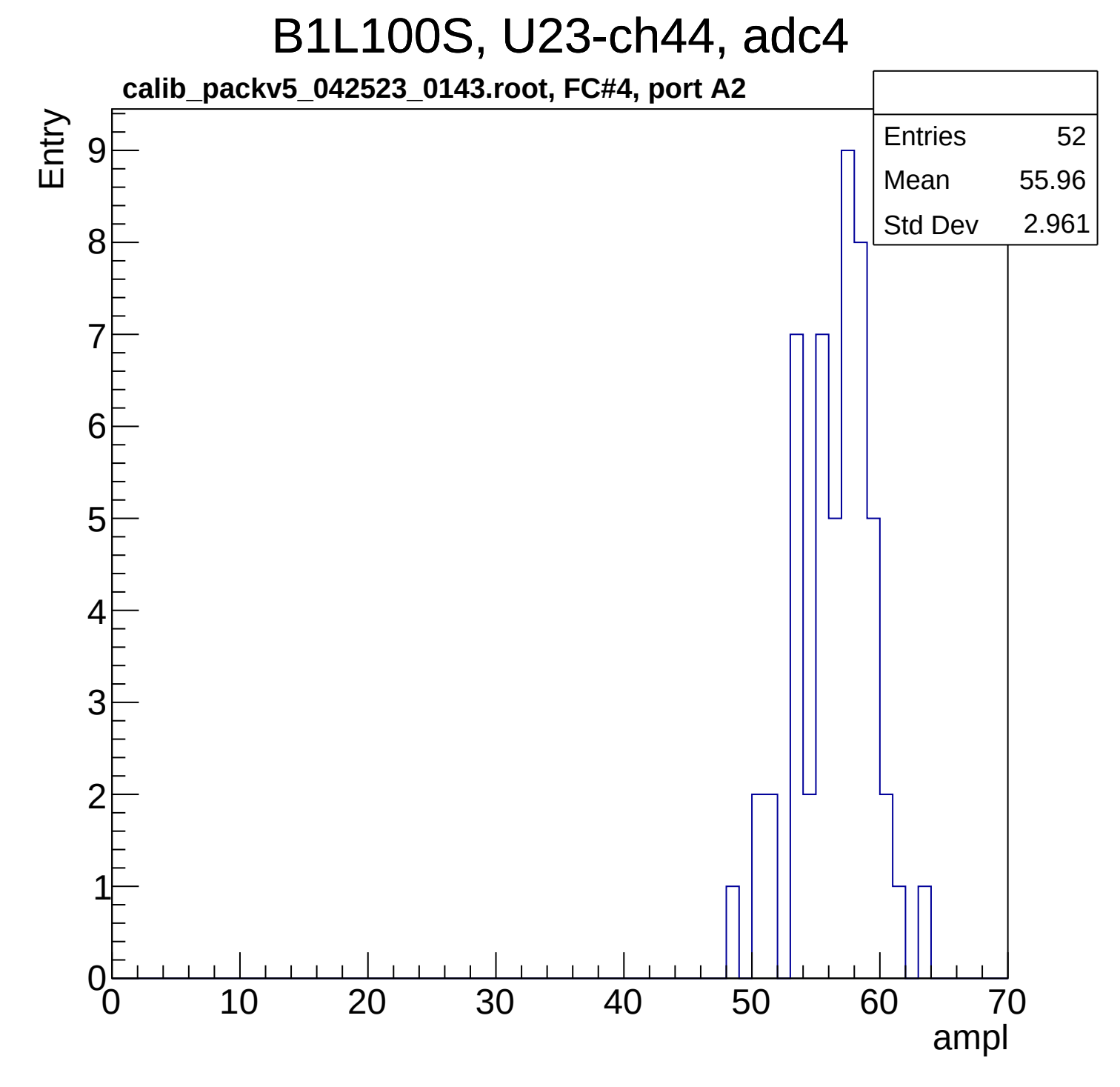
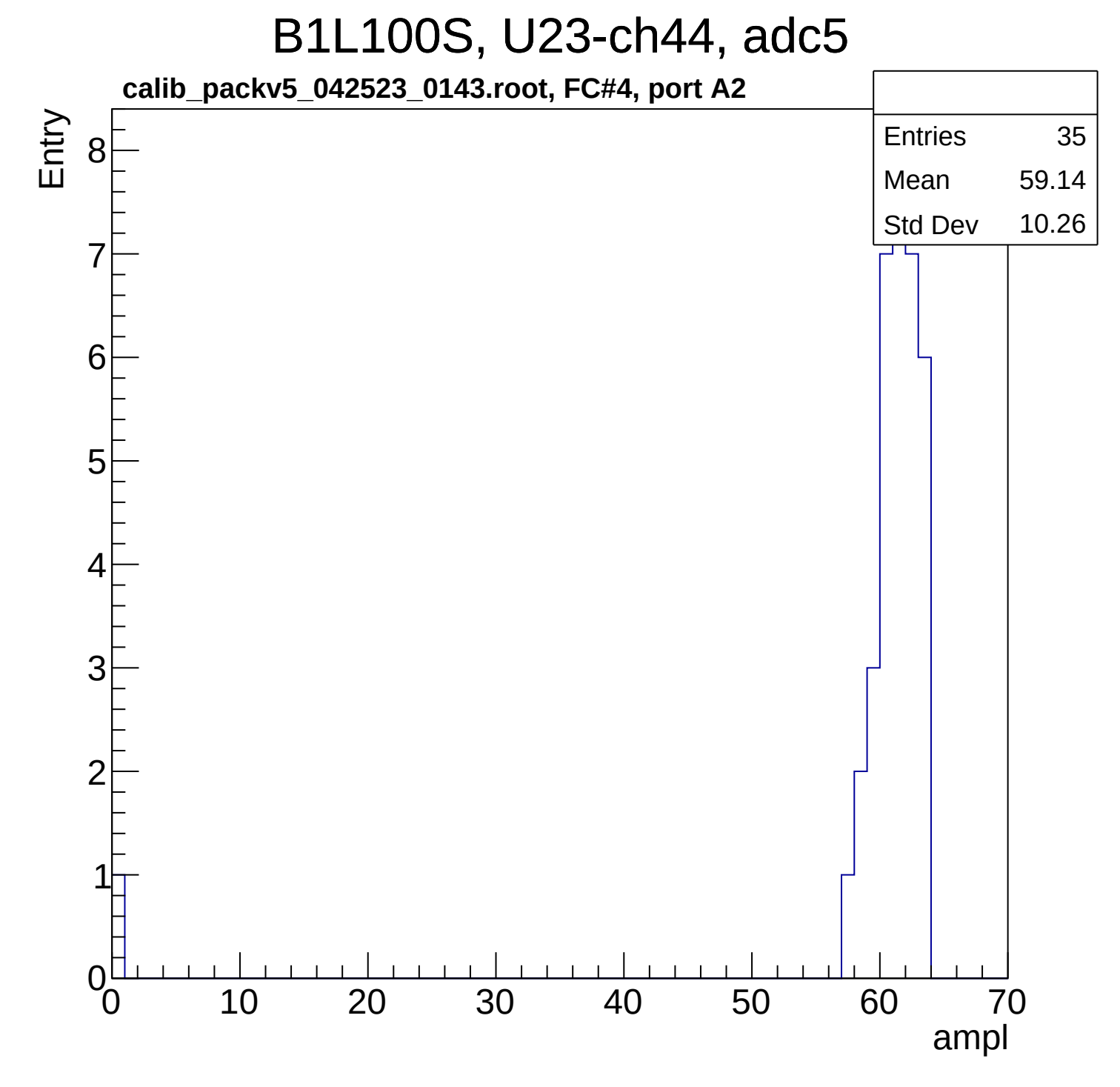

B1L100S, U23-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.43
Std Dev 5.759
Gaus mean : 30.2524
Gaus Width: 3.9008
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.55
Std Dev 3.573
Gaus mean : 37.3333
Gaus Width: 3.7307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 43.85
Std Dev 2.682
Gaus mean : 44.1157
Gaus Width: 2.9531
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 50.29
Std Dev 3.943
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.96
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.14
Std Dev 10.26
Entry
ampl
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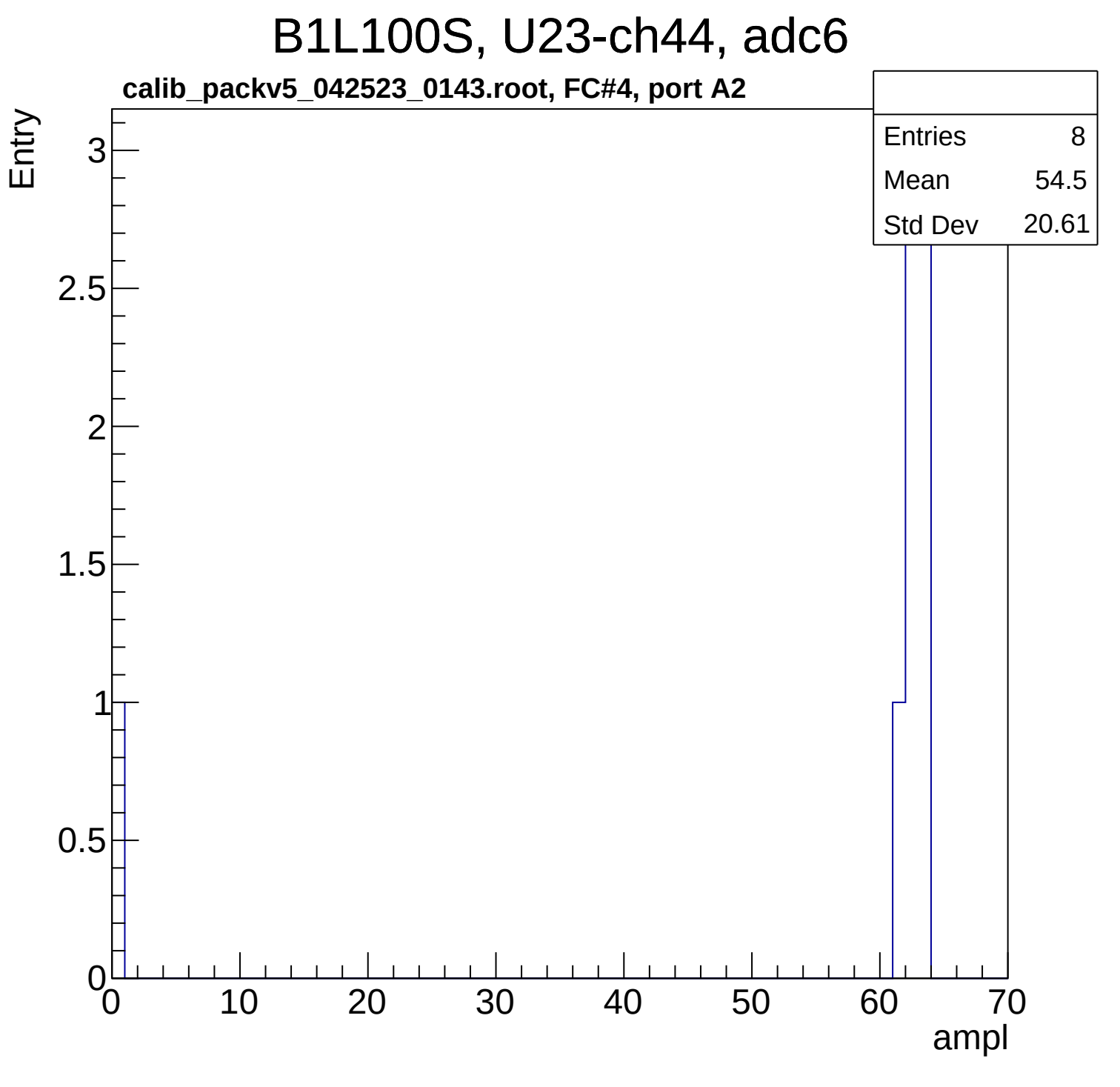
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54.5
Std Dev 20.61
Entry
ampl
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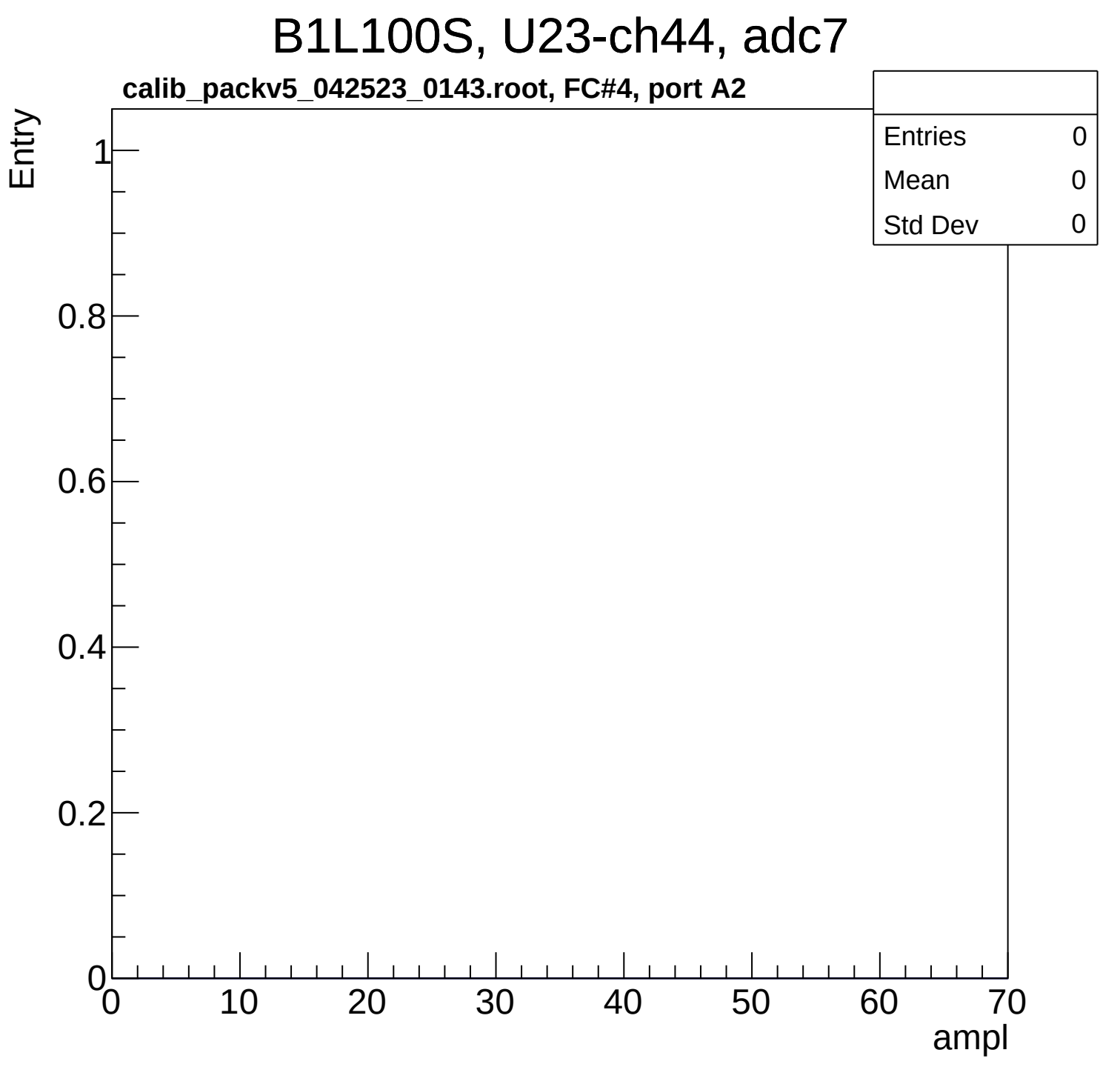
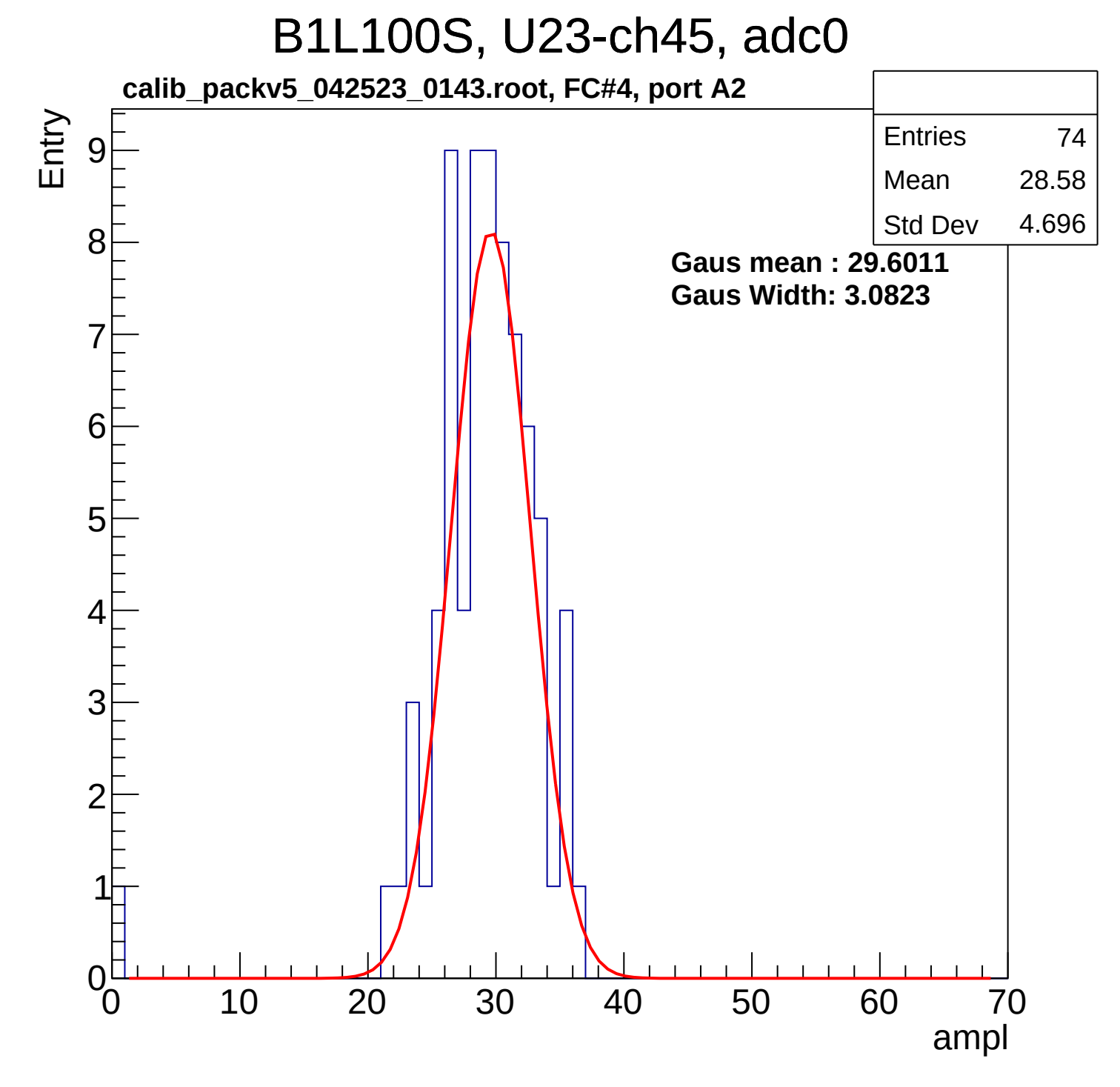
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.58
Std Dev 4.696
Gaus mean : 29.6011
Gaus Width: 3.0823
Entry
ampl
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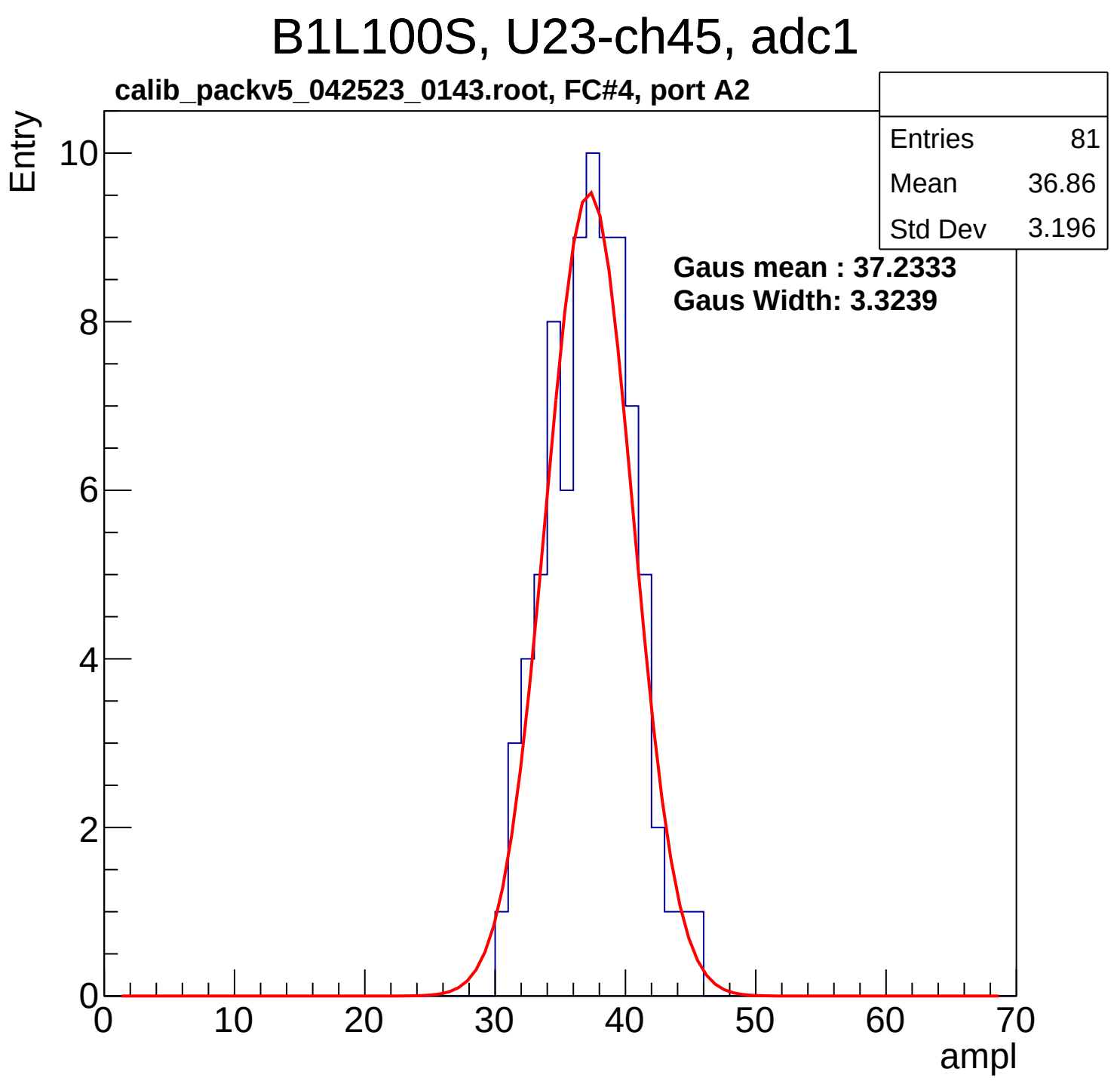
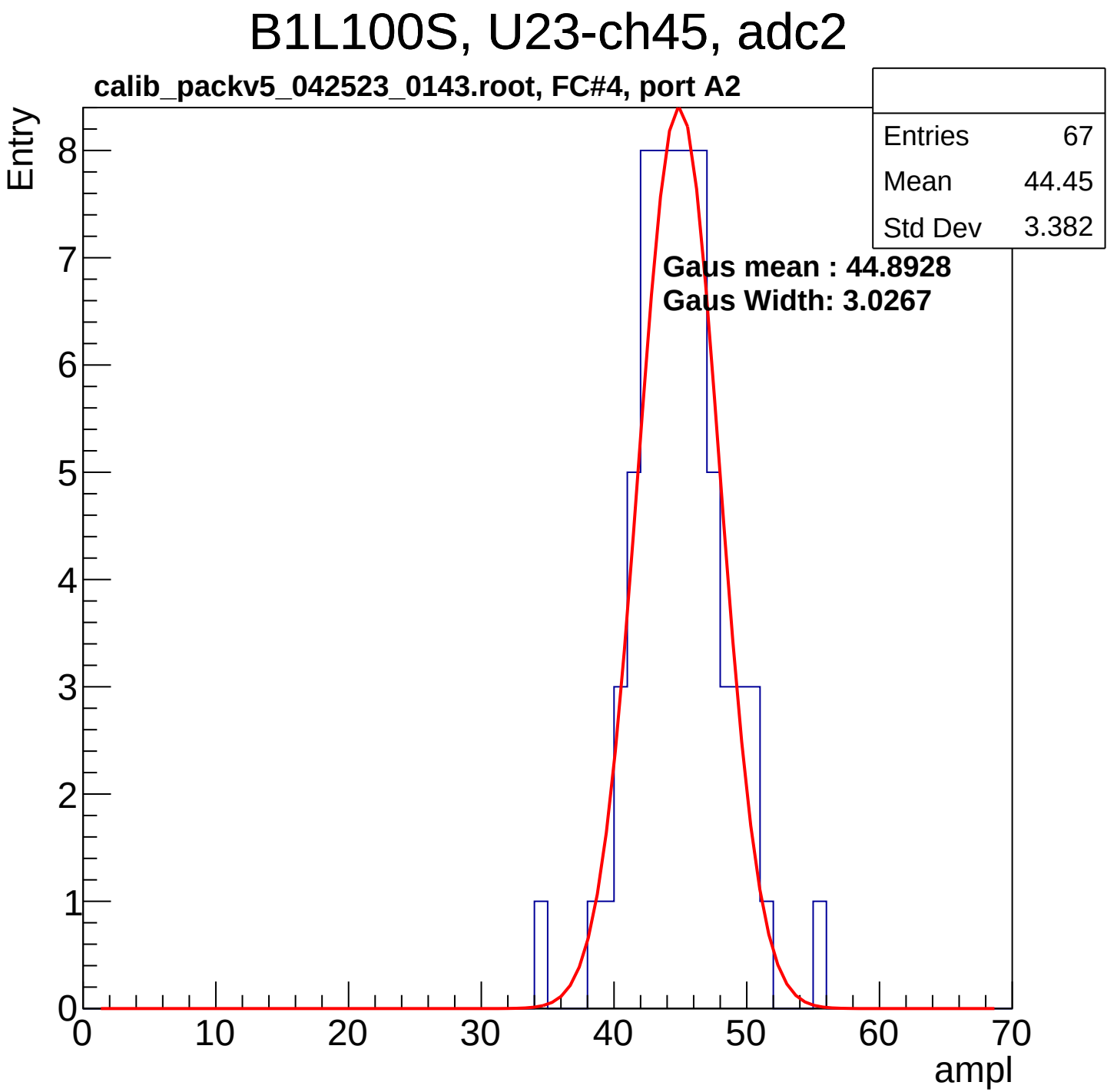
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 36.86
Std Dev 3.196
Gaus mean : 37.2333
Gaus Width: 3.3239
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.45
Std Dev 3.382
Gaus mean : 44.8928
Gaus Width: 3.0267
Entry
ampl
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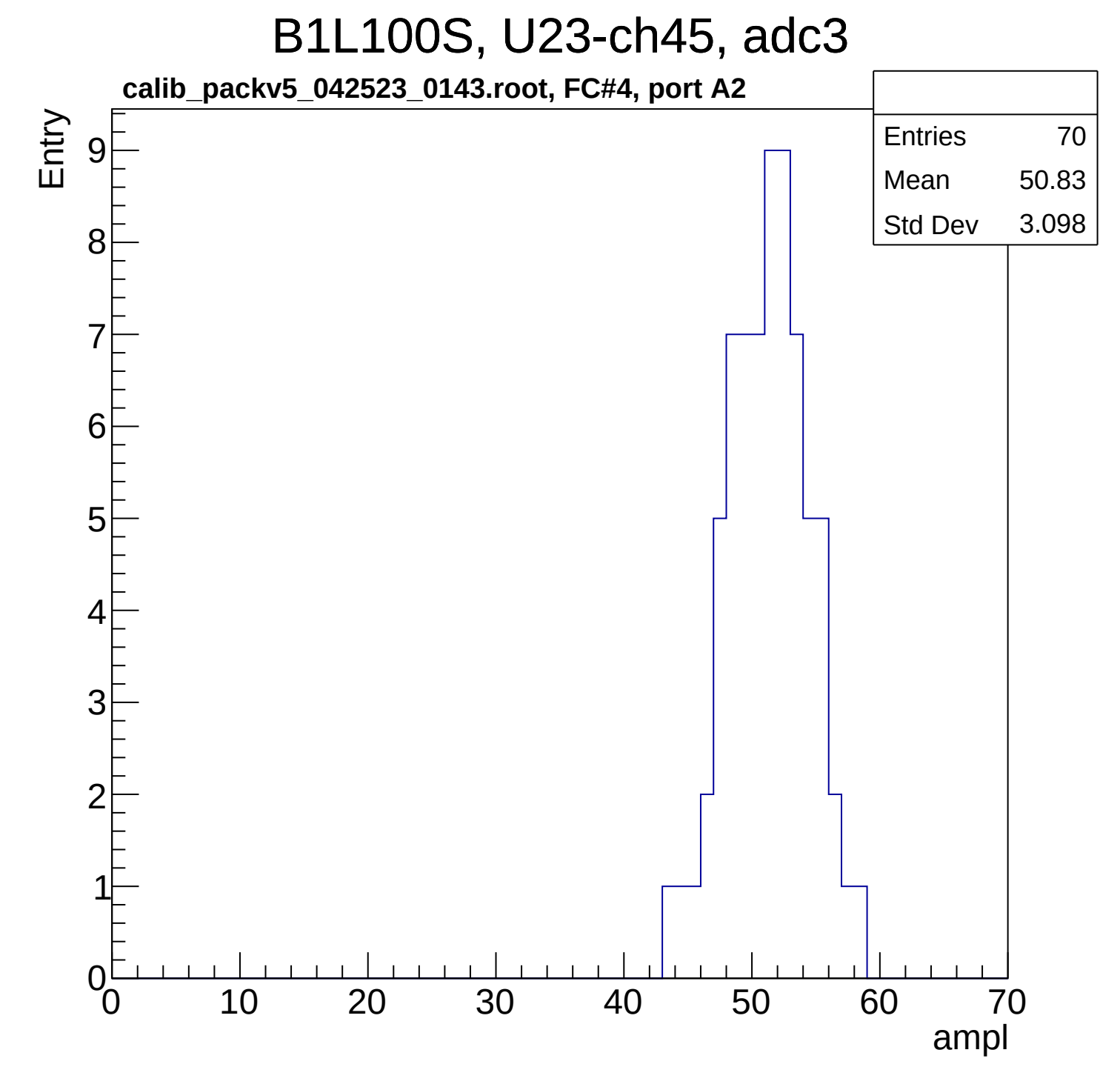
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.83
Std Dev 3.098
Entry
ampl
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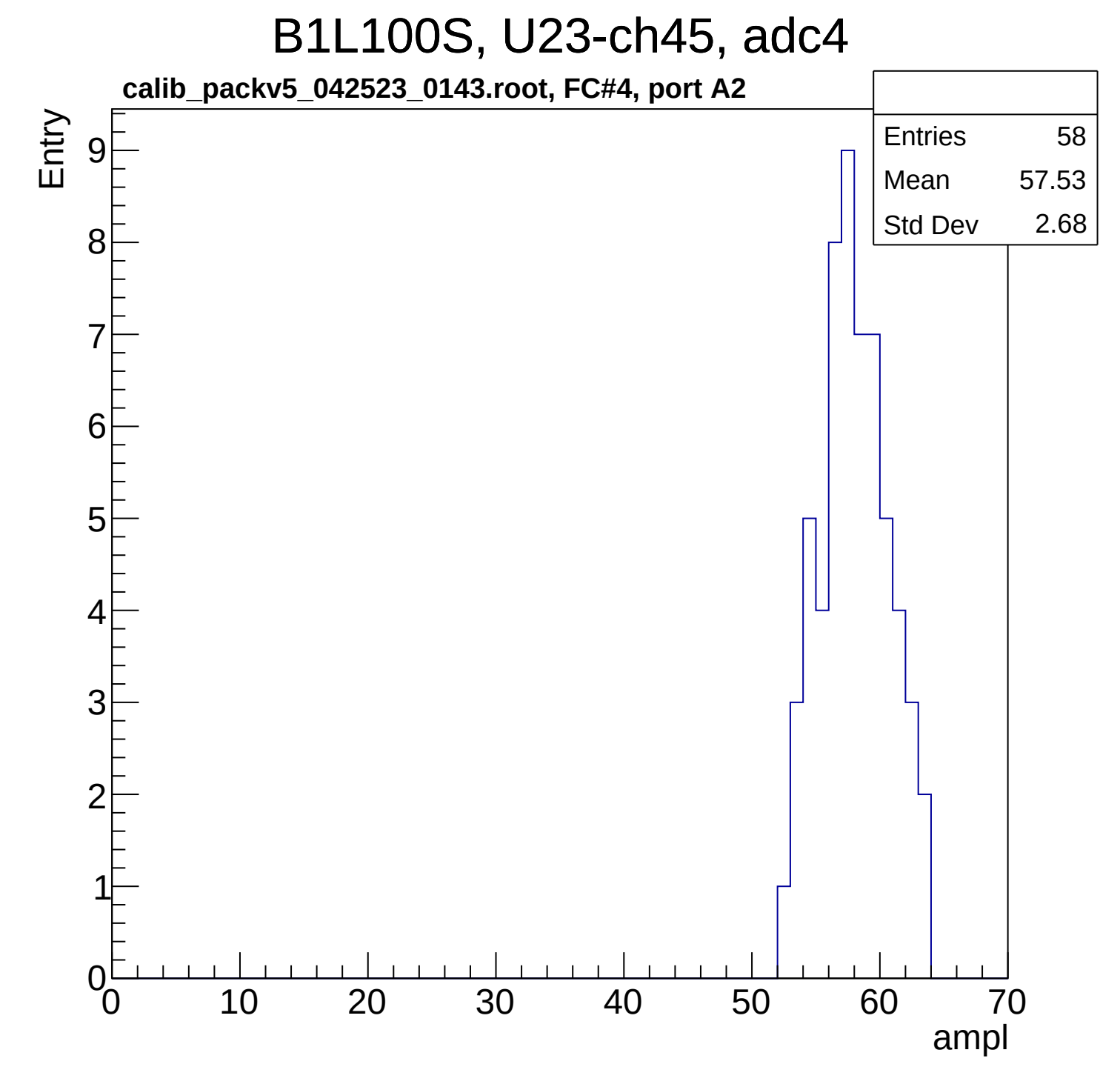
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.53
Std Dev 2.68
Entry
ampl
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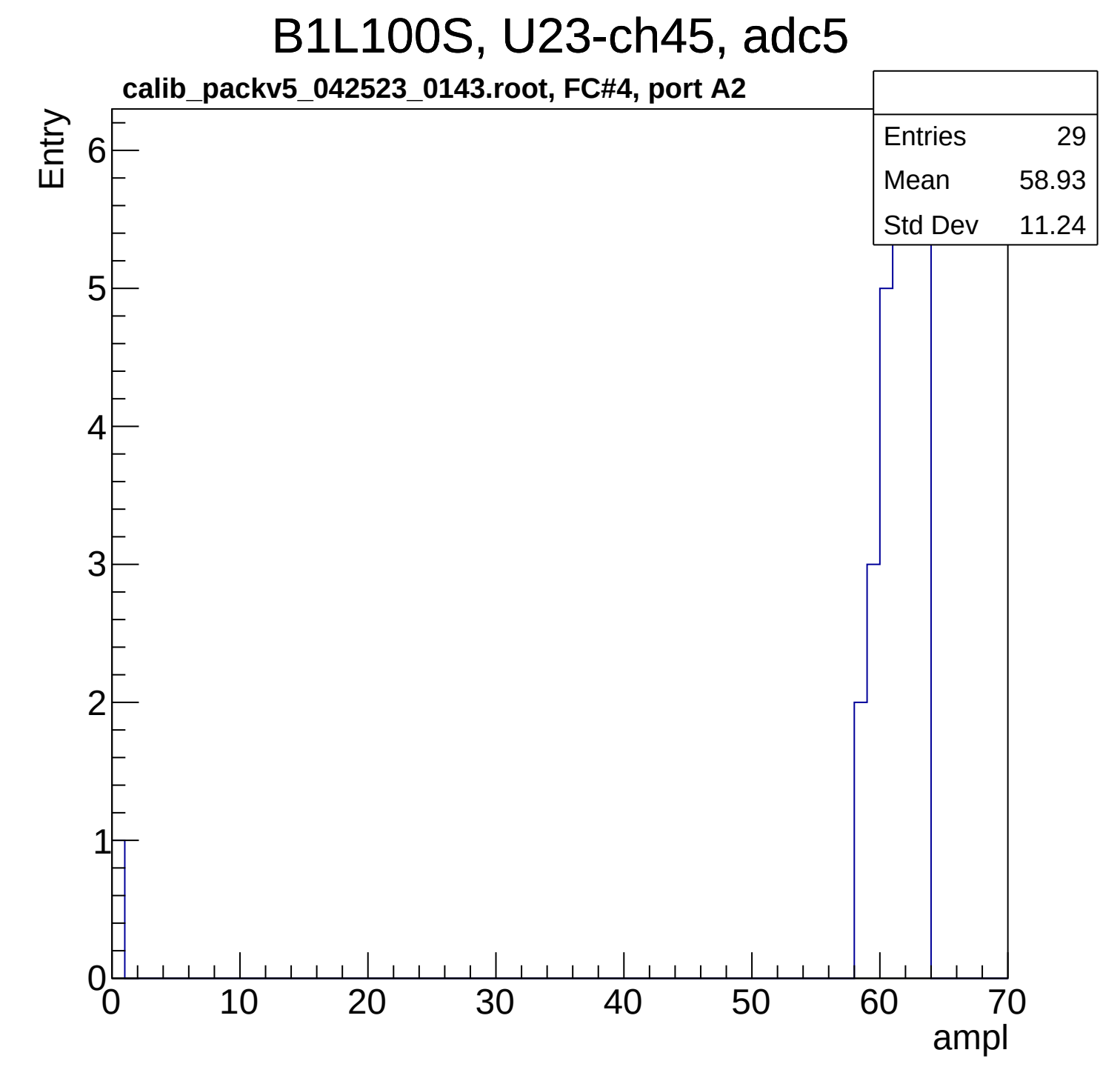
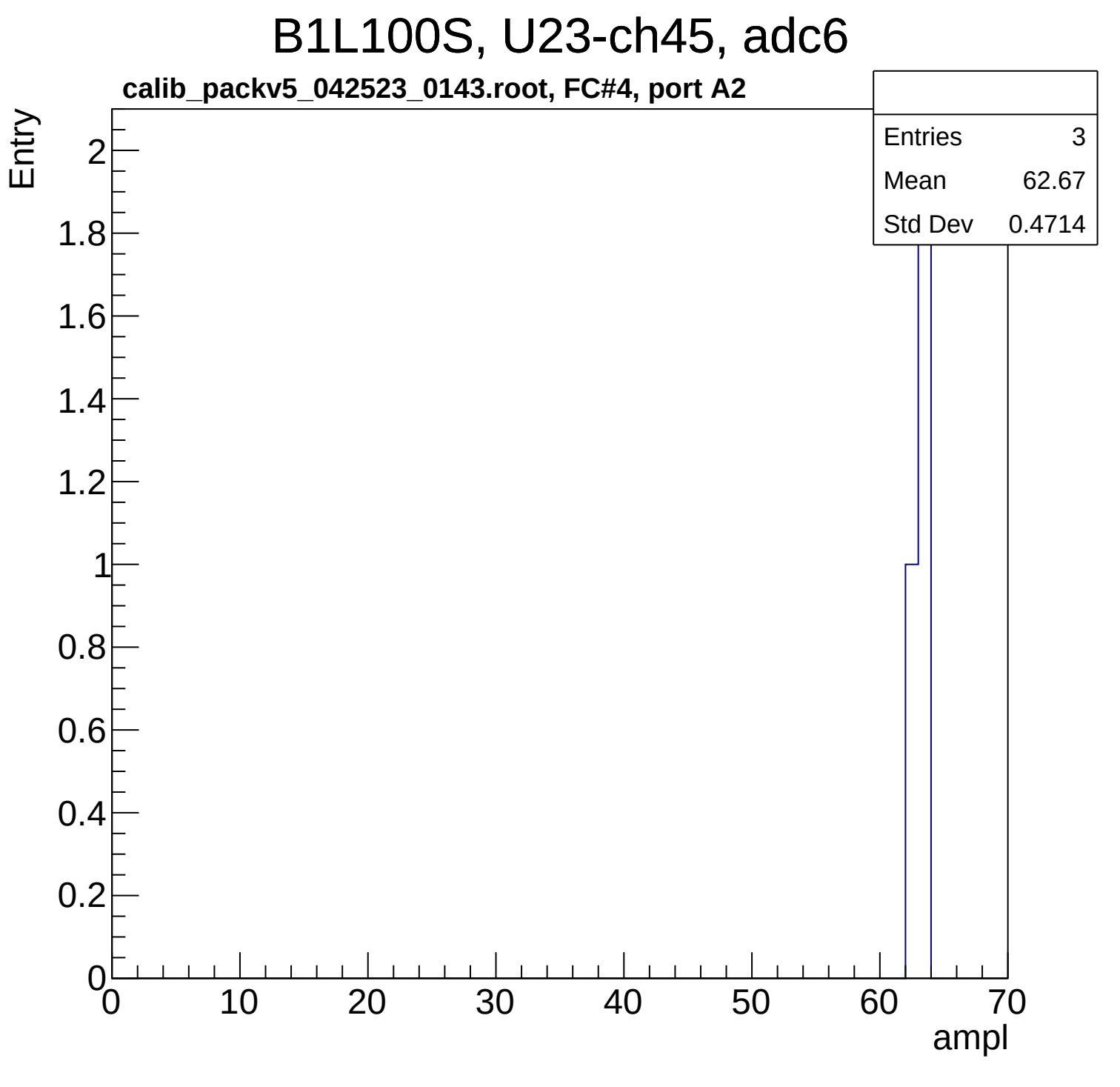
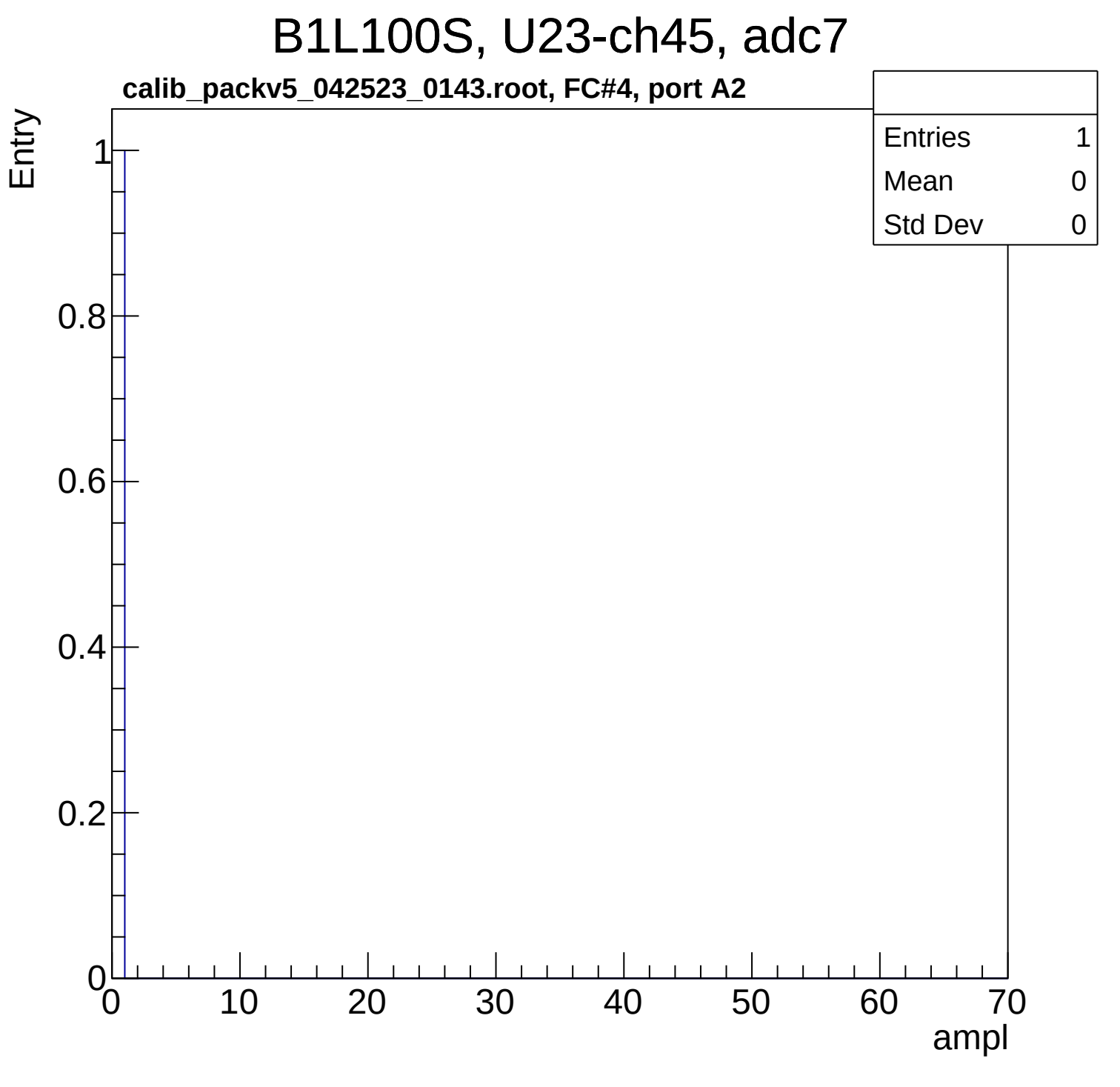
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.93
Std Dev 11.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch45, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch45, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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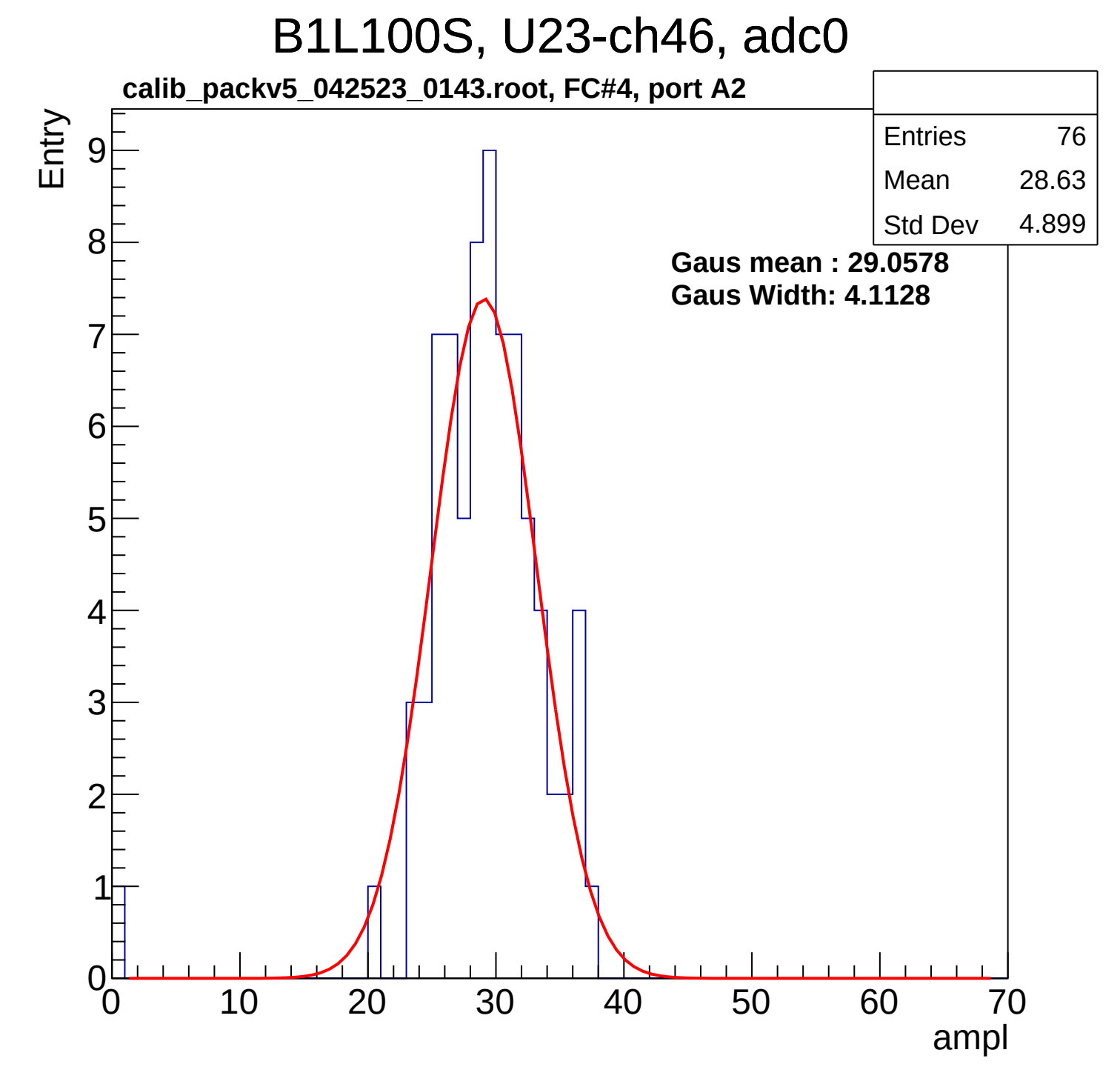
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.63
Std Dev 4.899
Gaus mean : 29.0578
Gaus Width: 4.1128
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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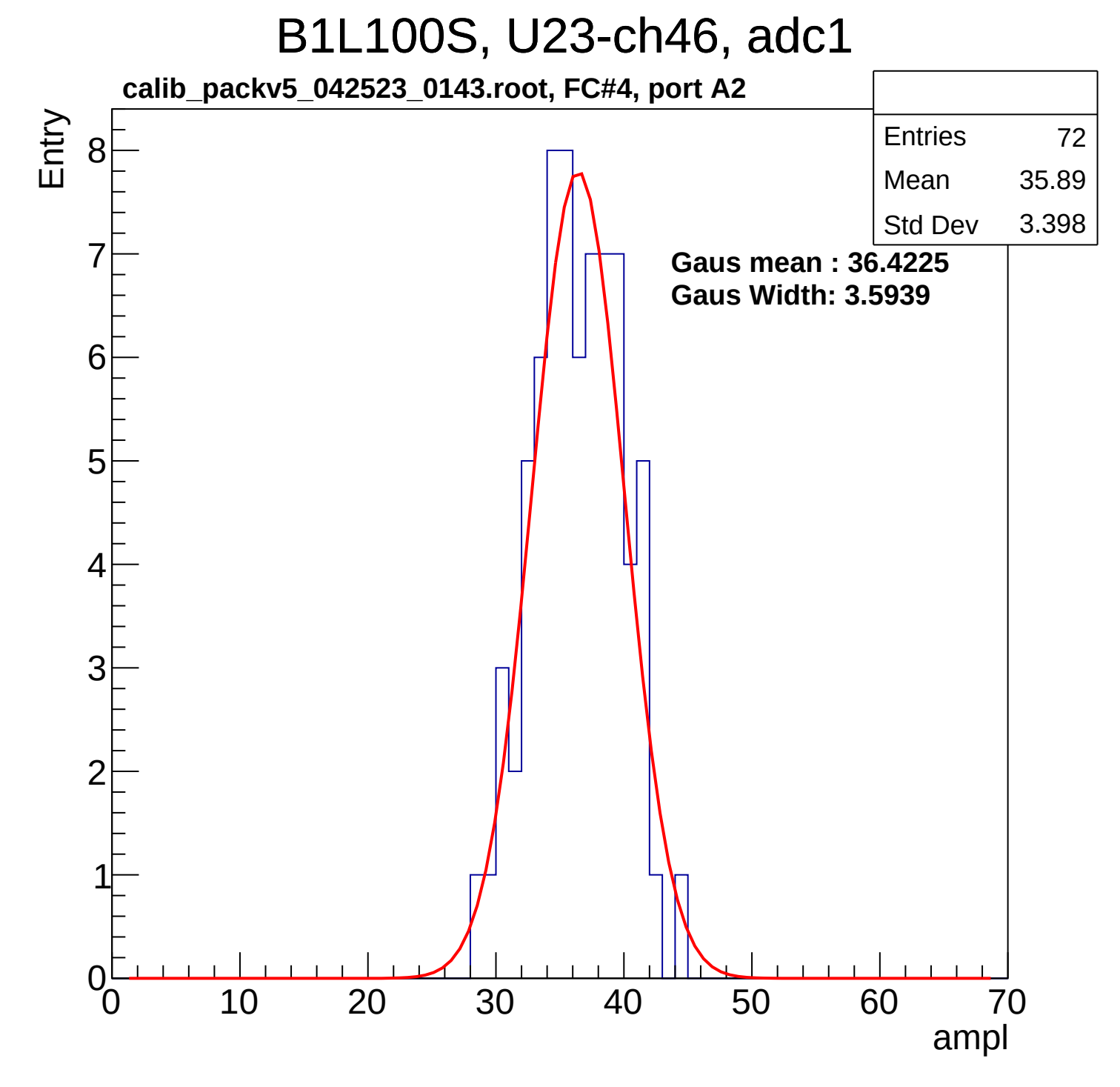
40
50
60
70


B1L100S, U23-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 35.89
Std Dev 3.398
Gaus mean : 36.4225
Gaus Width: 3.5939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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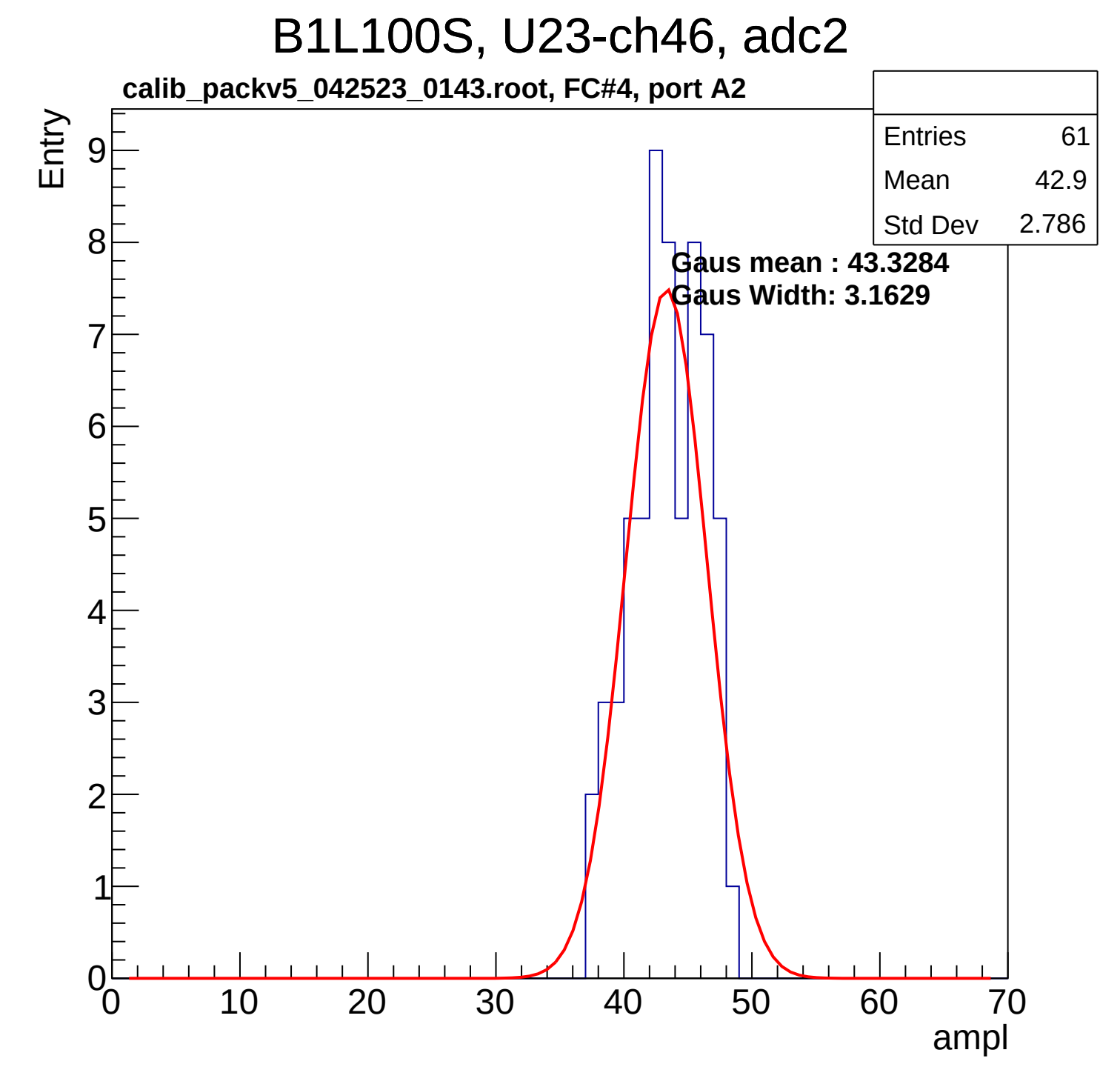
B1L100S, U23-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.9
Std Dev 2.786
Gaus mean : 43.3284
Gaus Width: 3.1629
Entry
ampl
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2
3
4
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6
7
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0
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40
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60
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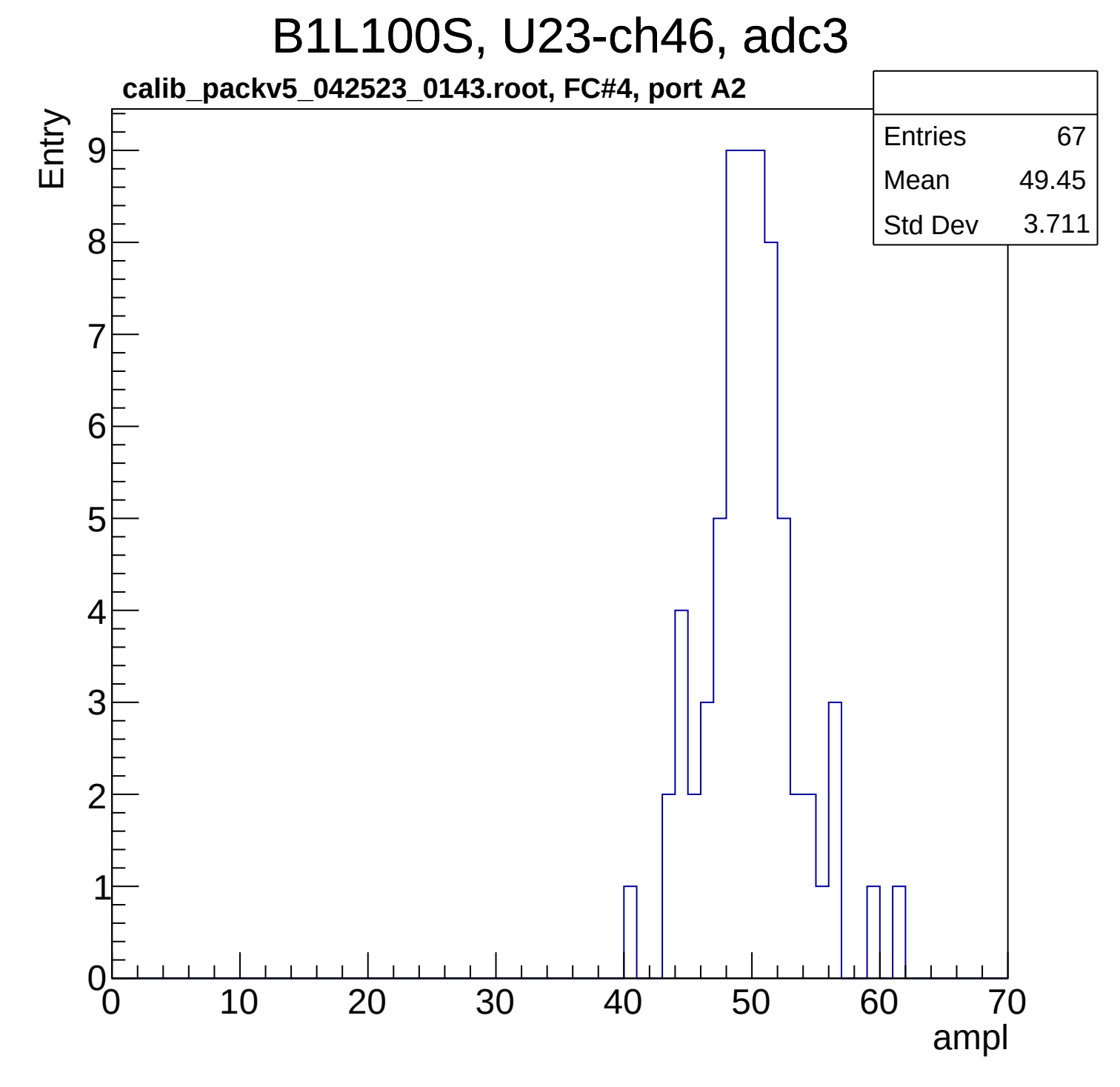
B1L100S, U23-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.45
Std Dev 3.711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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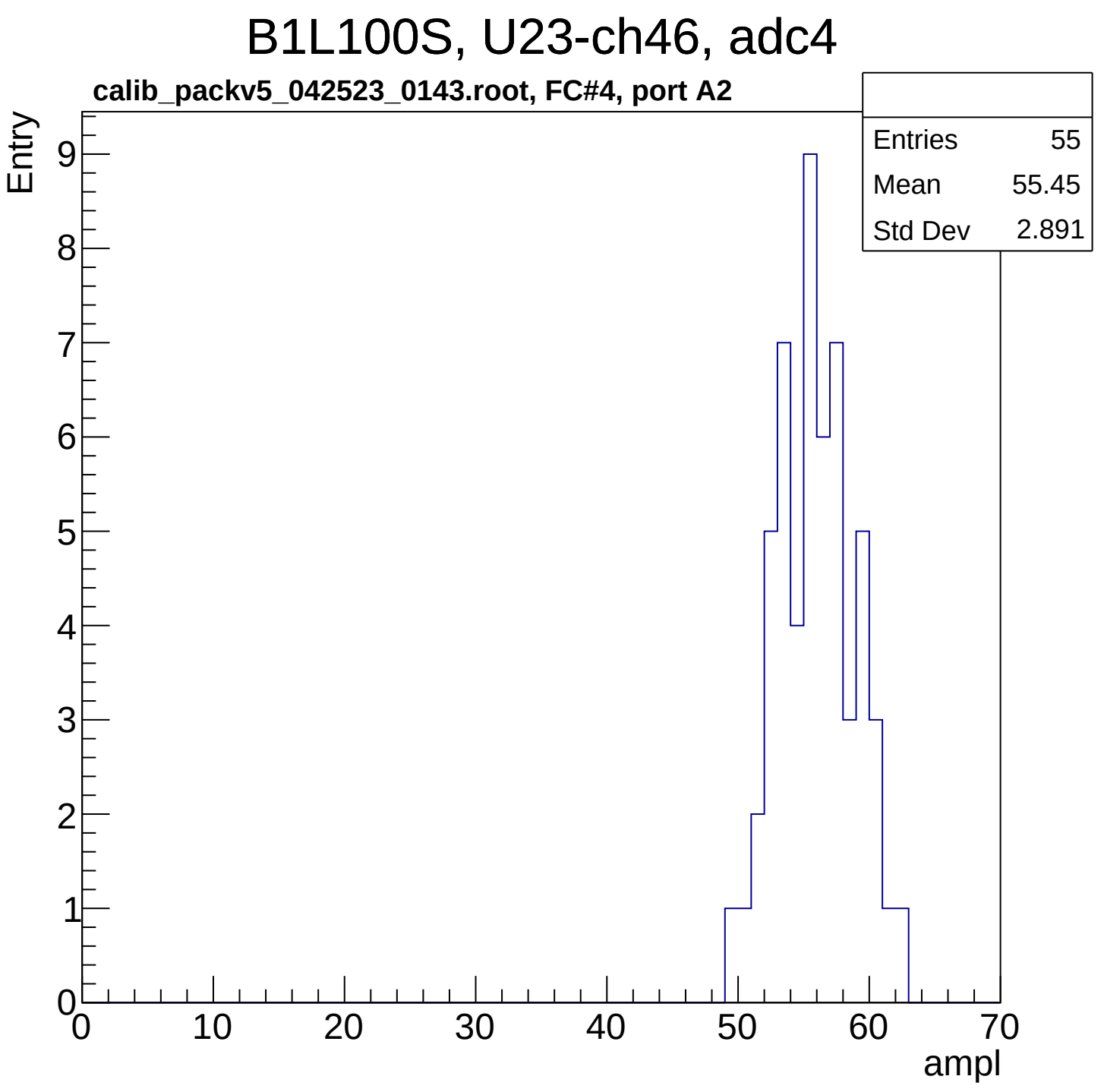
B1L100S, U23-ch46, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.45
Std Dev 2.891
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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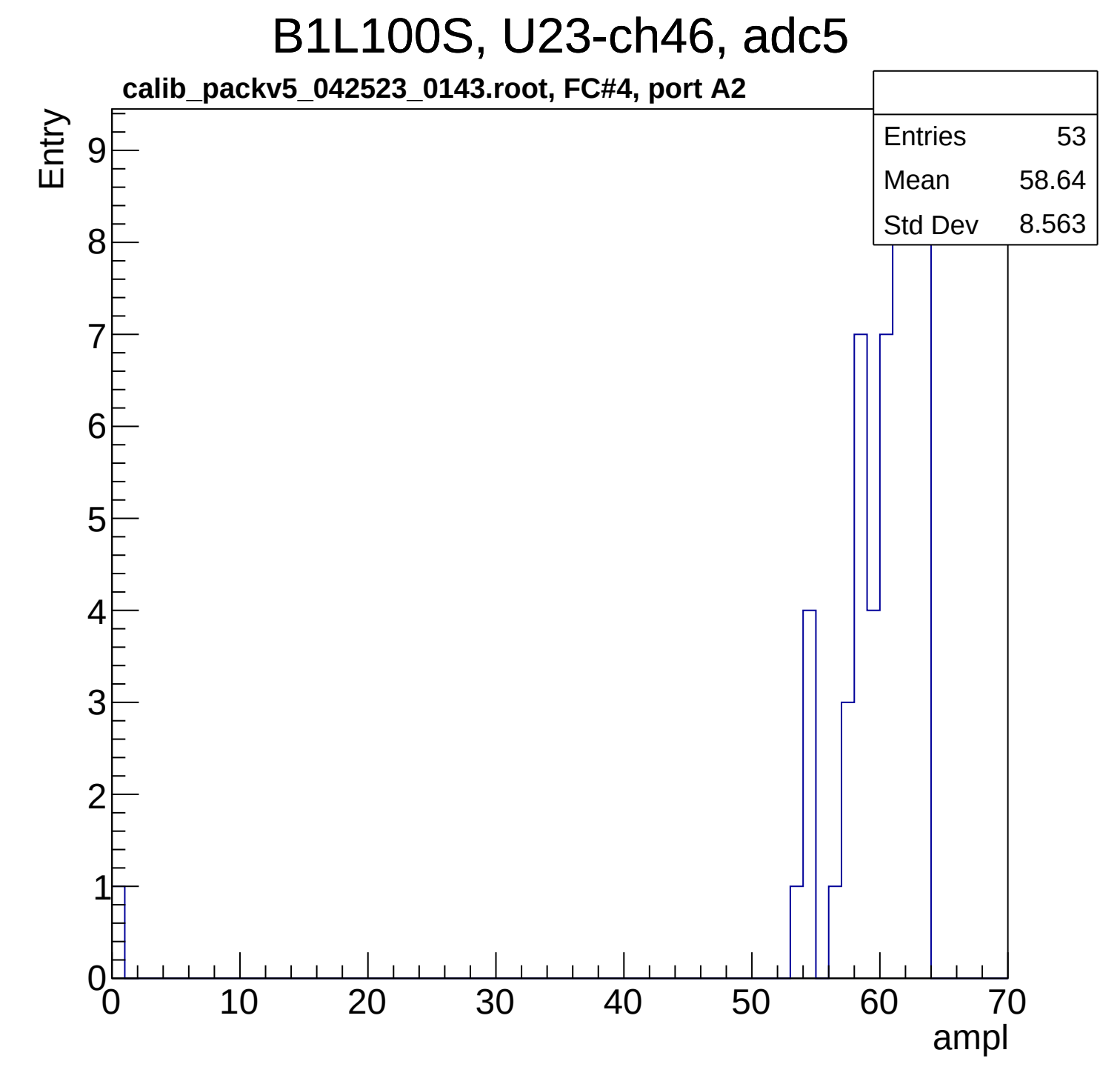
B1L100S, U23-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.64
Std Dev 8.563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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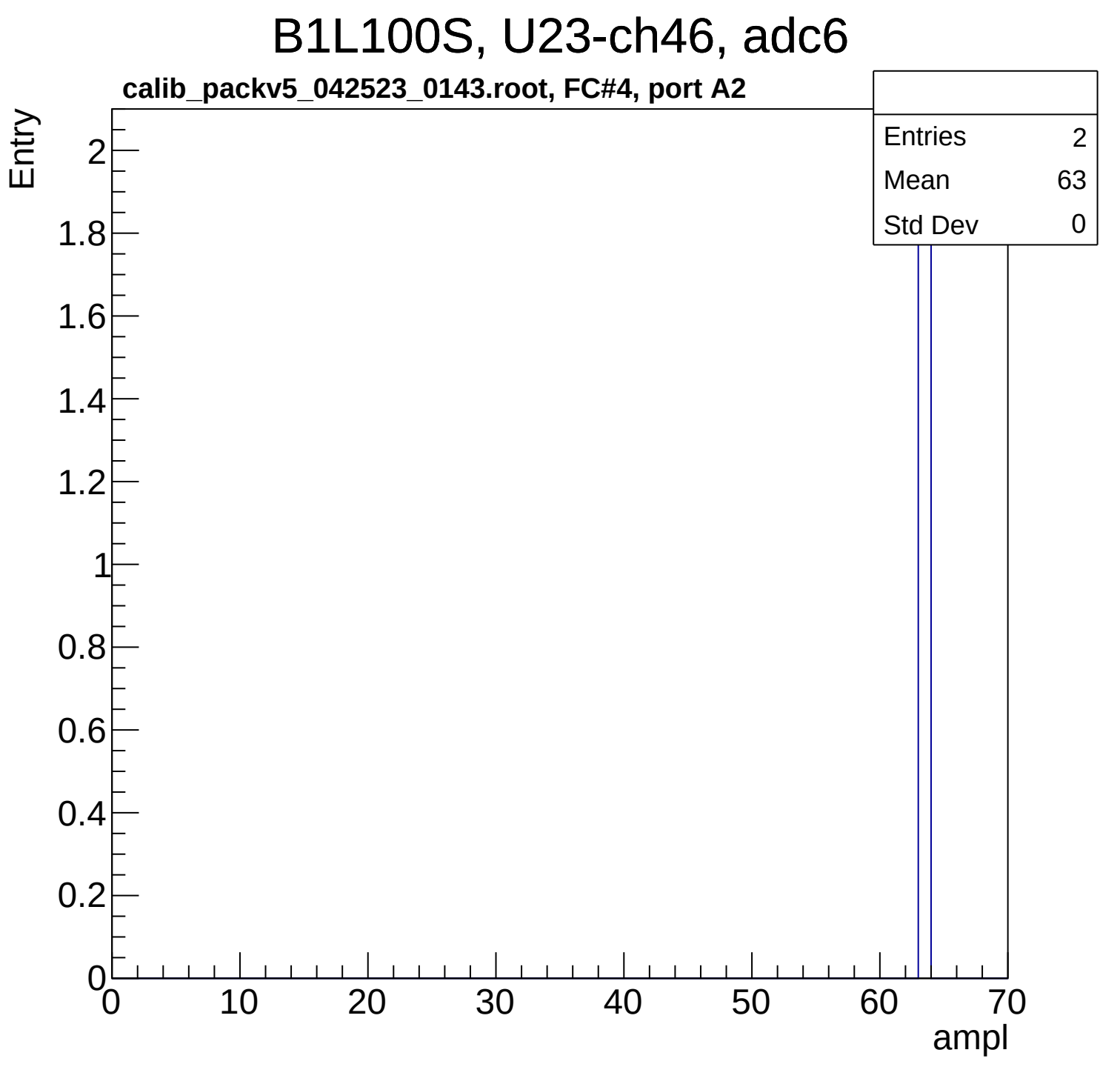
B1L100S, U23-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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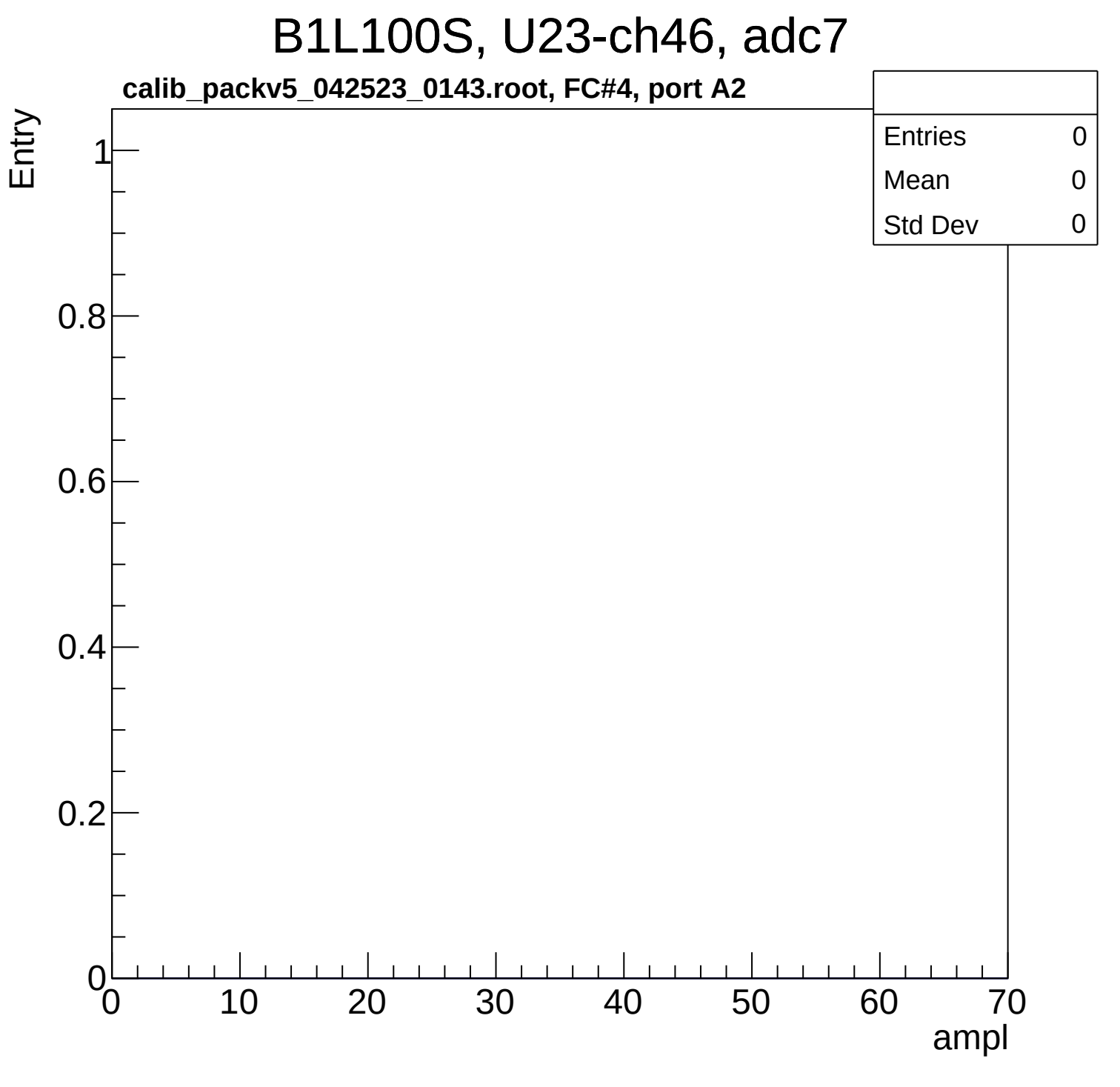
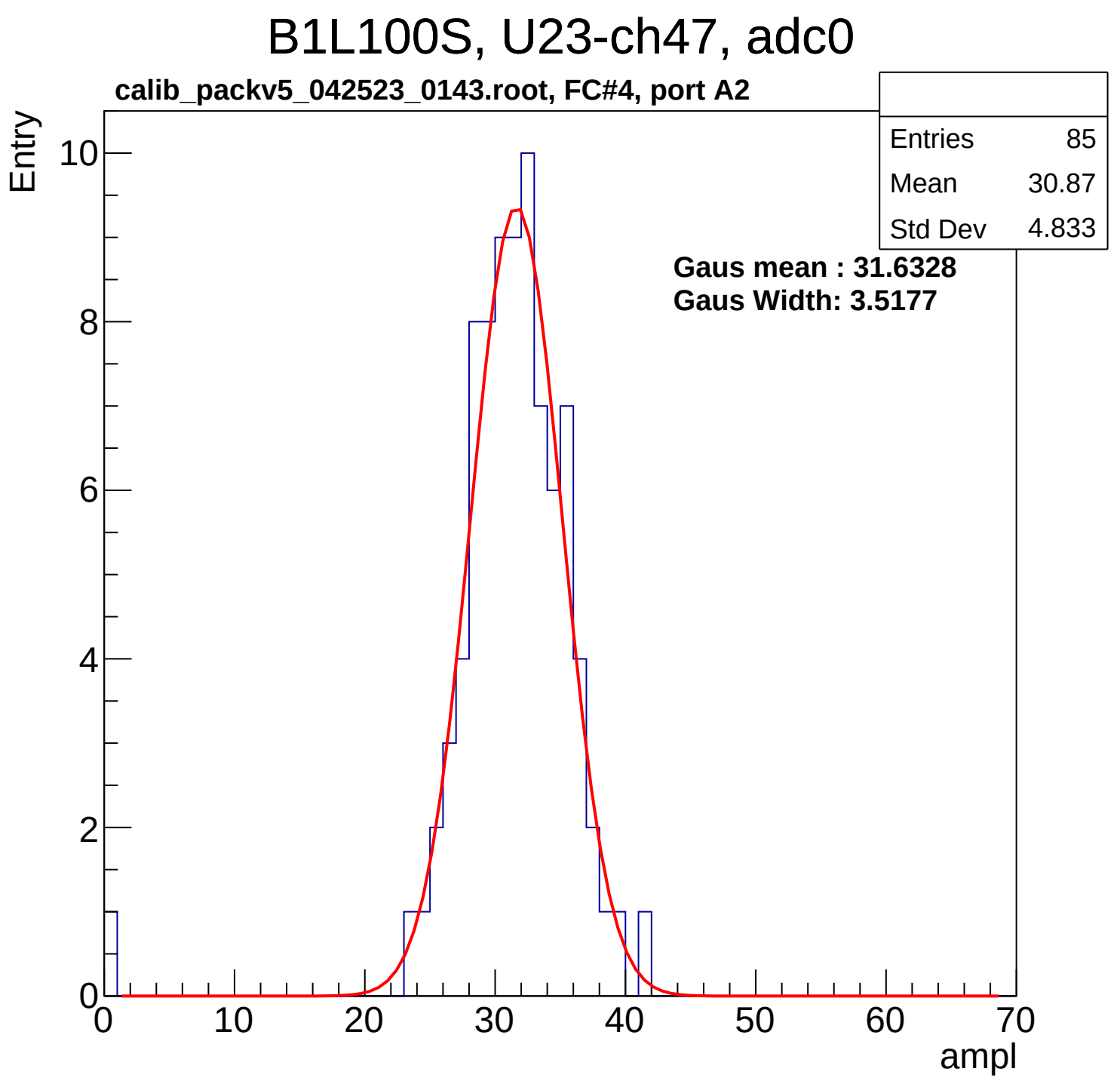
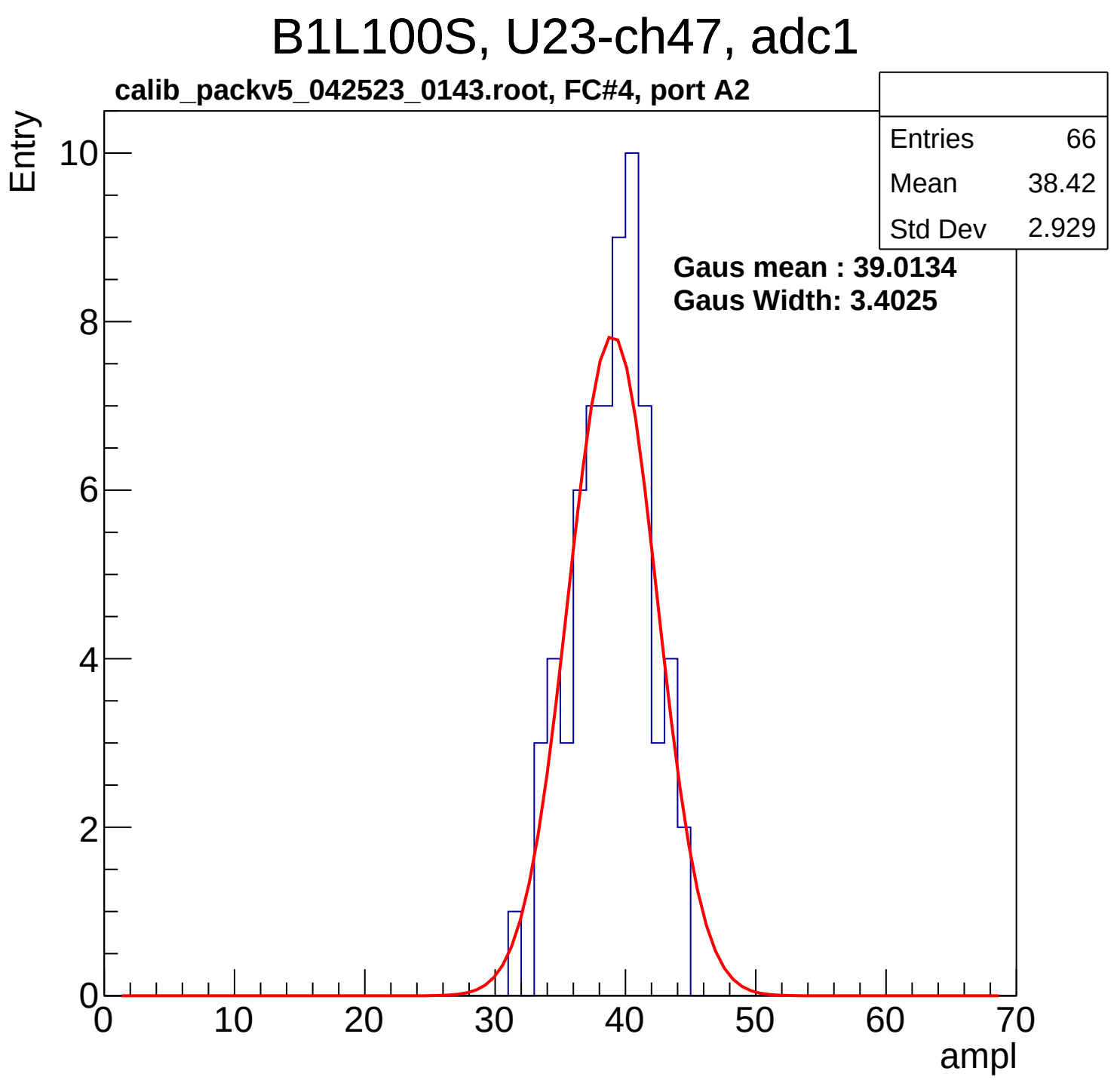

B1L100S, U23-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 30.87
Std Dev 4.833
Gaus mean : 31.6328
Gaus Width: 3.5177
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 38.42
Std Dev 2.929
Gaus mean : 39.0134
Gaus Width: 3.4025
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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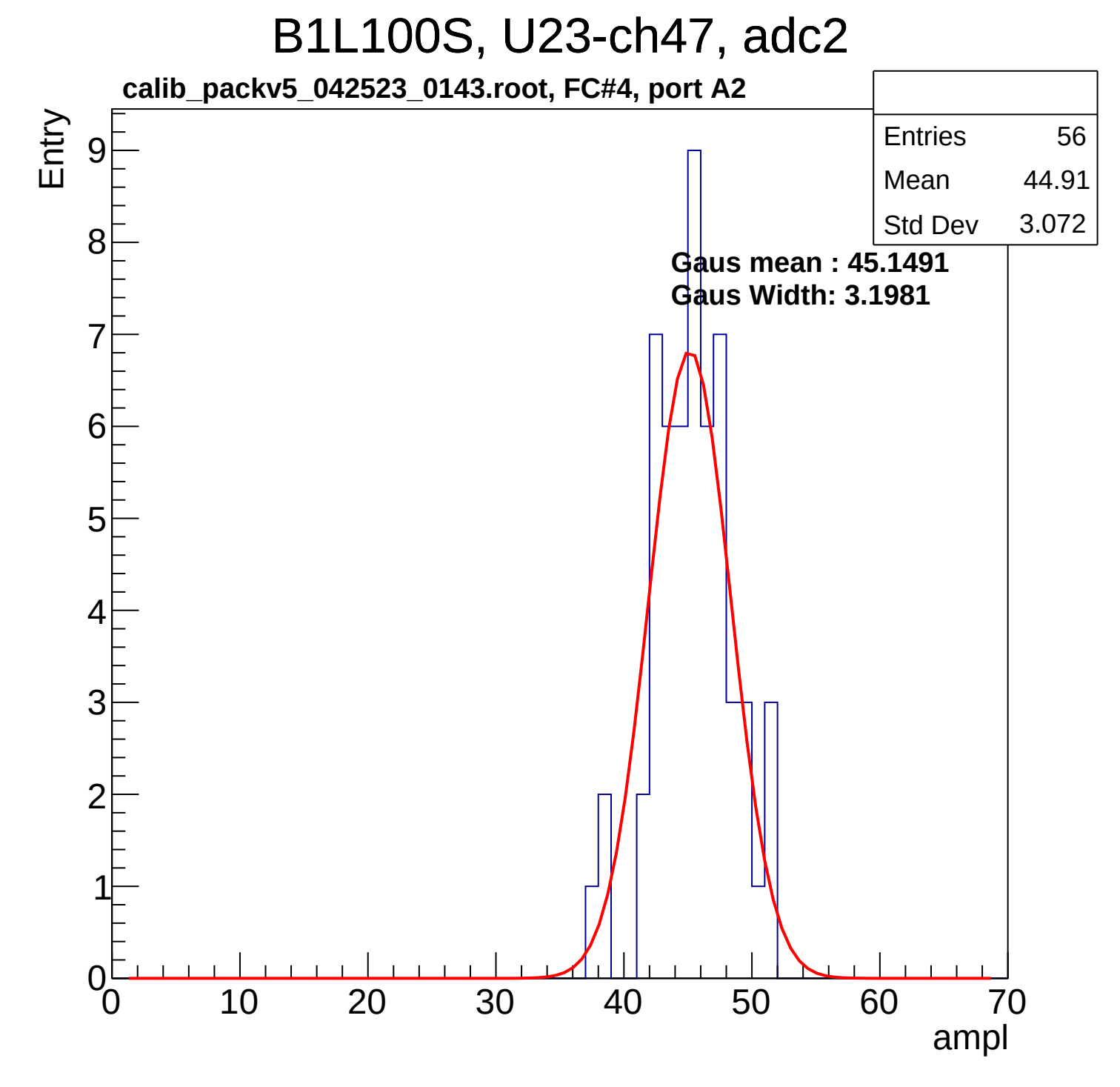
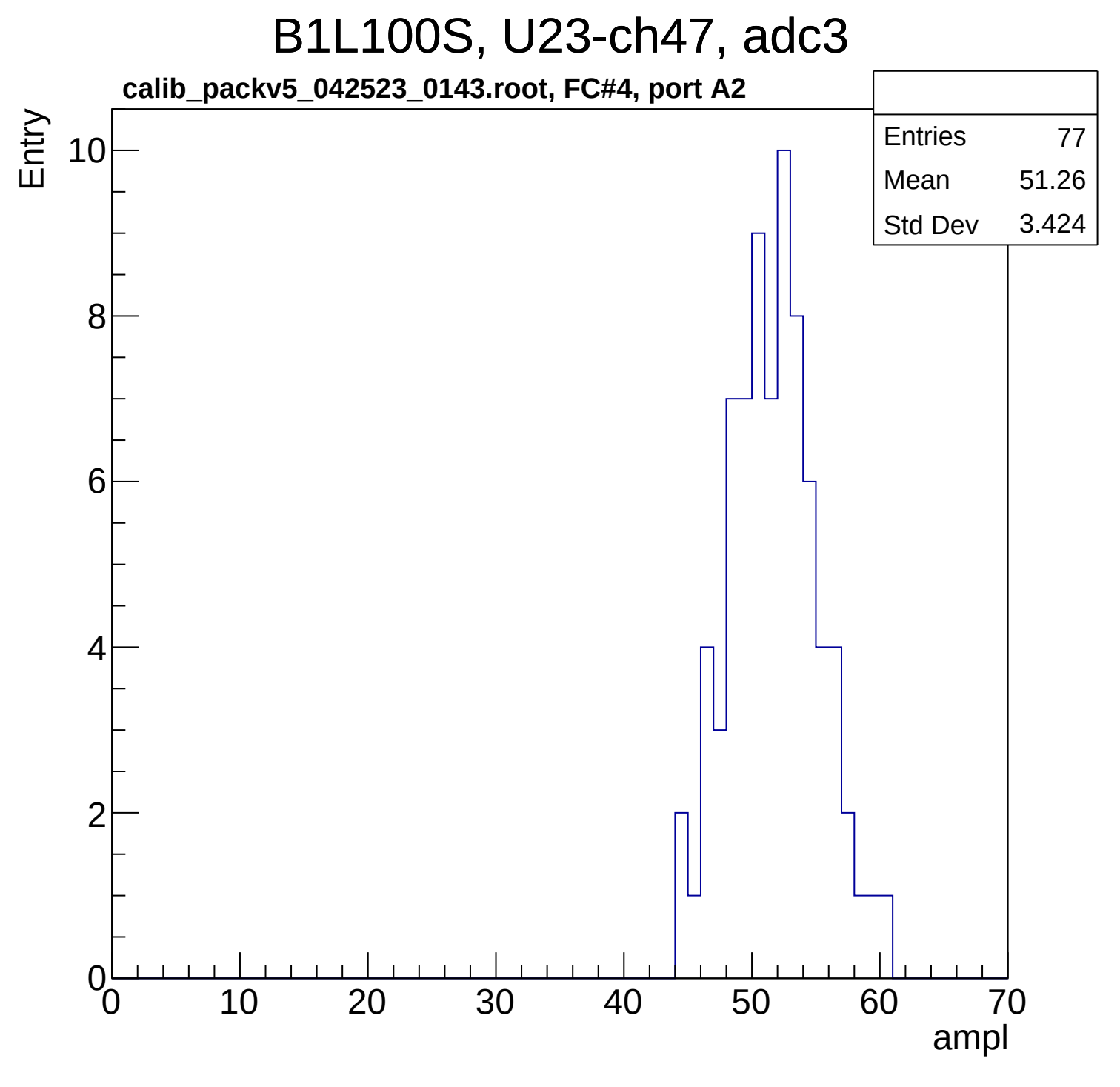
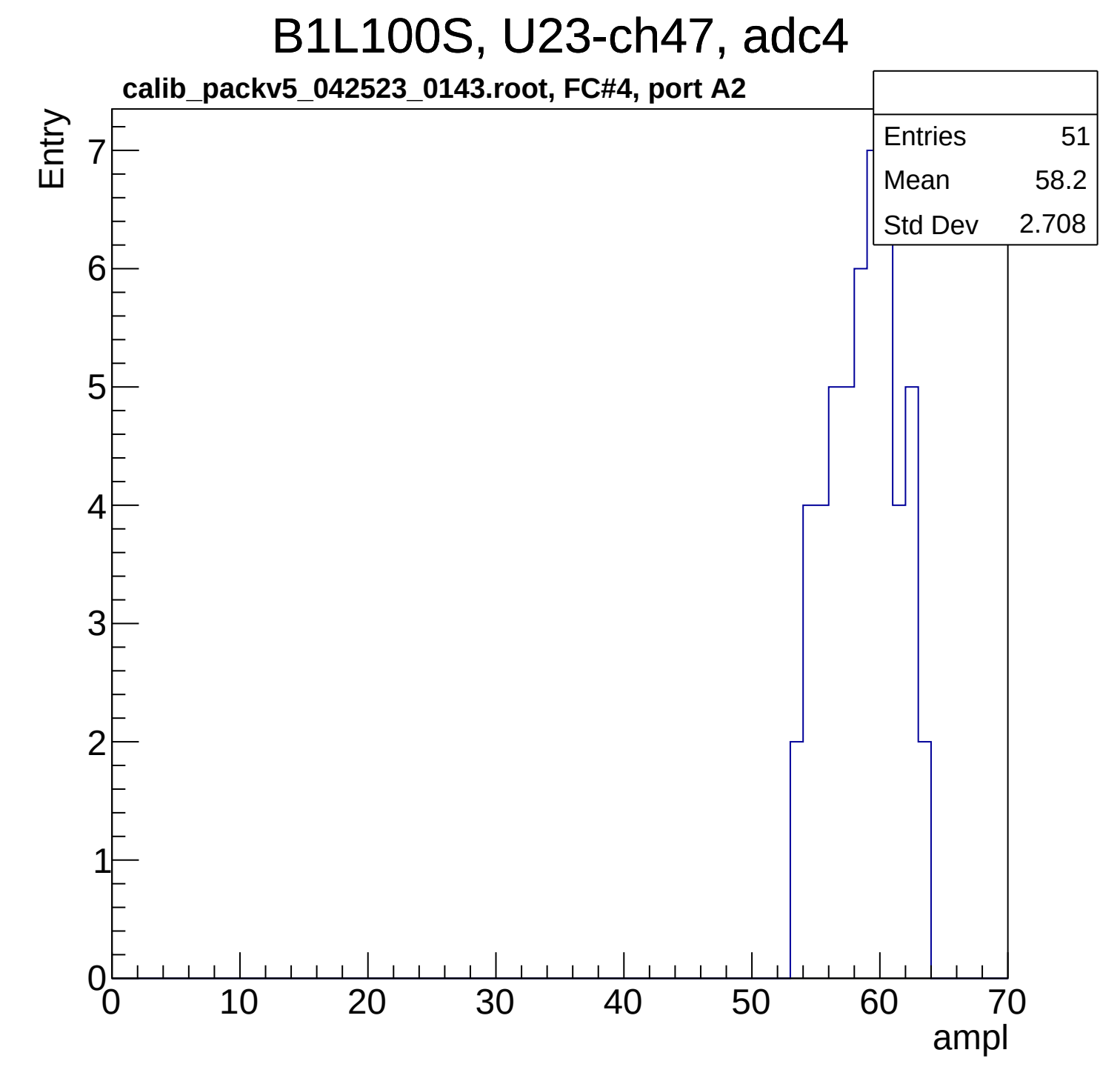
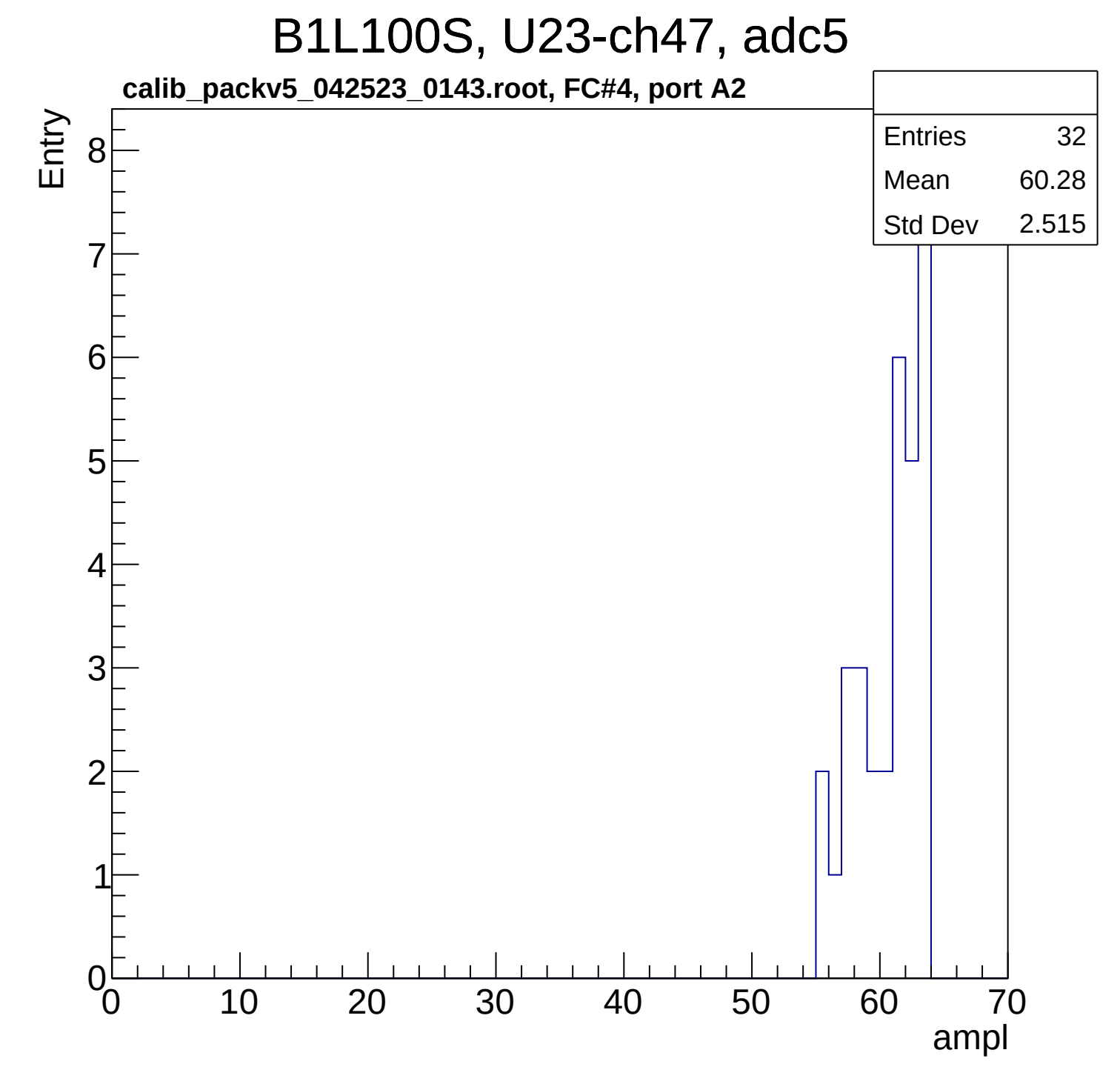

B1L100S, U23-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.91
Std Dev 3.072
Gaus mean : 45.1491
Gaus Width: 3.1981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 51.26
Std Dev 3.424
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.2
Std Dev 2.708
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 60.28
Std Dev 2.515
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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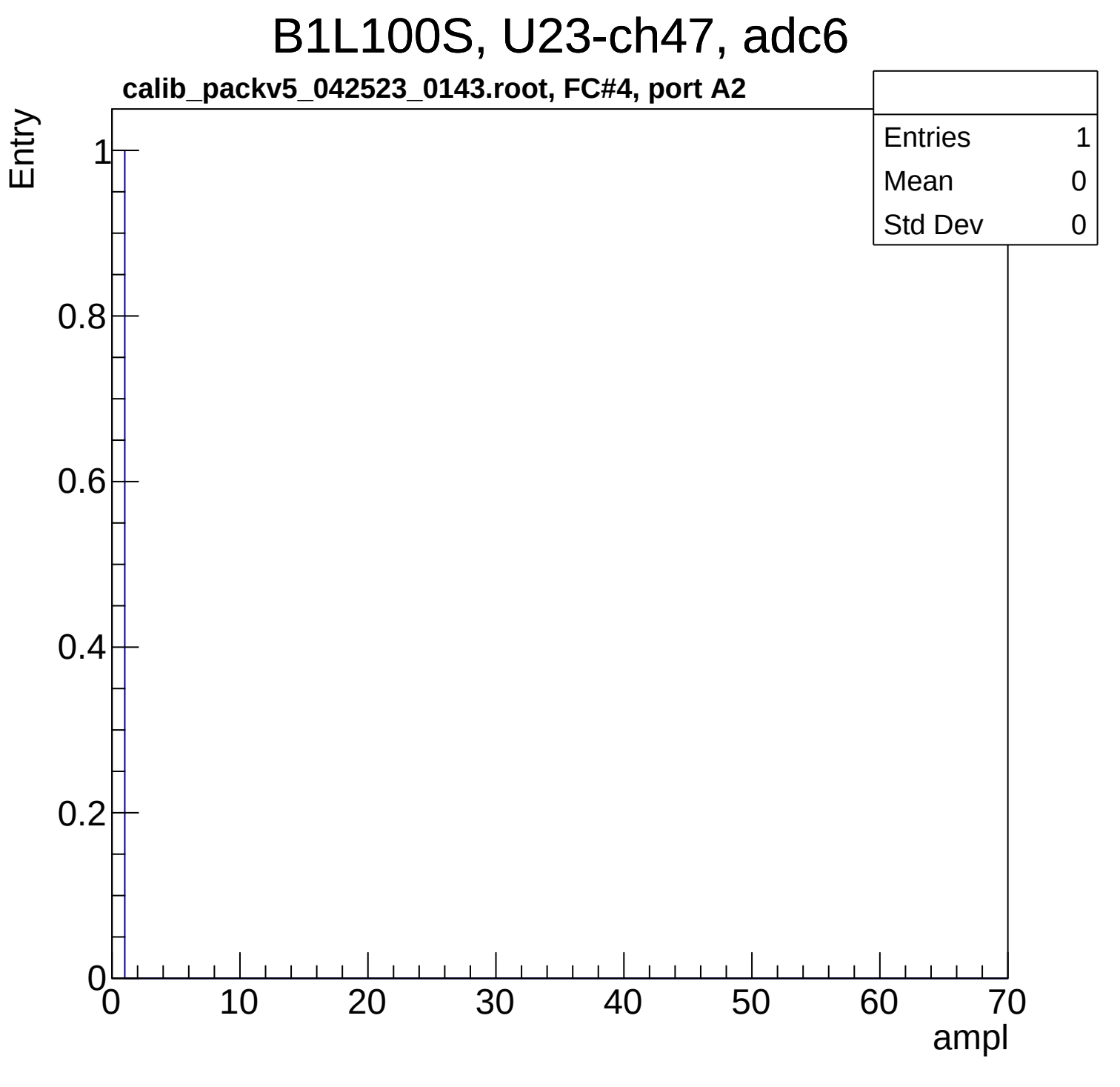
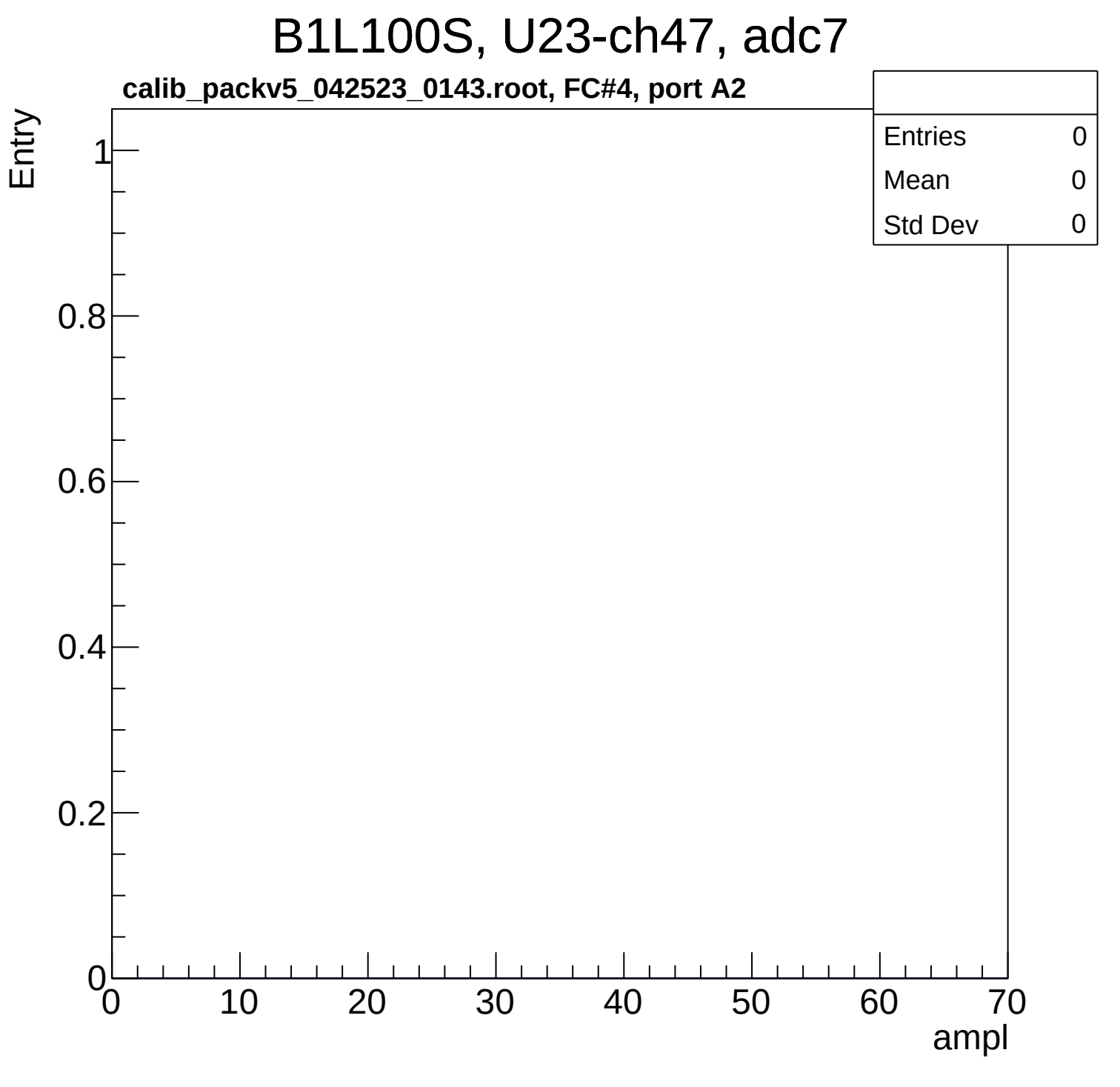
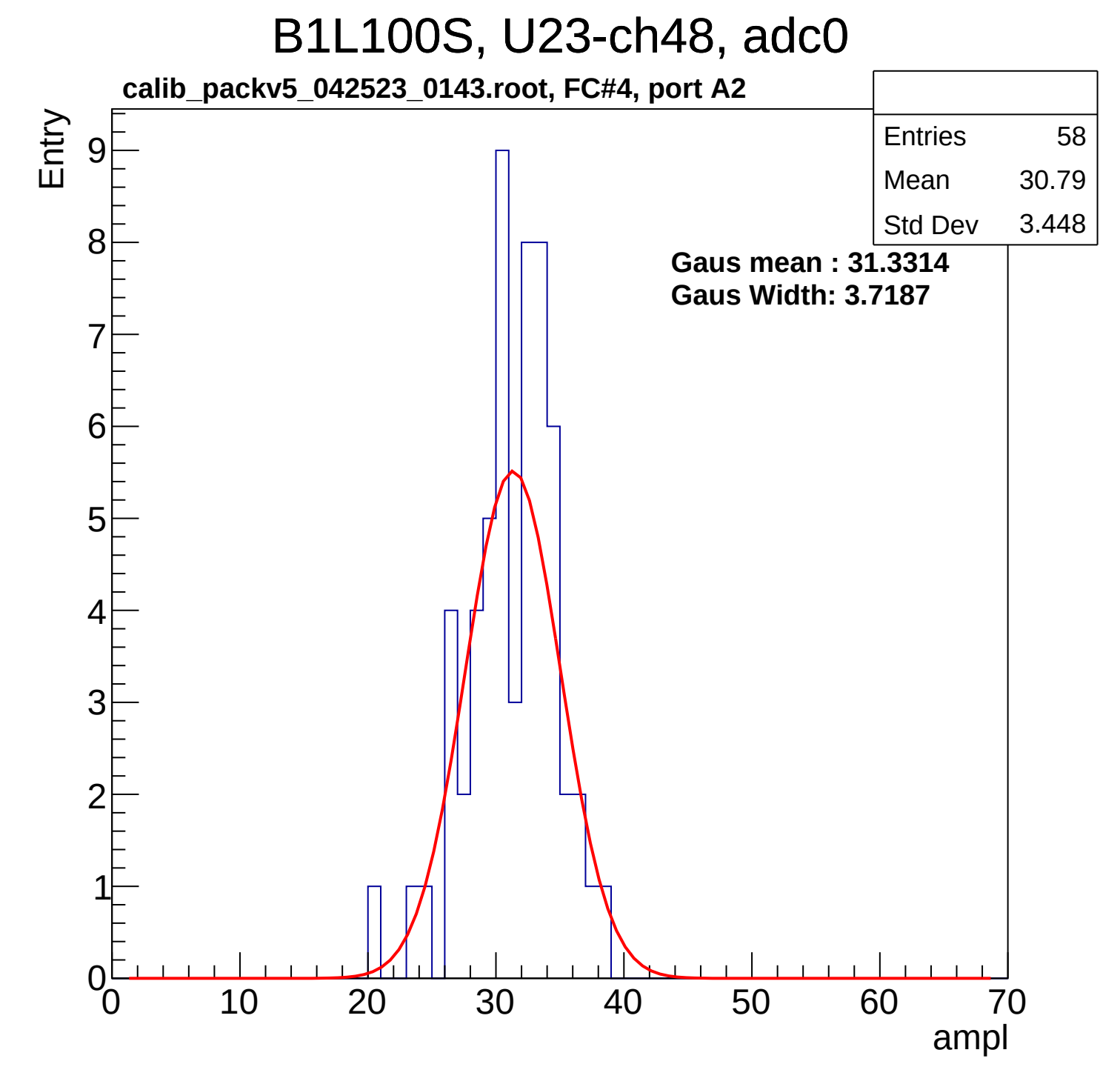
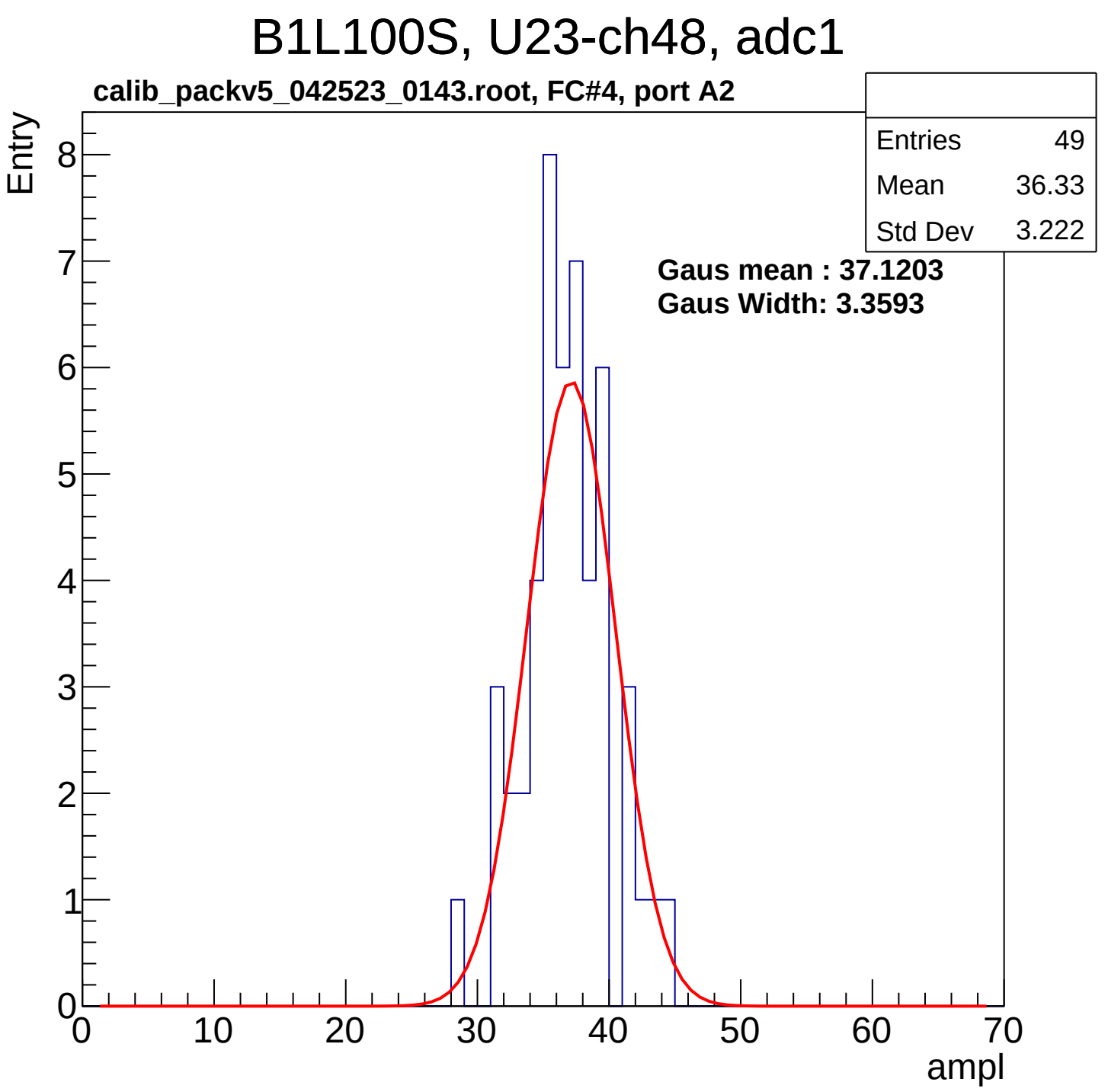
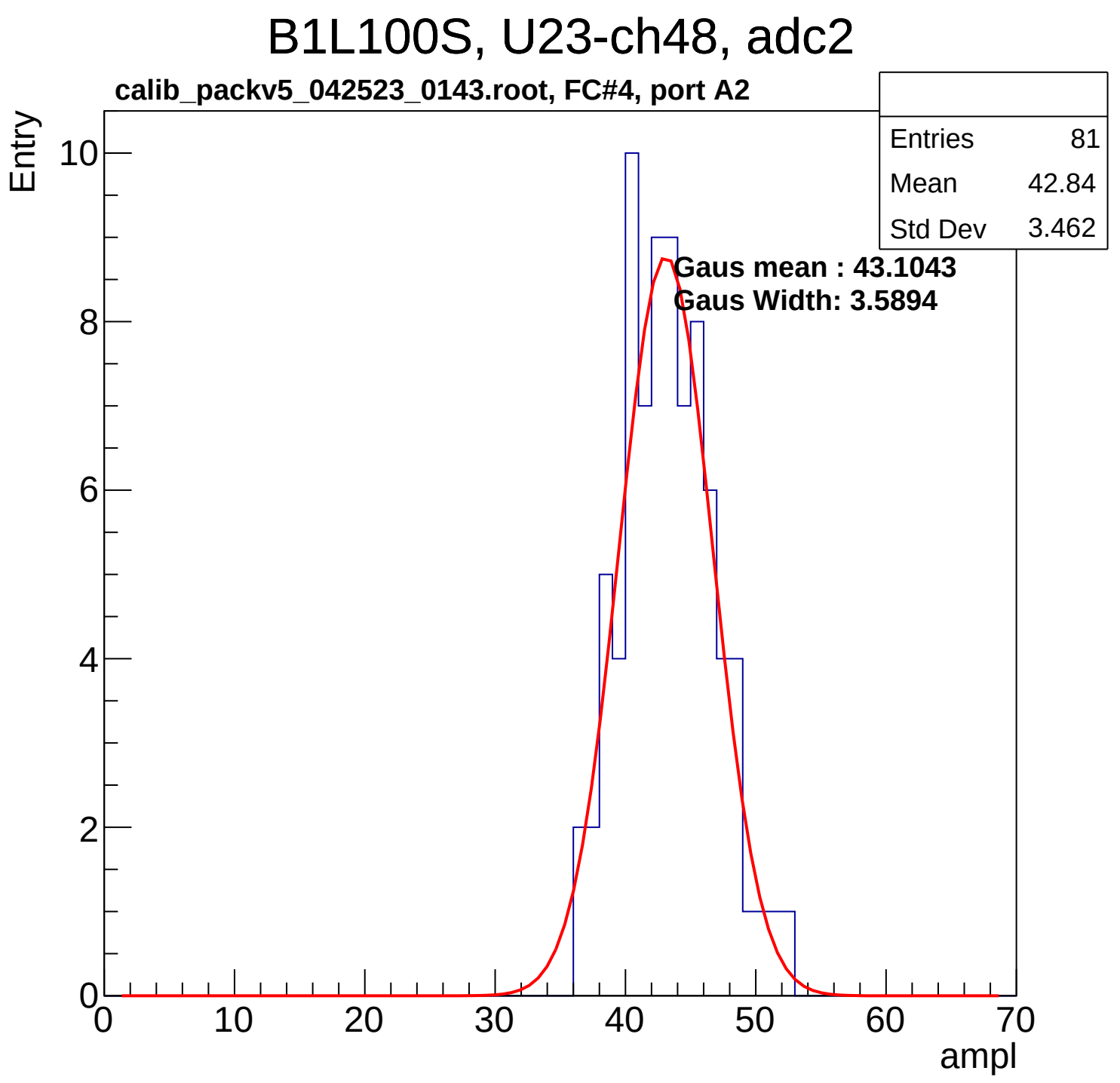
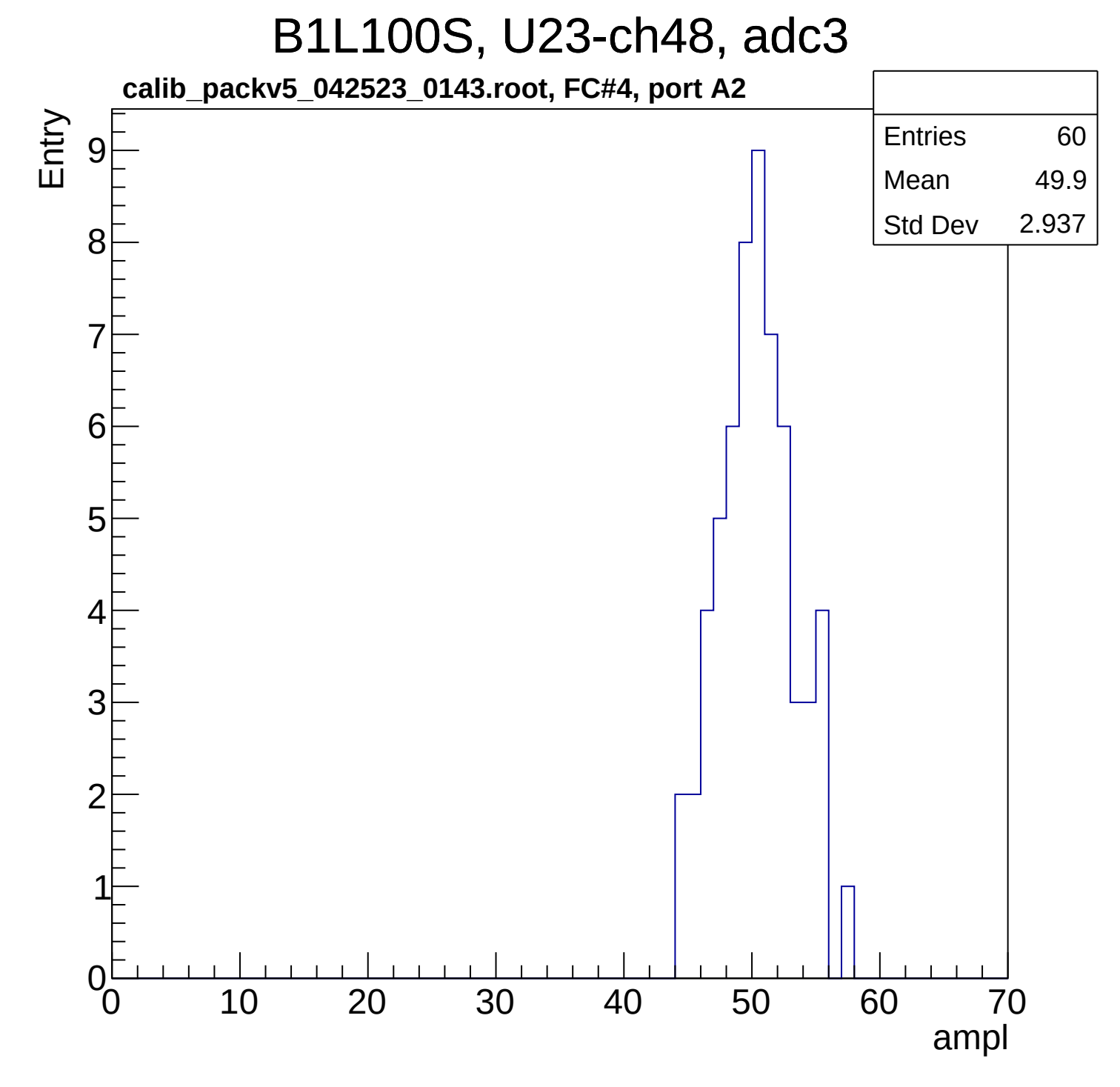

B1L100S, U23-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

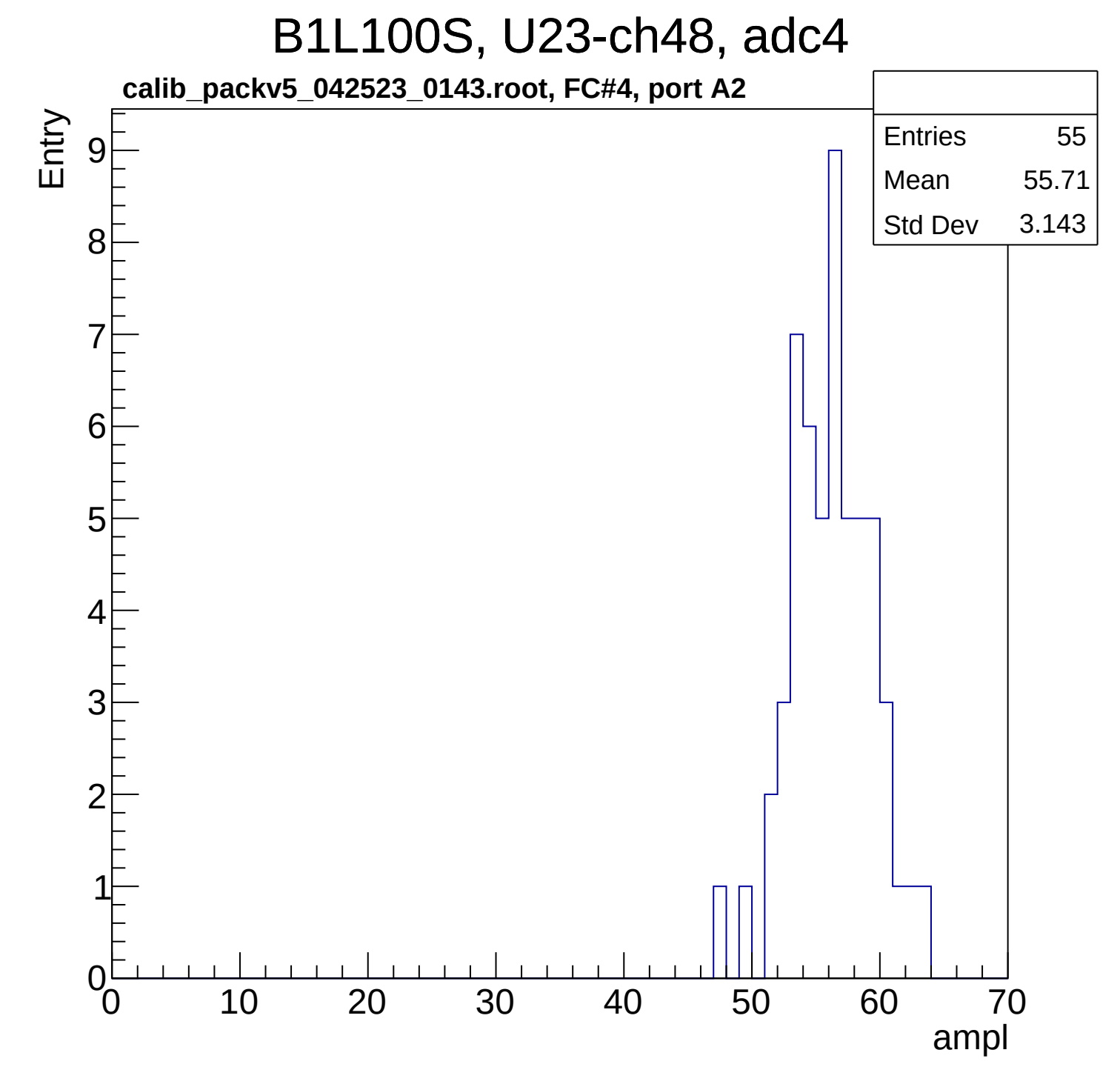

B1L100S, U23-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 30.79
Std Dev 3.448
Gaus mean : 31.3314
Gaus Width: 3.7187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 36.33
Std Dev 3.222
Gaus mean : 37.1203
Gaus Width: 3.3593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch48, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 42.84
Std Dev 3.462
Gaus mean : 43.1043
Gaus Width: 3.5894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.9
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.71
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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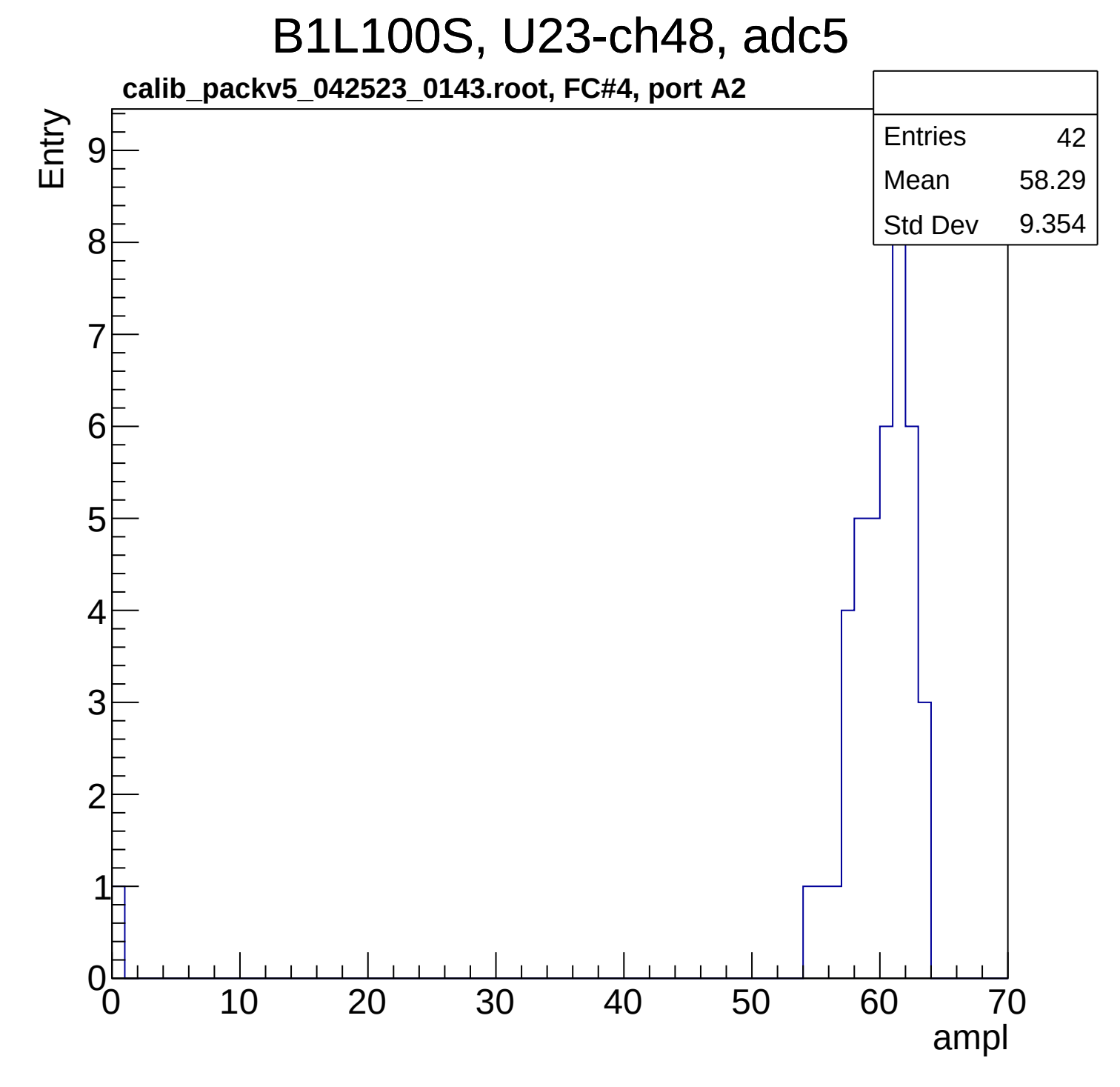
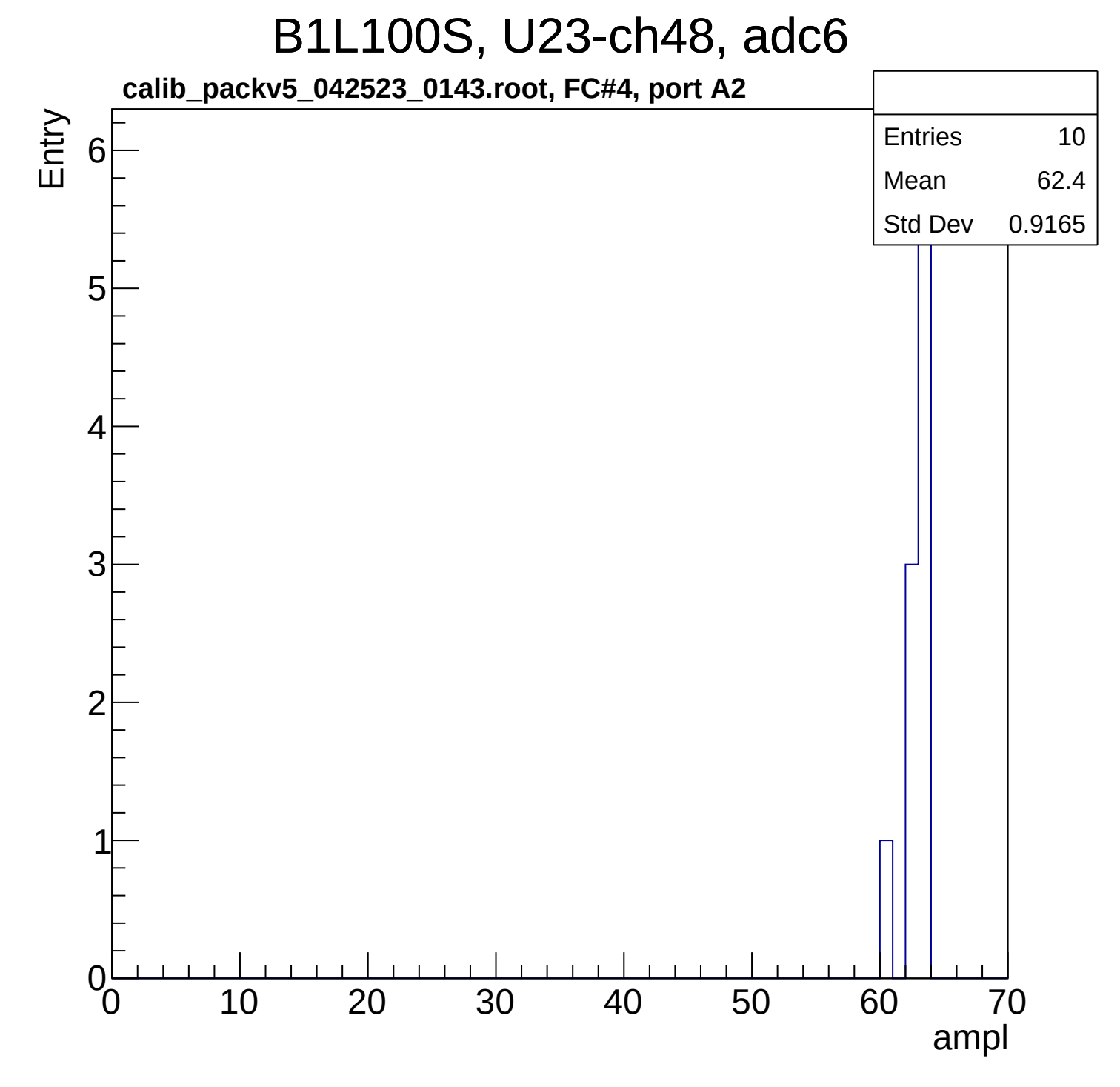
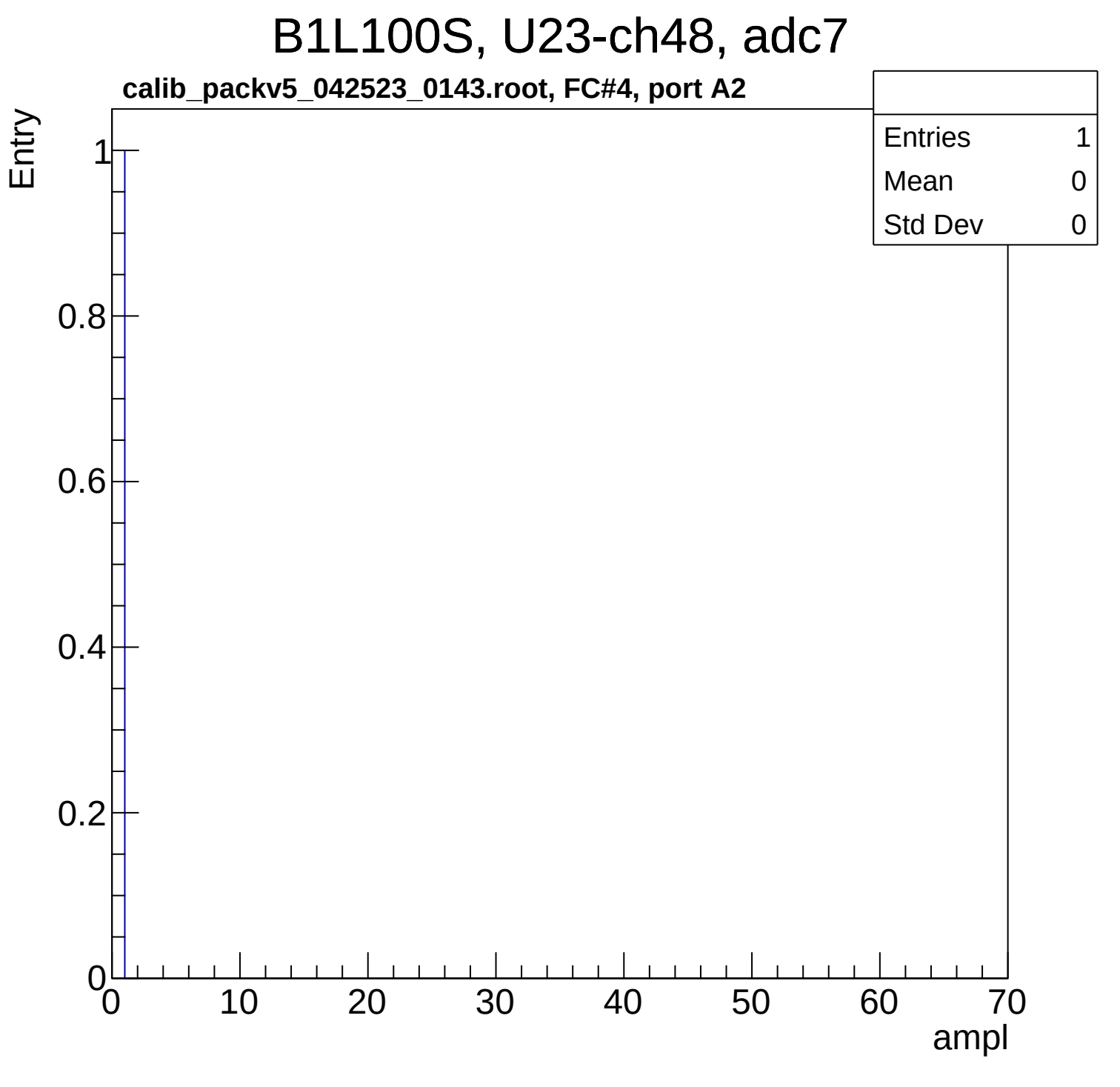
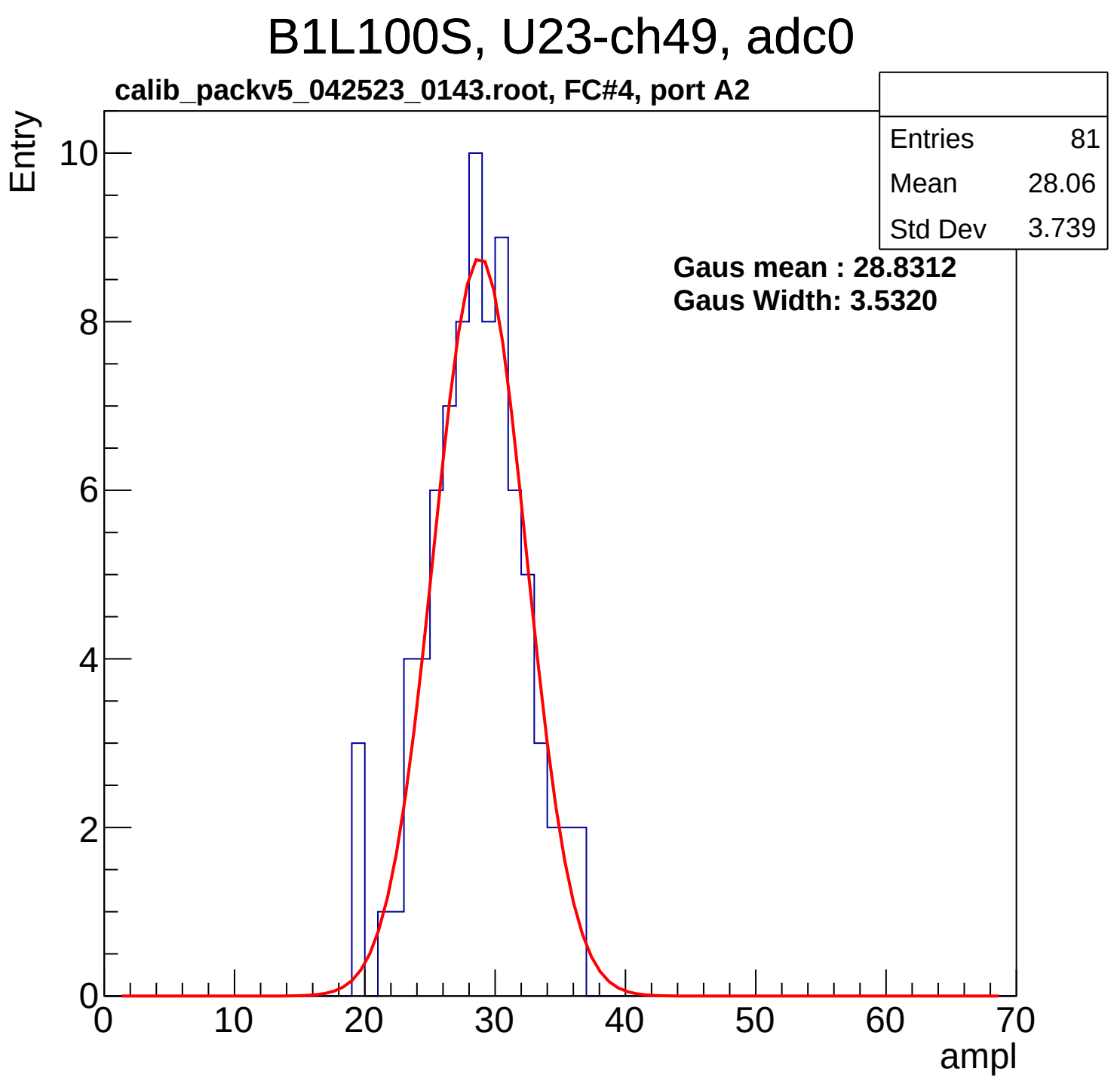
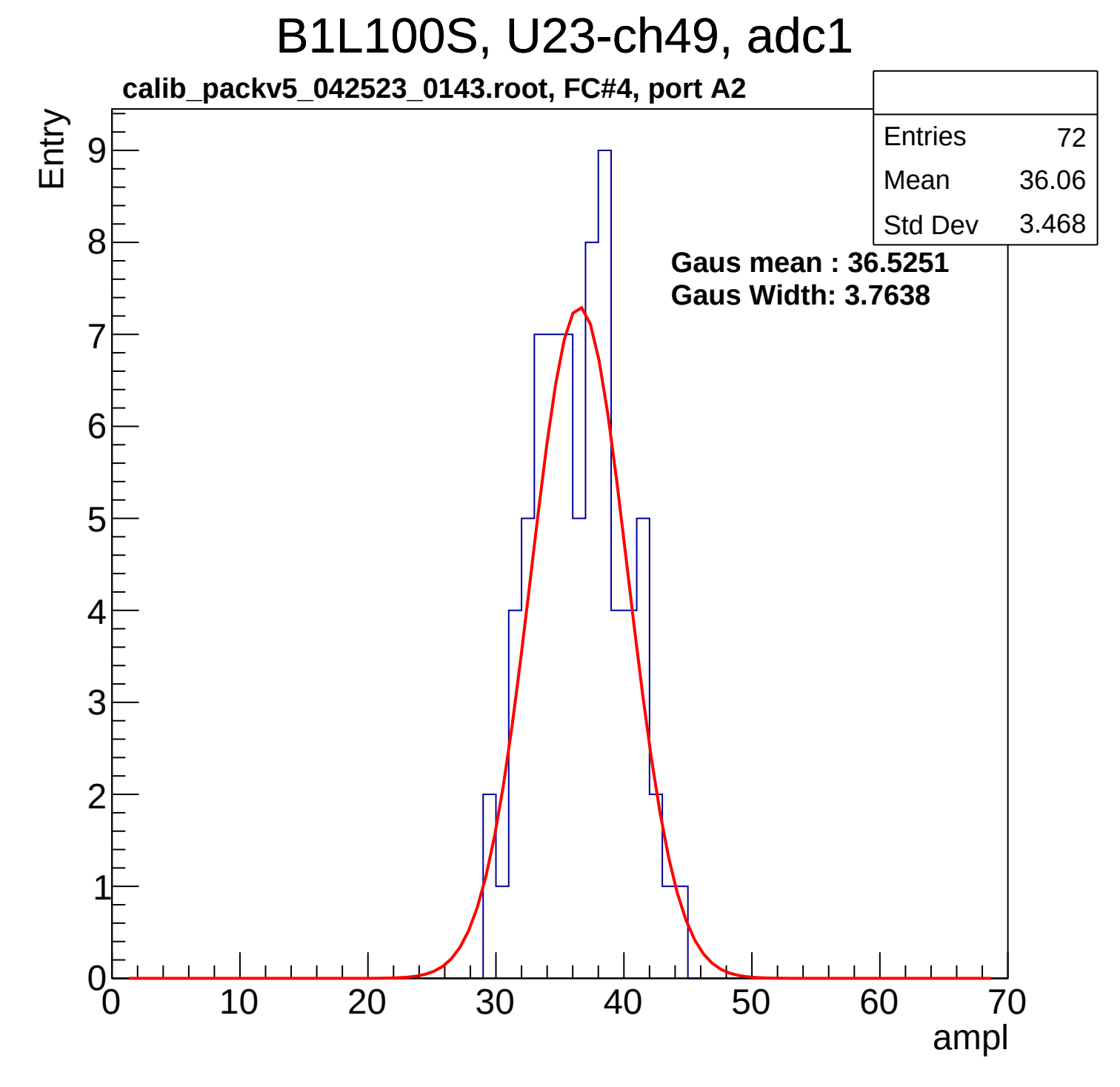

B1L100S, U23-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.29
Std Dev 9.354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch48, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 62.4
Std Dev 0.9165
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch48, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch49, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 28.06
Std Dev 3.739
Gaus mean : 28.8312
Gaus Width: 3.5320
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.06
Std Dev 3.468
Gaus mean : 36.5251
Gaus Width: 3.7638
Entry
ampl
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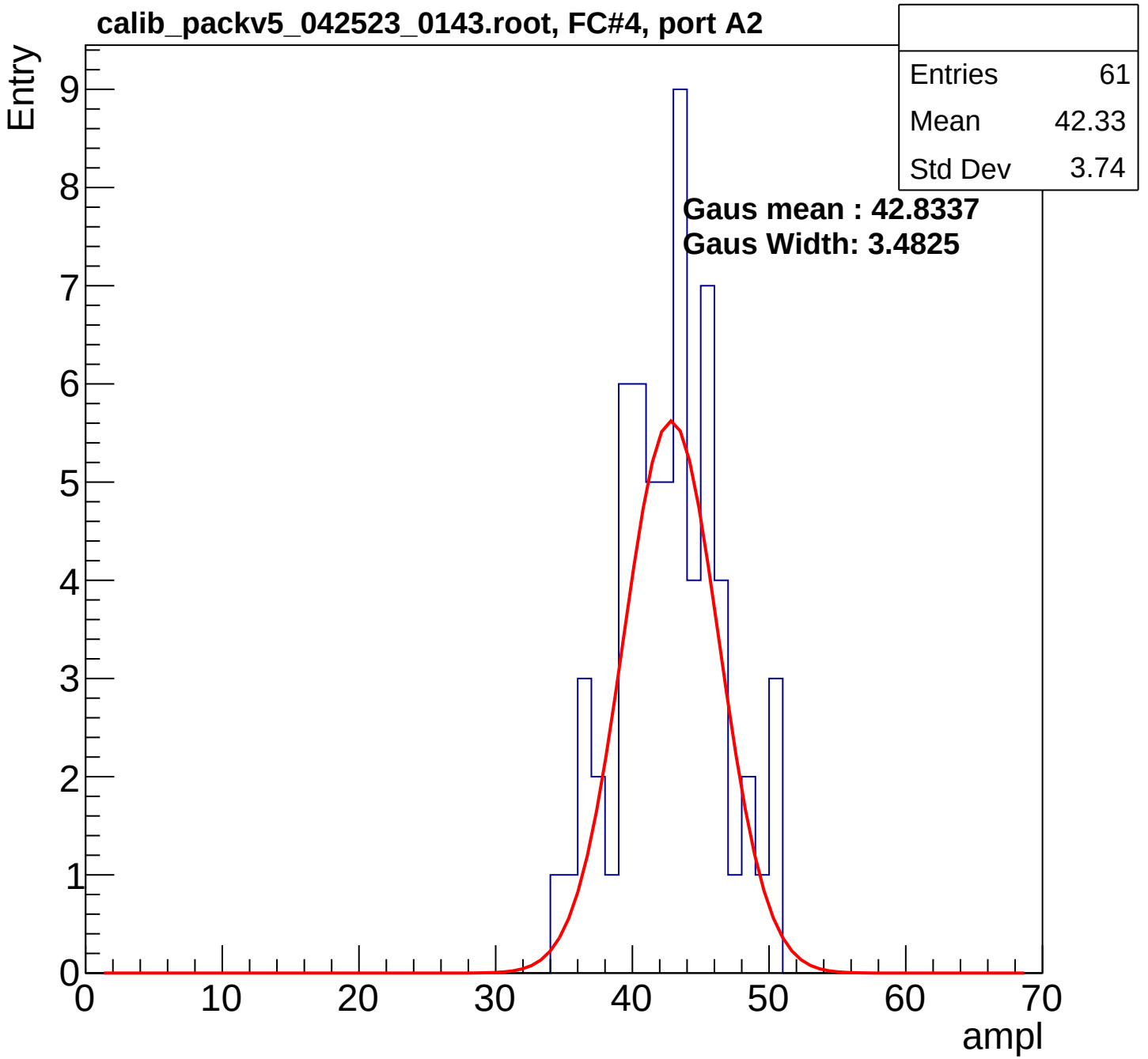
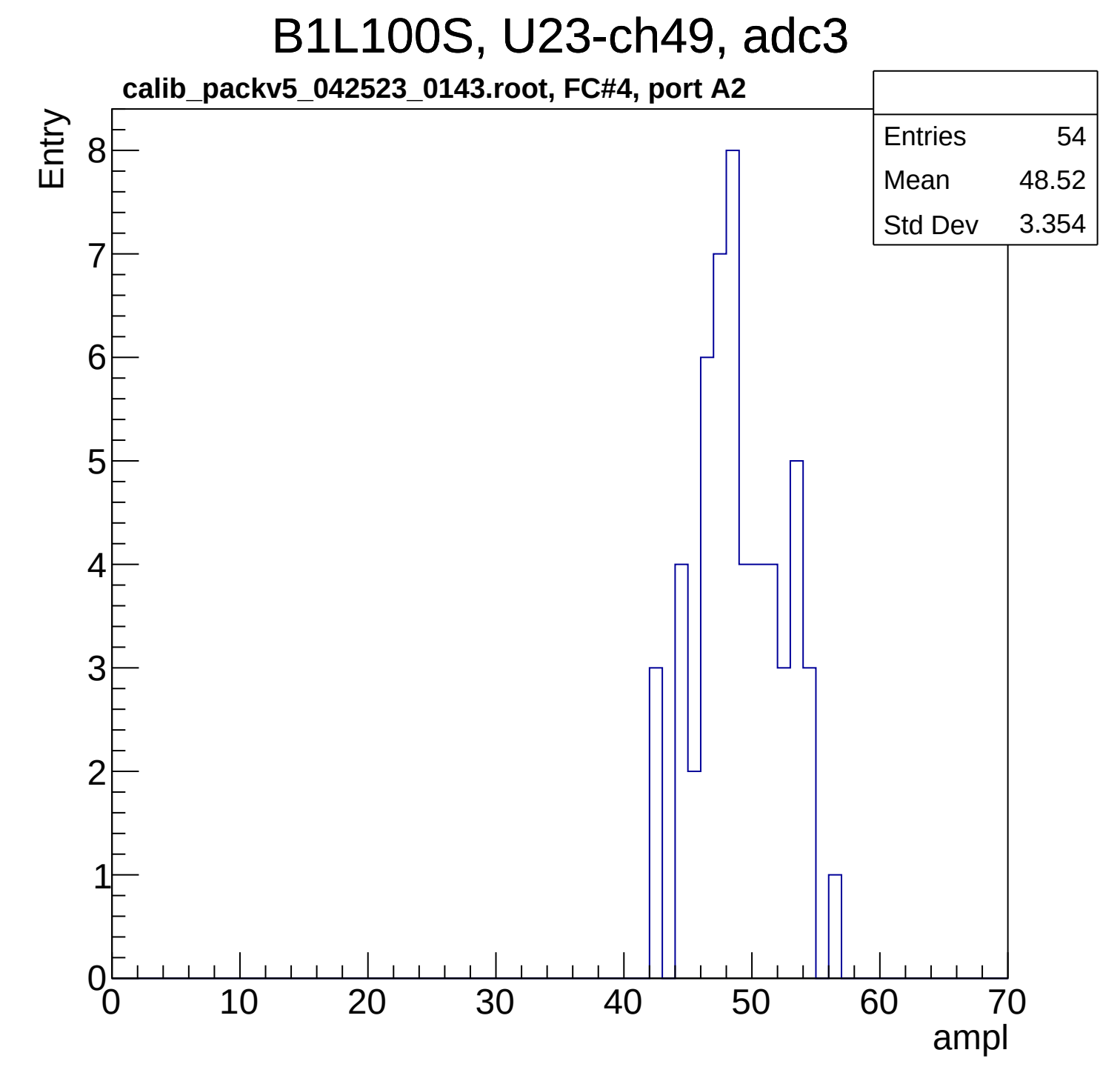
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.33
Std Dev 3.74
Gaus mean : 42.8337
Gaus Width: 3.4825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 48.52
Std Dev 3.354
Entry
ampl
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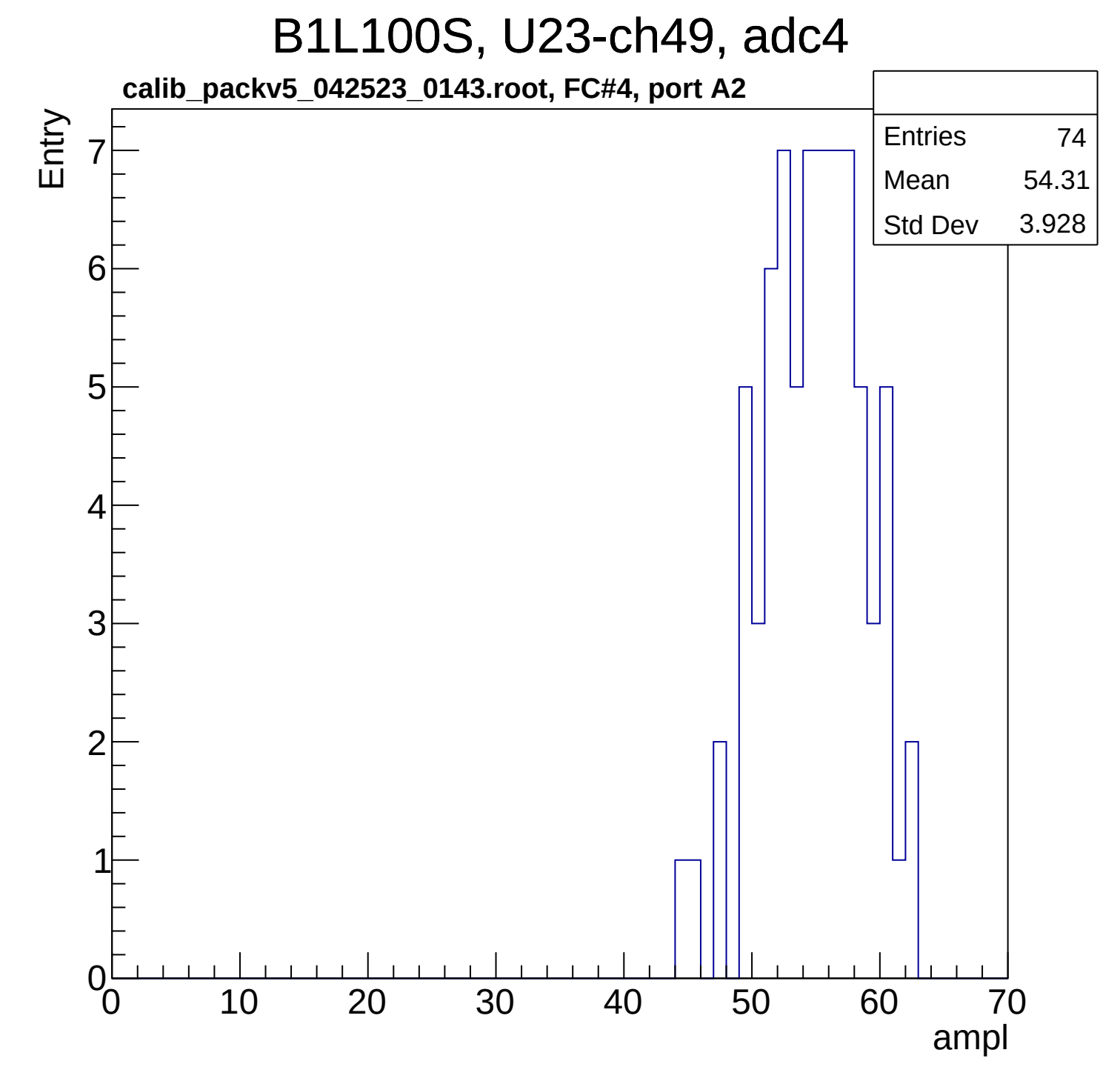
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch49, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 54.31
Std Dev 3.928
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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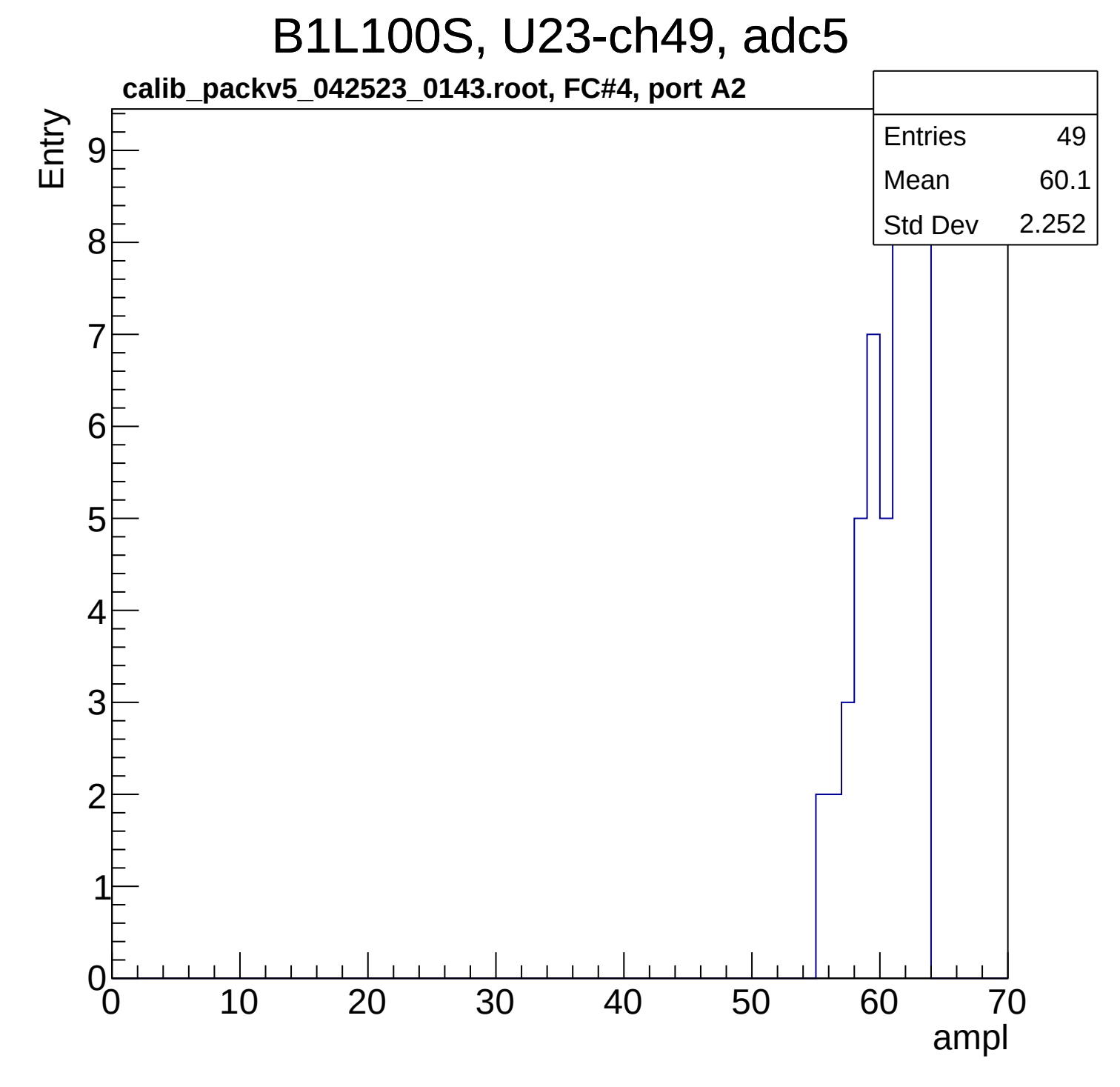
B1L100S, U23-ch49, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 60.1
Std Dev 2.252
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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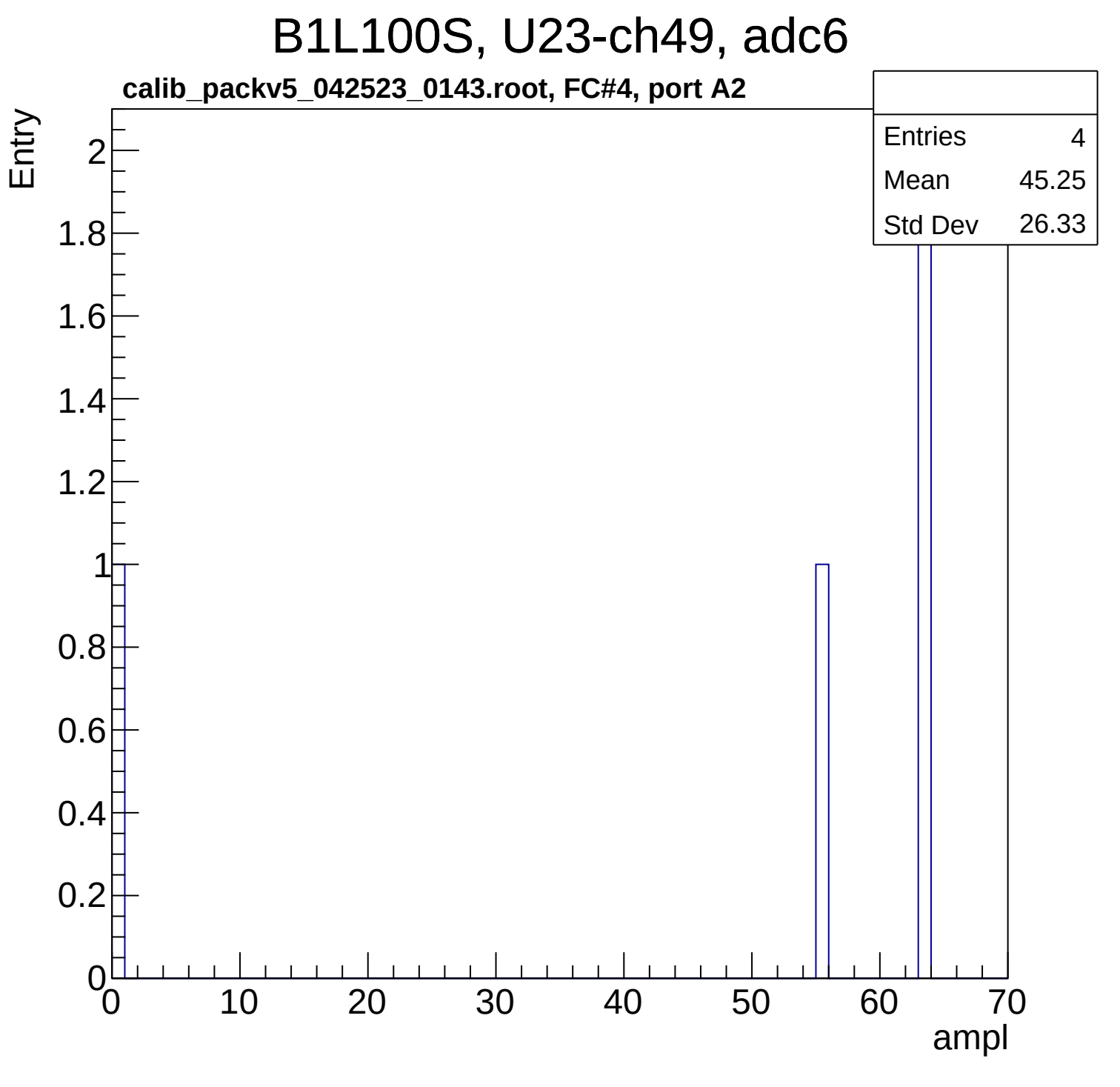
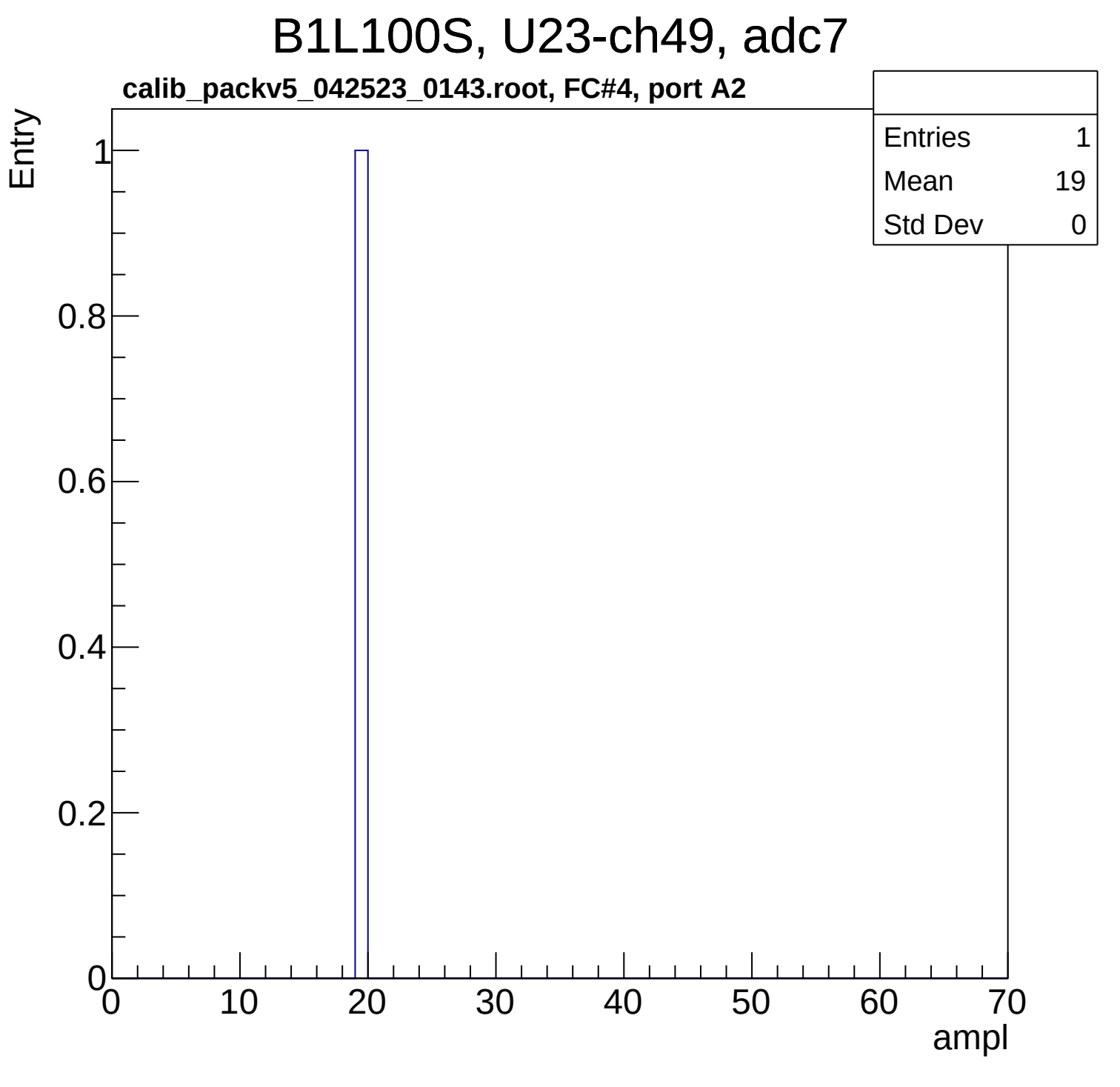
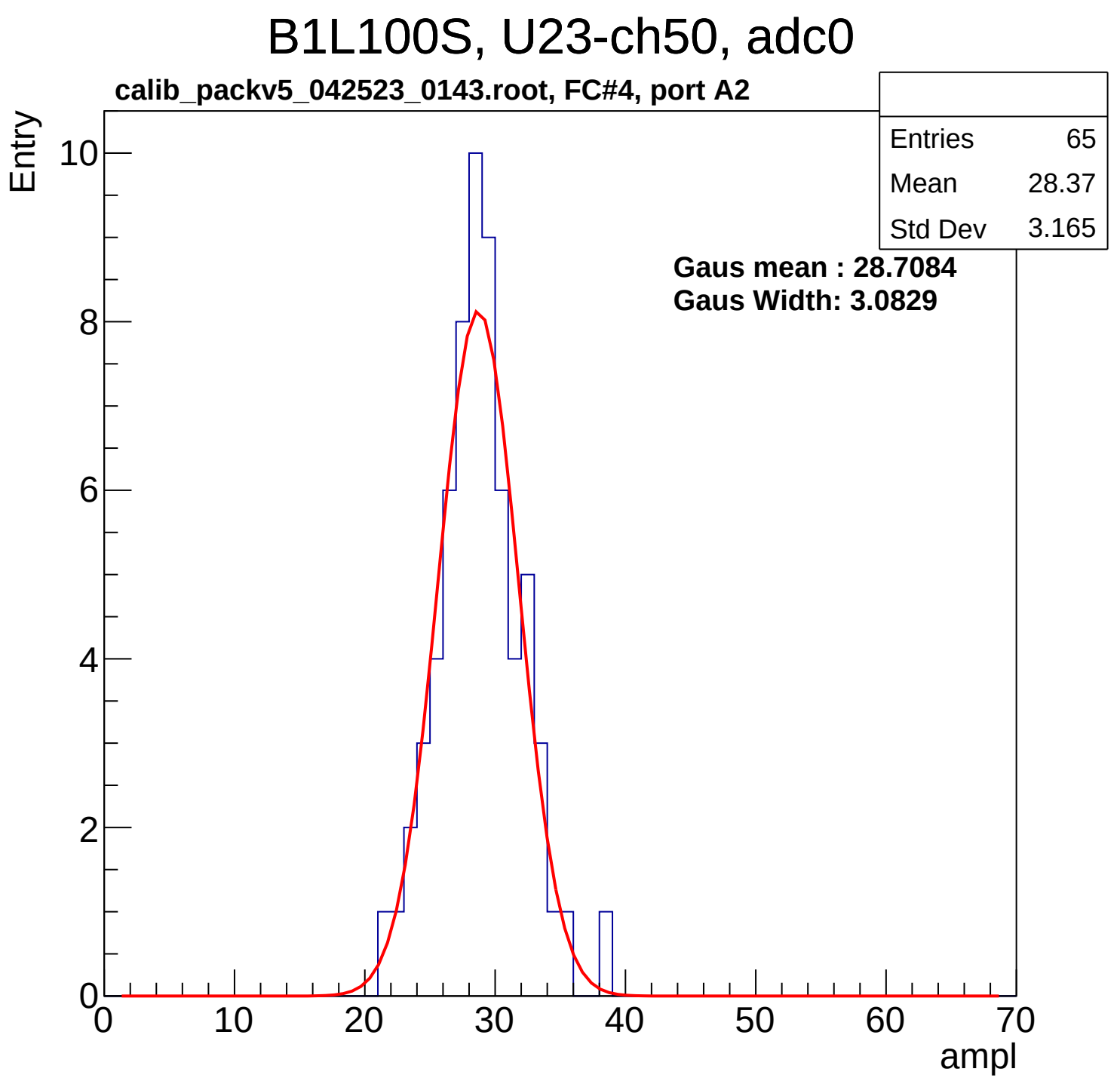

B1L100S, U23-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 45.25
Std Dev 26.33
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.37
Std Dev 3.165
Gaus mean : 28.7084
Gaus Width: 3.0829
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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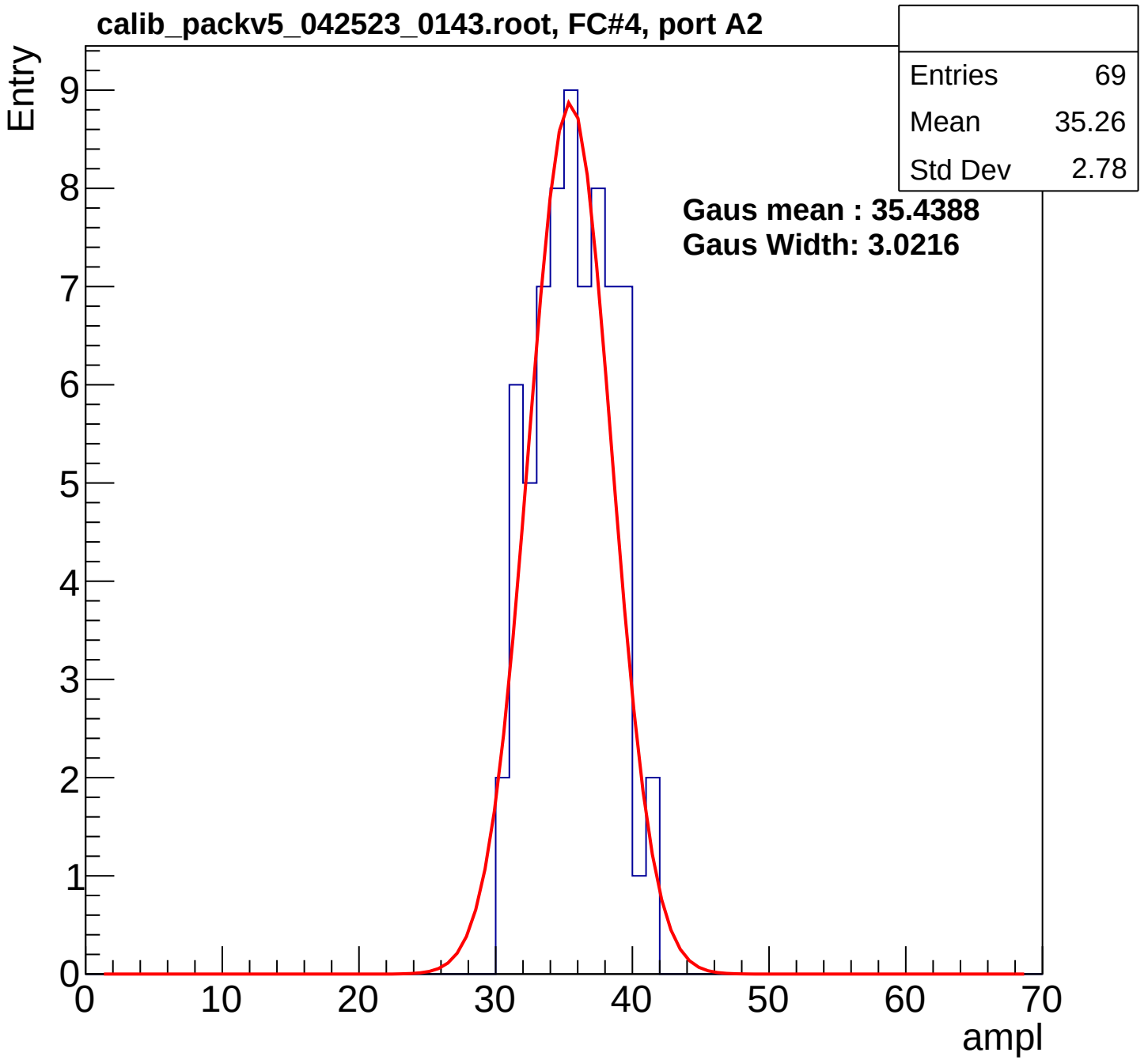
B1L100S, U23-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 35.26
Std Dev 2.78
Gaus mean : 35.4388
Gaus Width: 3.0216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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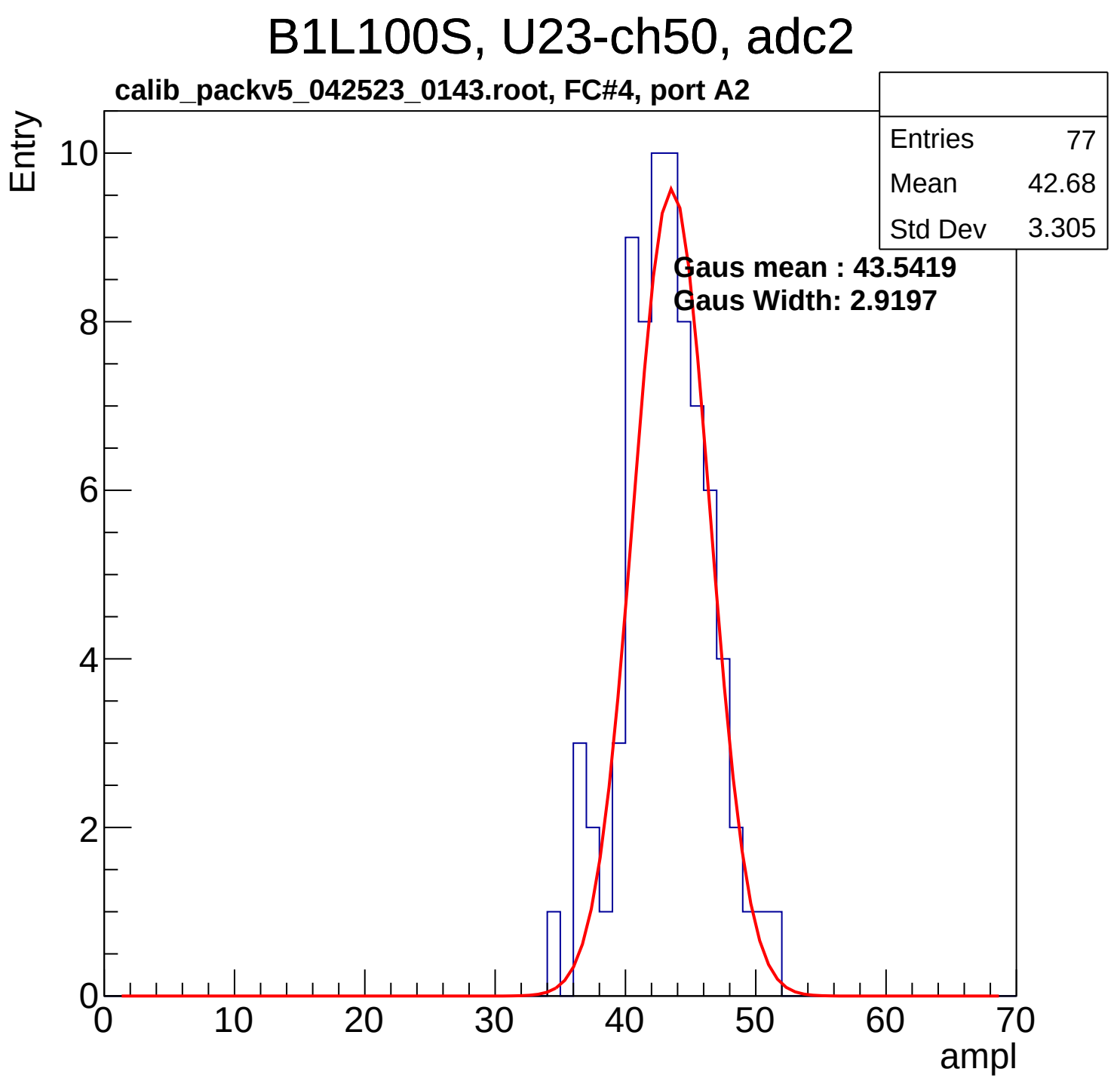
B1L100S, U23-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 42.68
Std Dev 3.305
Gaus mean : 43.5419
Gaus Width: 2.9197
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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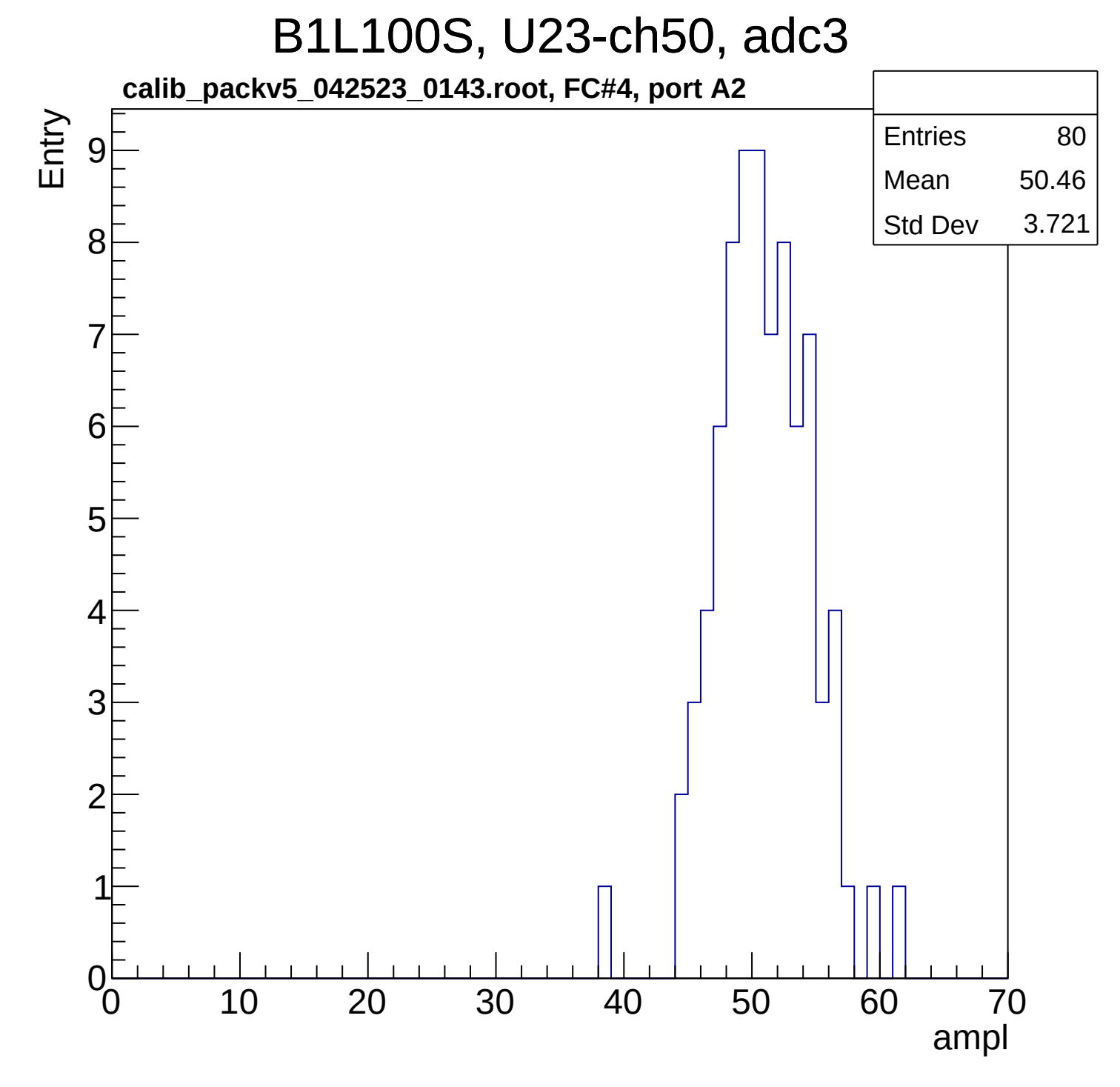
B1L100S, U23-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 50.46
Std Dev 3.721
Entry
ampl
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3
4
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6
7
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0
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60
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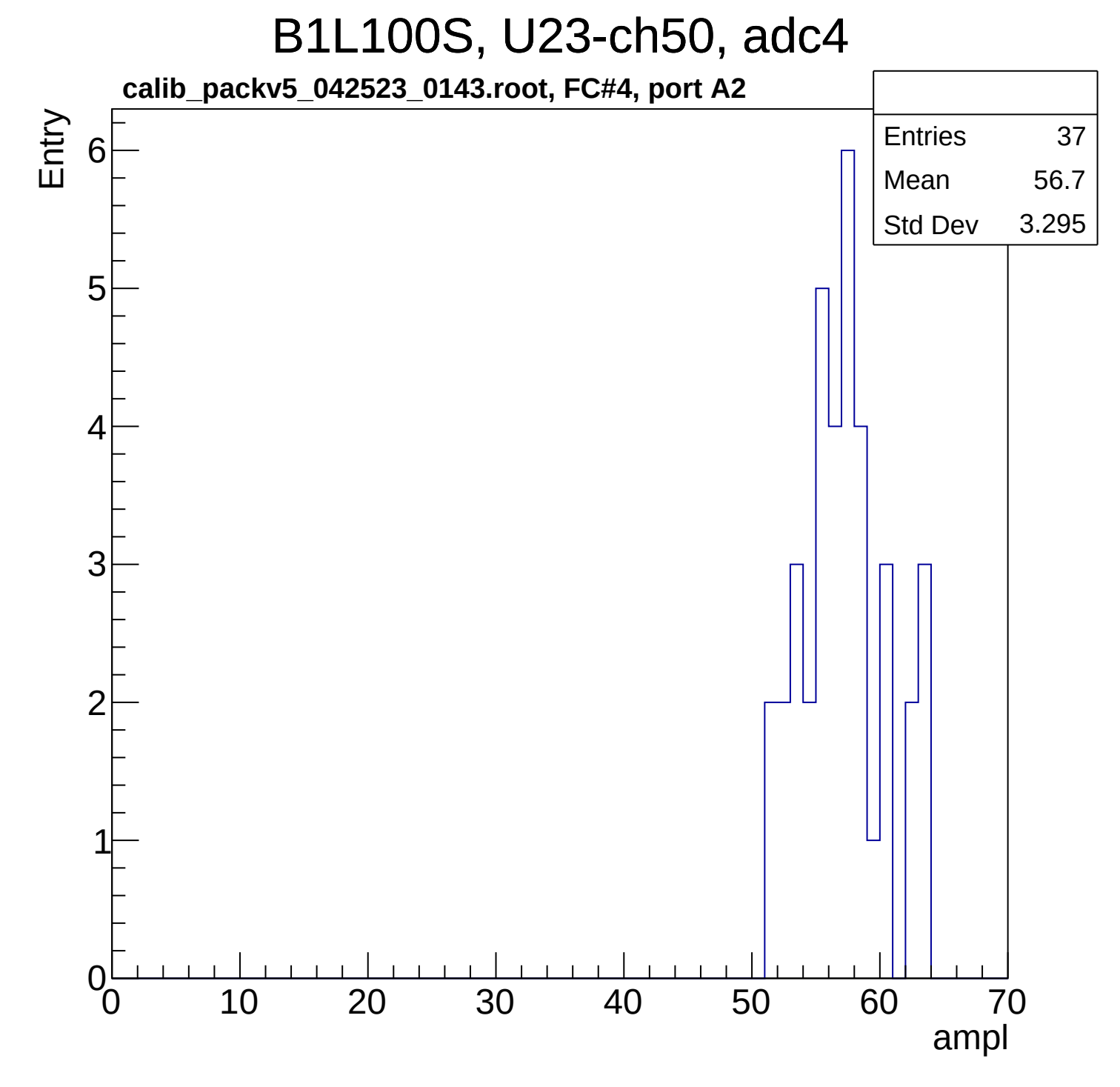
B1L100S, U23-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 56.7
Std Dev 3.295
Entry
ampl
0
1
2
3
4
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0
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60
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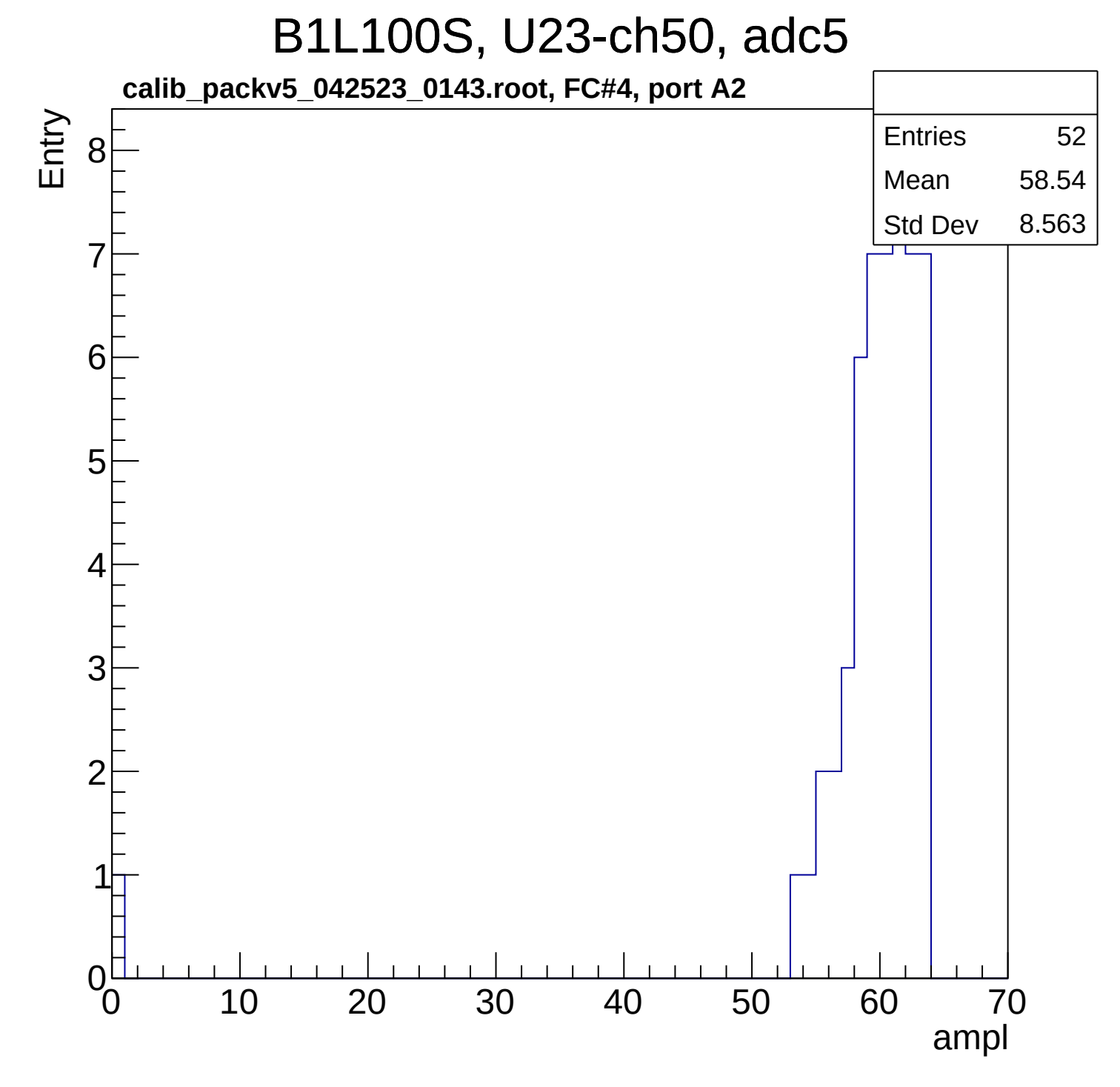
B1L100S, U23-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.54
Std Dev 8.563
Entry
ampl
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3
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7
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0
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60
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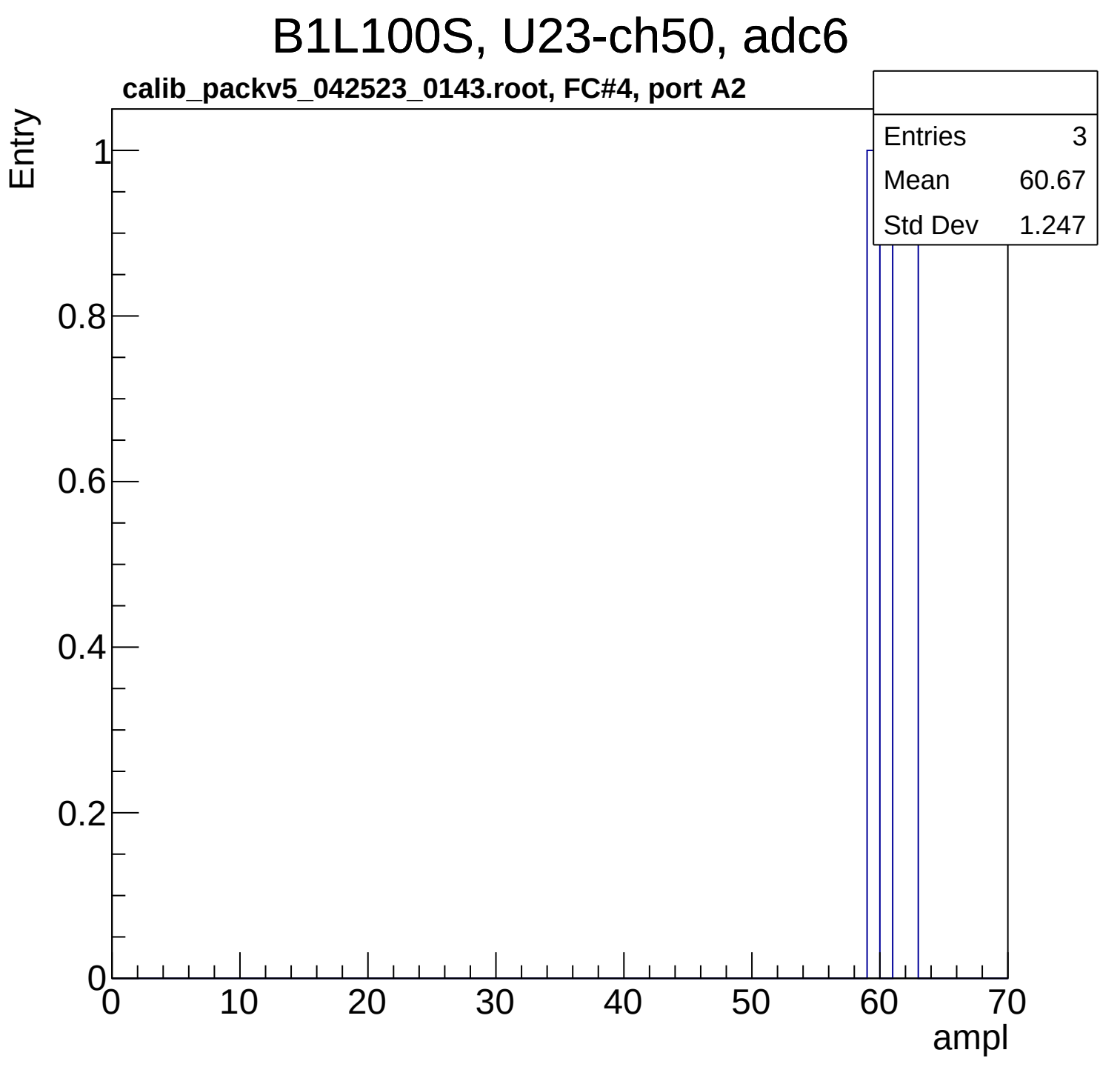
B1L100S, U23-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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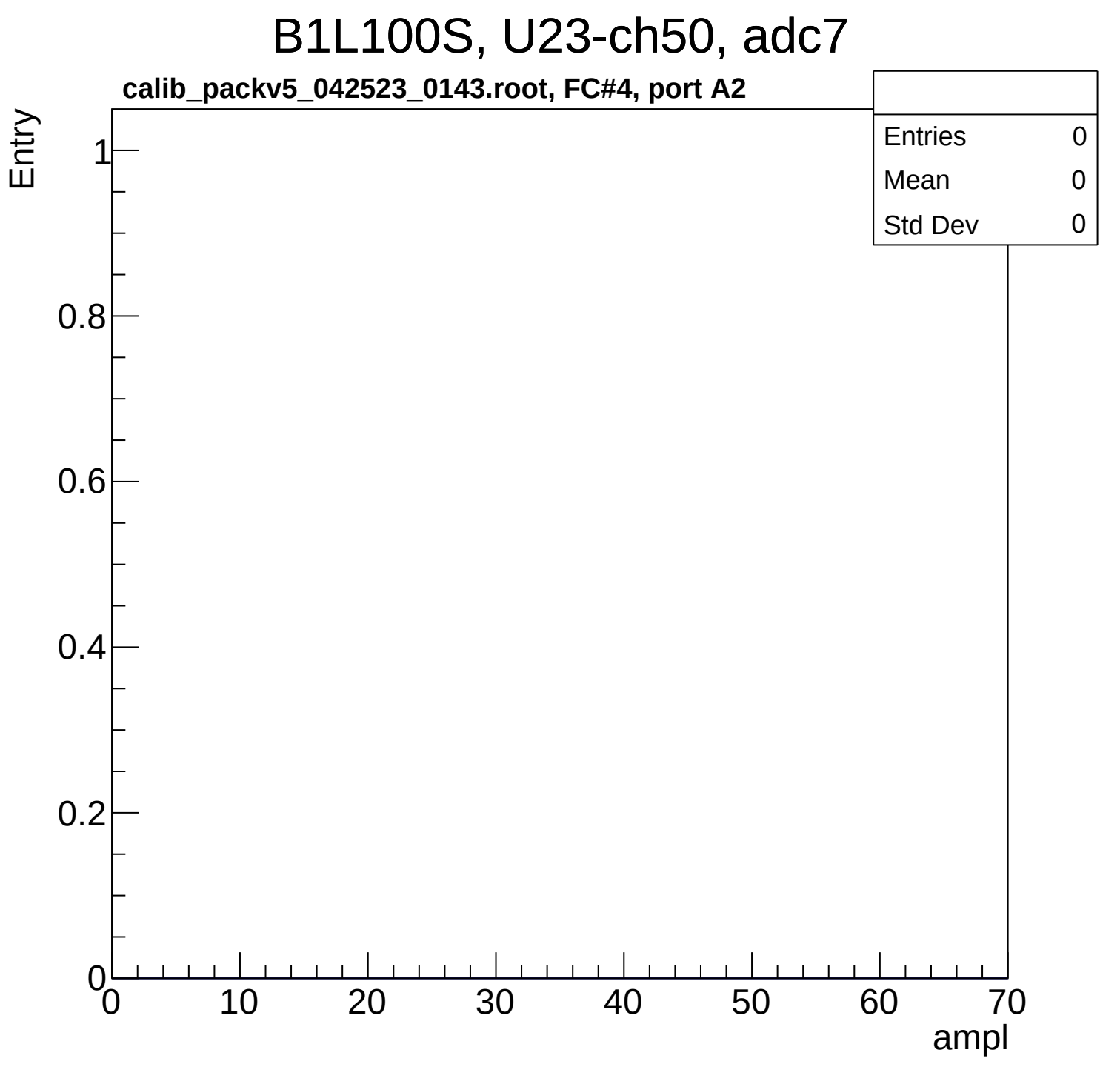
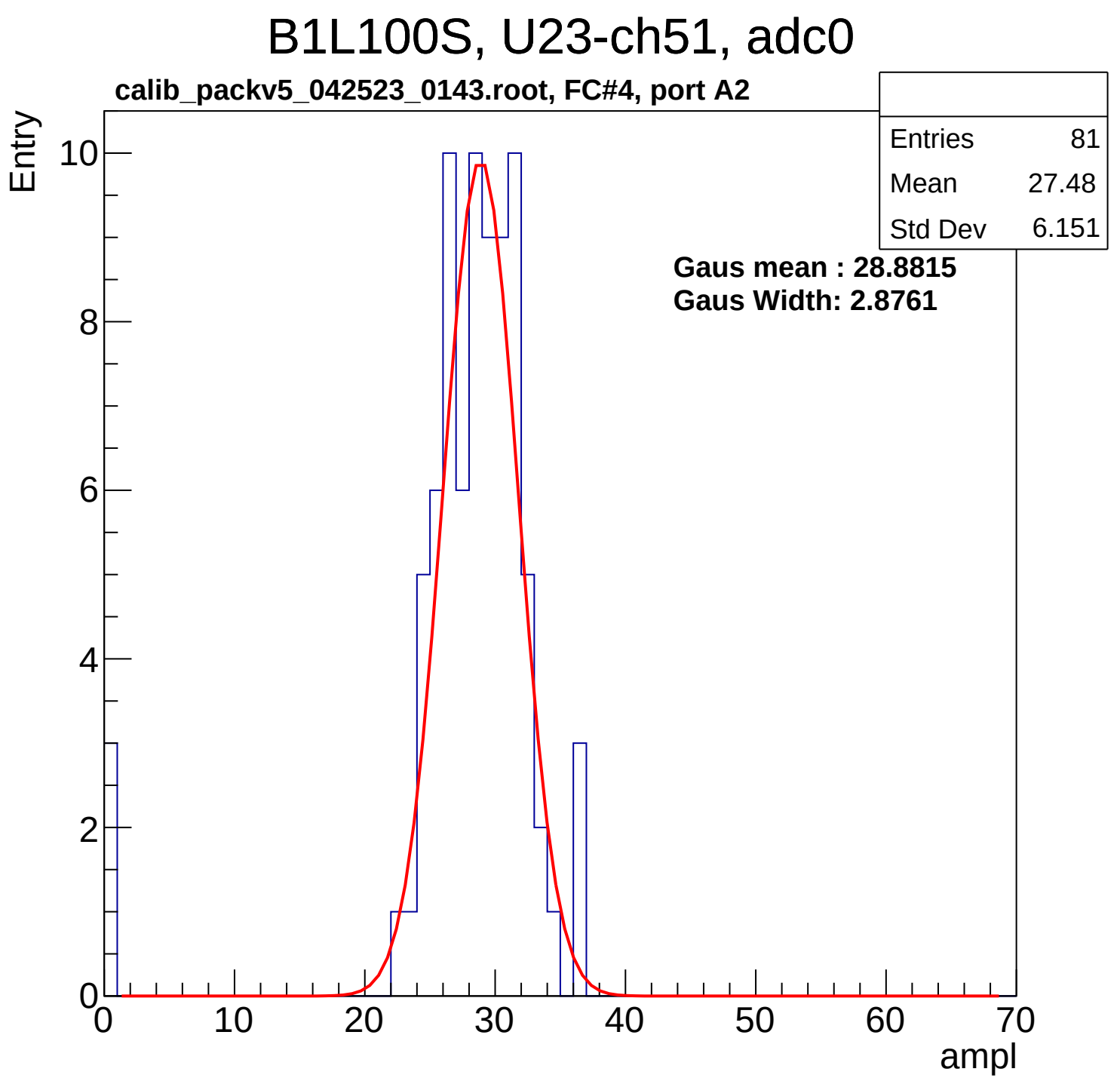
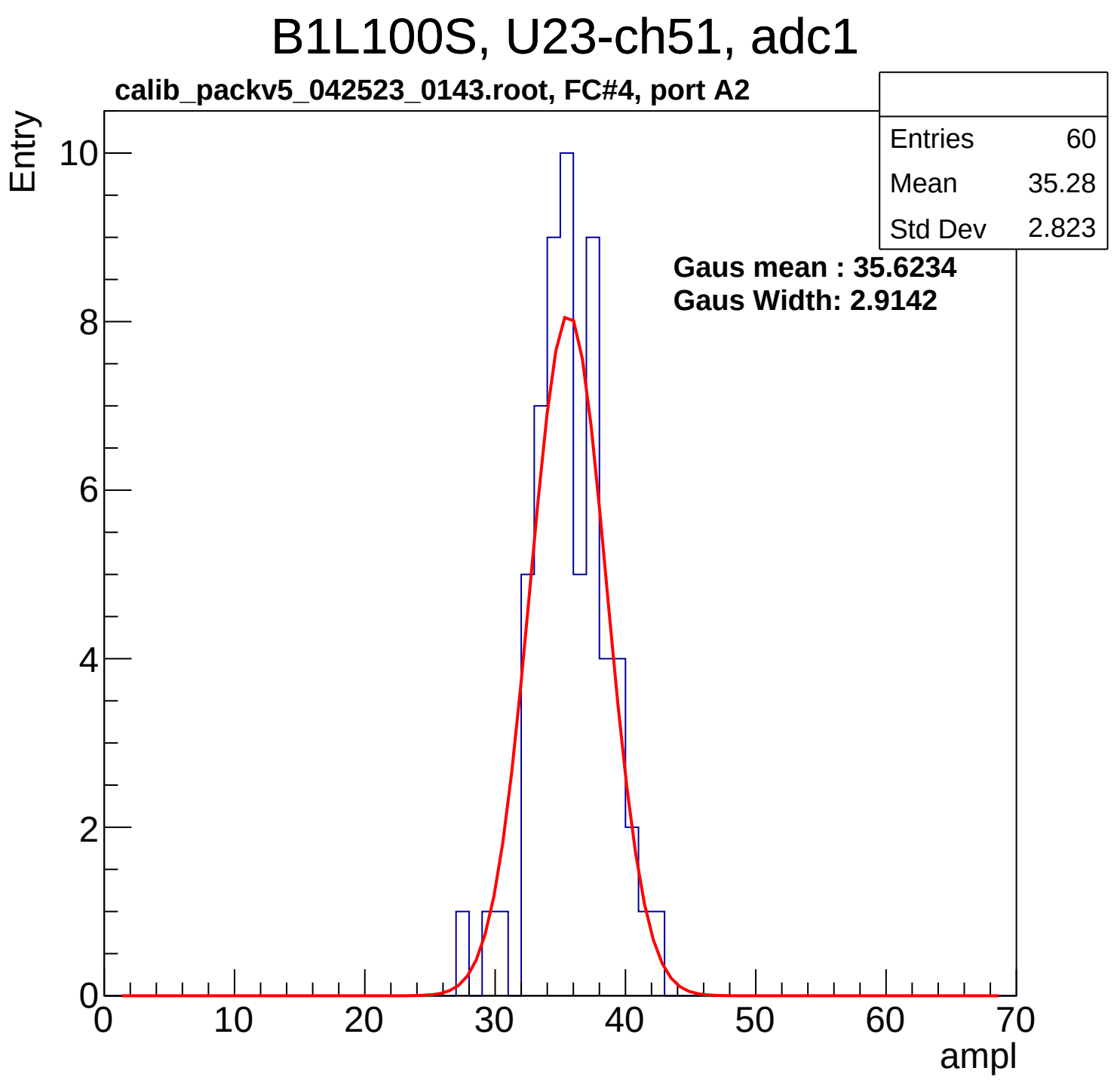

B1L100S, U23-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 27.48
Std Dev 6.151
Gaus mean : 28.8815
Gaus Width: 2.8761
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.28
Std Dev 2.823
Gaus mean : 35.6234
Gaus Width: 2.9142
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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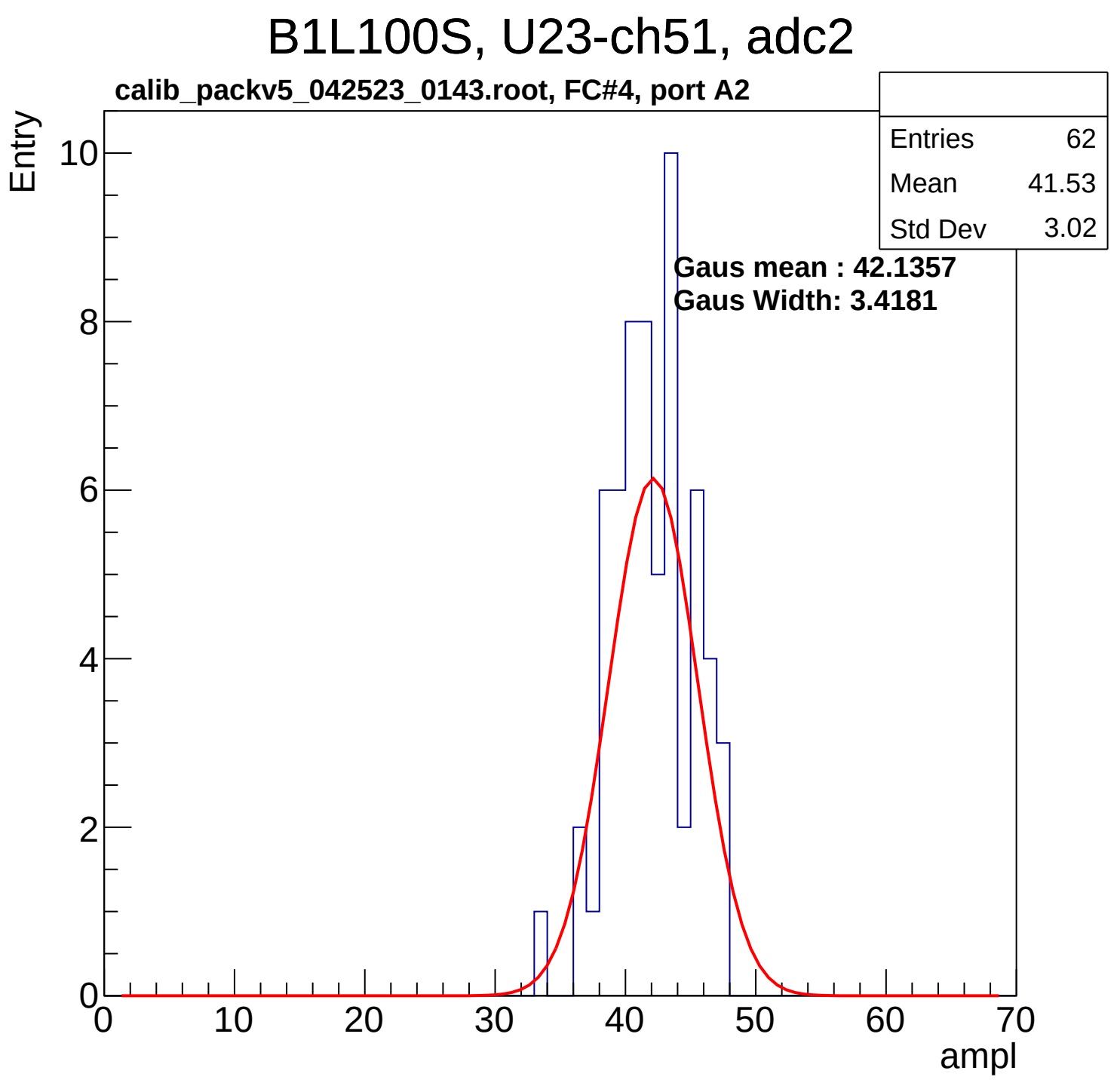
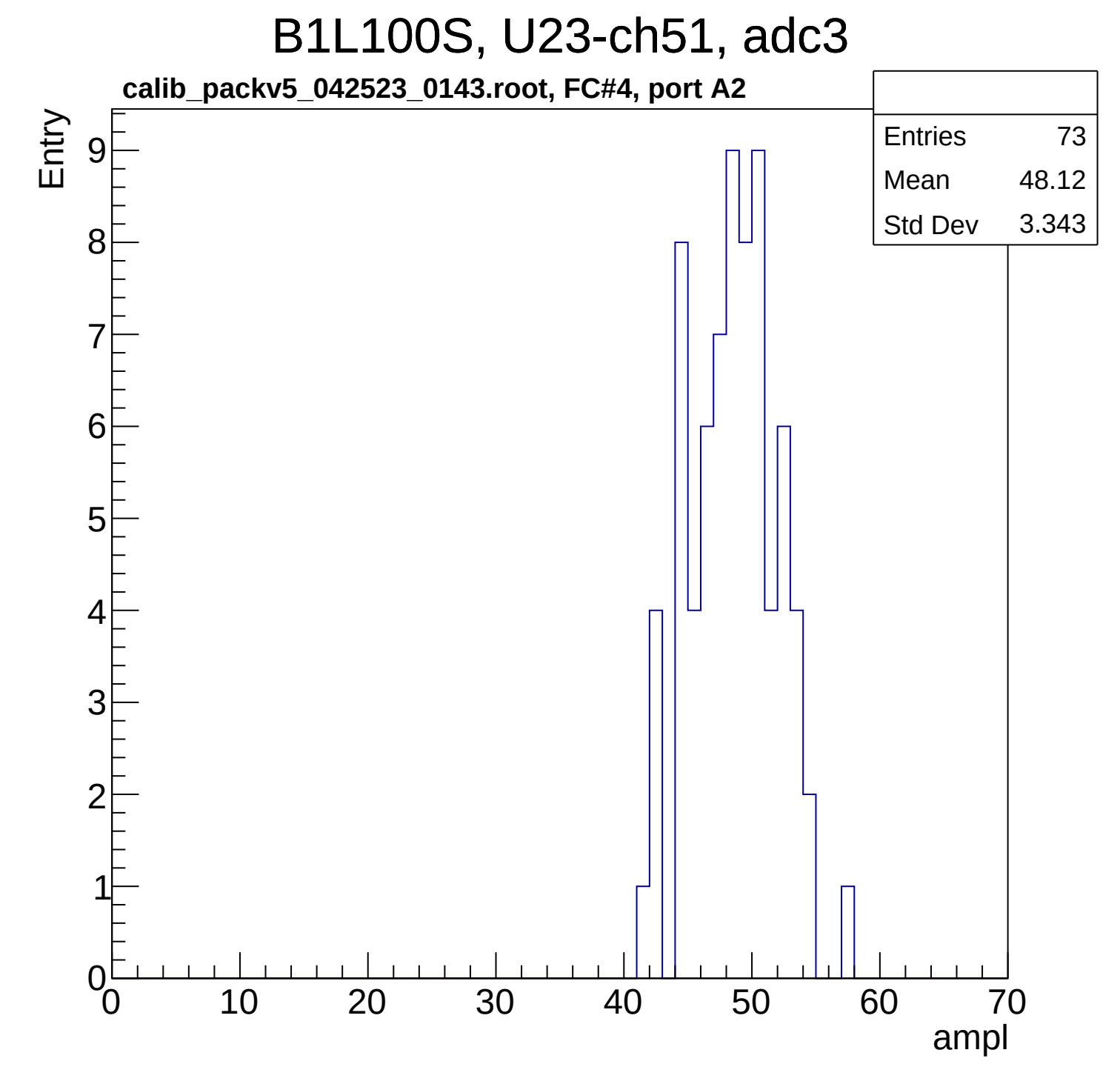

B1L100S, U23-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 41.53
Std Dev 3.02
Gaus mean : 42.1357
Gaus Width: 3.4181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch51, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 48.12
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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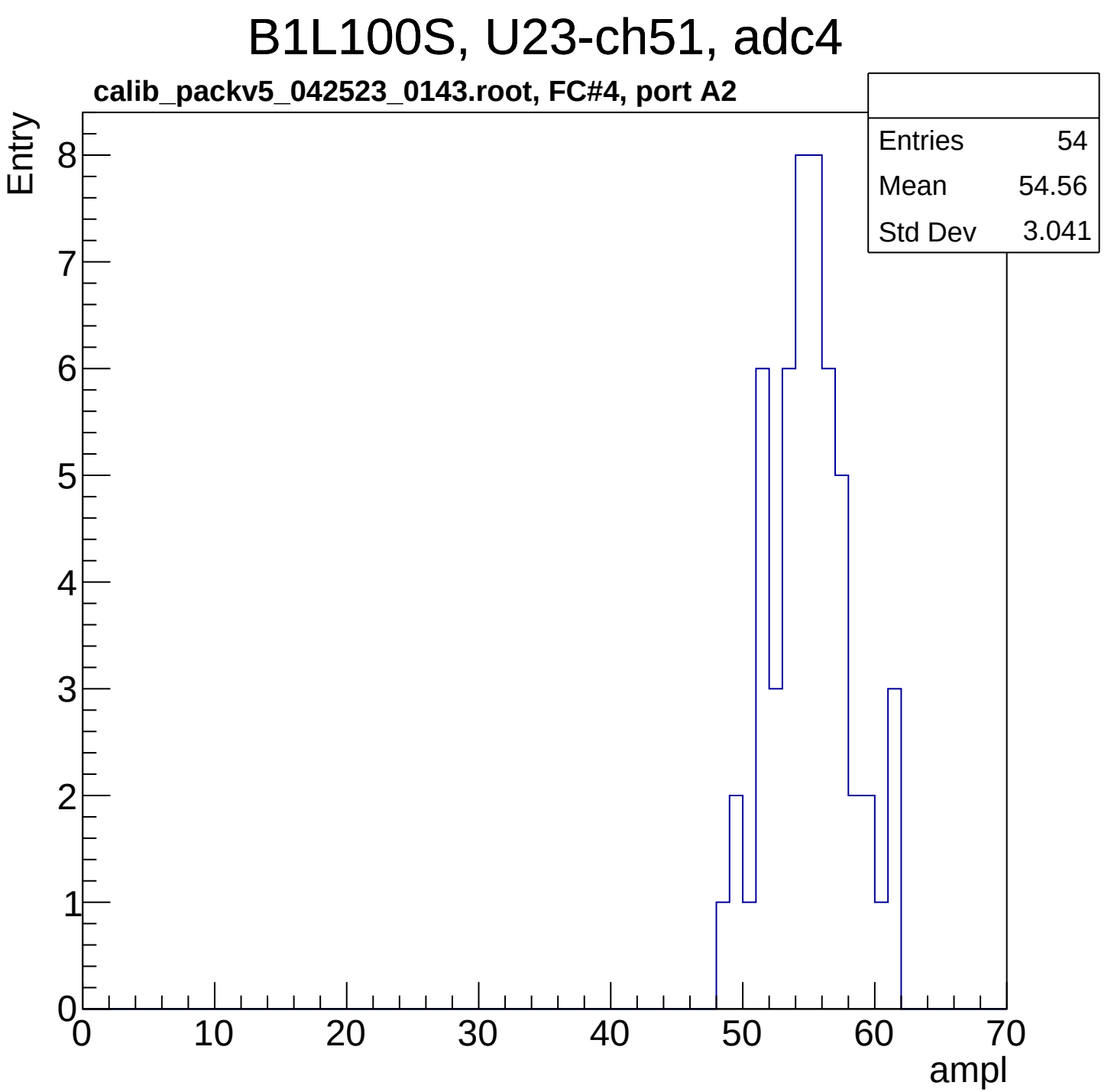
B1L100S, U23-ch51, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 54.56
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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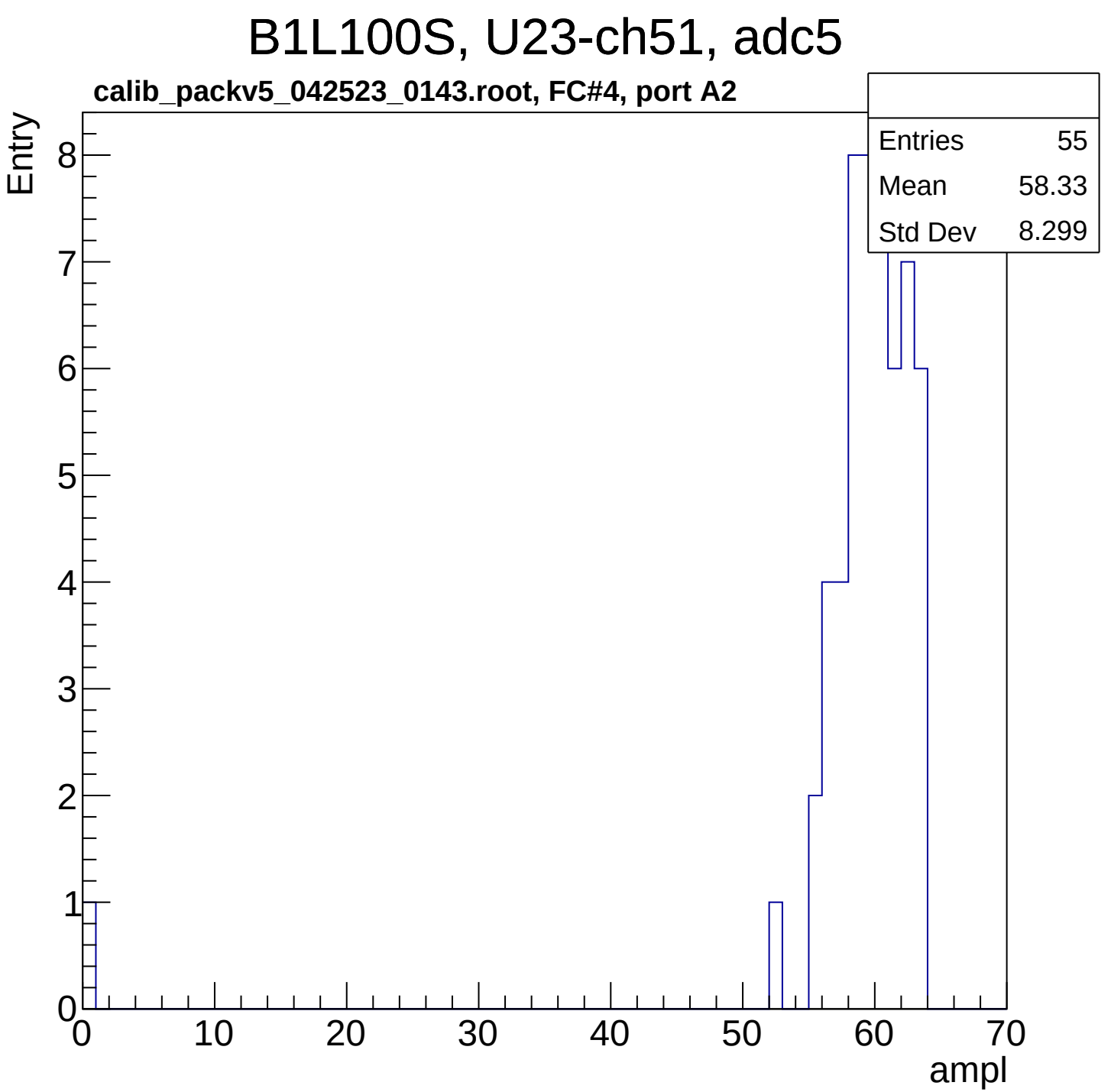
B1L100S, U23-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 58.33
Std Dev 8.299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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60
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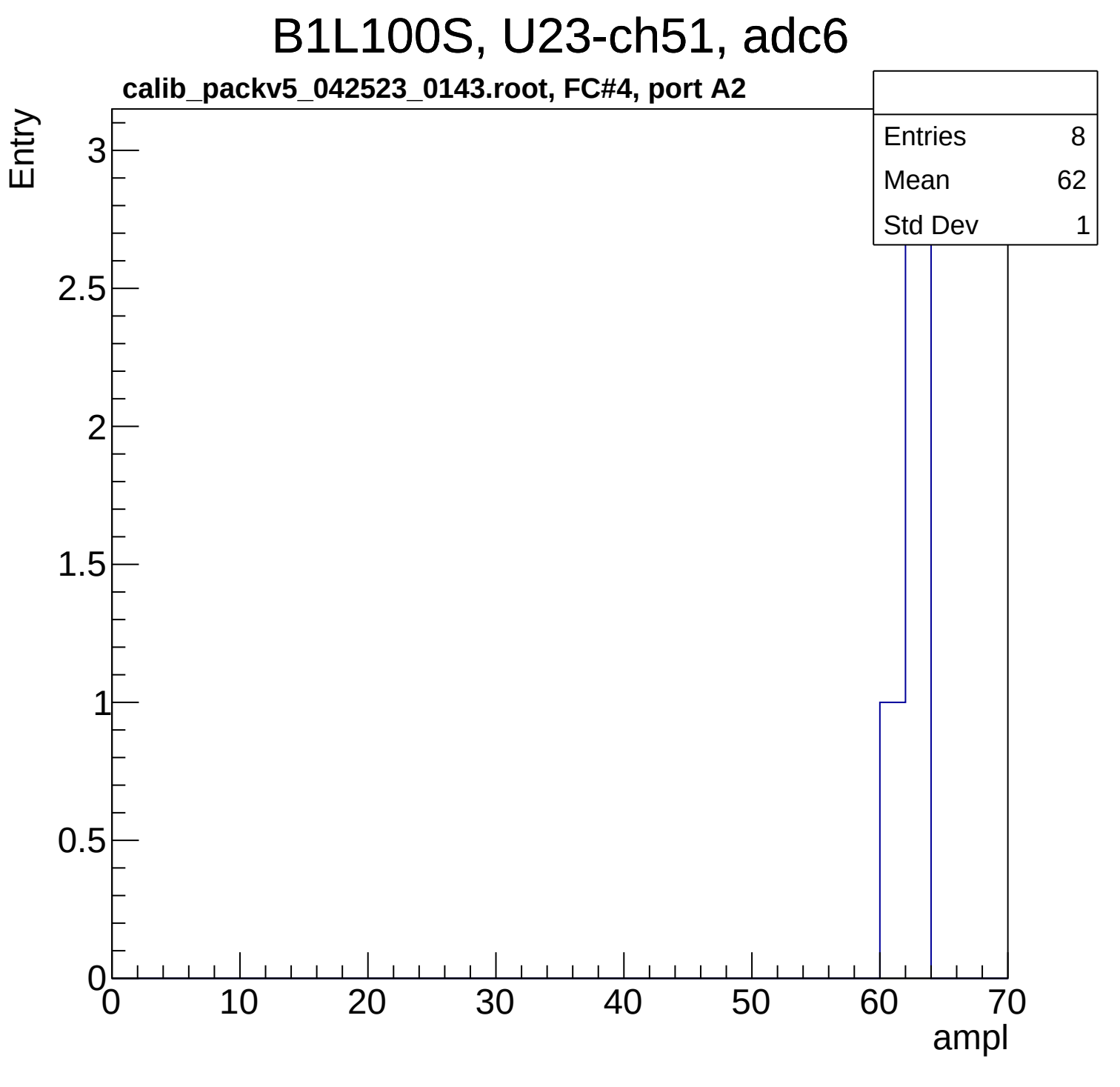
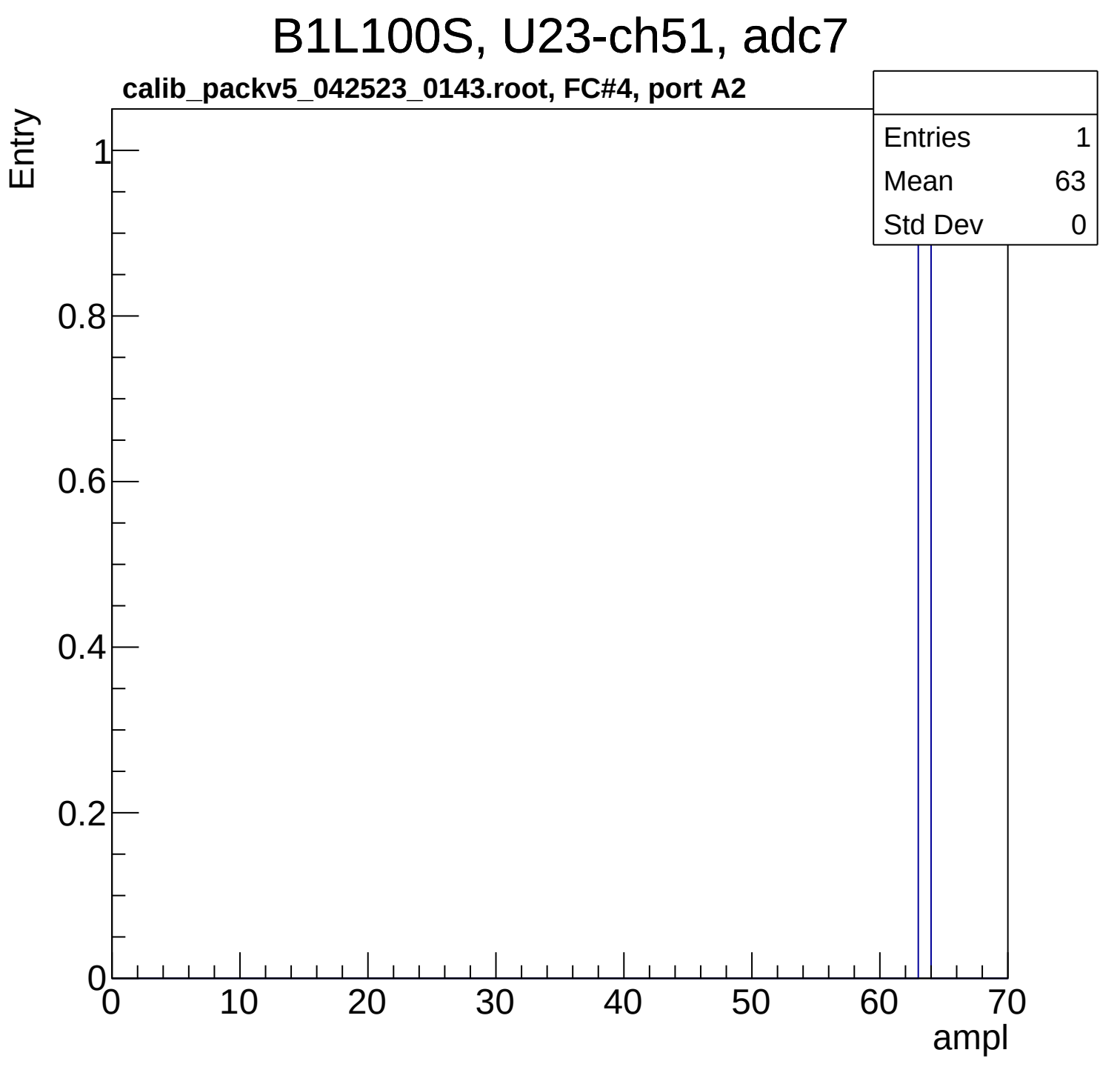
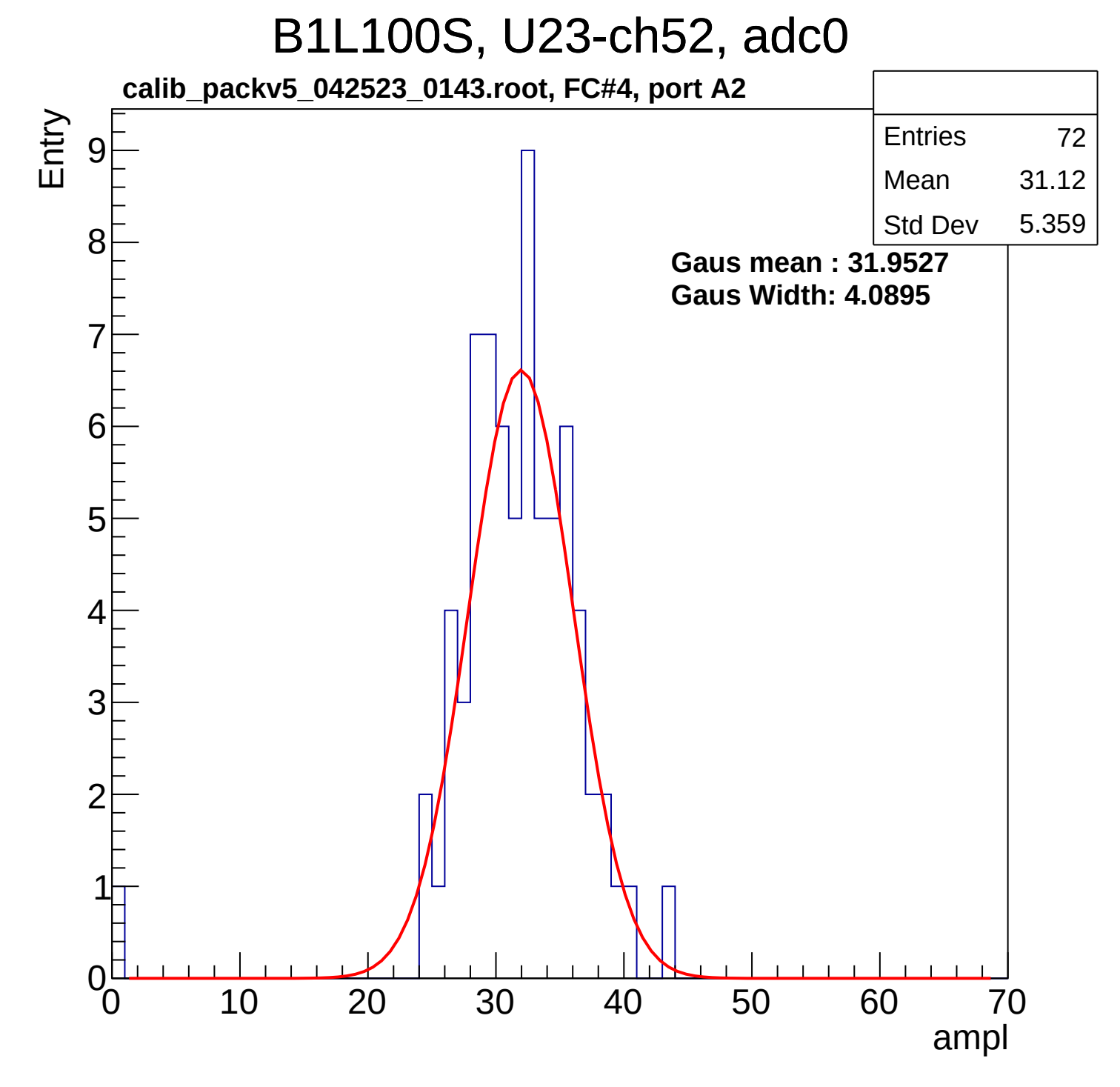
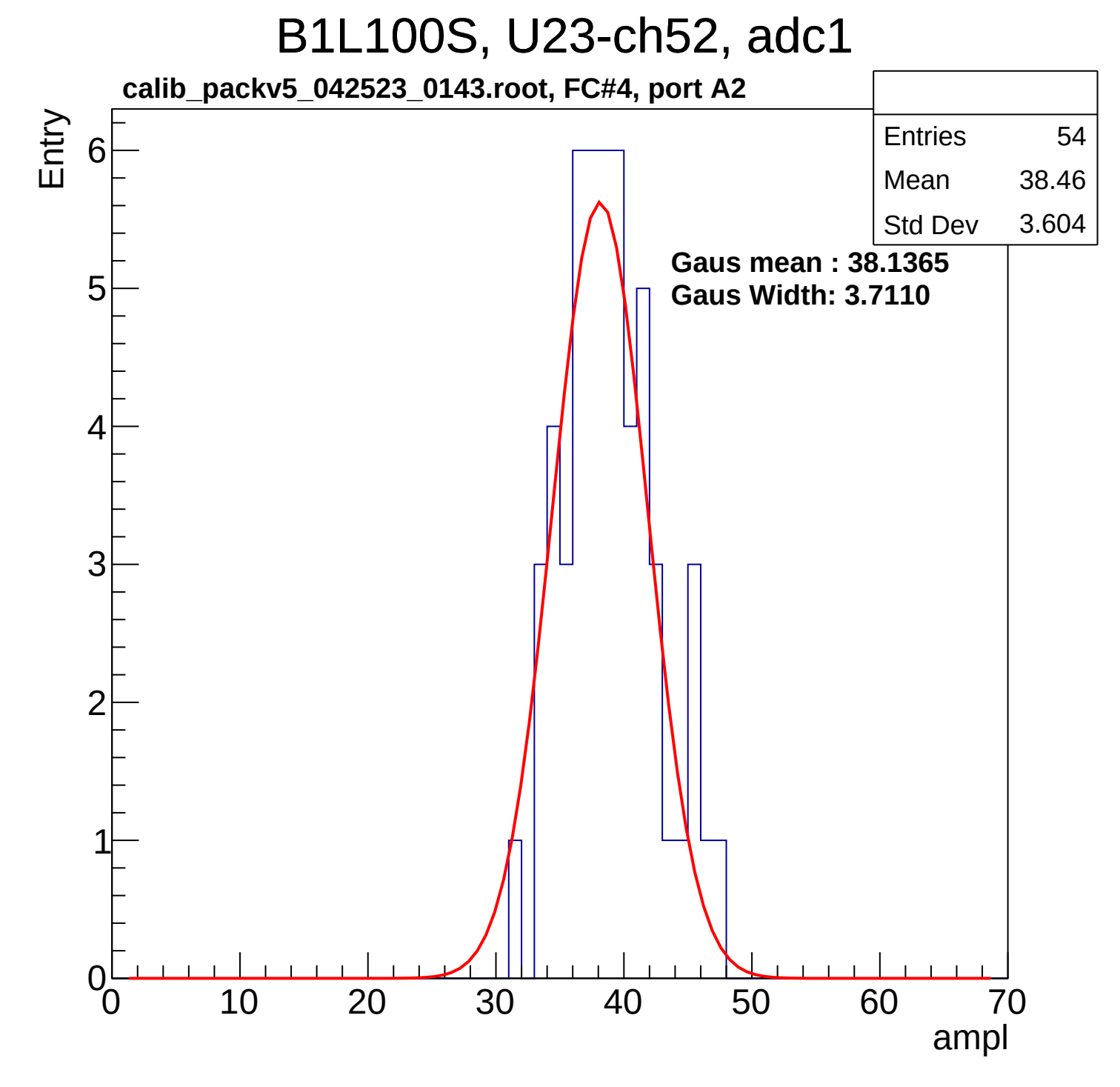
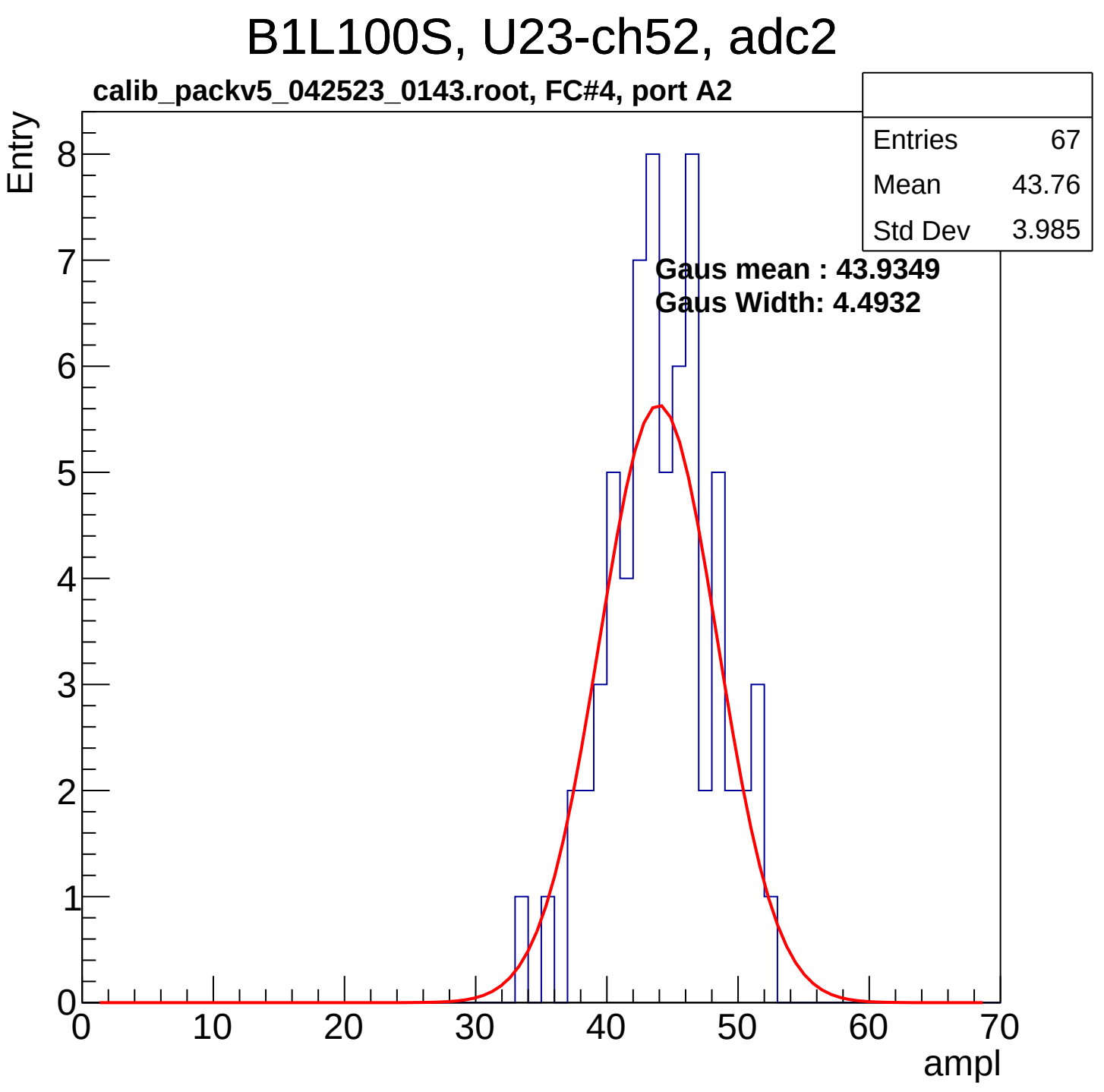

B1L100S, U23-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 31.12
Std Dev 5.359
Gaus mean : 31.9527
Gaus Width: 4.0895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 38.46
Std Dev 3.604
Gaus mean : 38.1365
Gaus Width: 3.7110
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43.76
Std Dev 3.985
Gaus mean : 43.9349
Gaus Width: 4.4932
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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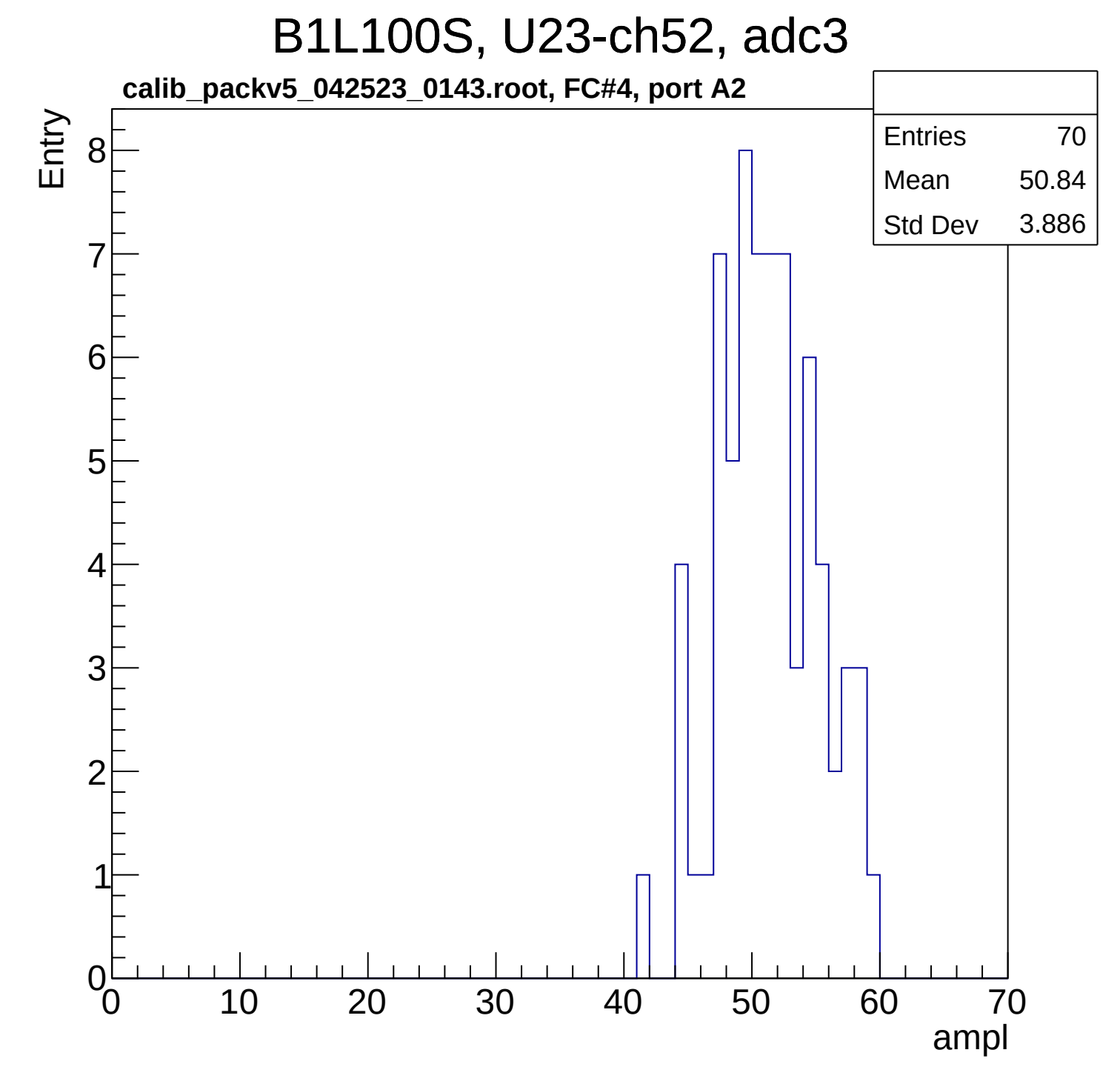
B1L100S, U23-ch52, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.84
Std Dev 3.886
Entry
ampl
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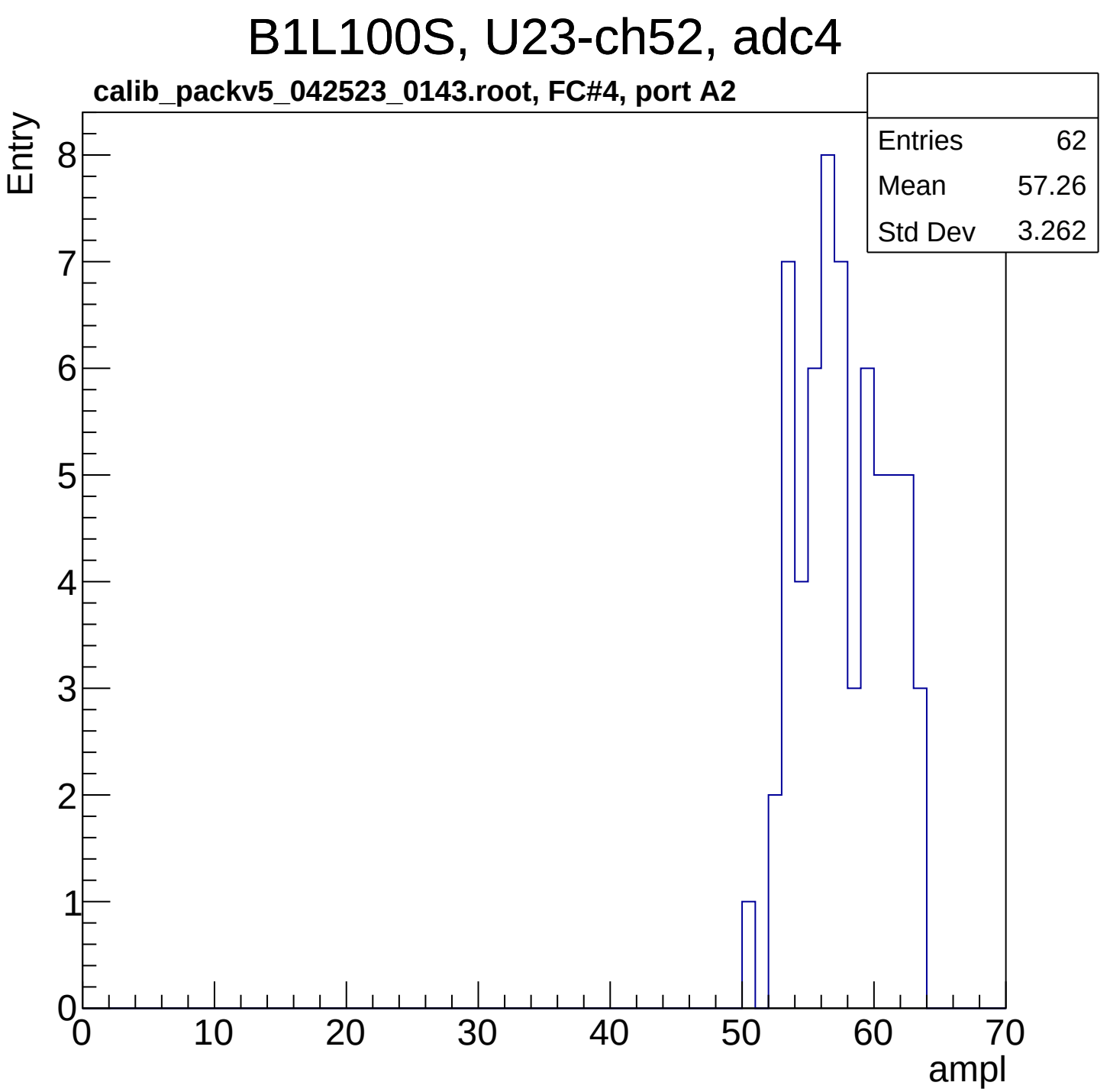
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.26
Std Dev 3.262
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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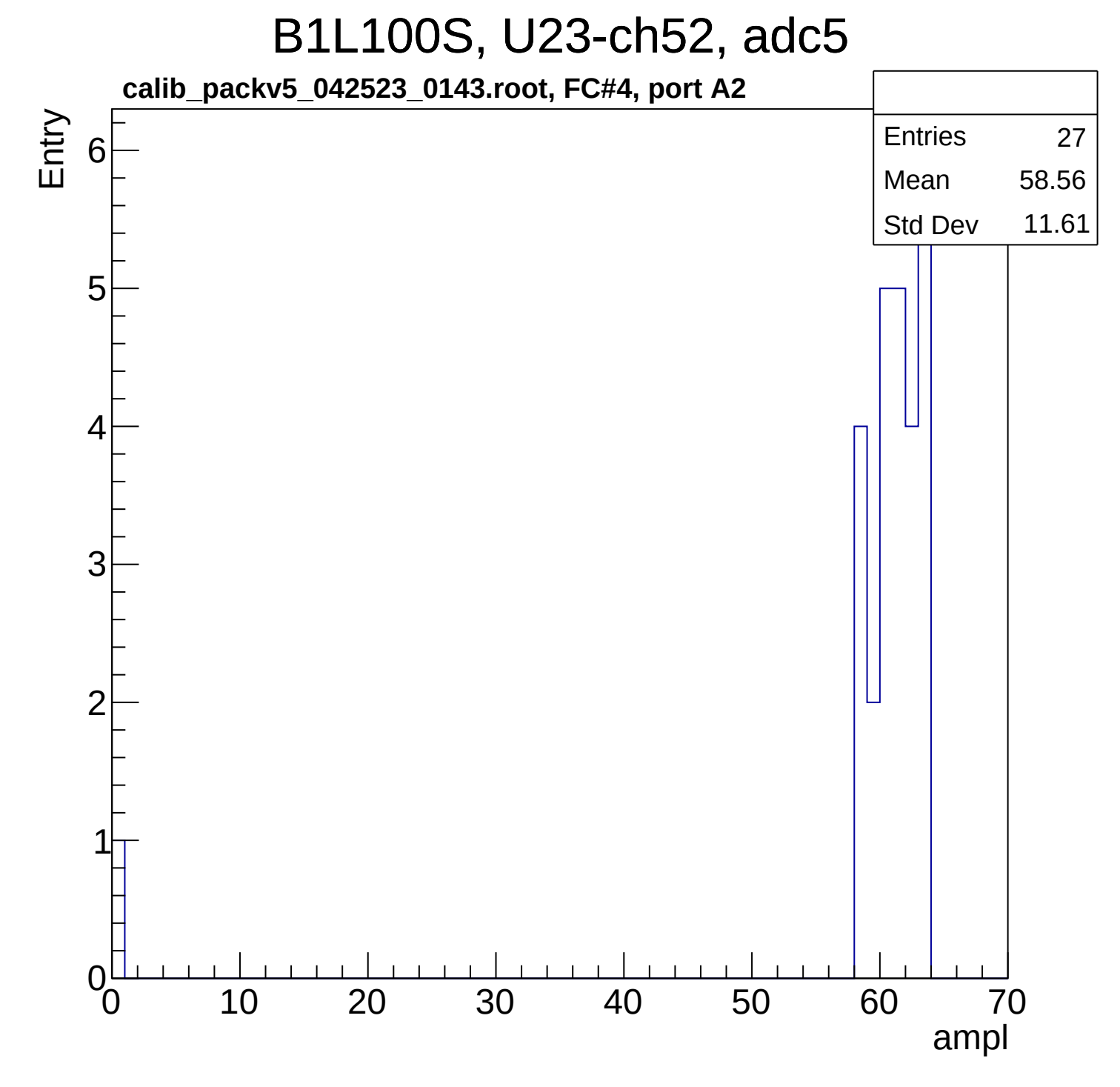
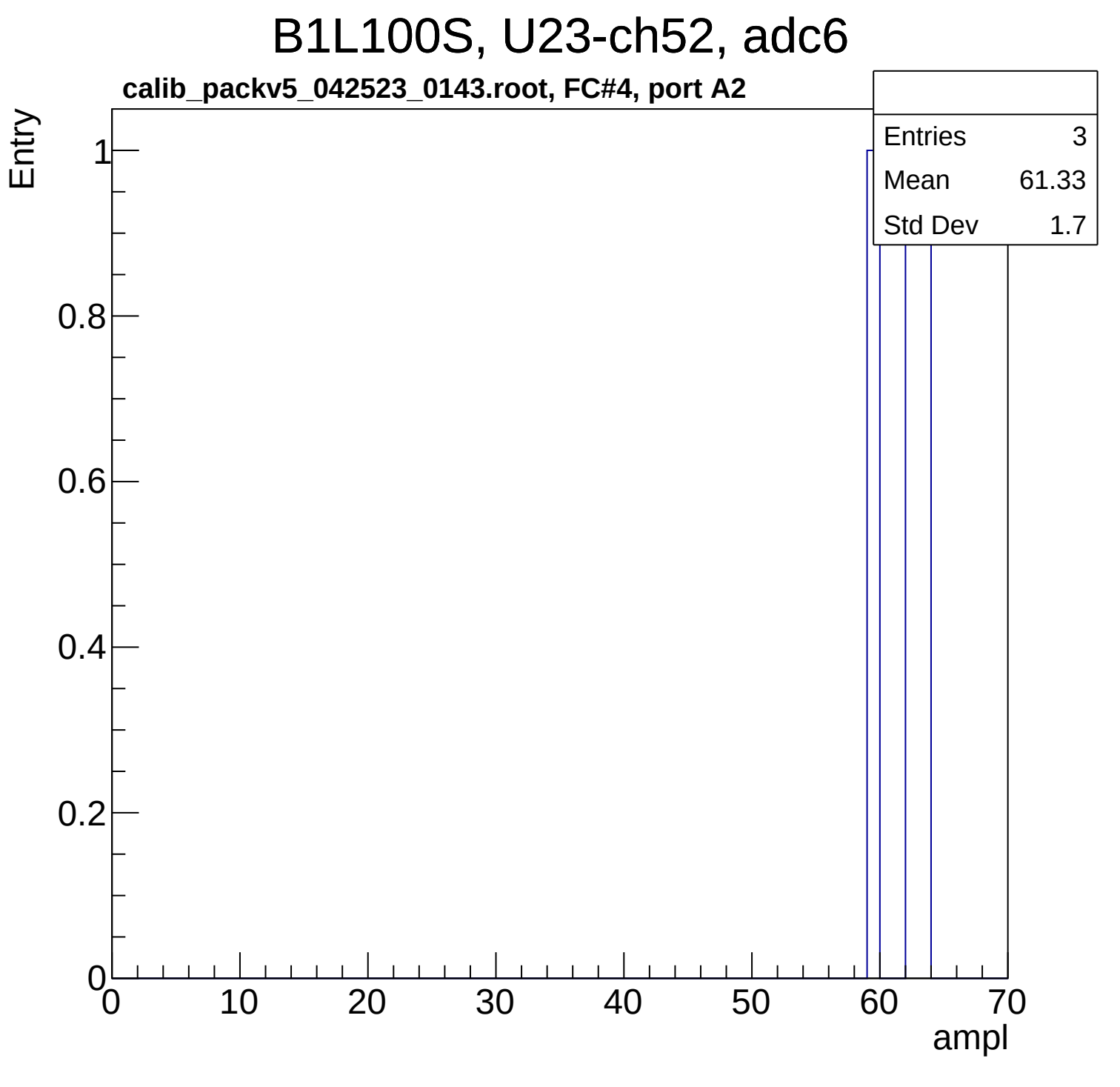

B1L100S, U23-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.56
Std Dev 11.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch52, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
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40
50
60
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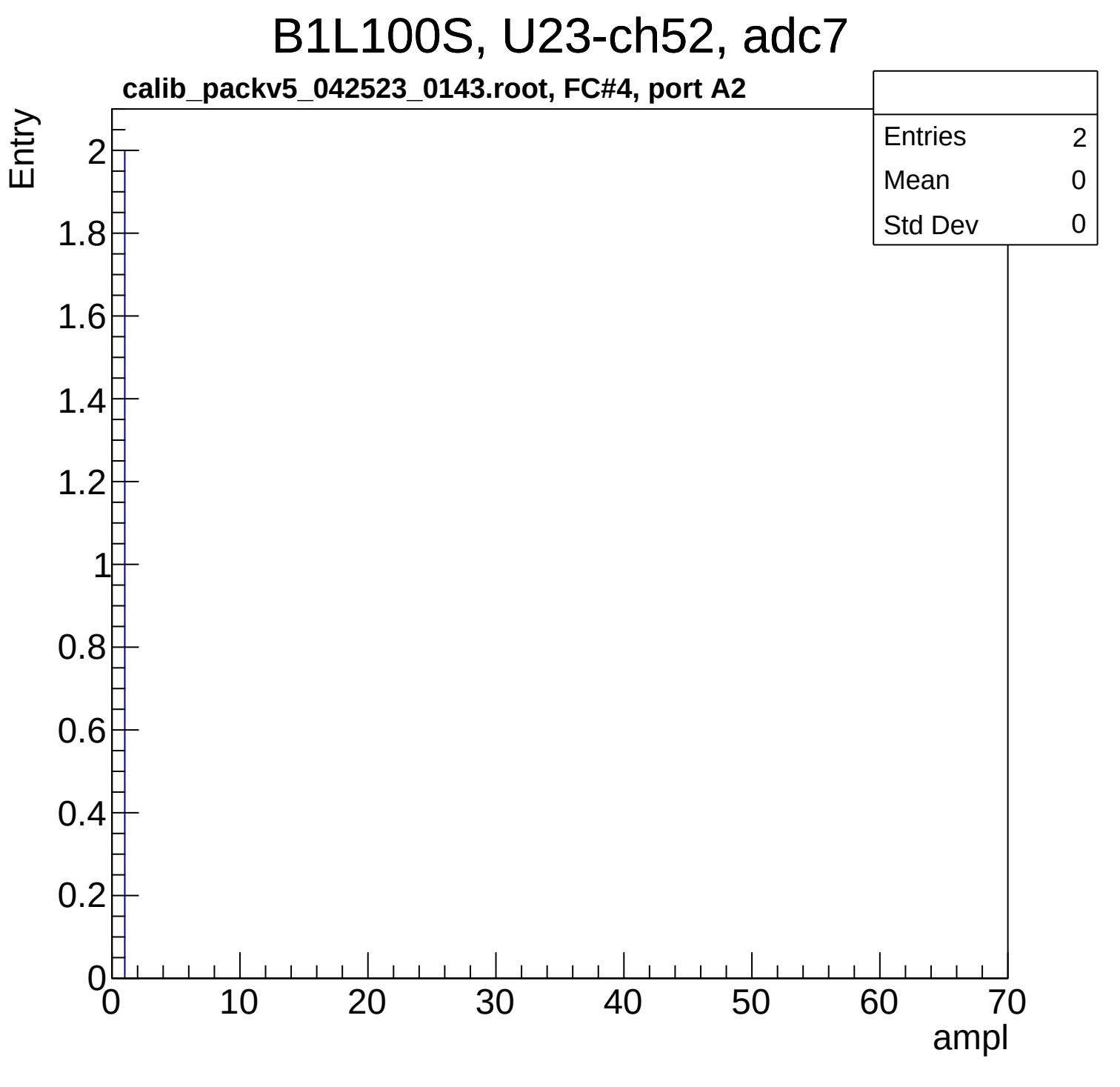
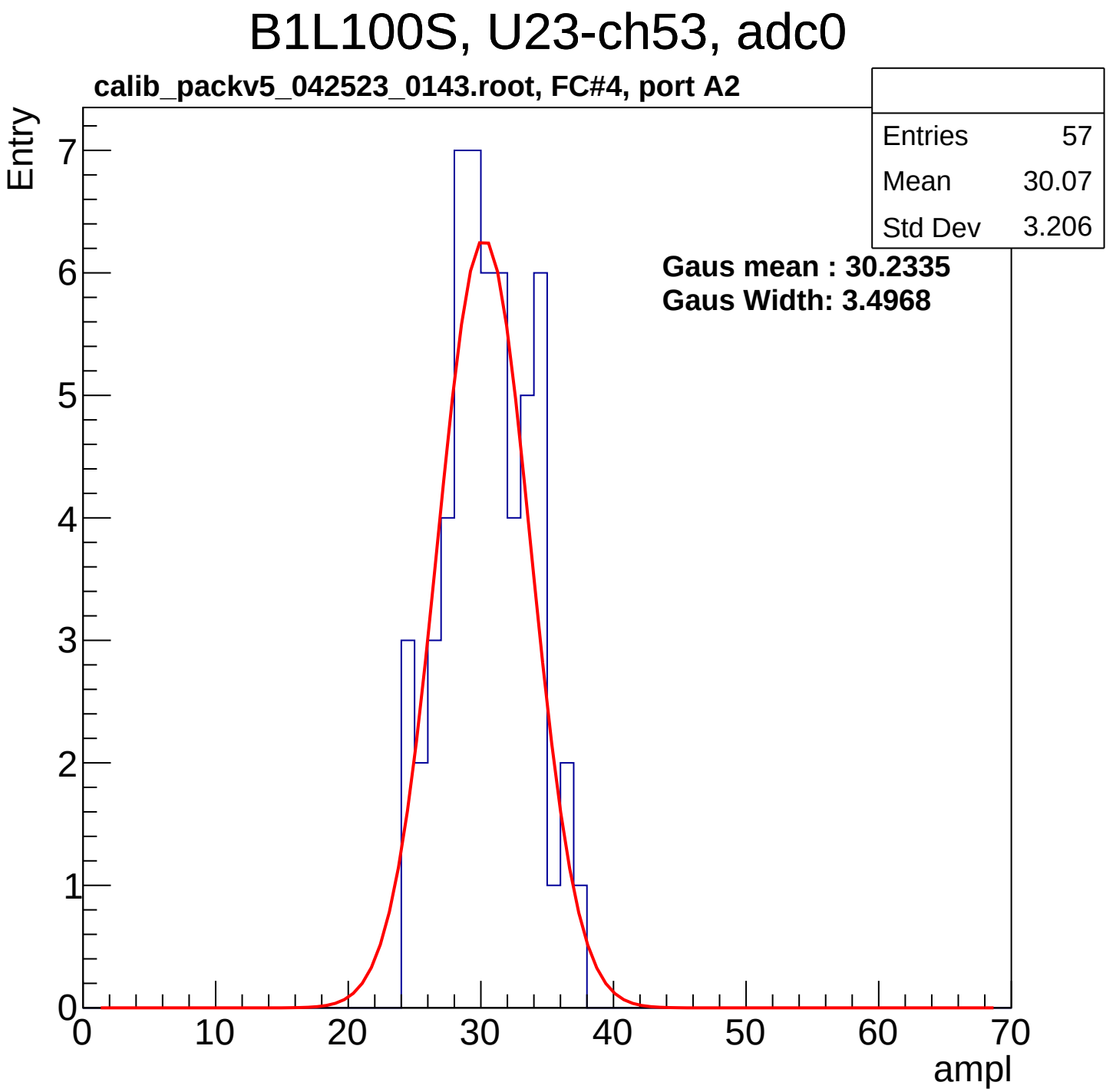
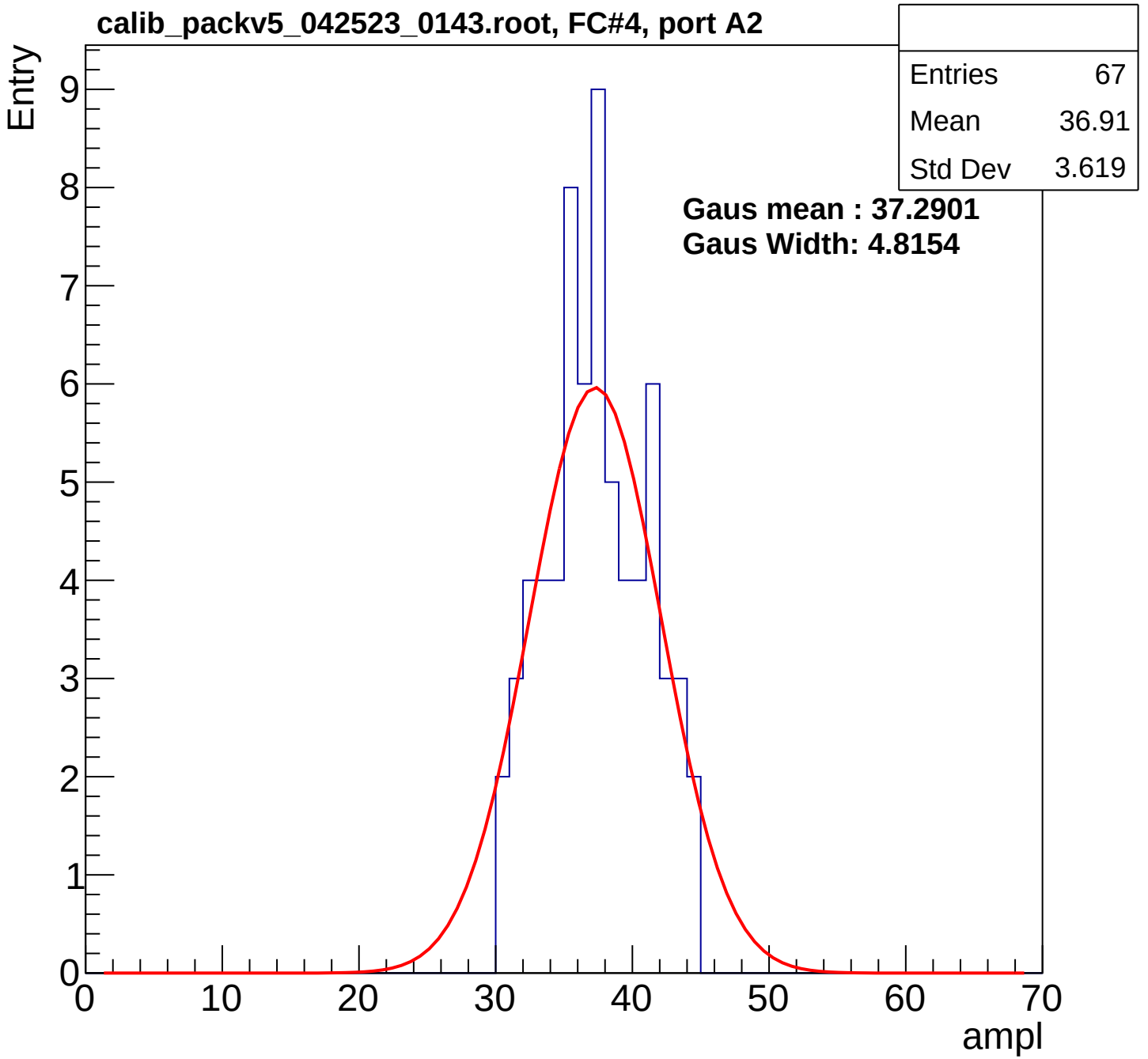
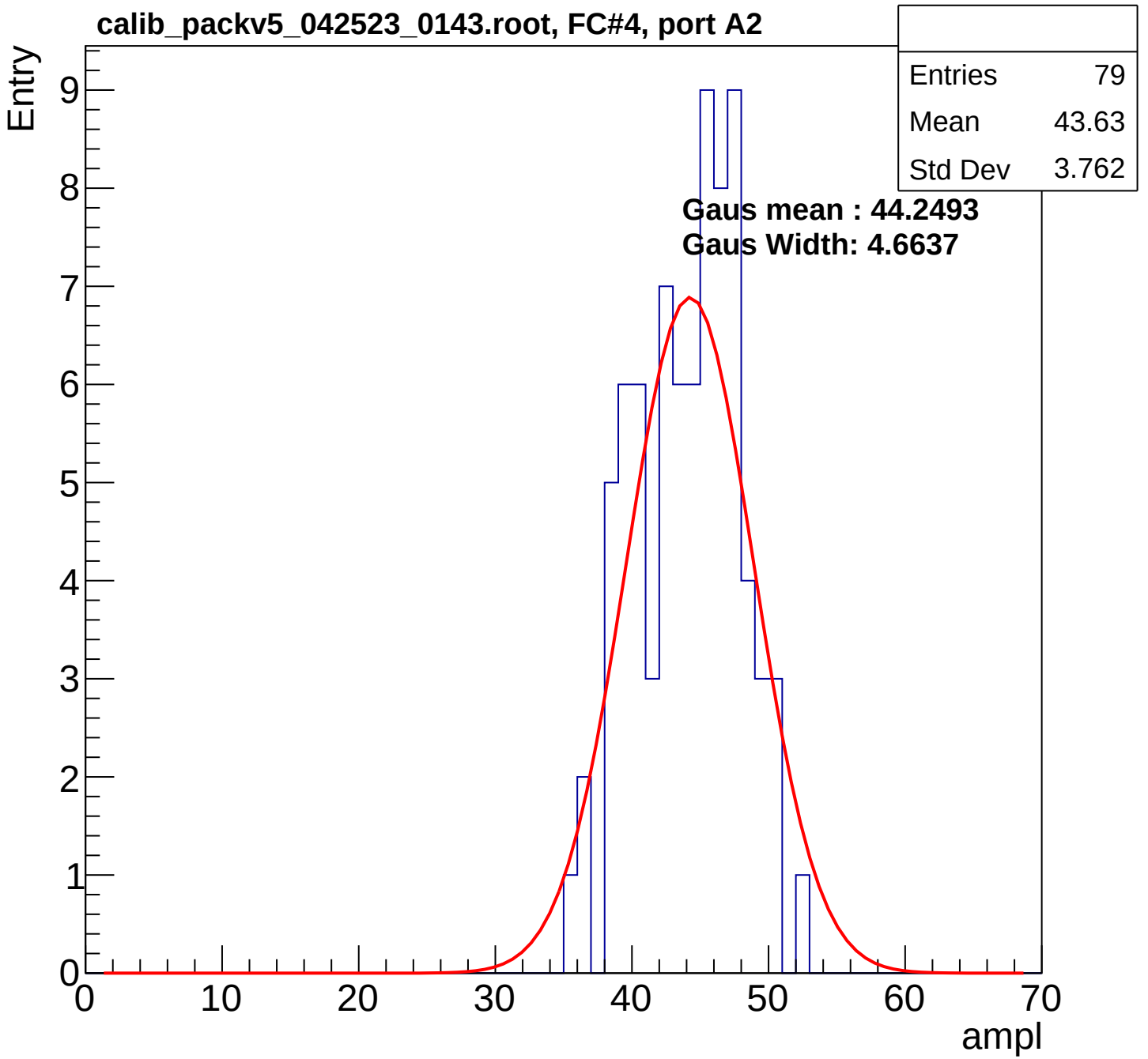

B1L100S, U23-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 30.07
Std Dev 3.206
Gaus mean : 30.2335
Gaus Width: 3.4968
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.91
Std Dev 3.619
Gaus mean : 37.2901
Gaus Width: 4.8154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 43.63
Std Dev 3.762
Gaus mean : 44.2493
Gaus Width: 4.6637
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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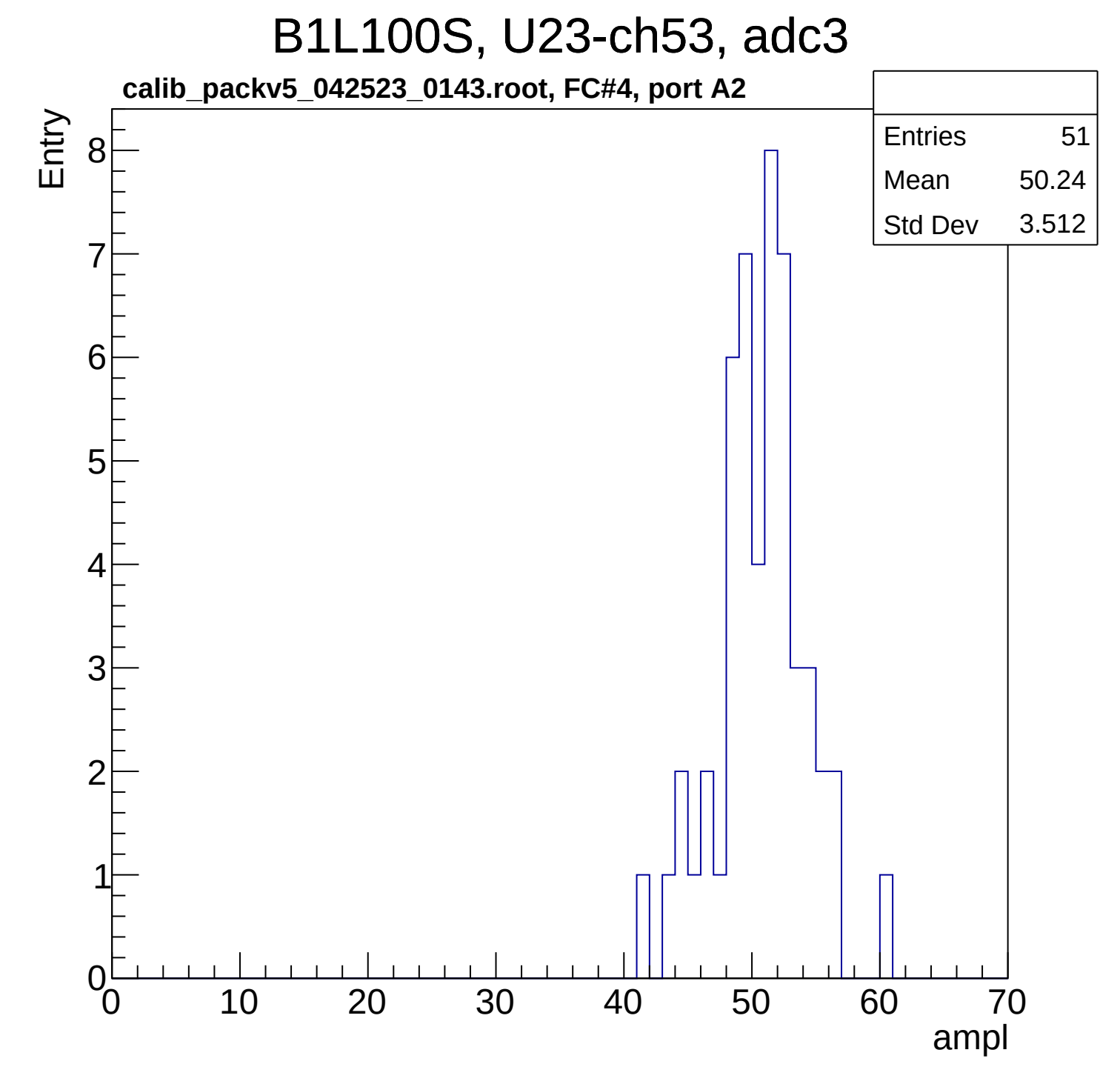
B1L100S, U23-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 50.24
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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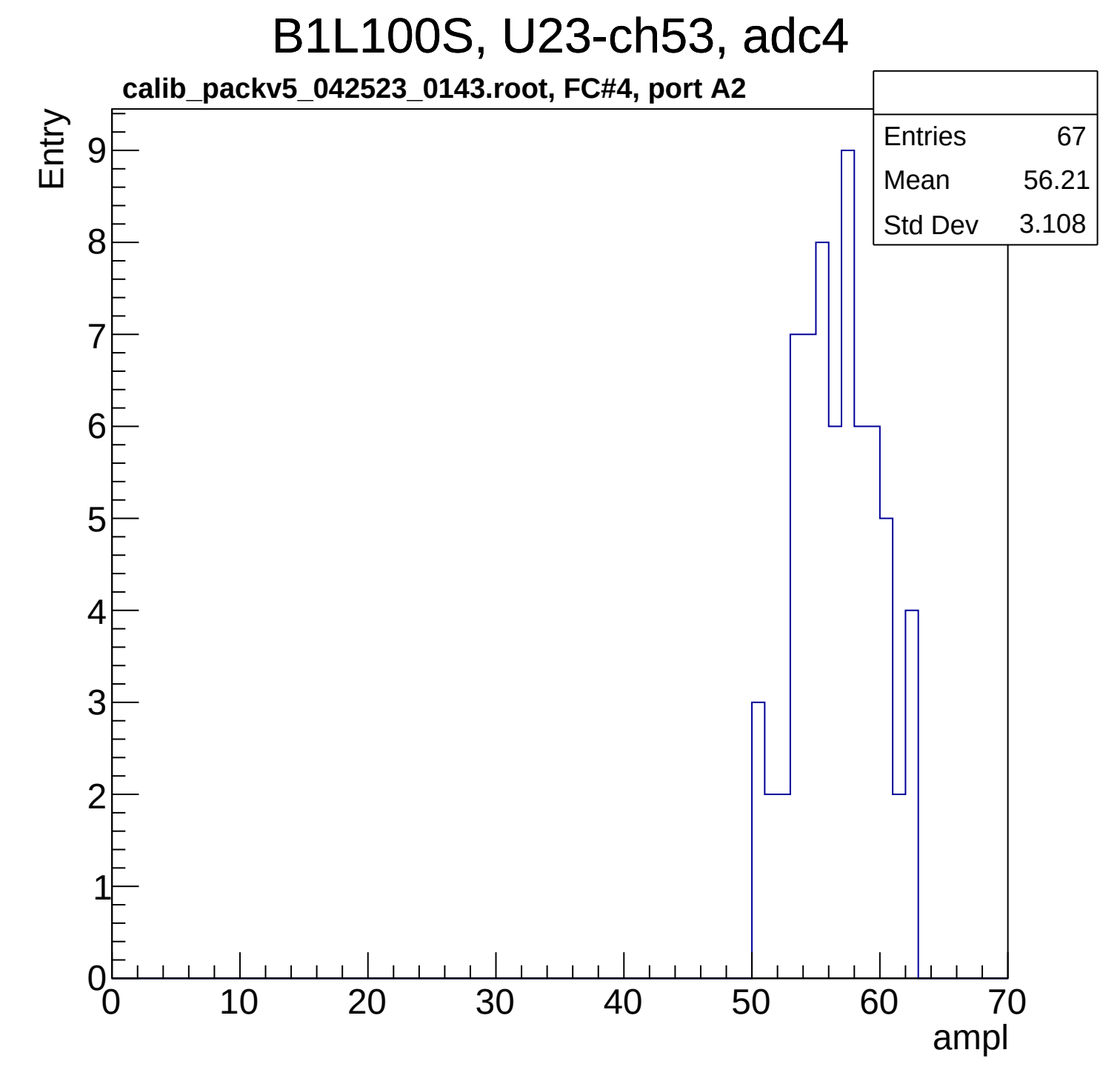
40
50
60
70


B1L100S, U23-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.21
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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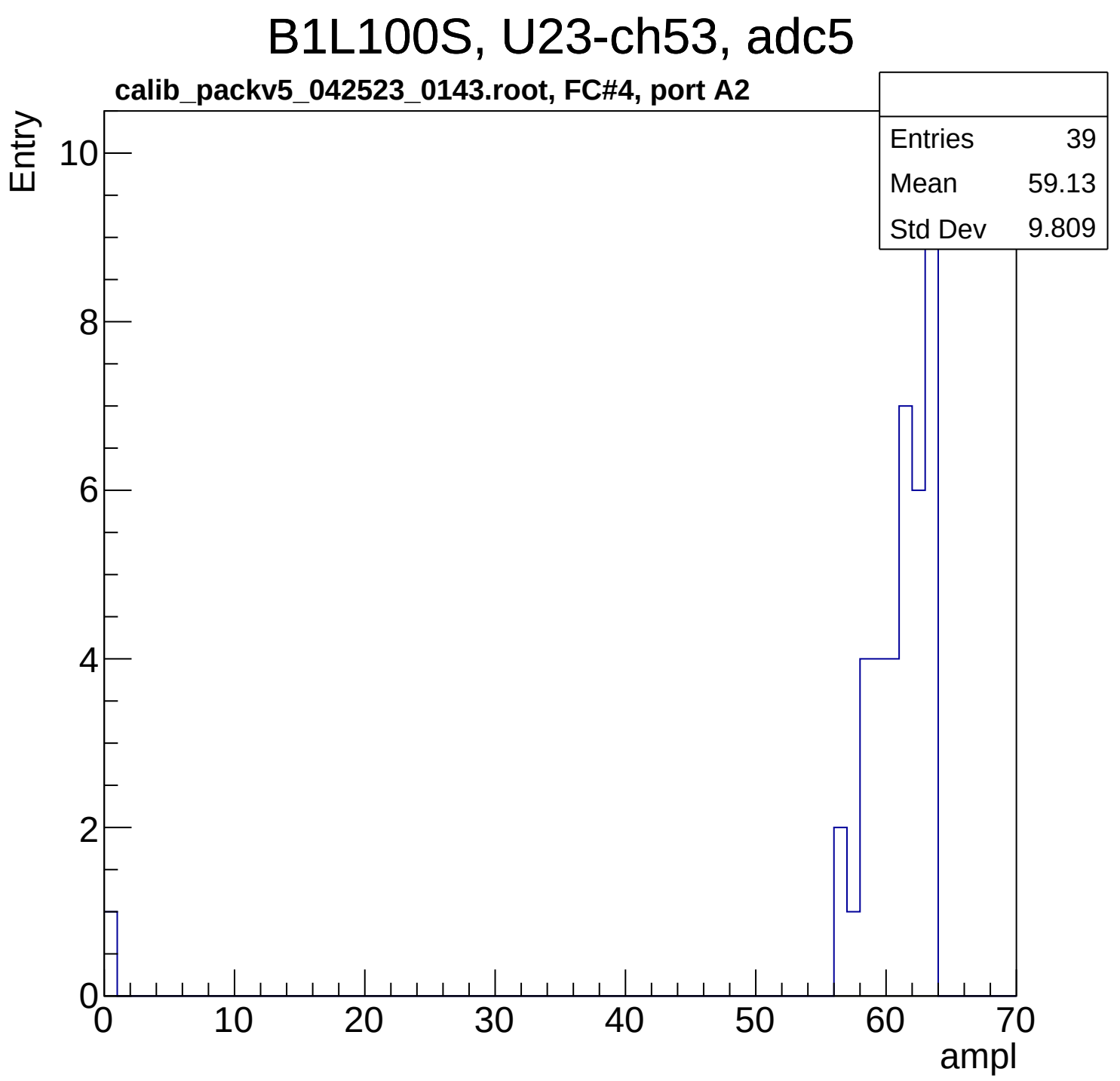
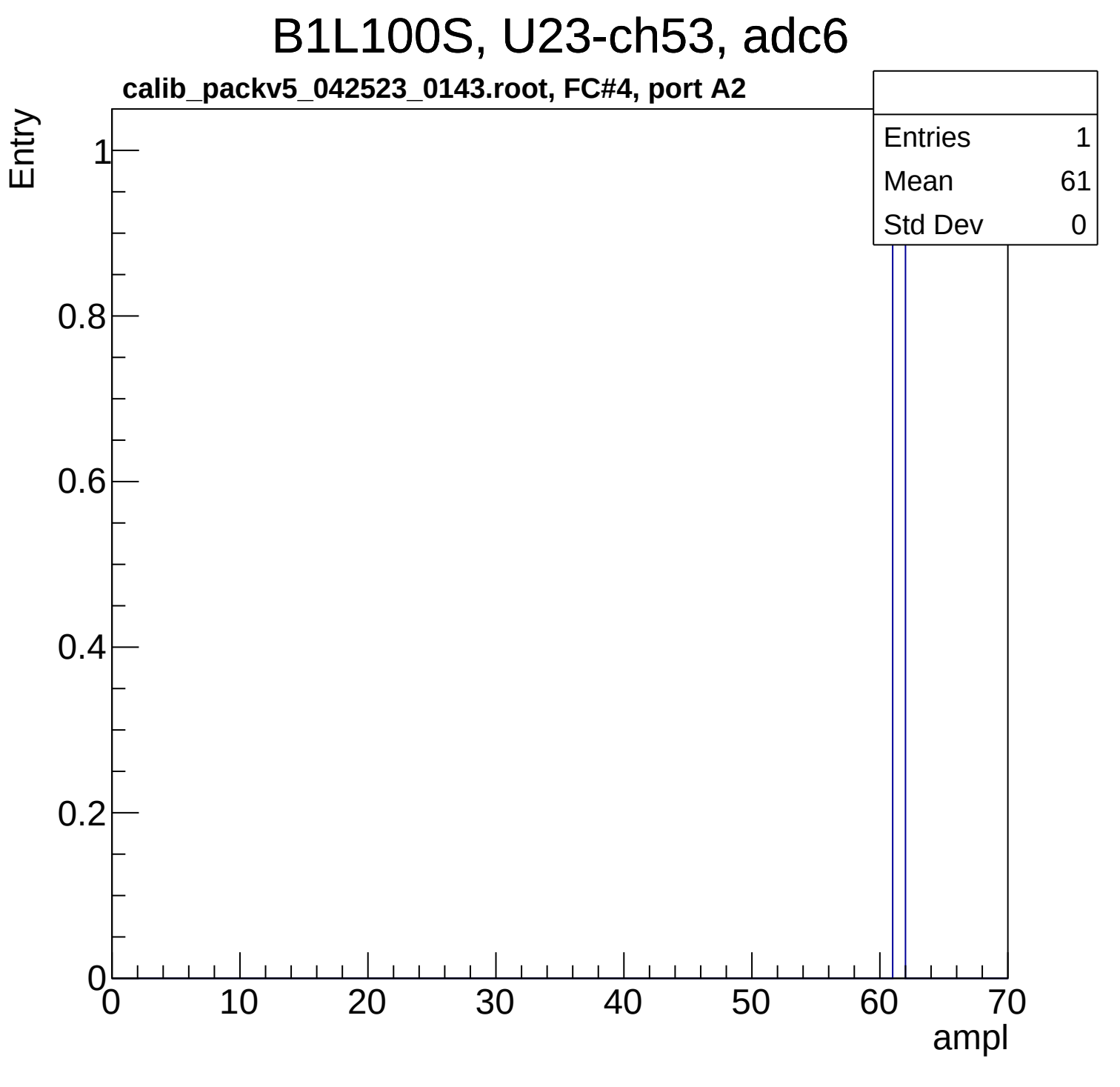

B1L100S, U23-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.13
Std Dev 9.809
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch53, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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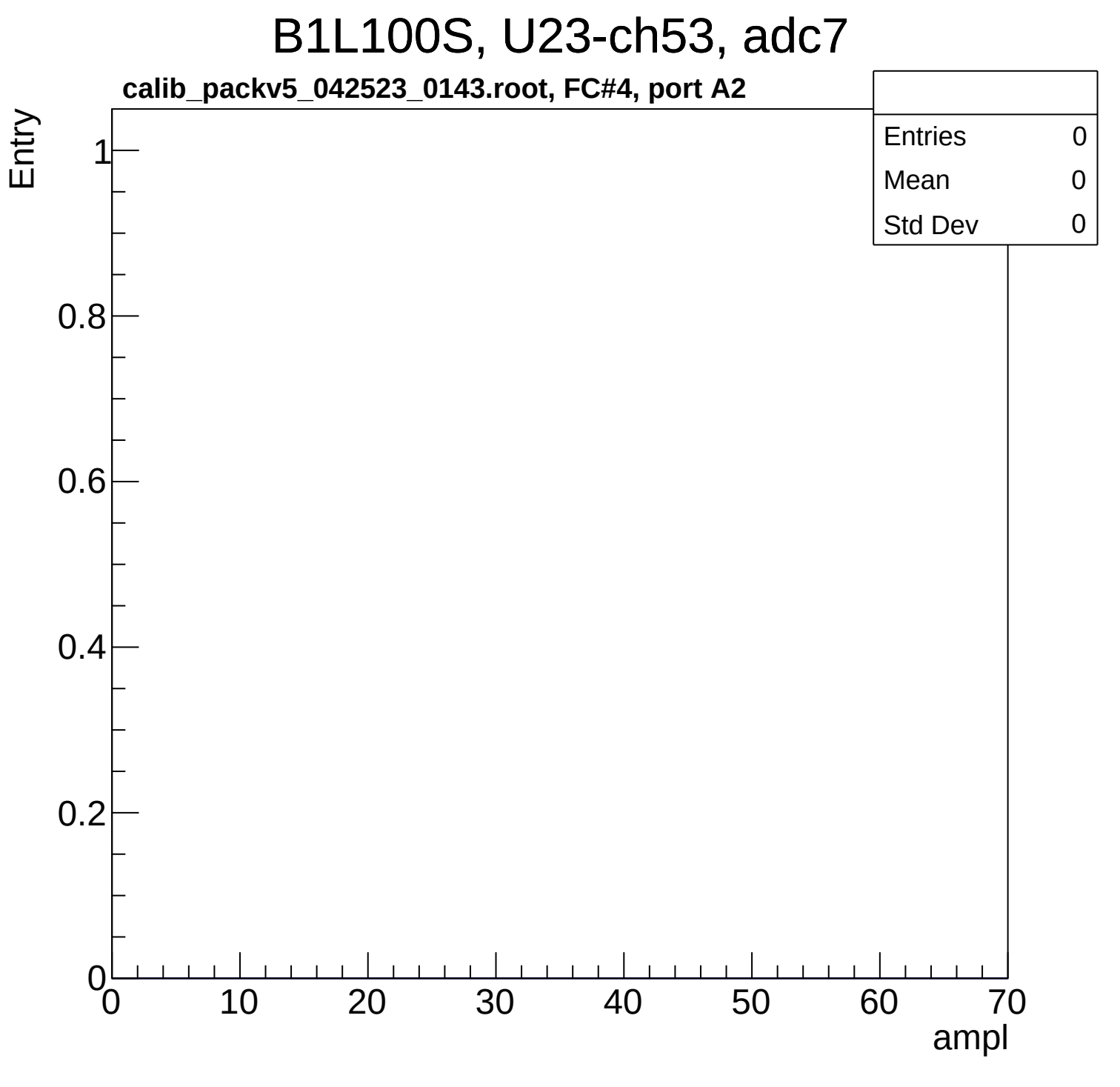
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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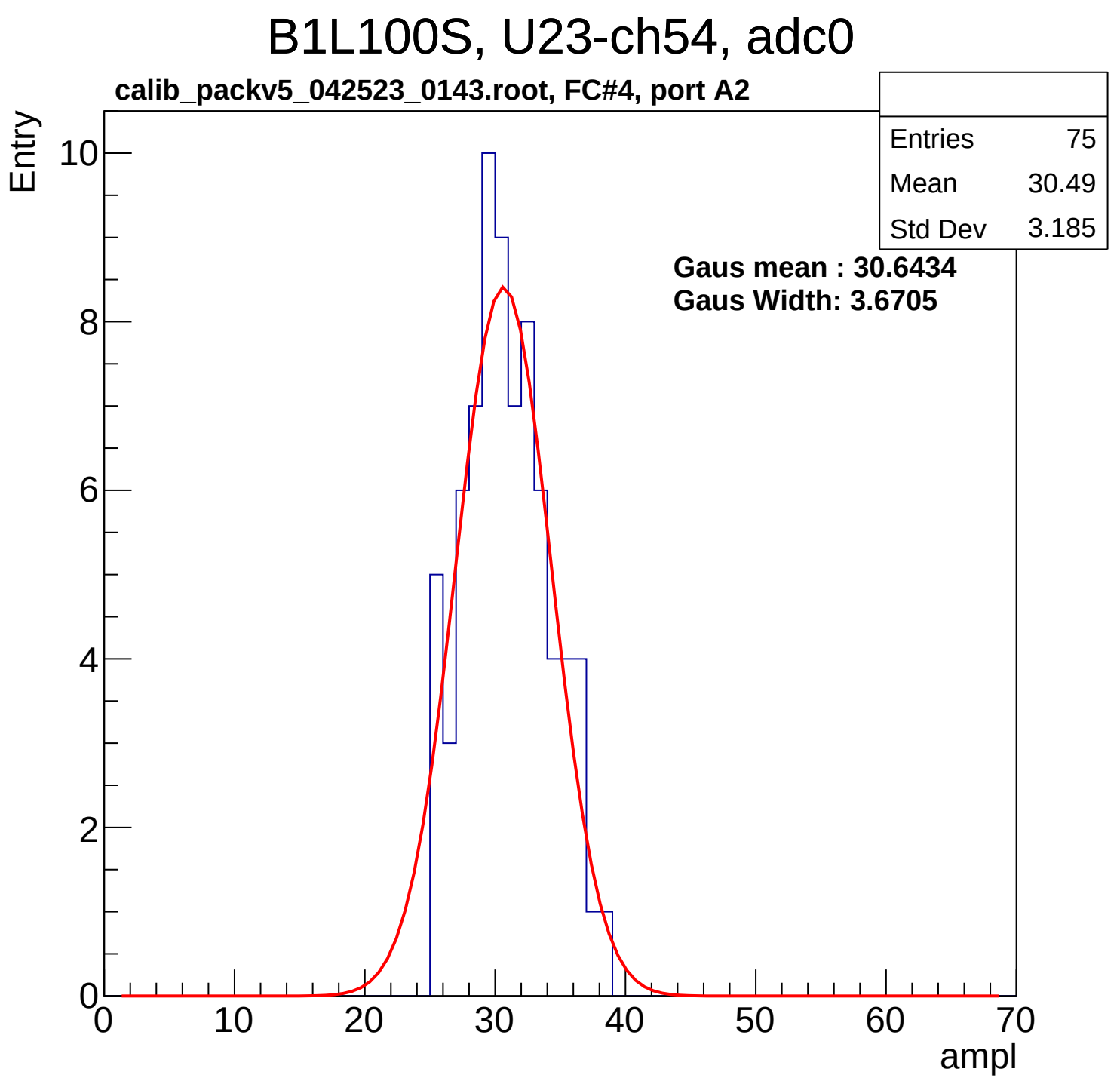
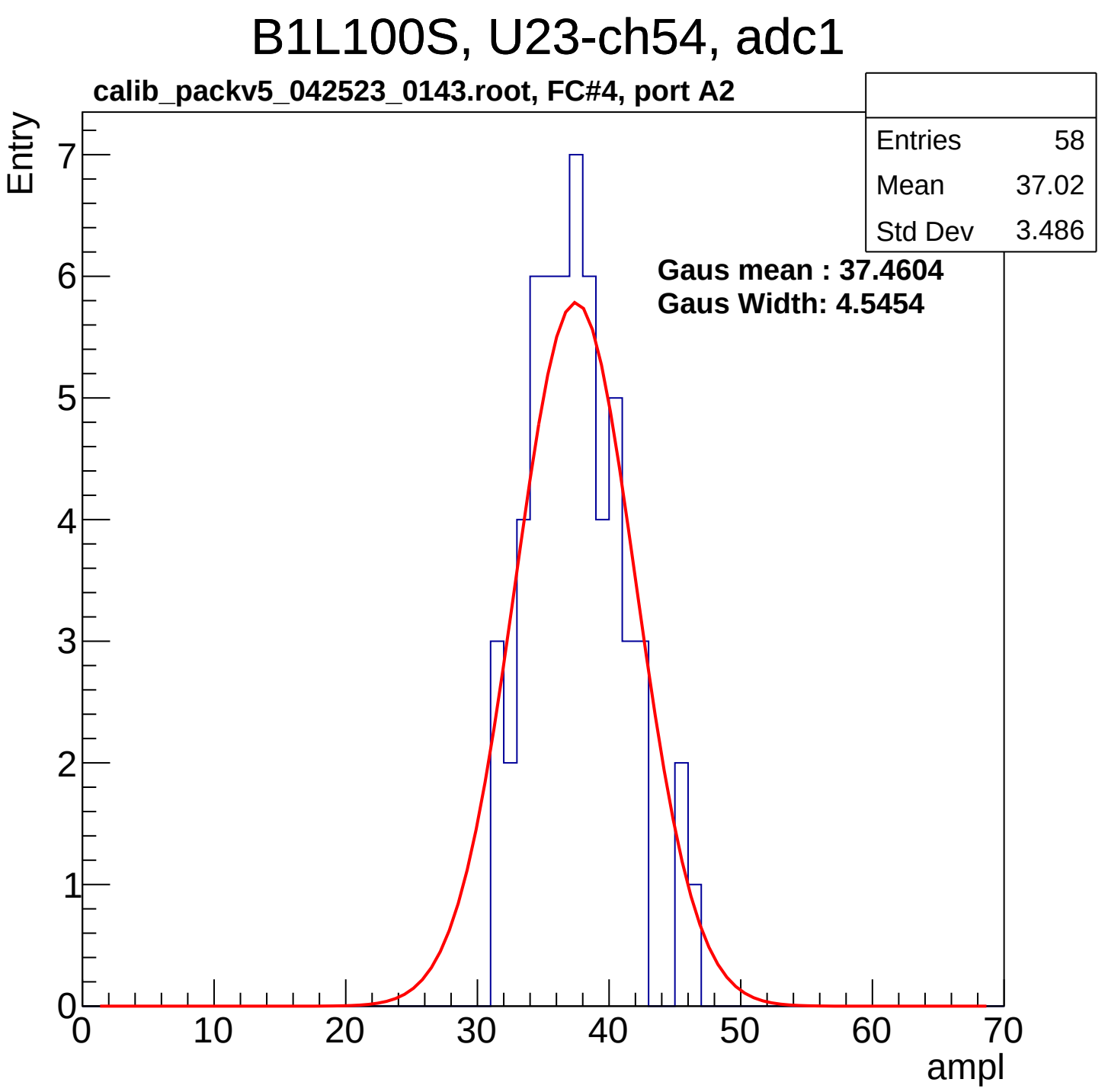
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.49
Std Dev 3.185
Gaus mean : 30.6434
Gaus Width: 3.6705
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 37.02
Std Dev 3.486
Gaus mean : 37.4604
Gaus Width: 4.5454
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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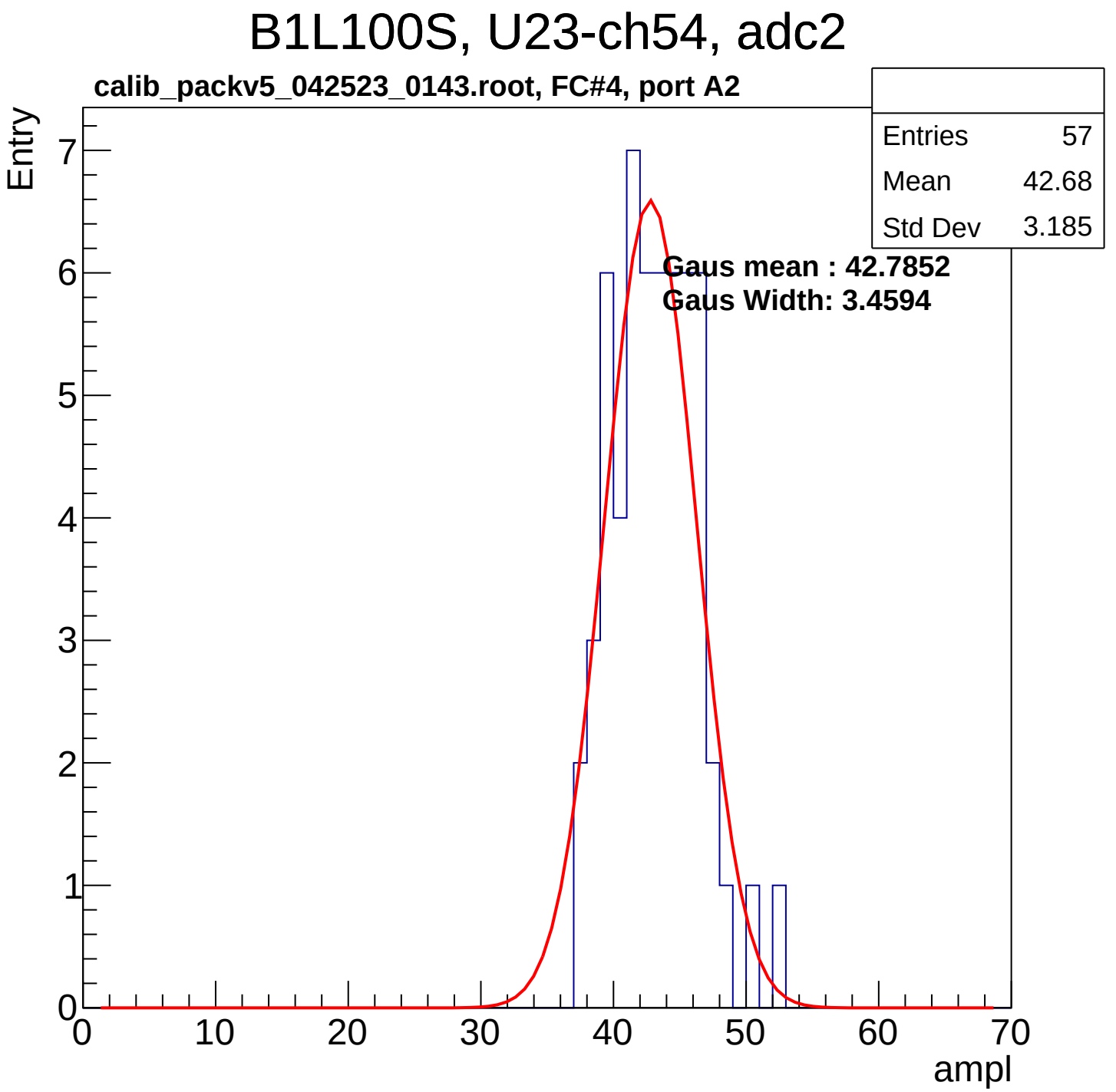
B1L100S, U23-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.68
Std Dev 3.185
Gaus mean : 42.7852
Gaus Width: 3.4594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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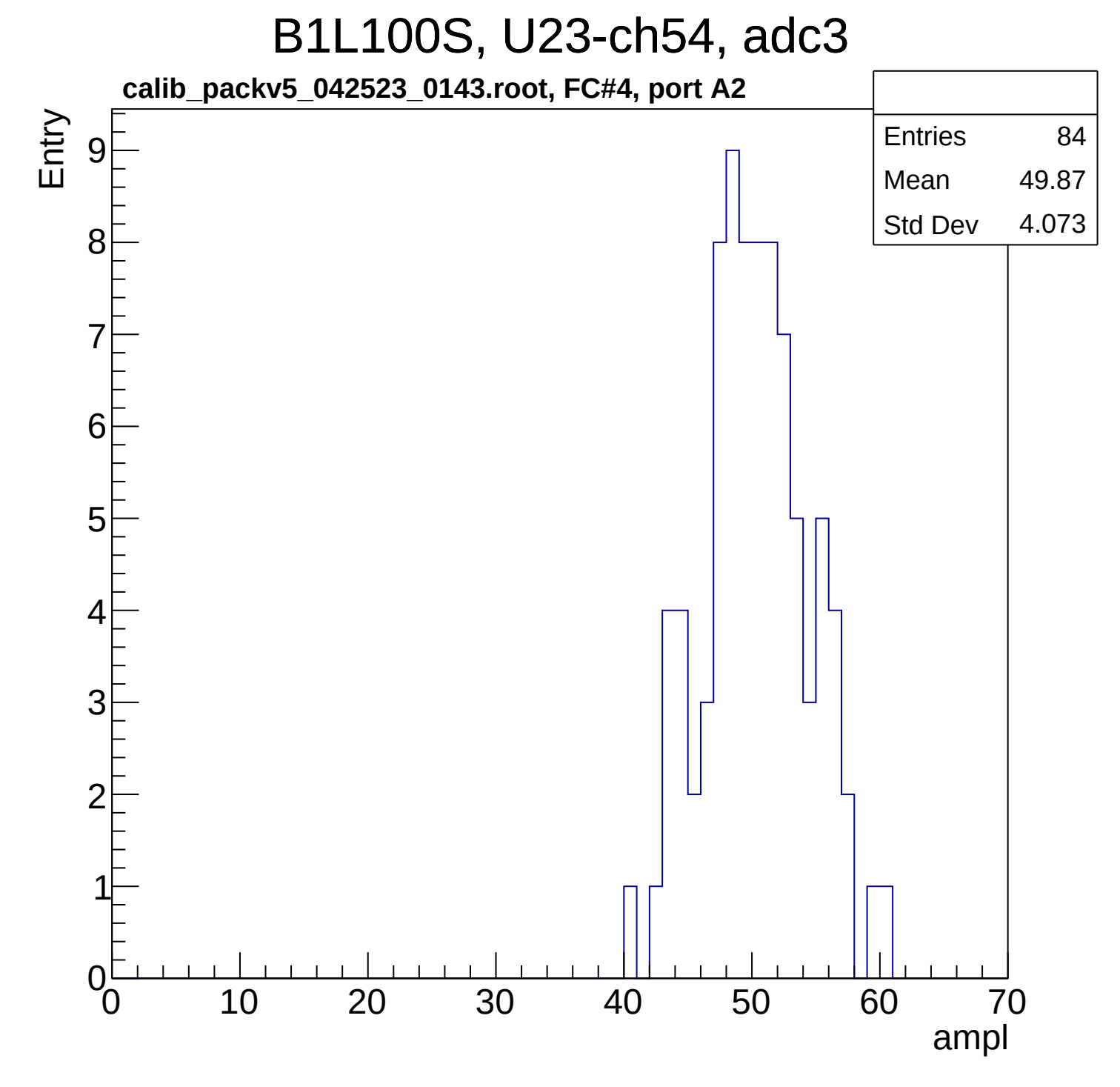
B1L100S, U23-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 49.87
Std Dev 4.073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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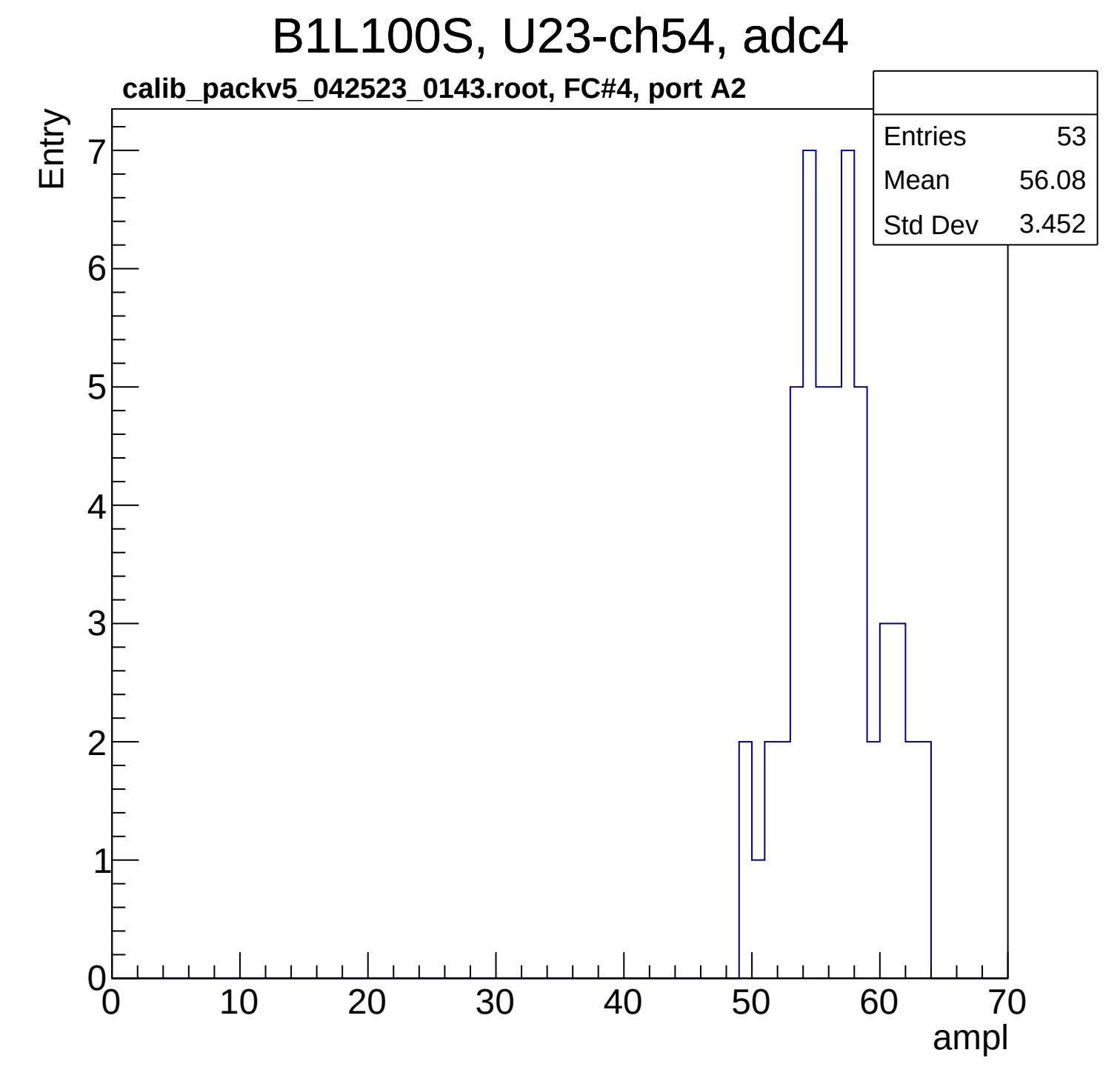
B1L100S, U23-ch54, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.08
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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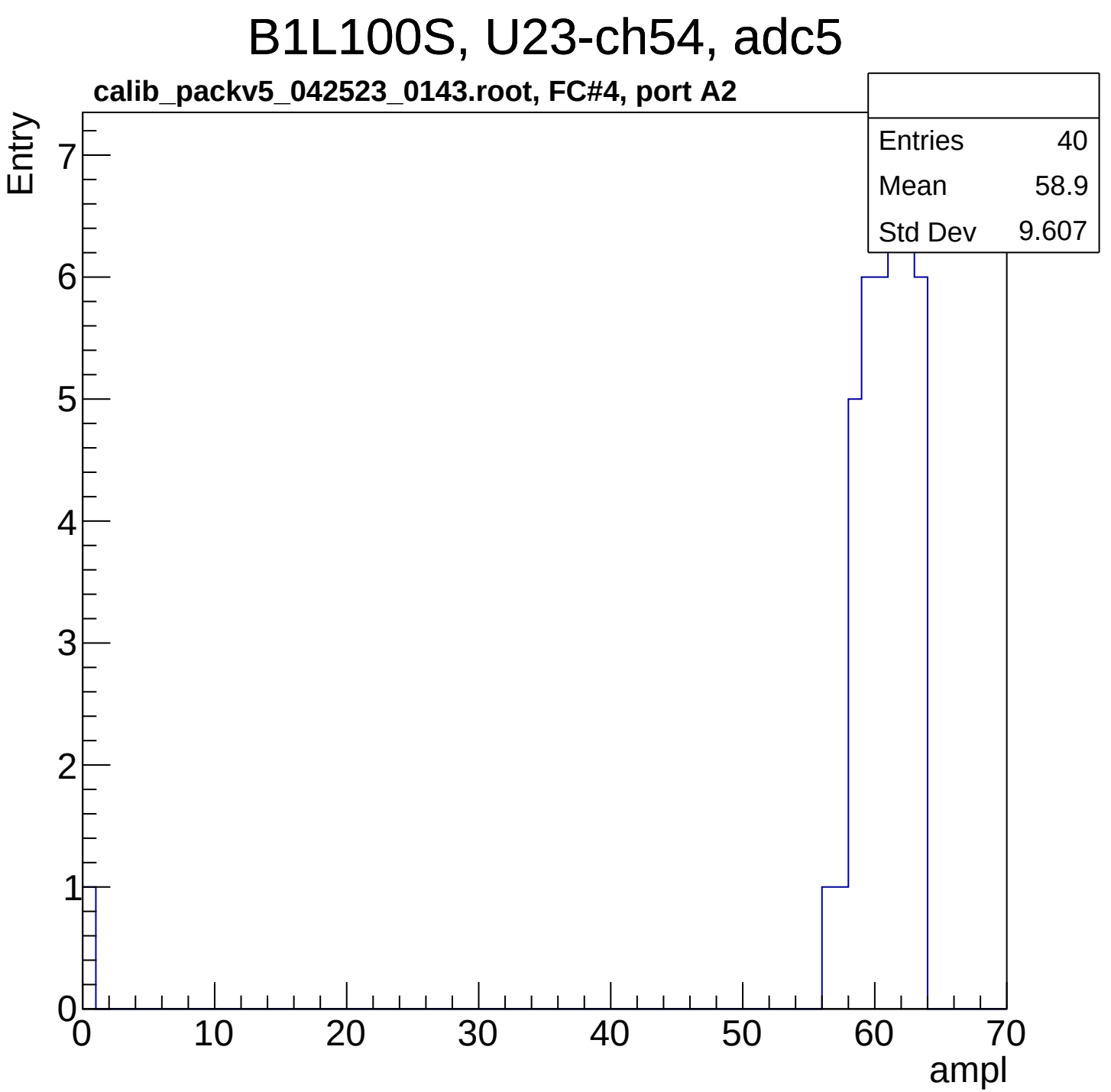
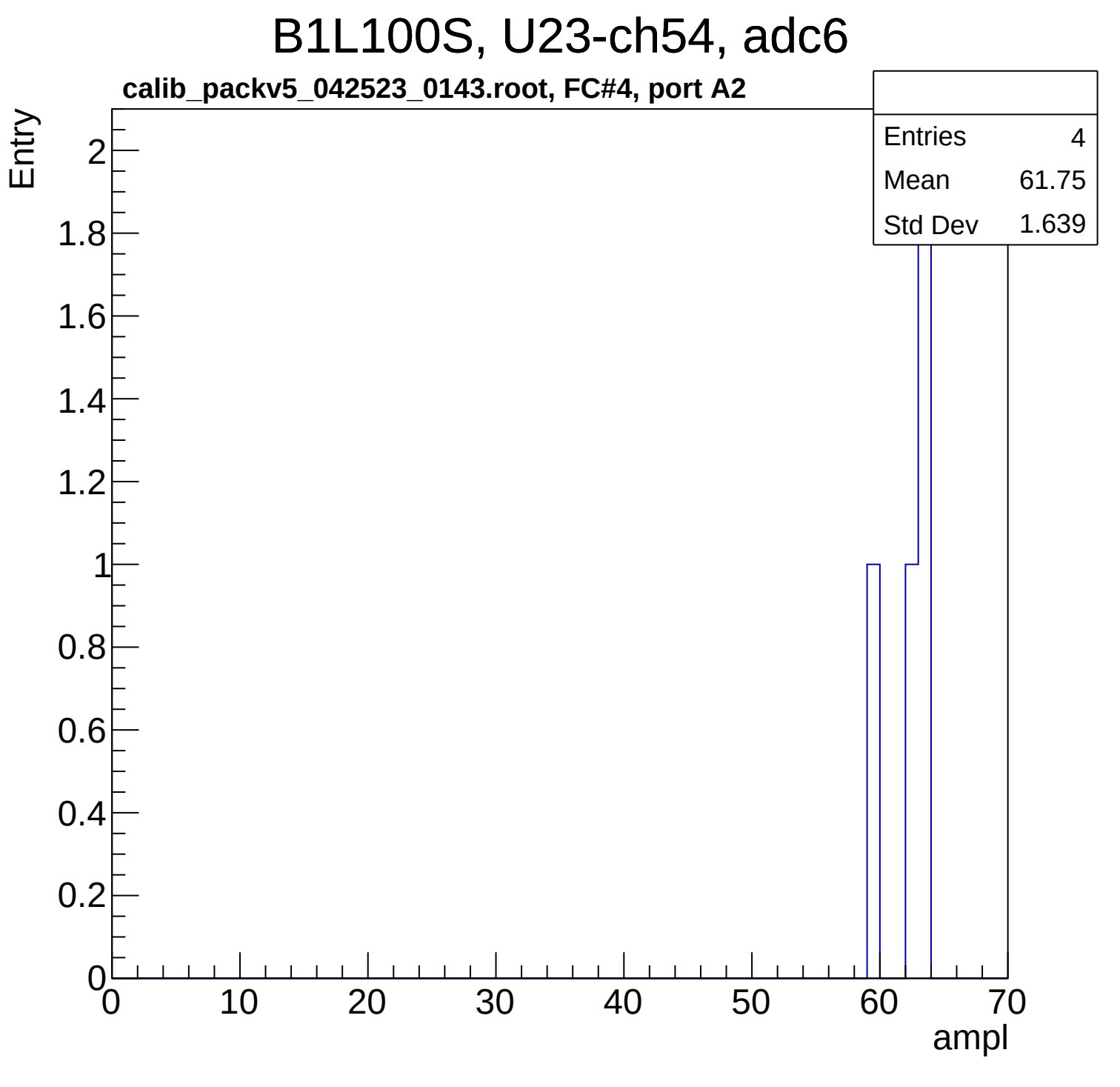

B1L100S, U23-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.9
Std Dev 9.607
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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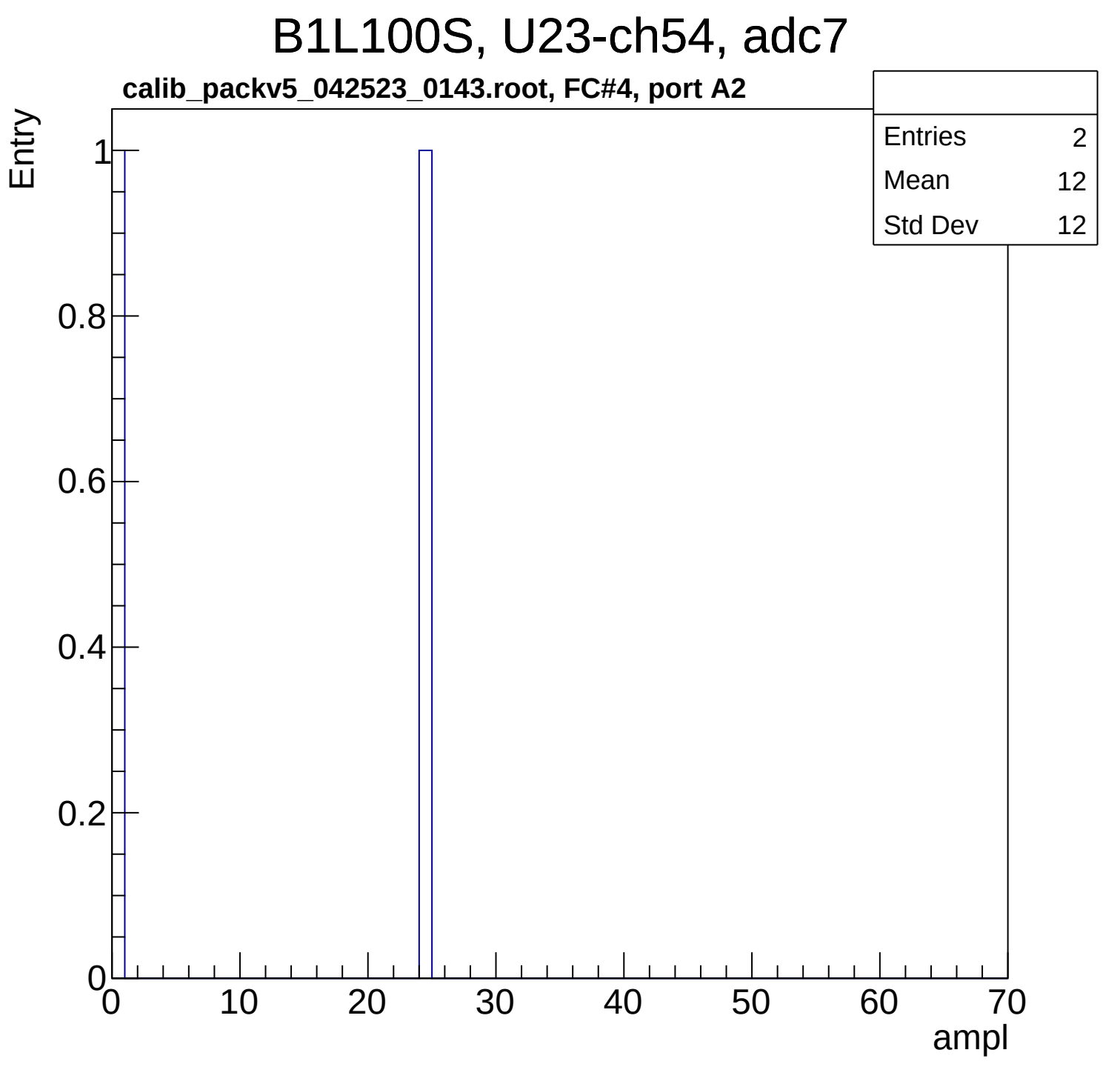
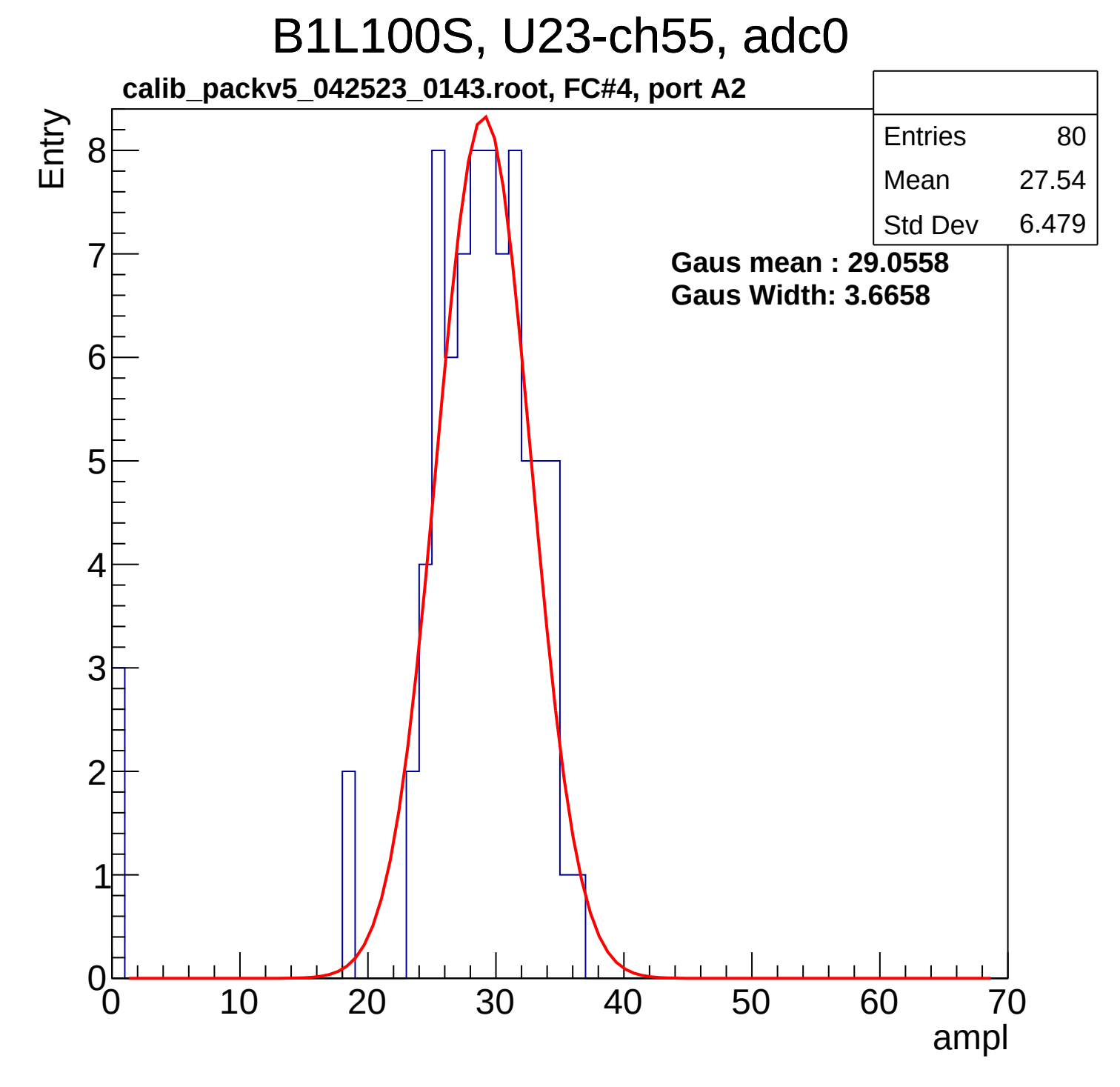

B1L100S, U23-ch54, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 27.54
Std Dev 6.479
Gaus mean : 29.0558
Gaus Width: 3.6658
Entry
ampl
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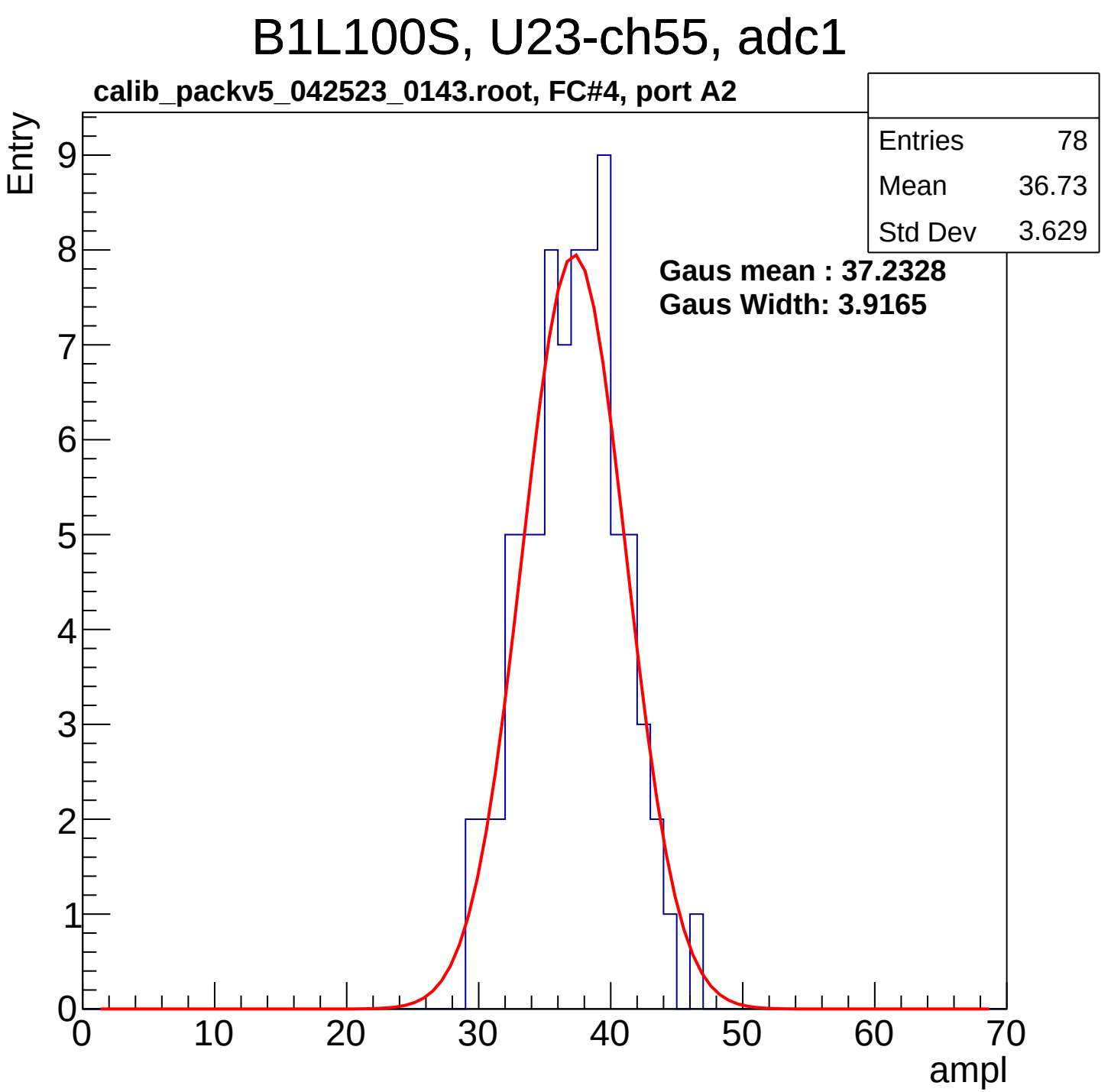
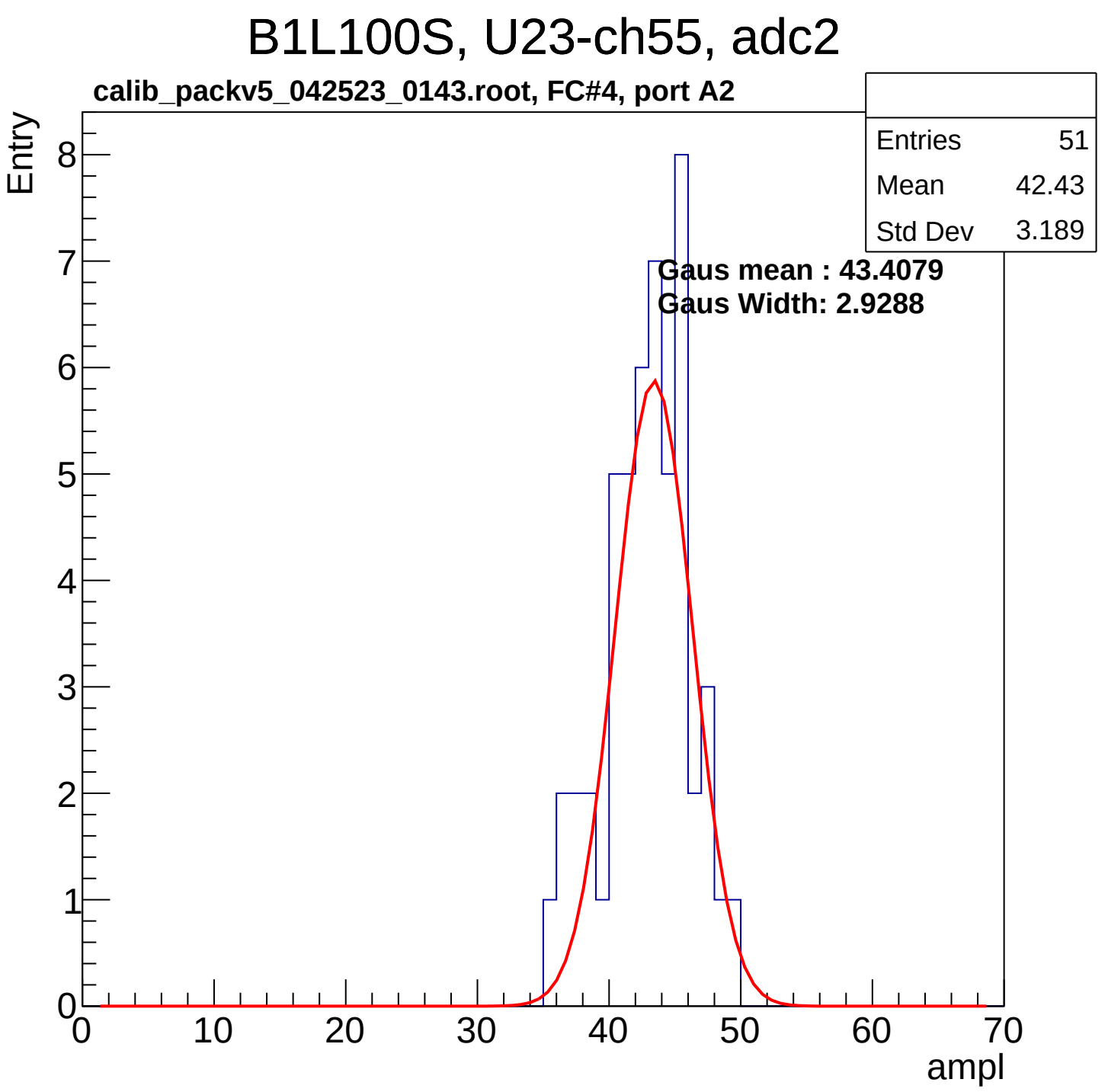
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.73
Std Dev 3.629
Gaus mean : 37.2328
Gaus Width: 3.9165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch55, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 42.43
Std Dev 3.189
Gaus mean : 43.4079
Gaus Width: 2.9288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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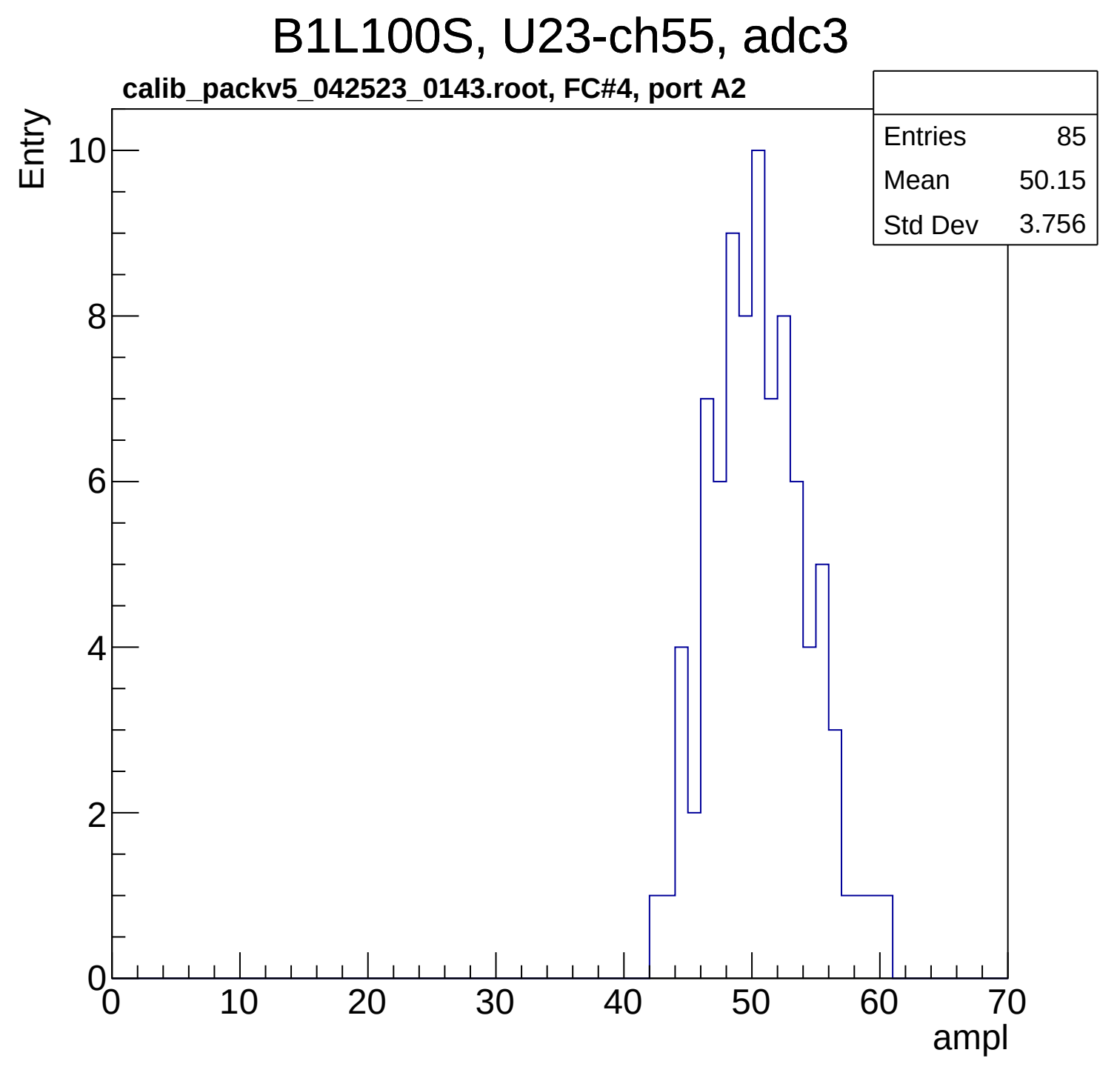
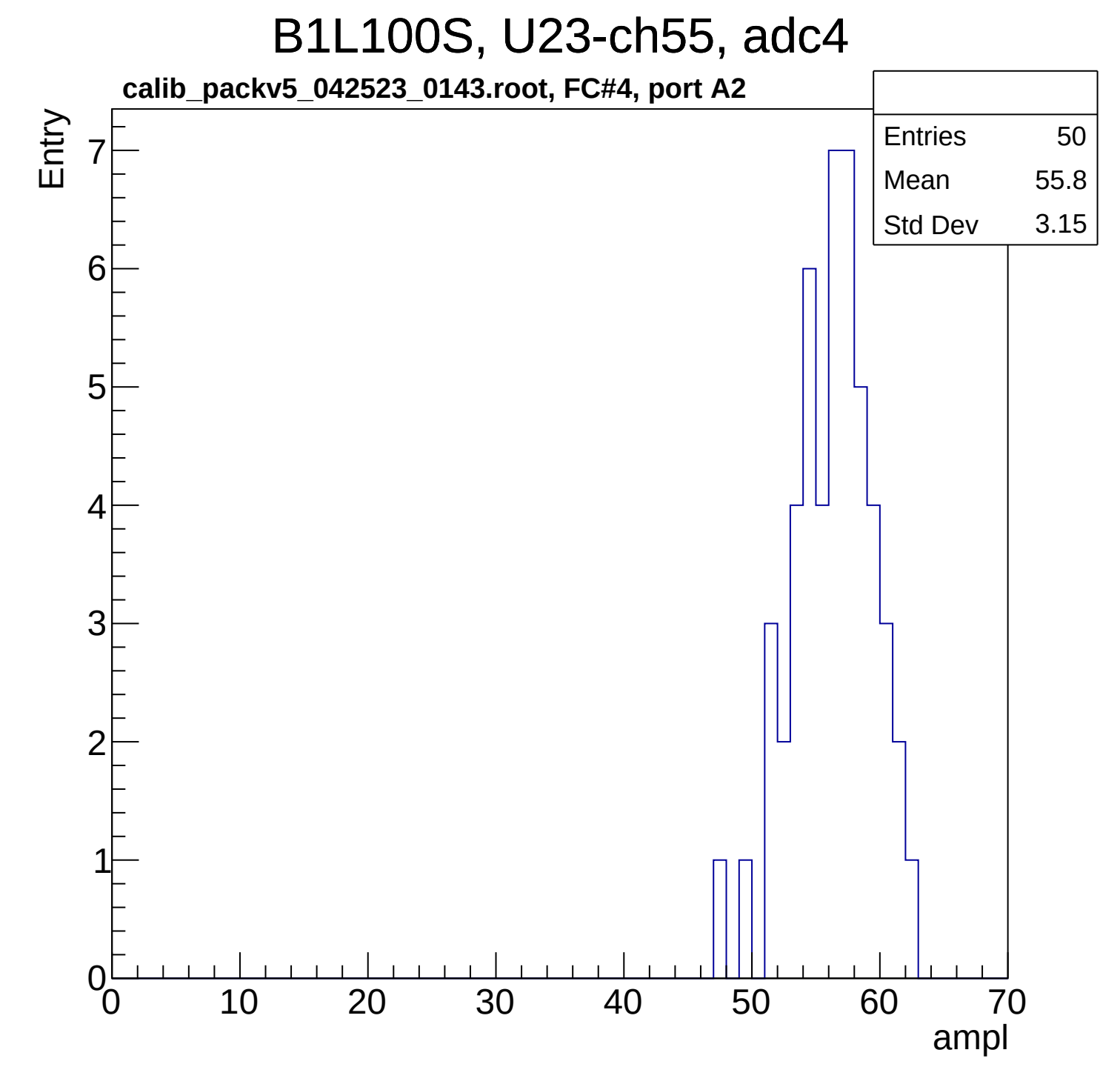
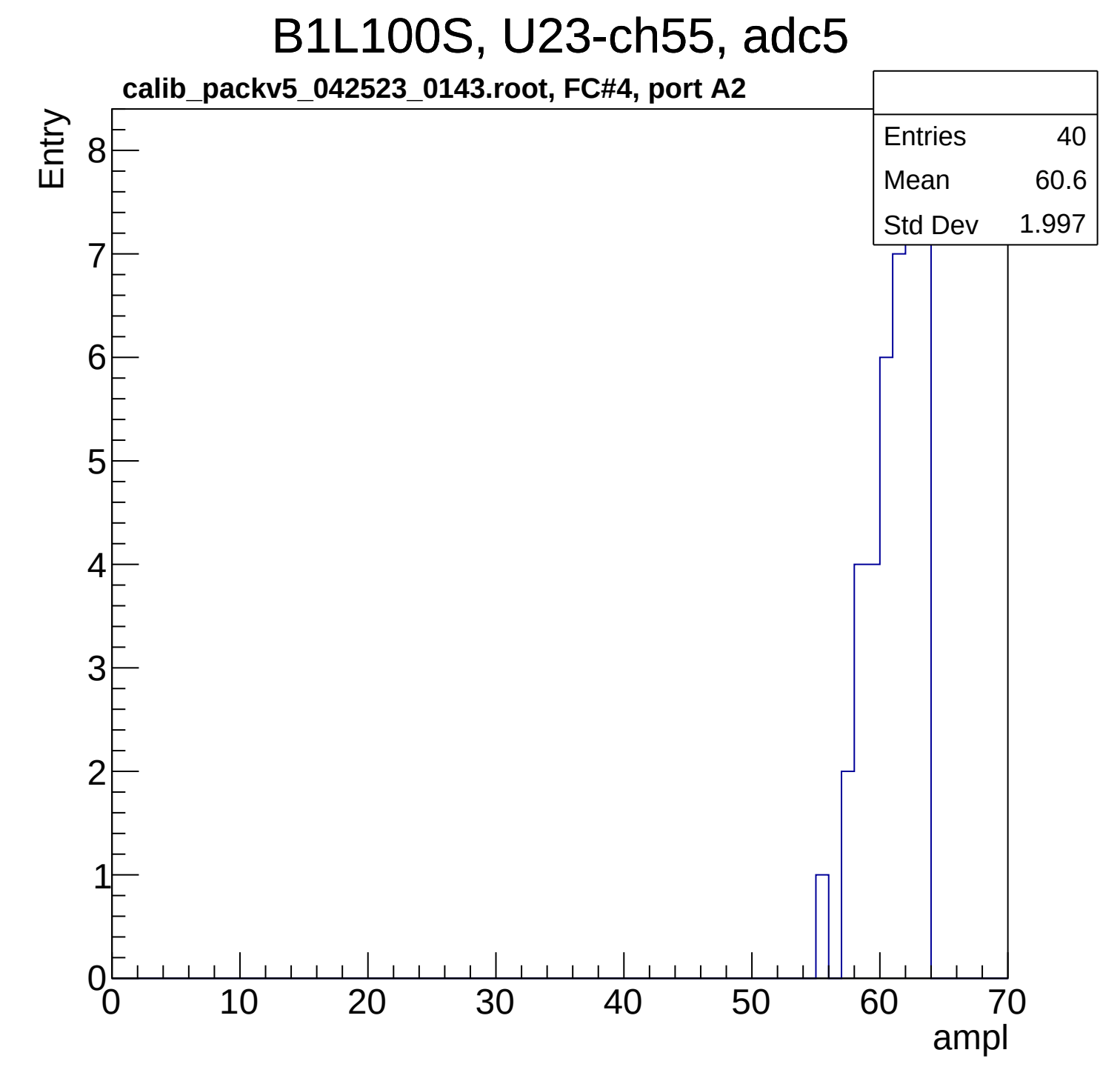
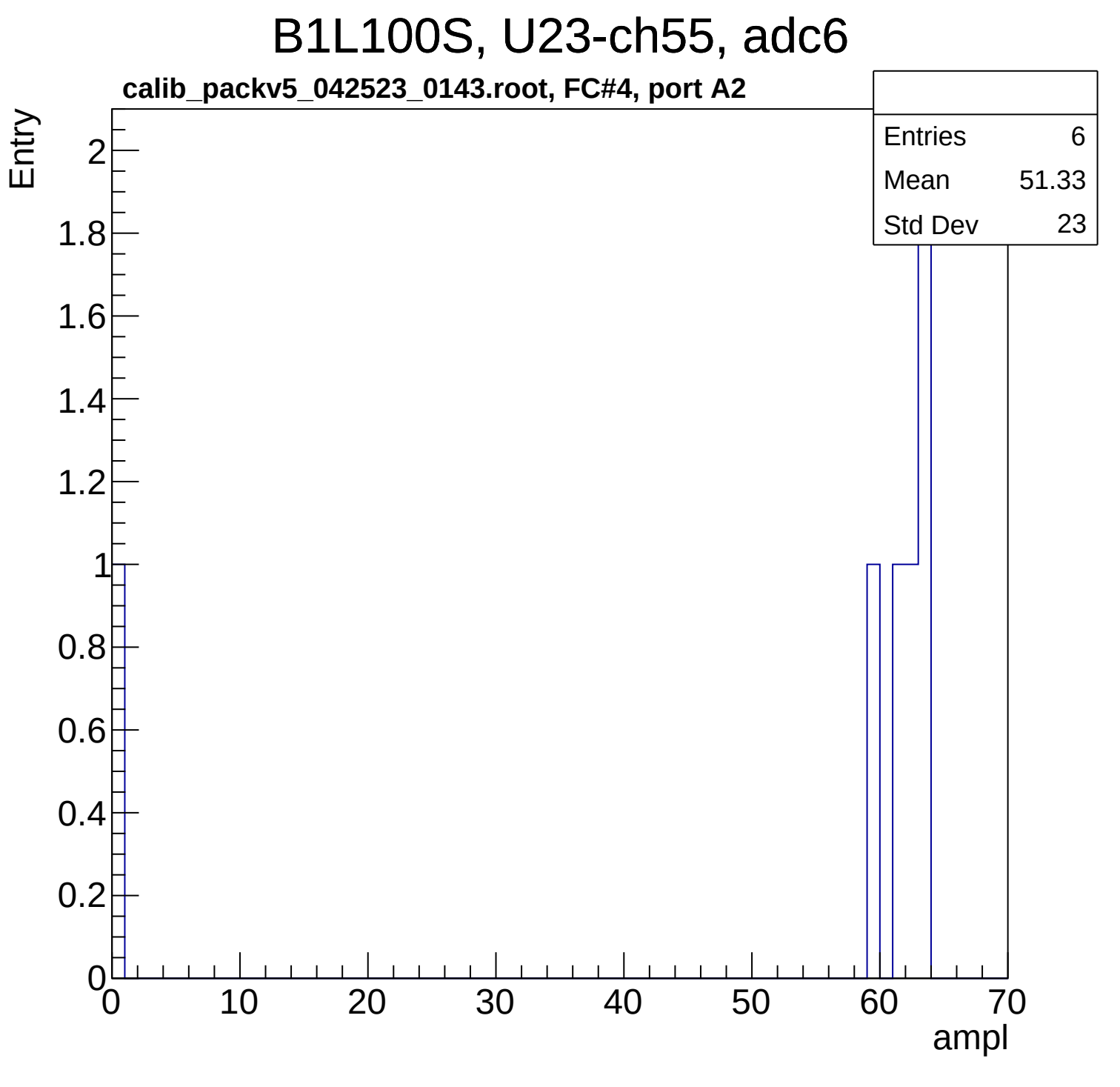
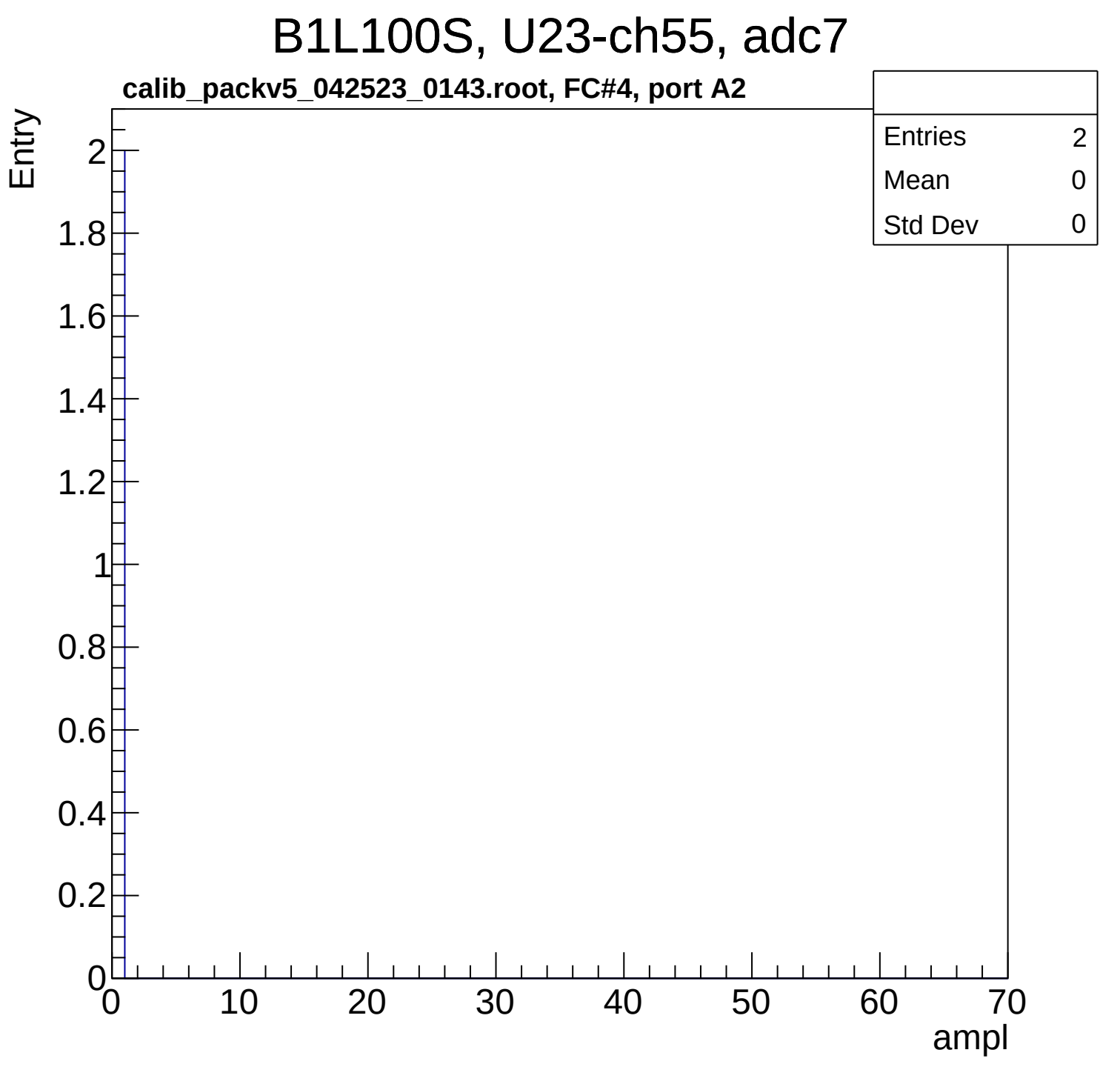
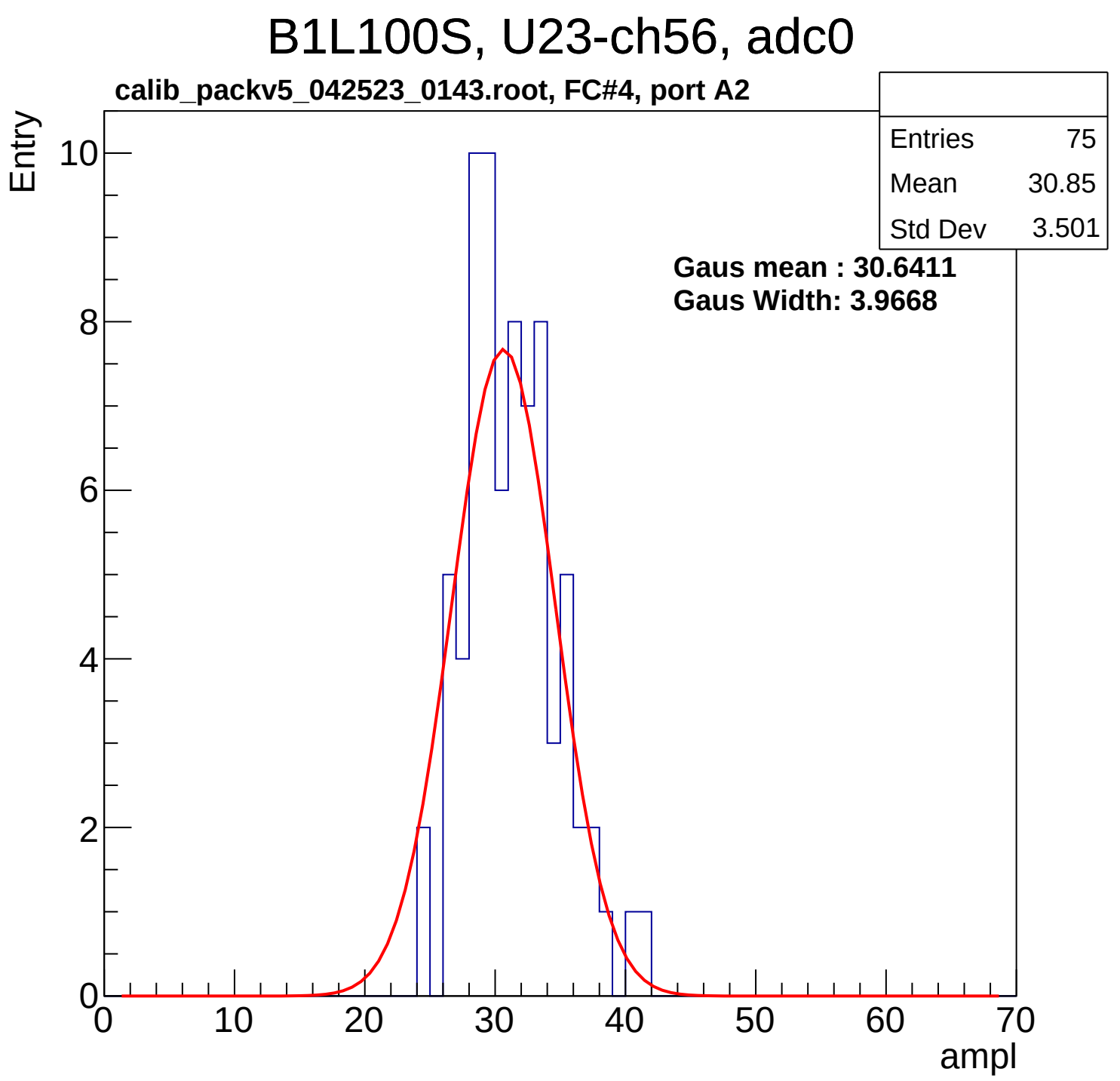

B1L100S, U23-ch55, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 50.15
Std Dev 3.756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

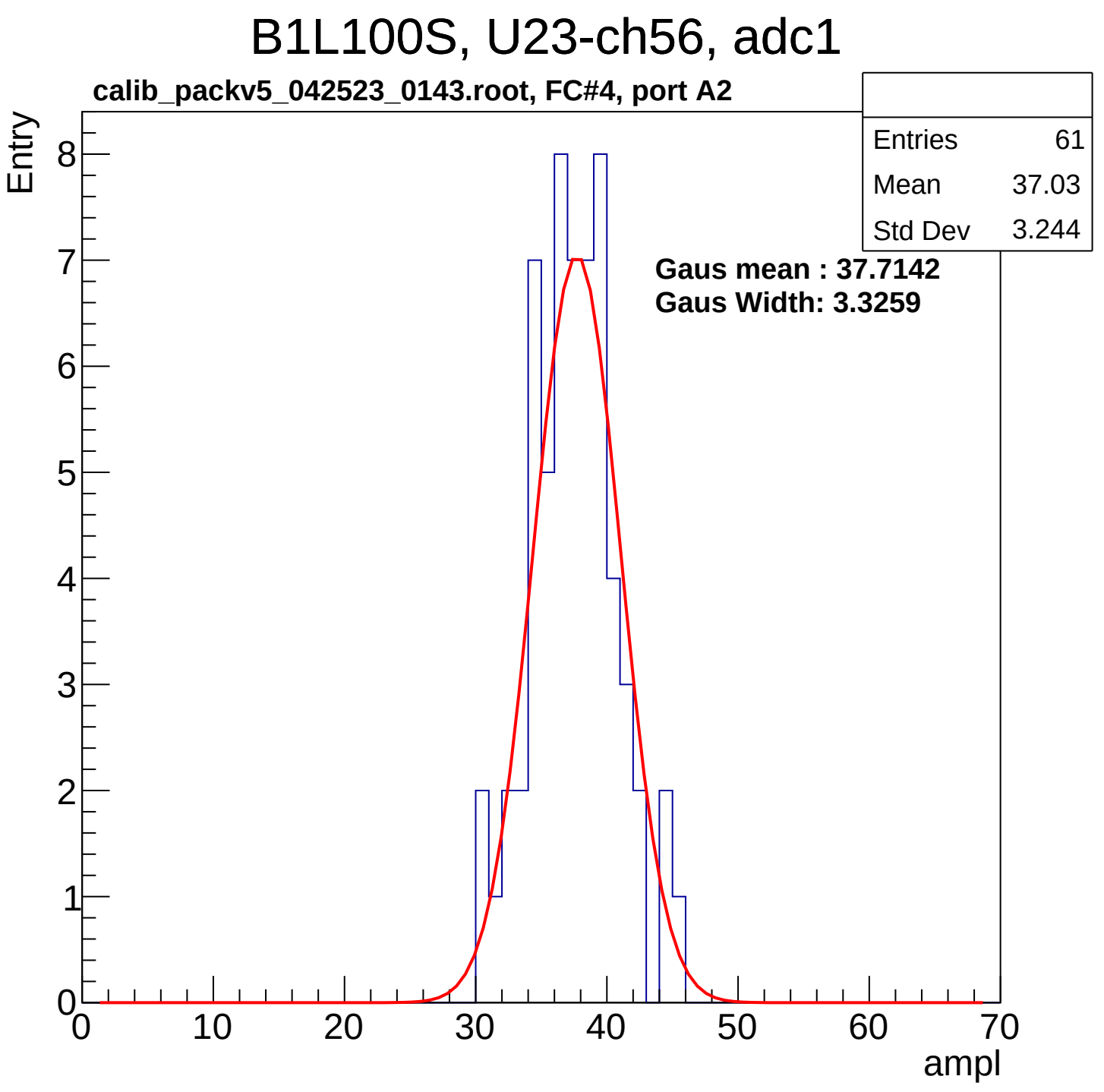

B1L100S, U23-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.8
Std Dev 3.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

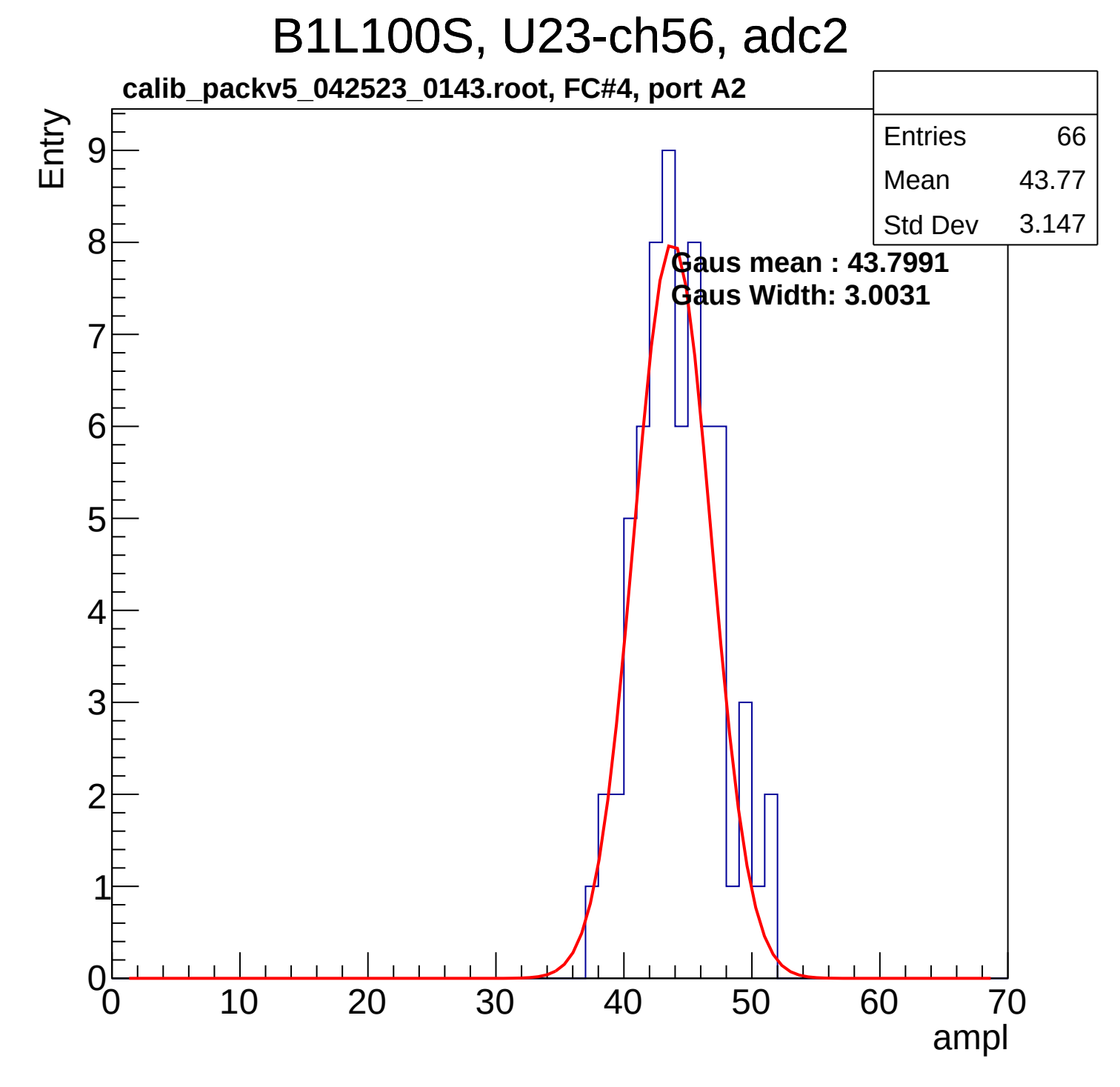

B1L100S, U23-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 60.6
Std Dev 1.997
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

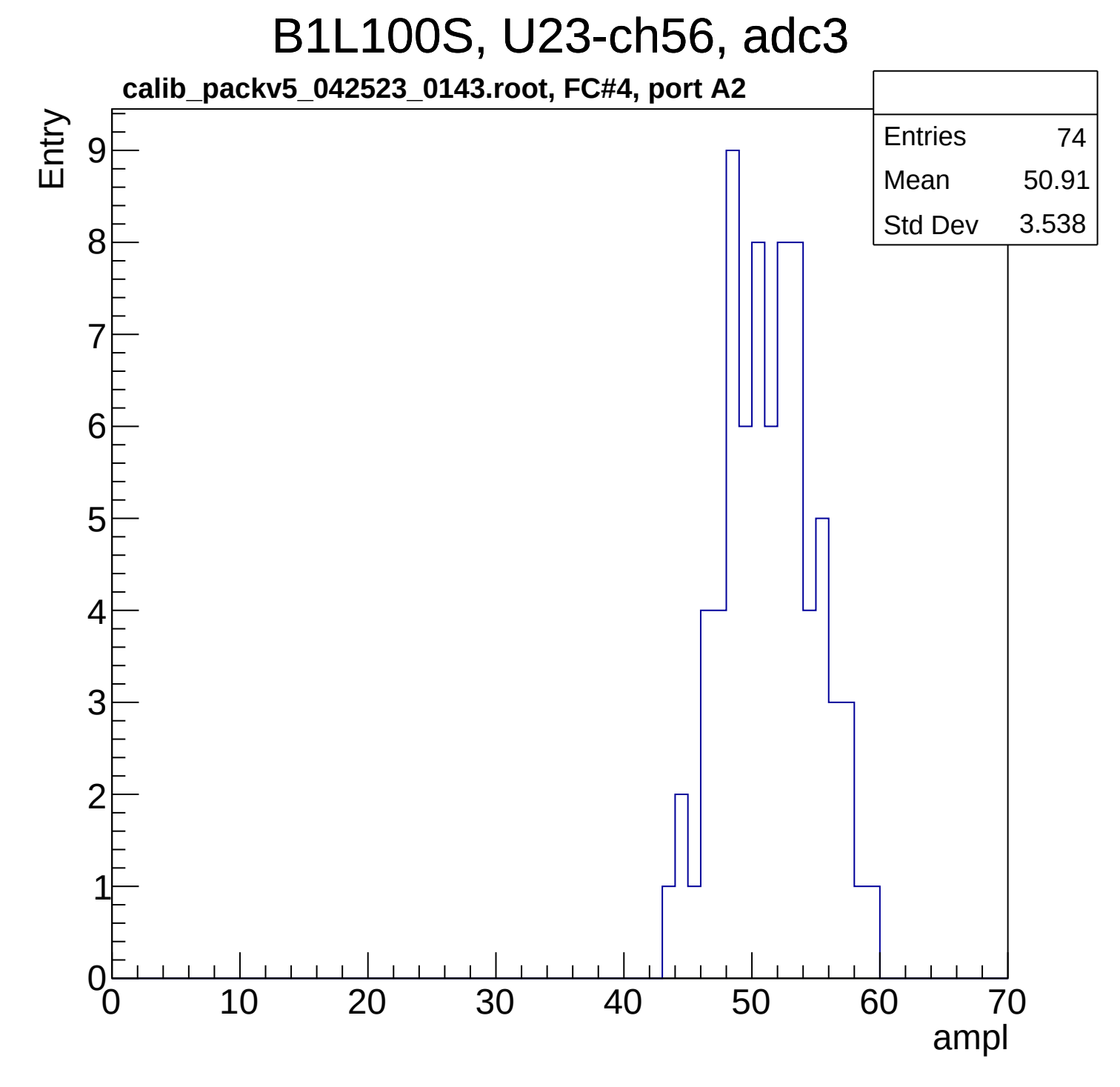

B1L100S, U23-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.33
Std Dev 23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch55, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.85
Std Dev 3.501
Gaus mean : 30.6411
Gaus Width: 3.9668
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.03
Std Dev 3.244
Gaus mean : 37.7142
Gaus Width: 3.3259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.77
Std Dev 3.147
Gaus mean : 43.7991
Gaus Width: 3.0031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.91
Std Dev 3.538
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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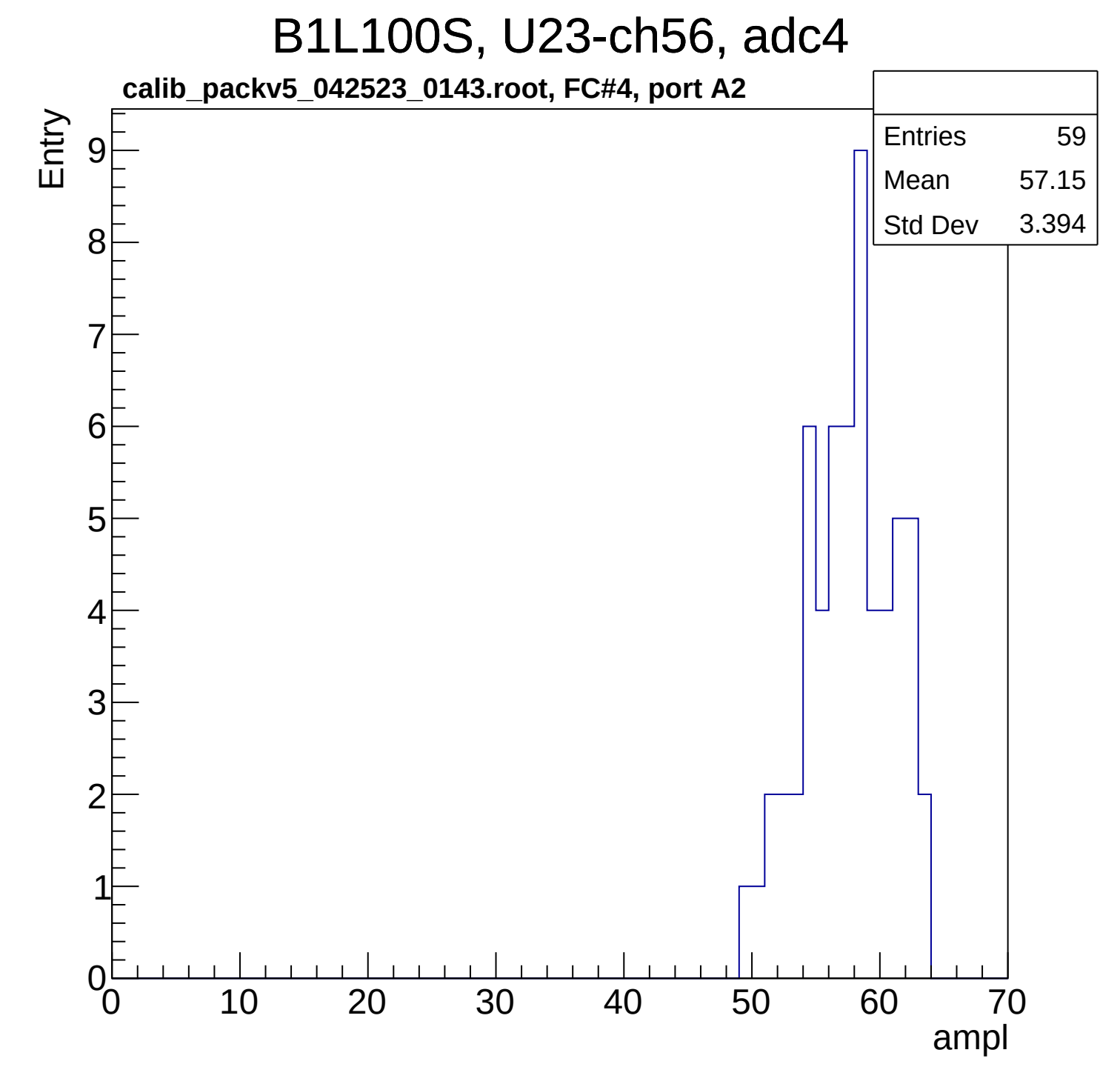
B1L100S, U23-ch56, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 57.15
Std Dev 3.394
Entry
ampl
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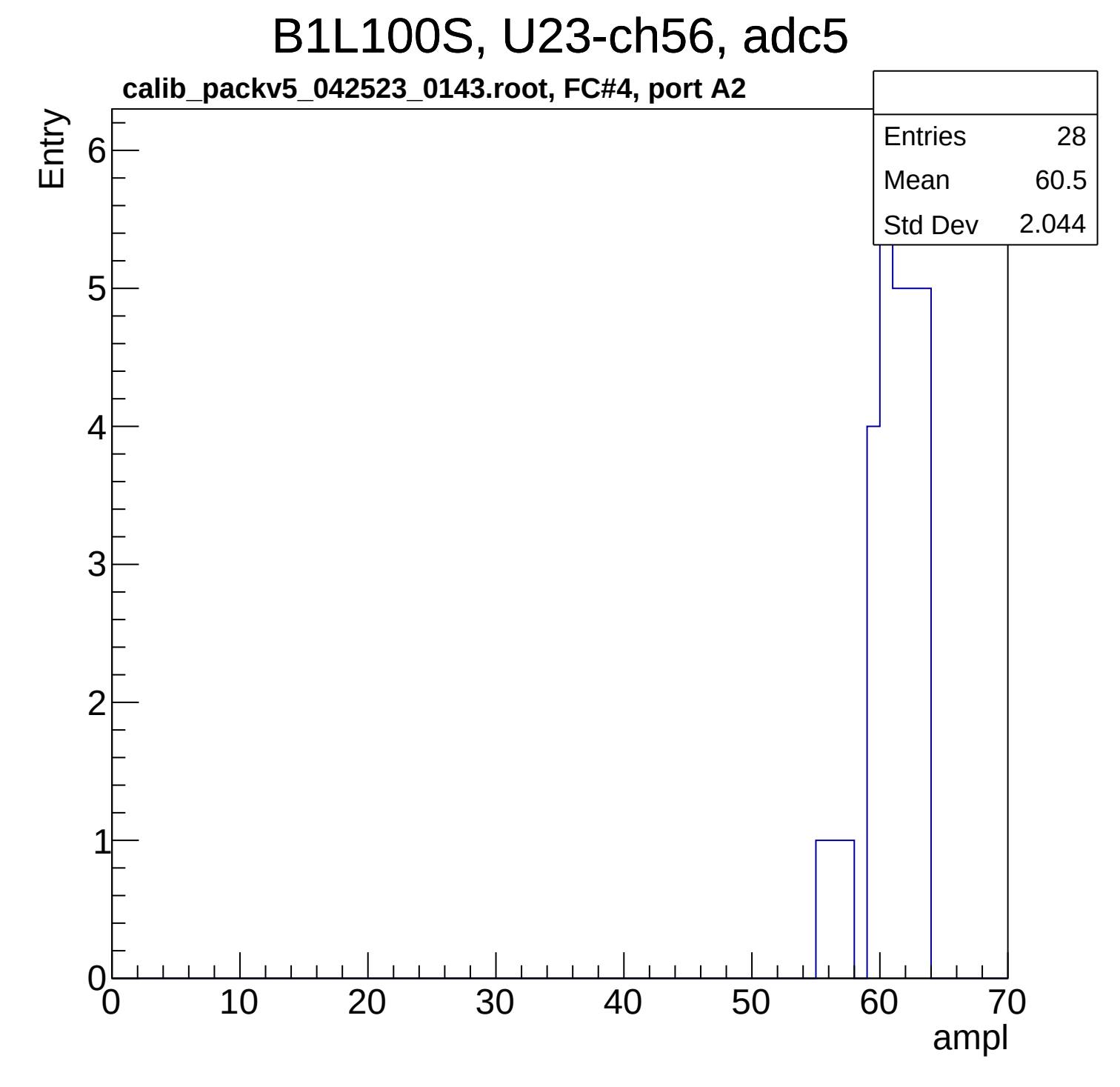
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 60.5
Std Dev 2.044
Entry
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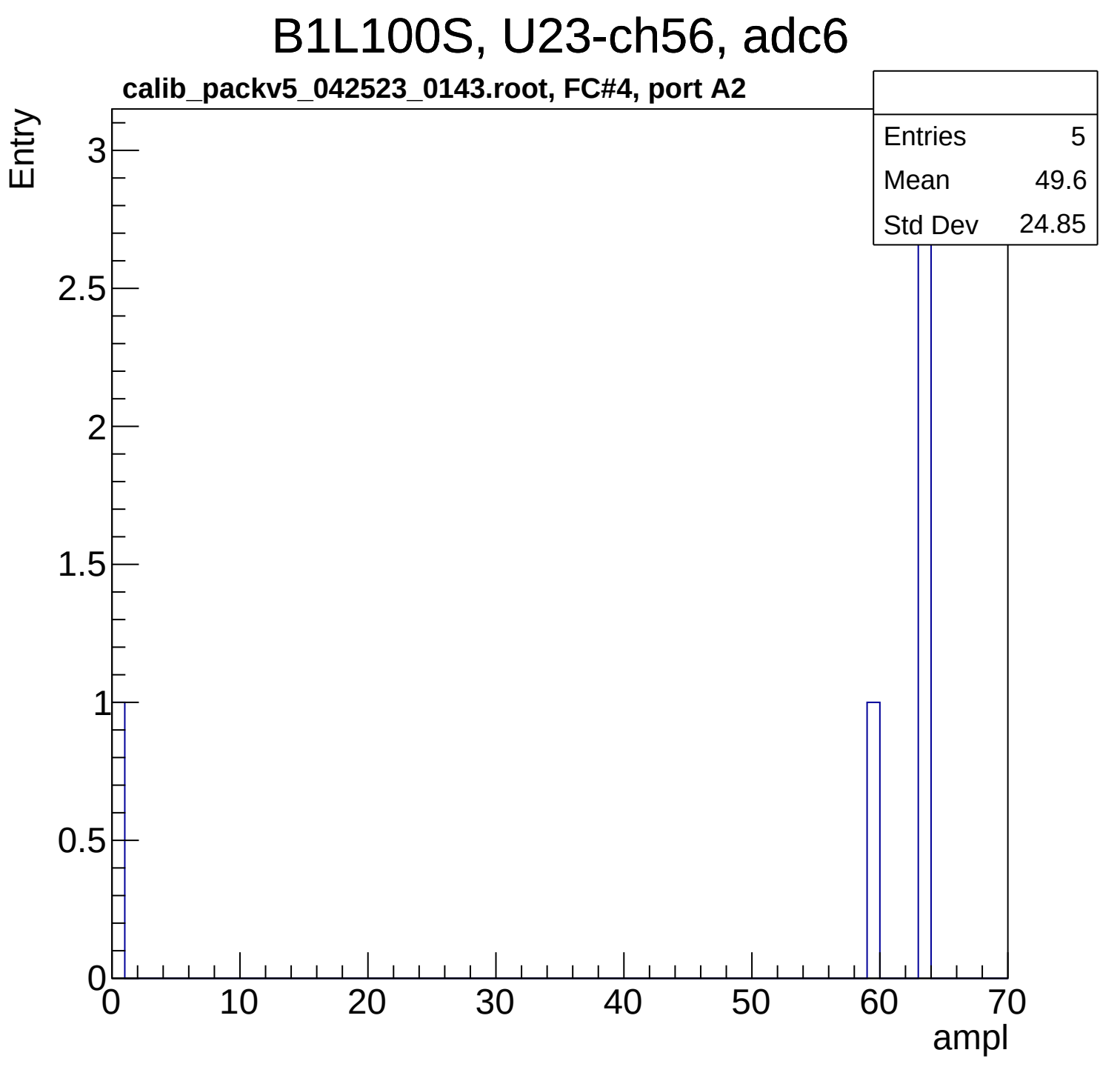
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.6
Std Dev 24.85
Entry
ampl
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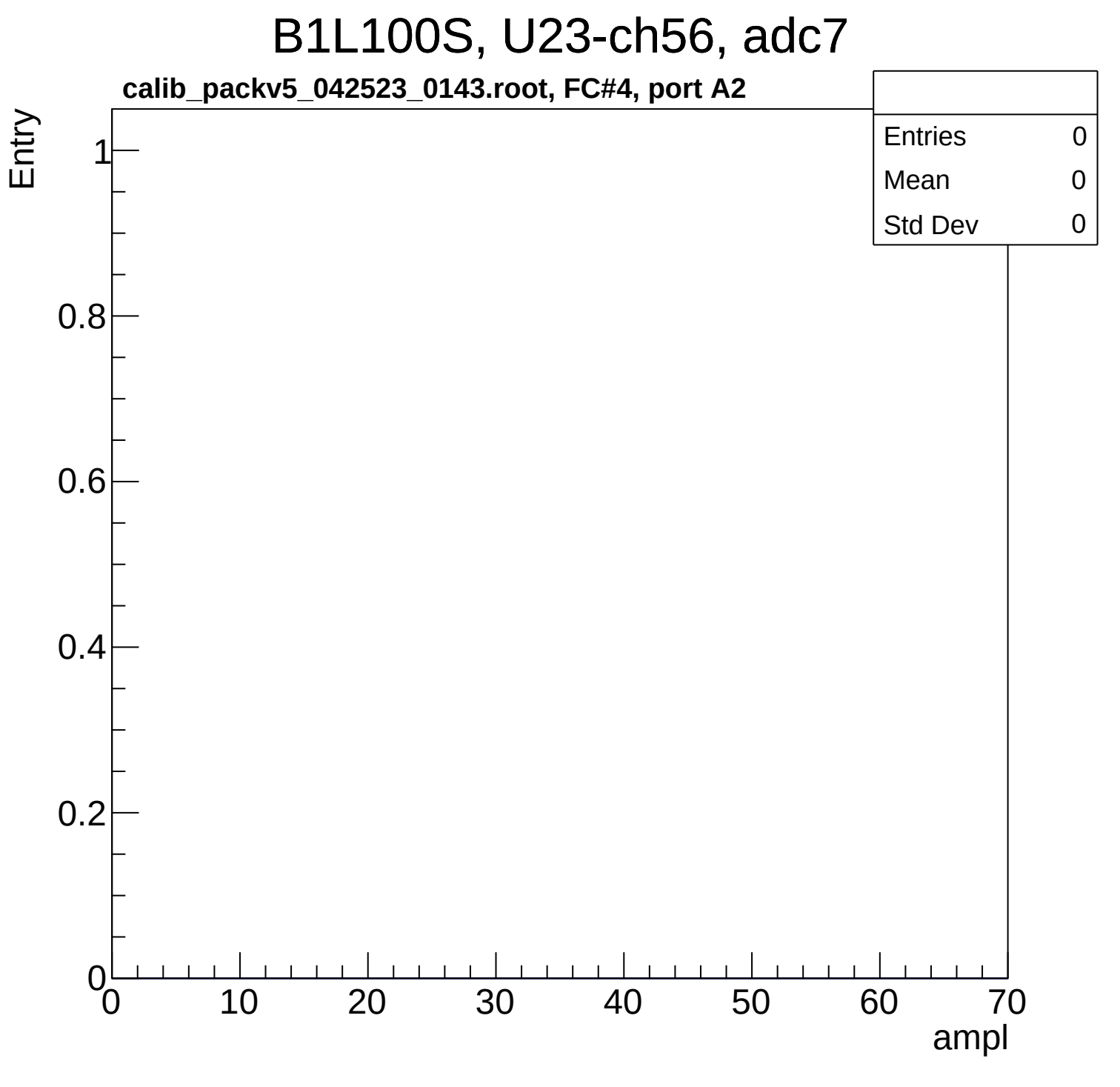
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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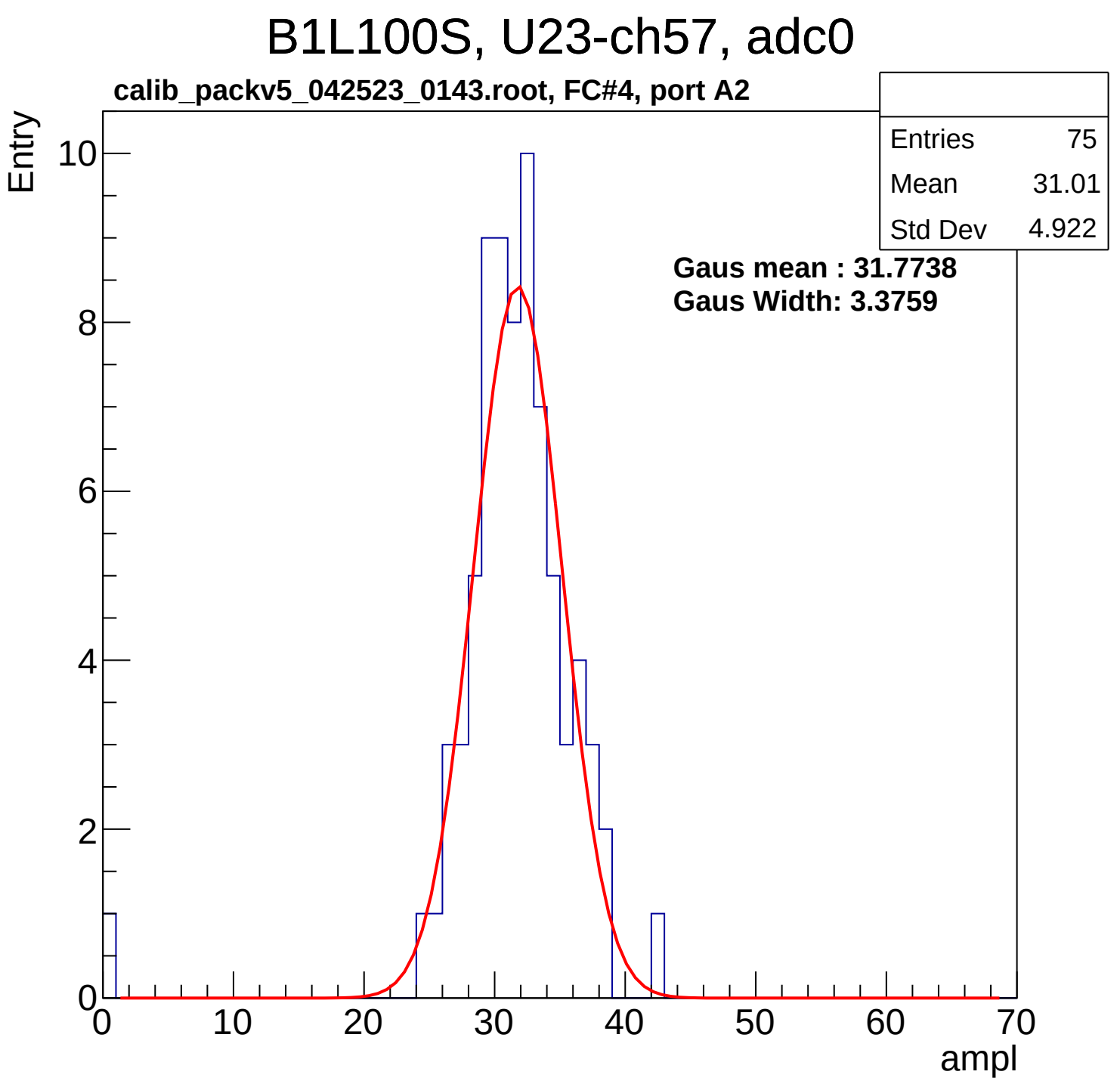
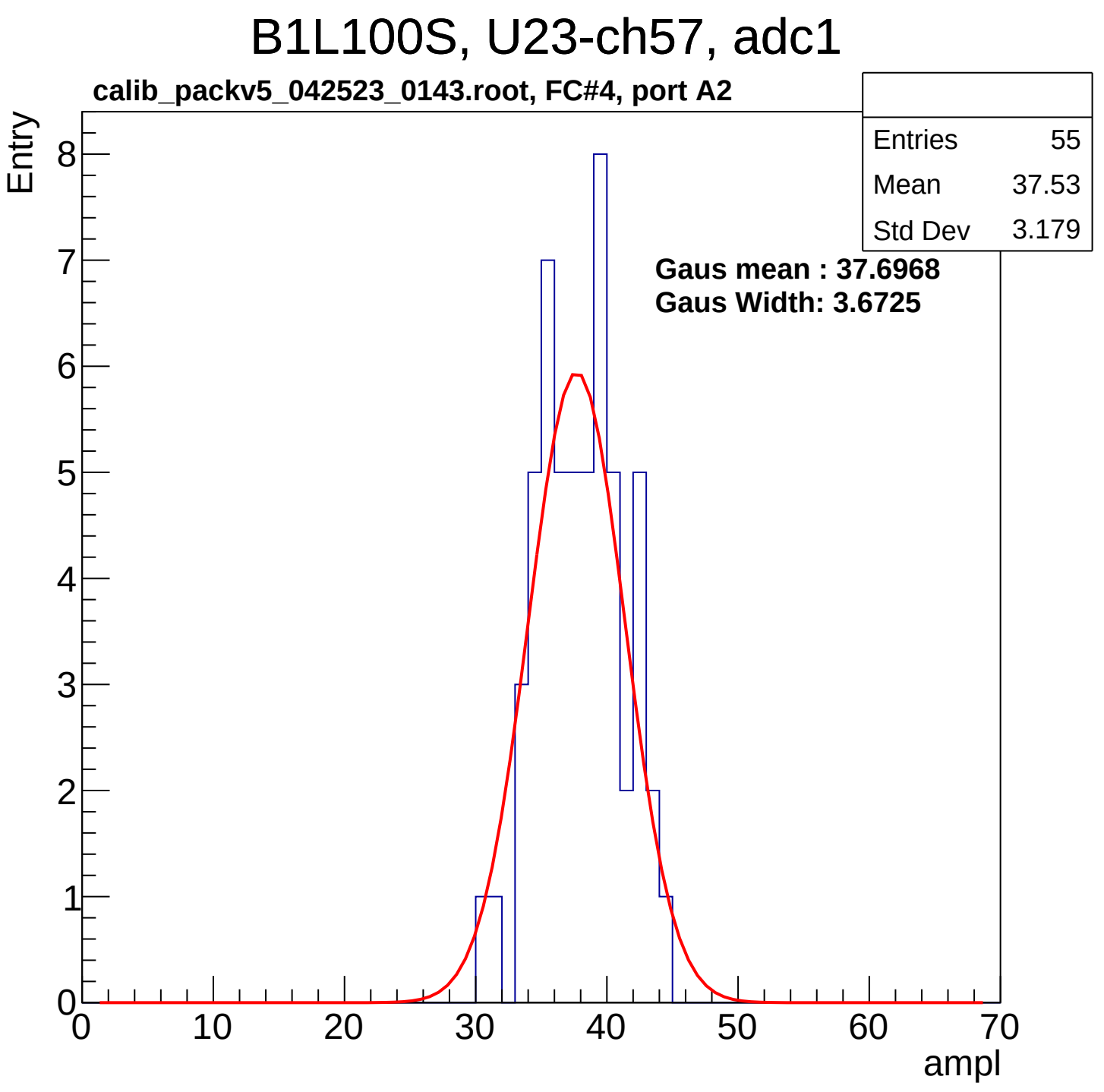
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 31.01
Std Dev 4.922
Gaus mean : 31.7738
Gaus Width: 3.3759
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 37.53
Std Dev 3.179
Gaus mean : 37.6968
Gaus Width: 3.6725
Entry
ampl
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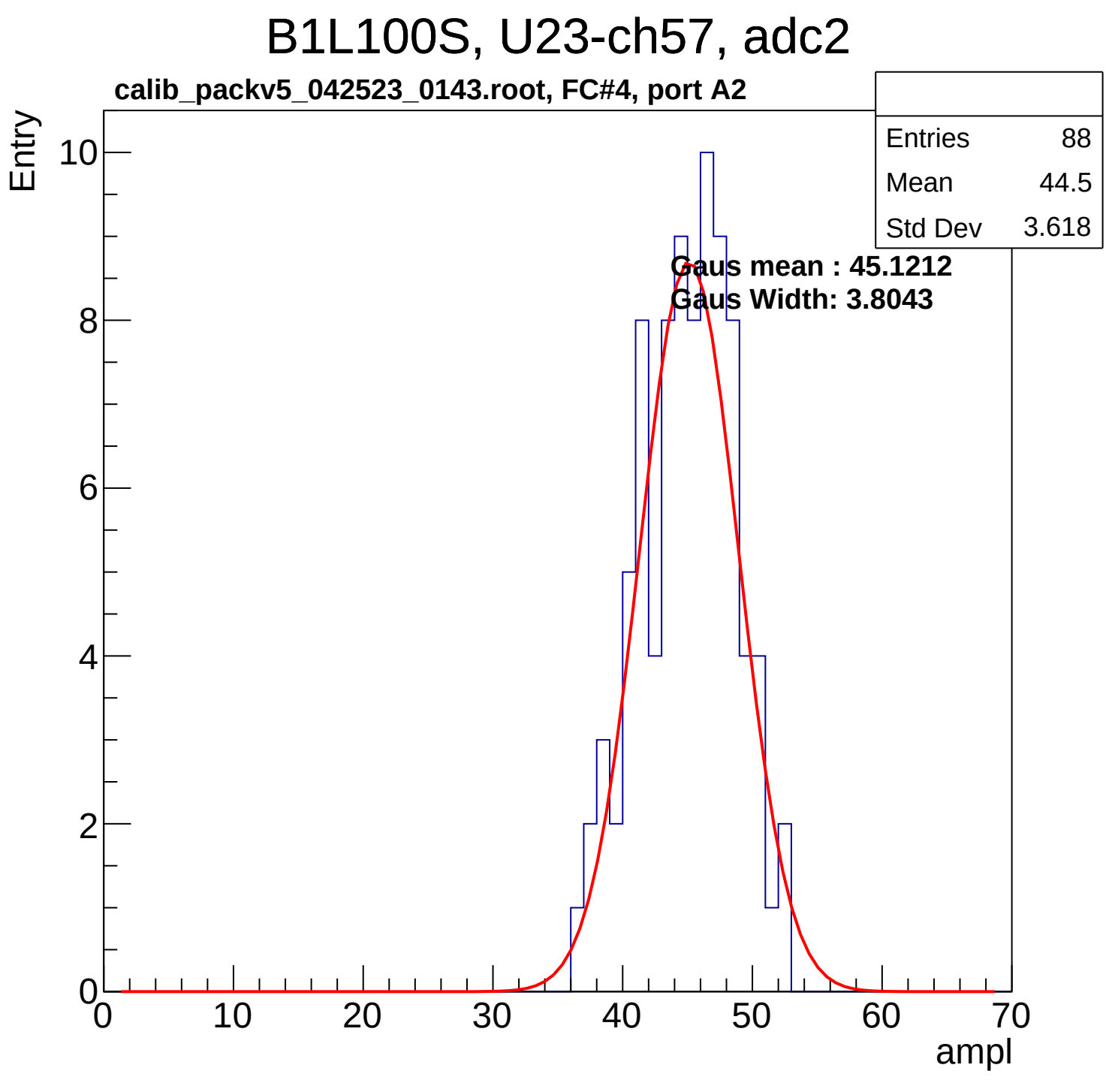
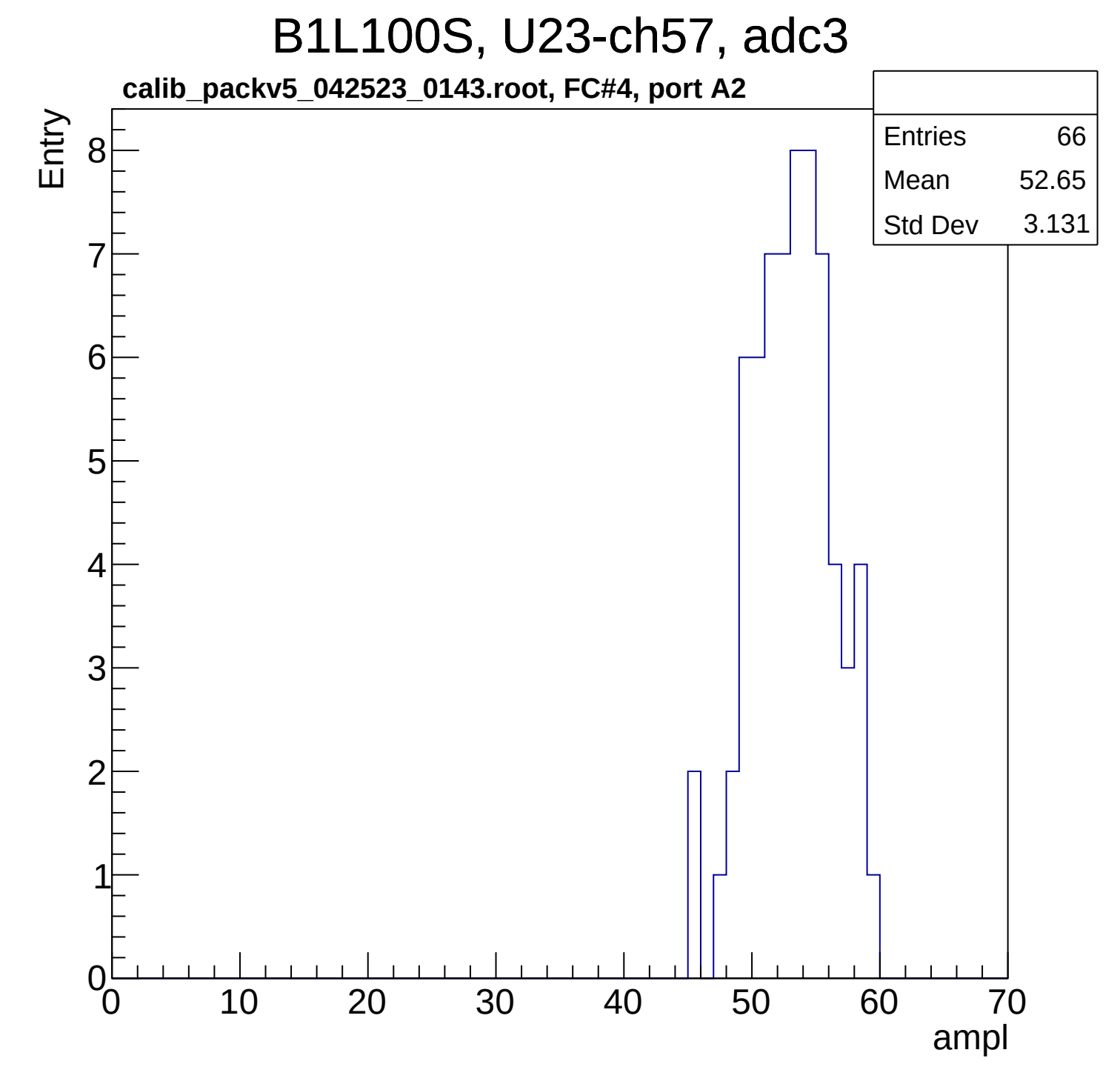
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 44.5
Std Dev 3.618
Gaus mean : 45.1212
Gaus Width: 3.8043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch57, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 52.65
Std Dev 3.131
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
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60
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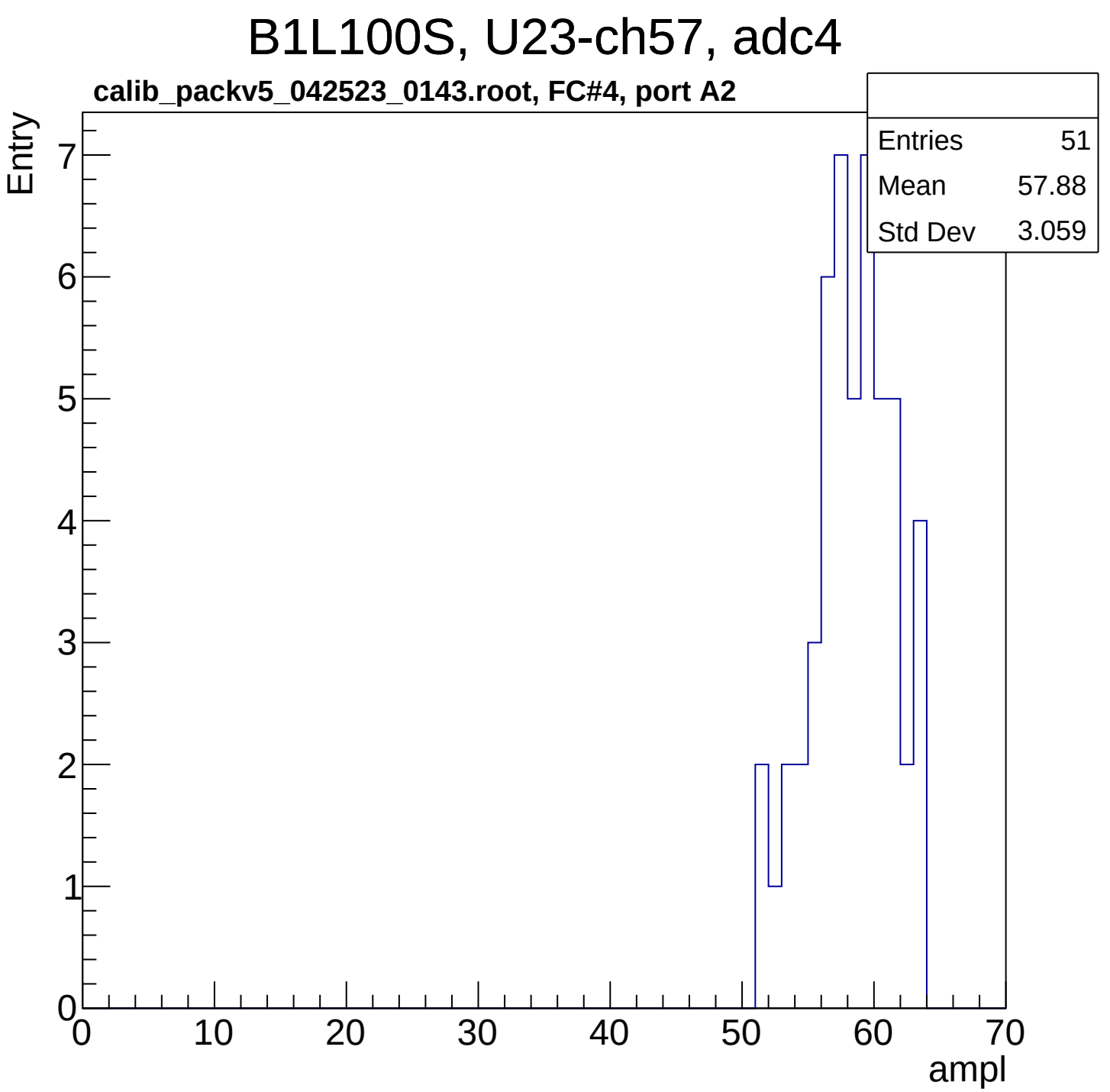
B1L100S, U23-ch57, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.88
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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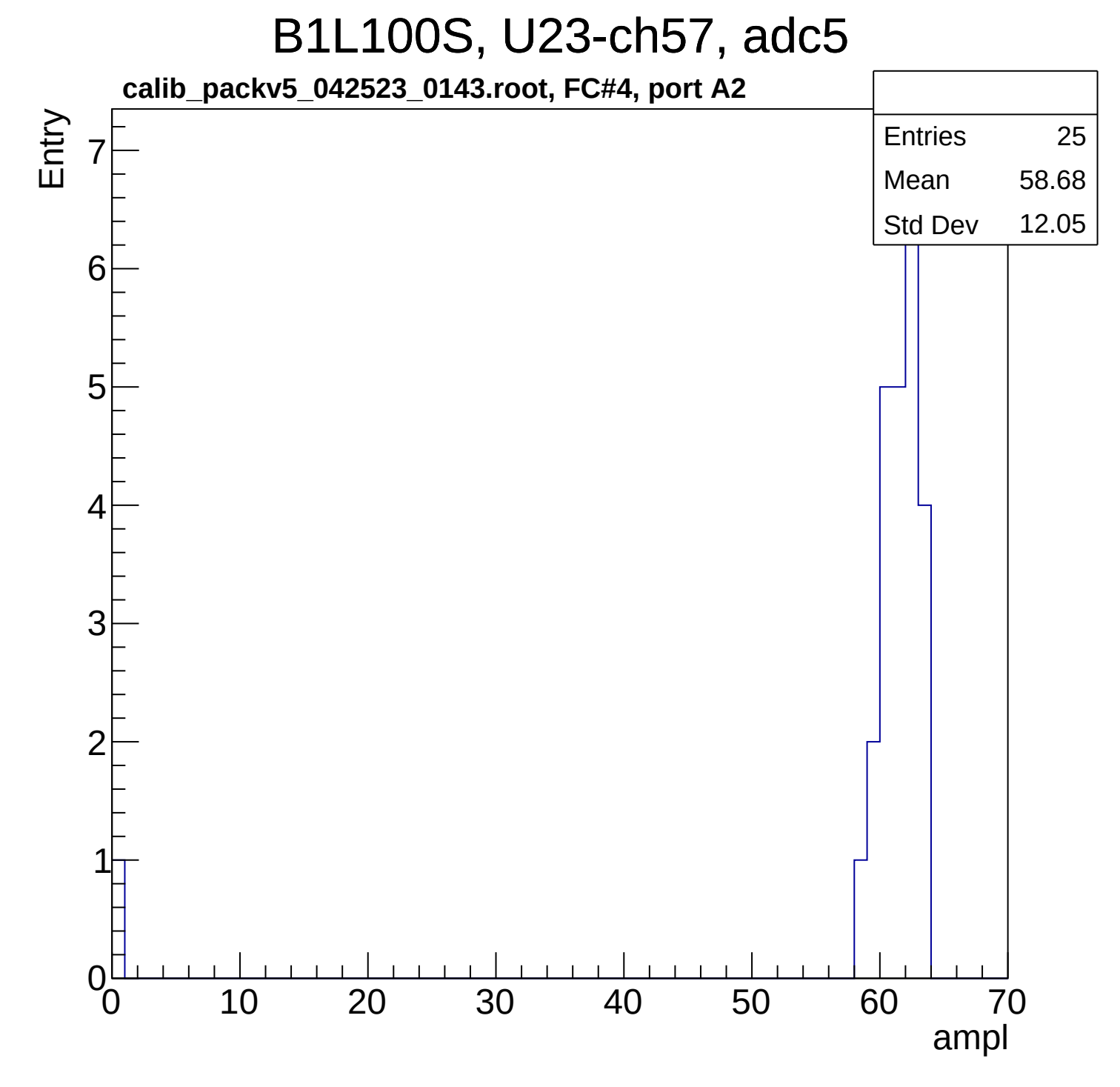
B1L100S, U23-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.68
Std Dev 12.05
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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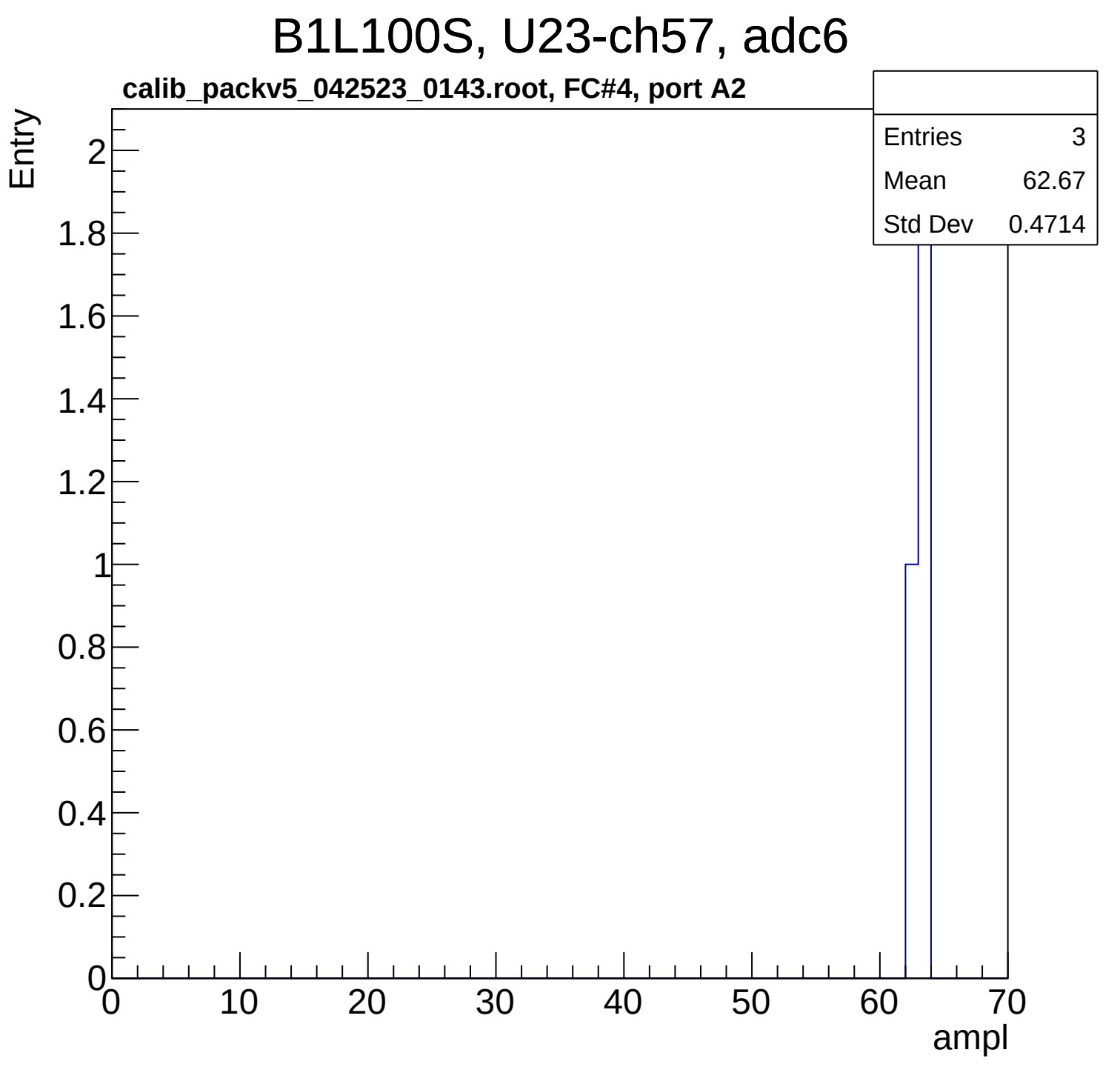
B1L100S, U23-ch57, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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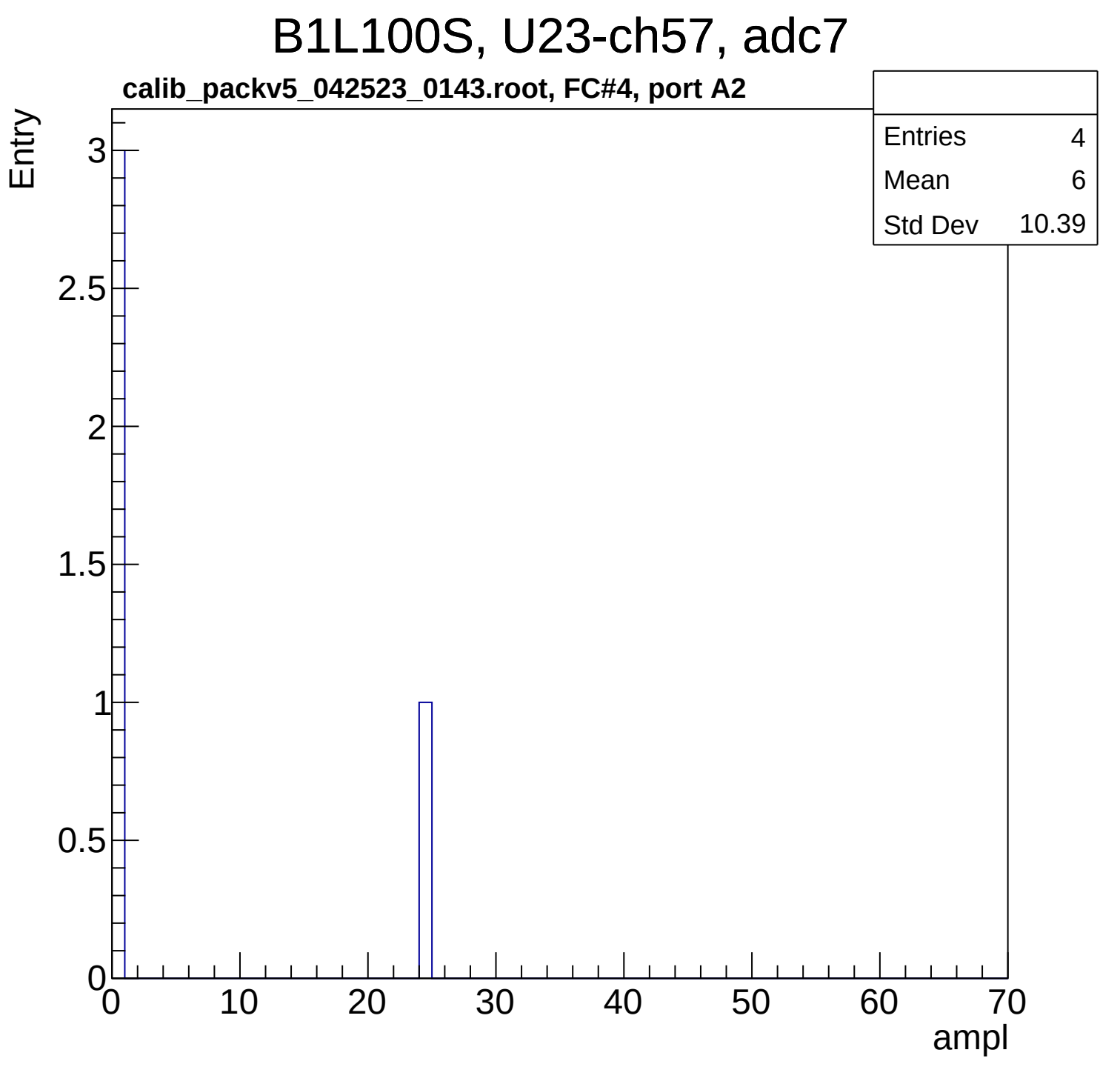
20
30
40
50
60
70


B1L100S, U23-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 6
Std Dev 10.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
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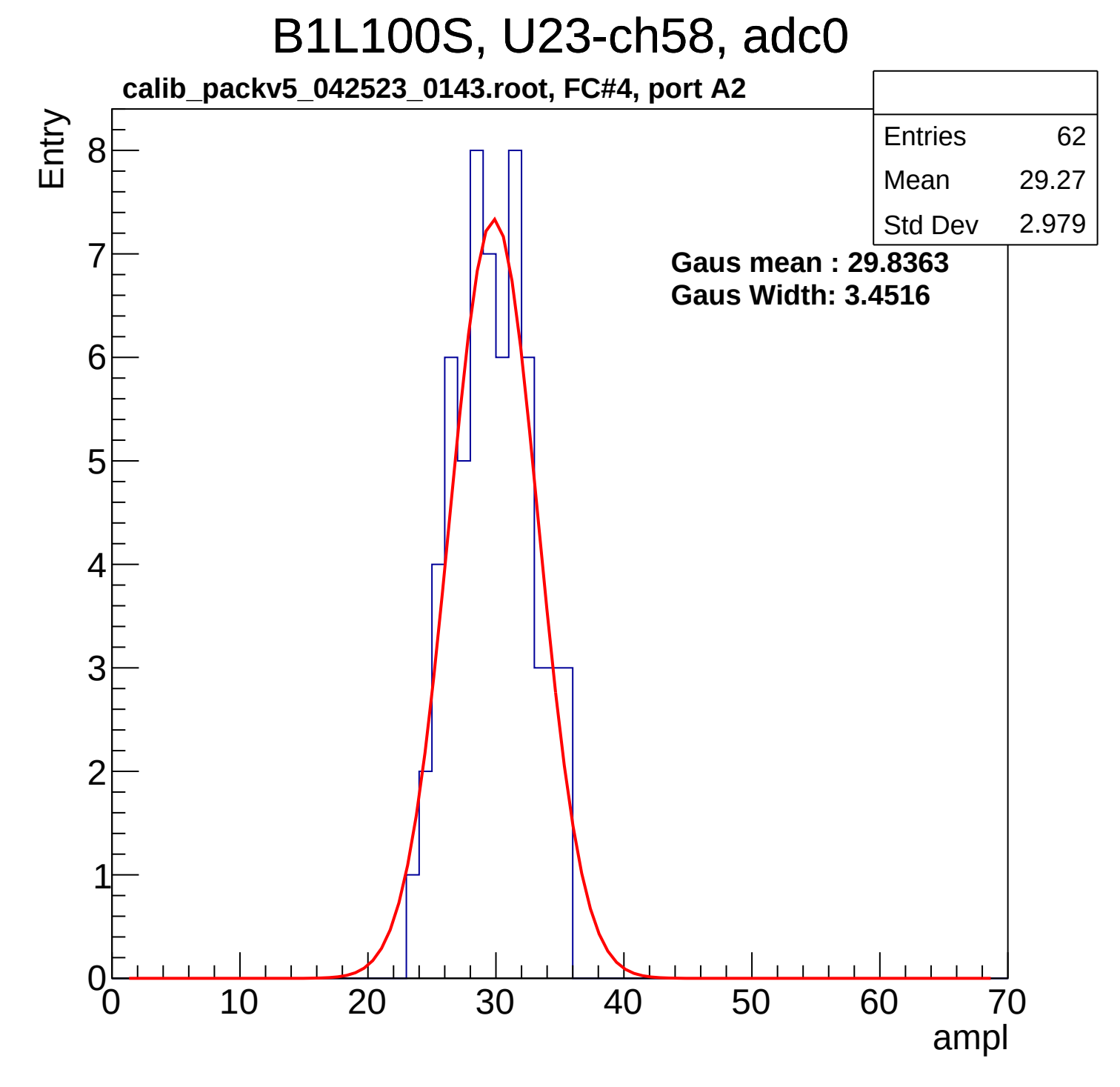
B1L100S, U23-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.27
Std Dev 2.979
Gaus mean : 29.8363
Gaus Width: 3.4516
Entry
ampl
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2
3
4
5
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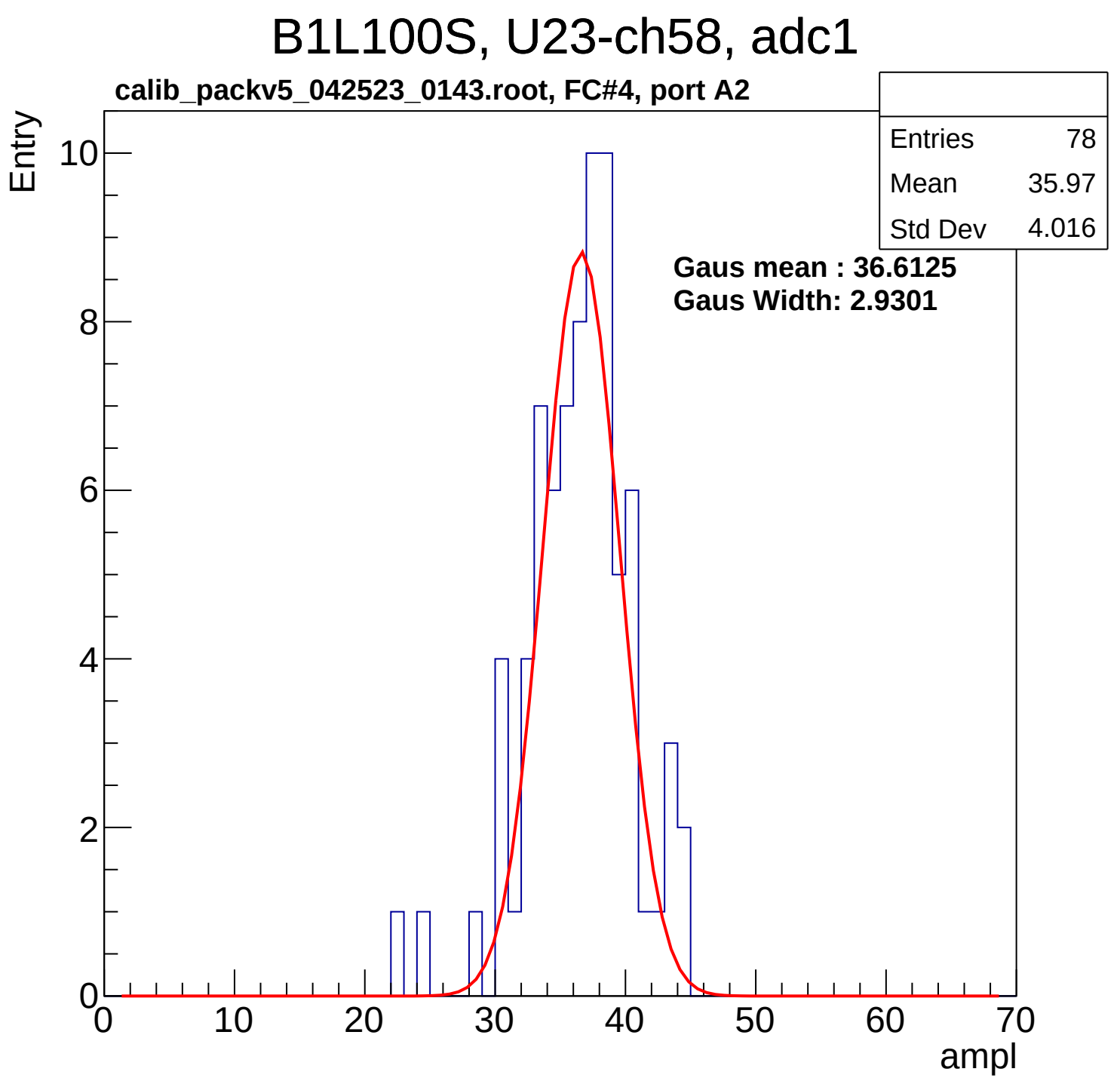
B1L100S, U23-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 35.97
Std Dev 4.016
Gaus mean : 36.6125
Gaus Width: 2.9301
Entry
ampl
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4
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0
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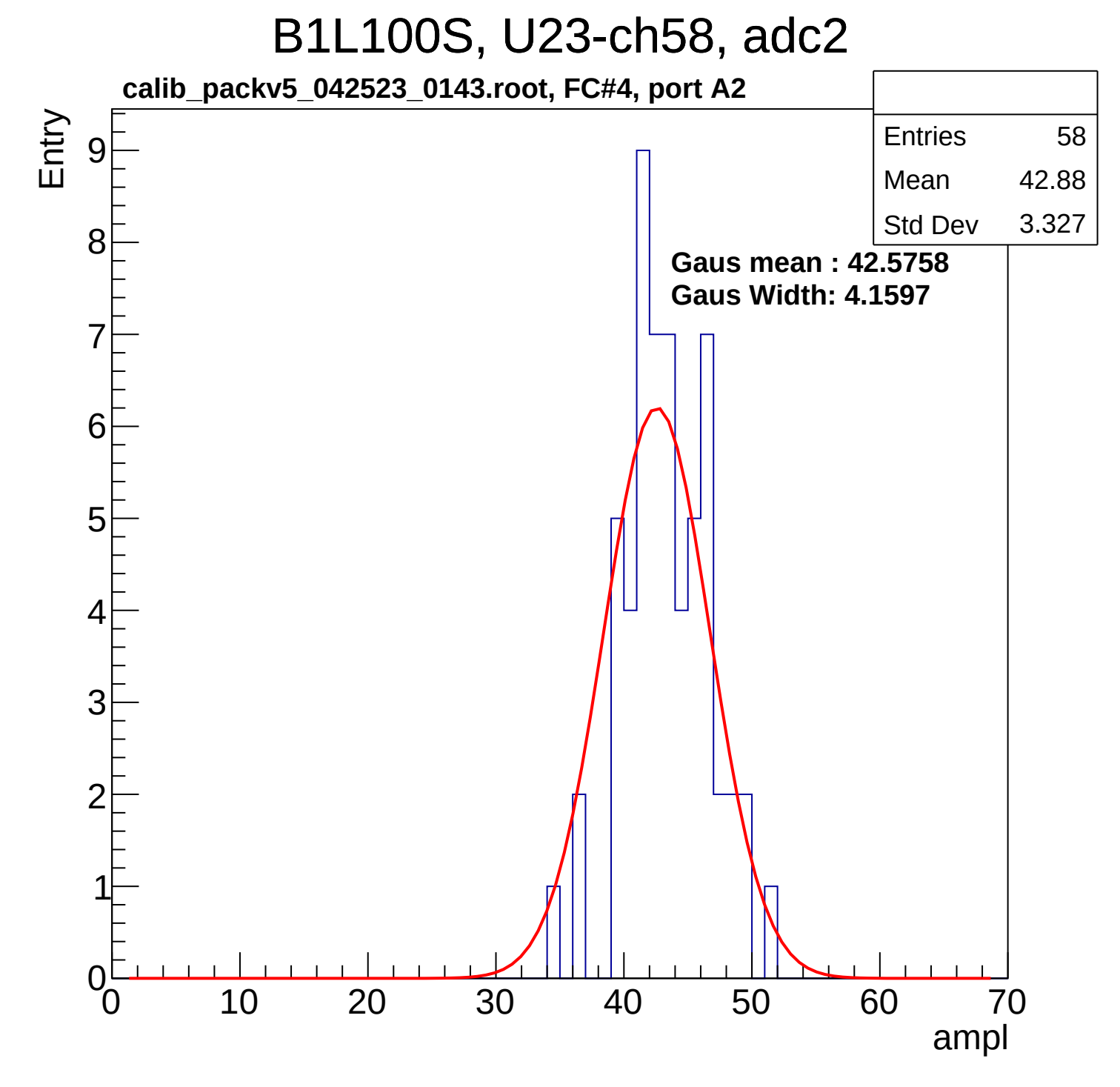
B1L100S, U23-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 42.88
Std Dev 3.327
Gaus mean : 42.5758
Gaus Width: 4.1597
Entry
ampl
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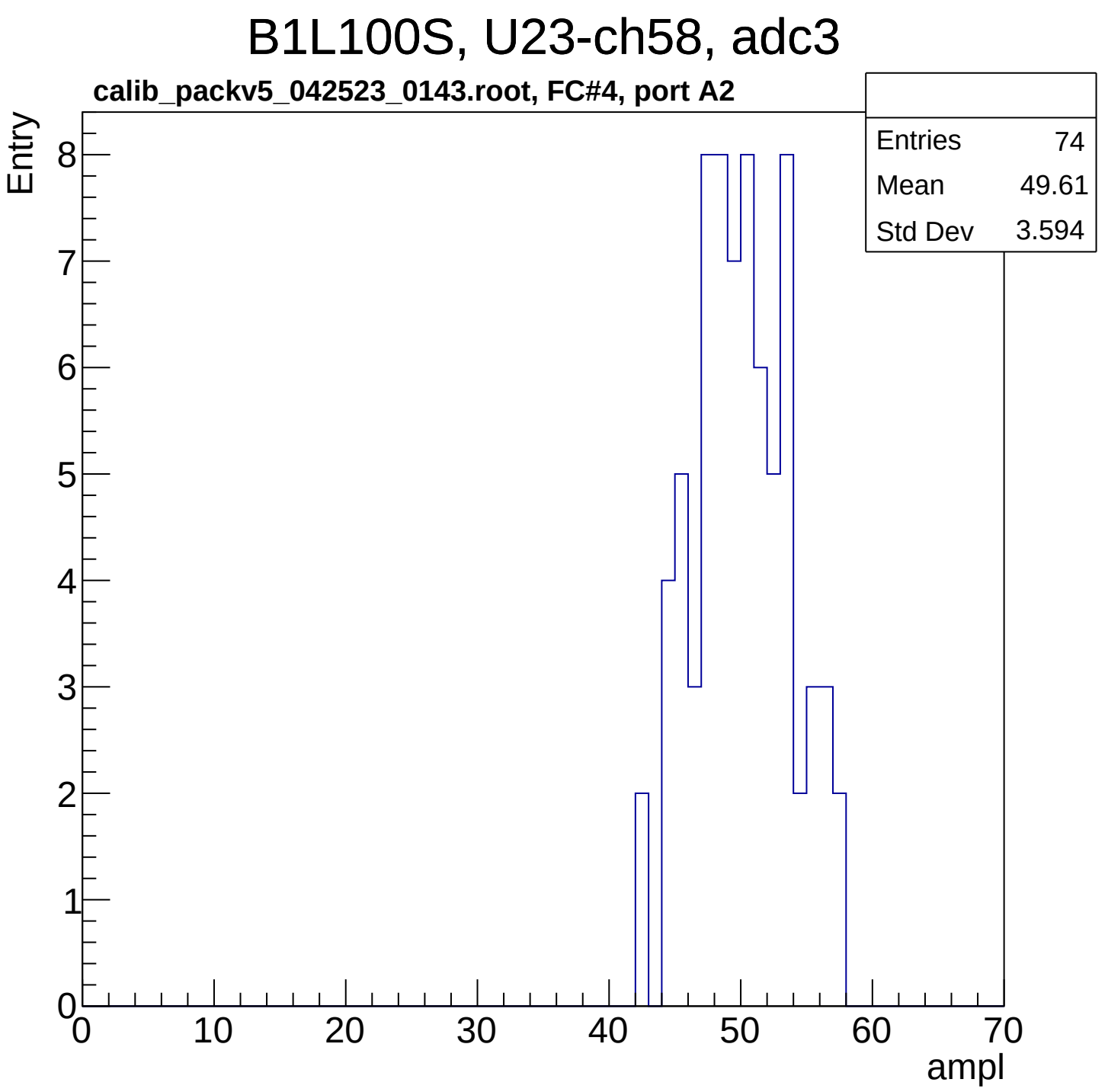
B1L100S, U23-ch58, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.61
Std Dev 3.594
Entry
ampl
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1
2
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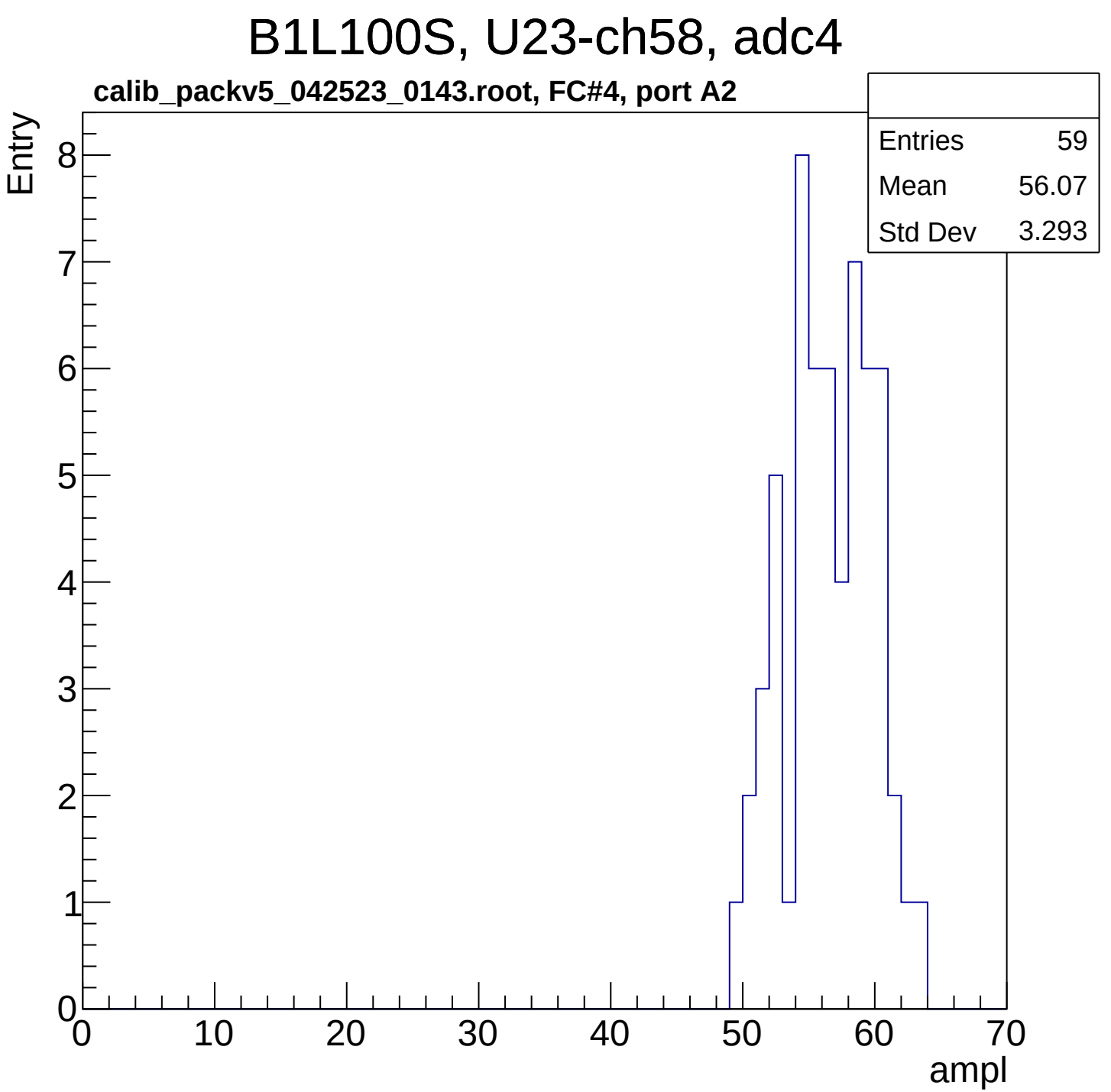
B1L100S, U23-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 56.07
Std Dev 3.293
Entry
ampl
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1
2
3
4
5
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7
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0
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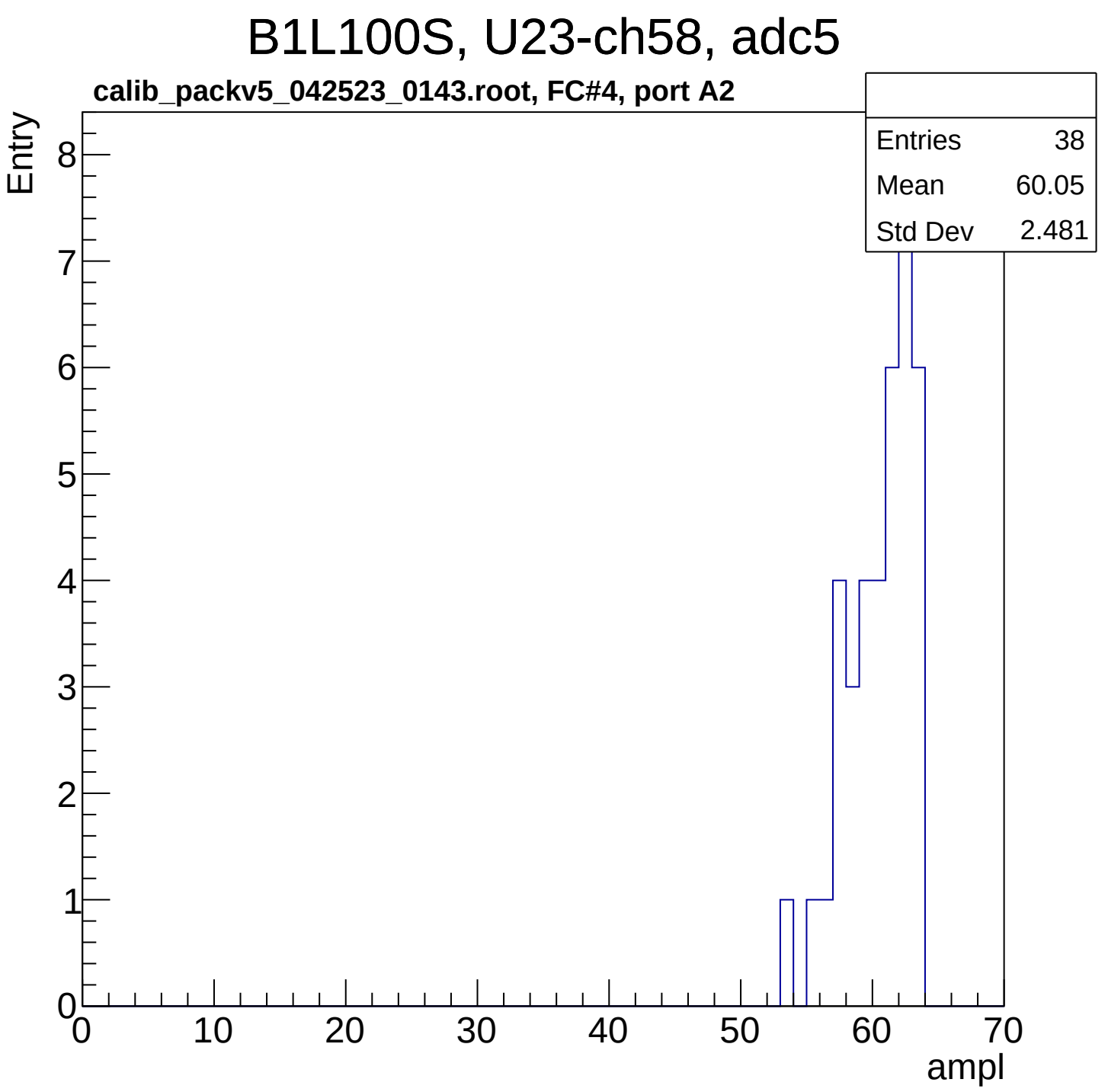
B1L100S, U23-ch58, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.05
Std Dev 2.481
Entry
ampl
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1
2
3
4
5
6
7
8
0
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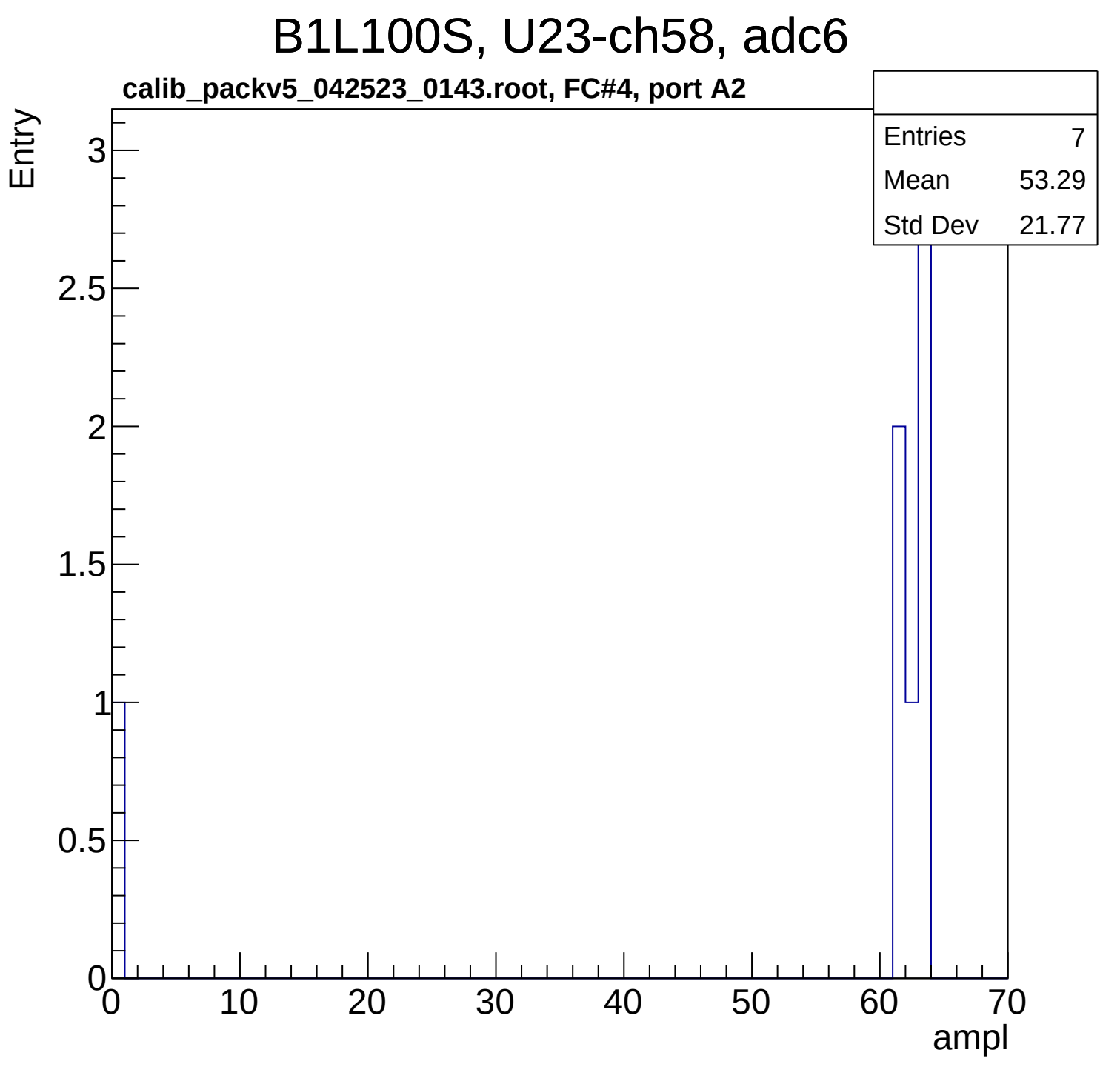
B1L100S, U23-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.29
Std Dev 21.77
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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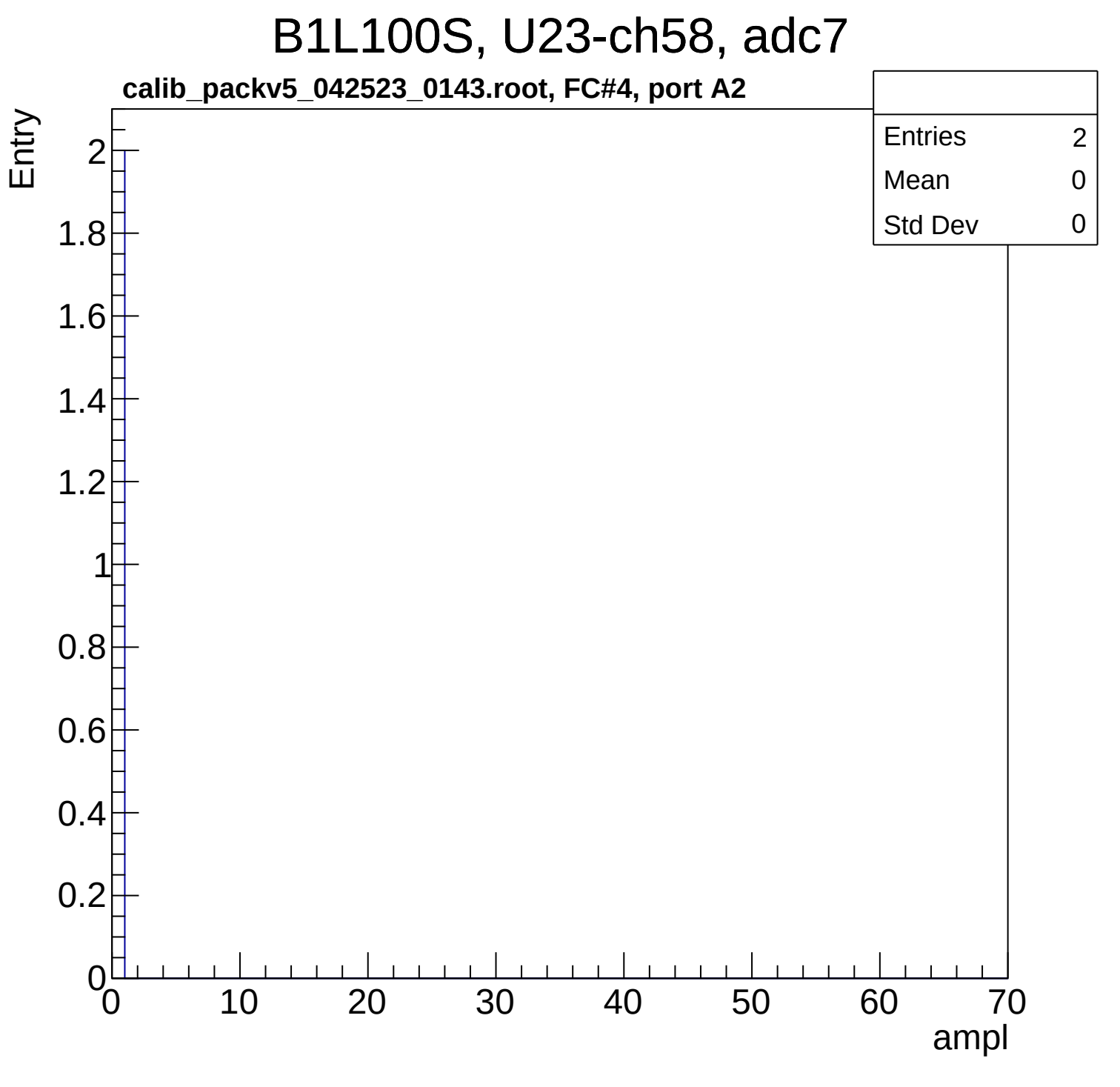
B1L100S, U23-ch58, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
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30
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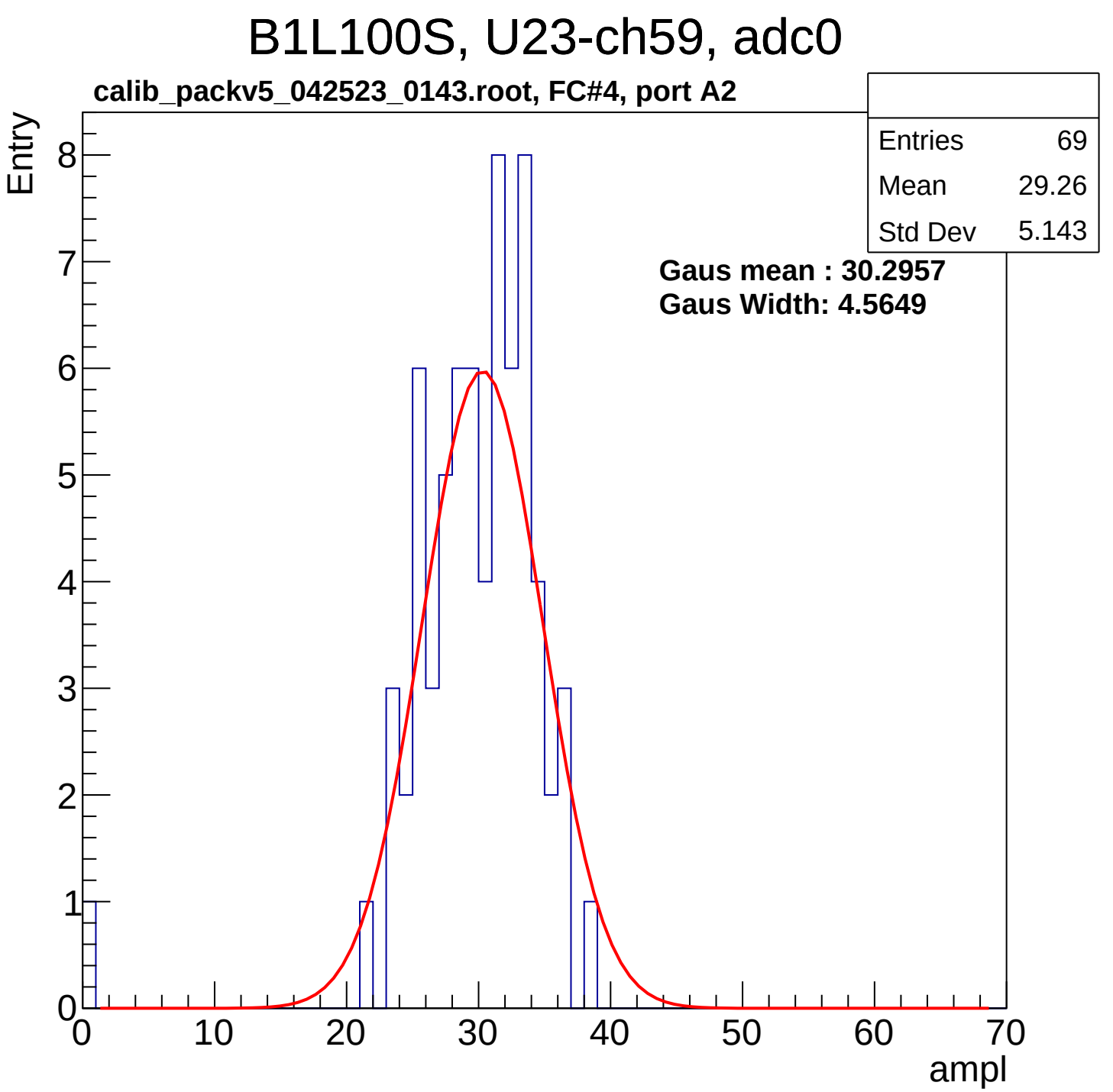
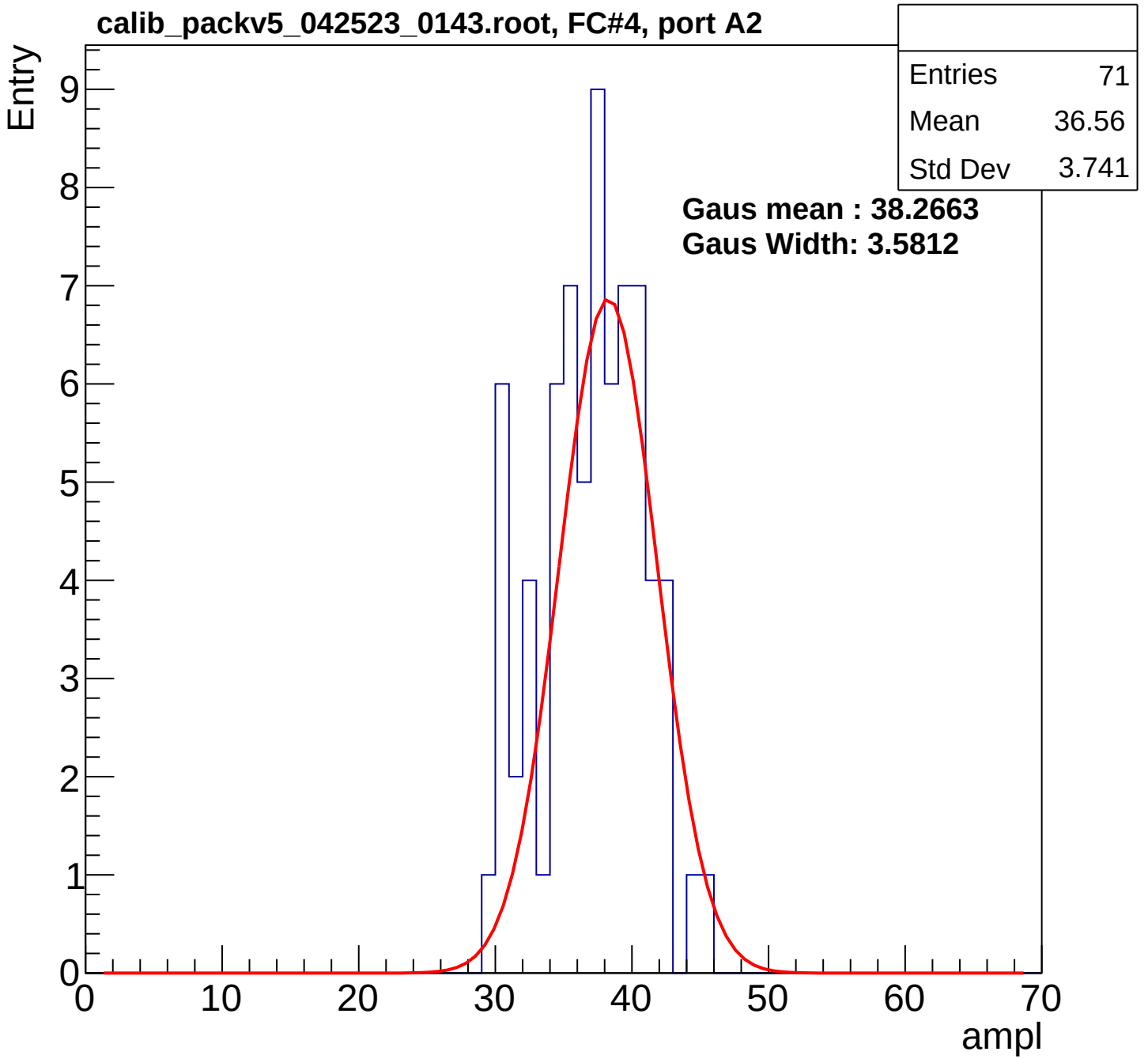
50
60
70
ampl


B1L100S, U23-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.26
Std Dev 5.143
Gaus mean : 30.2957
Gaus Width: 4.5649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.56
Std Dev 3.741
Gaus mean : 38.2663
Gaus Width: 3.5812
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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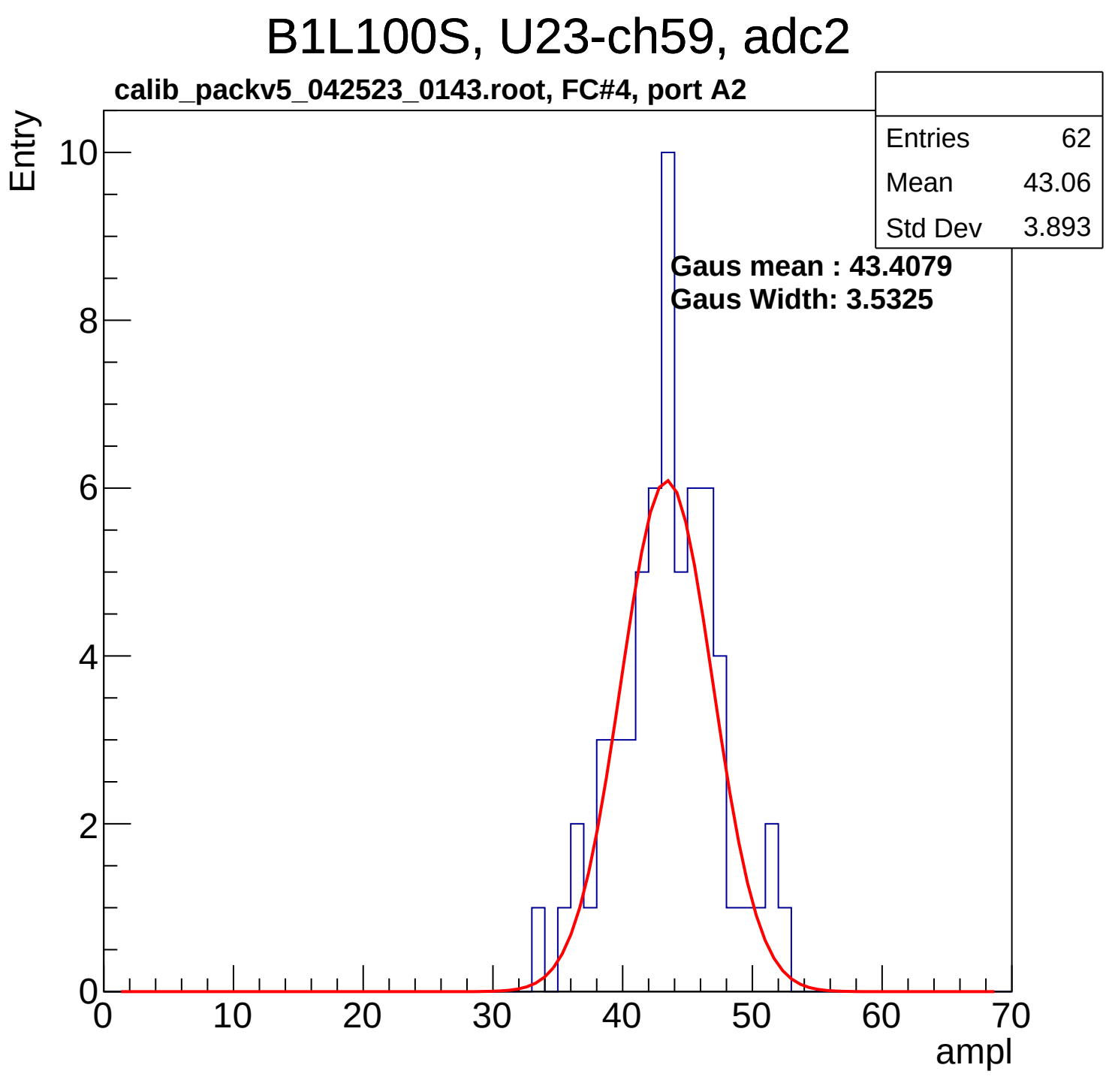
B1L100S, U23-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.06
Std Dev 3.893
Gaus mean : 43.4079
Gaus Width: 3.5325
Entry
ampl
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8
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0
10
20
30
40
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60
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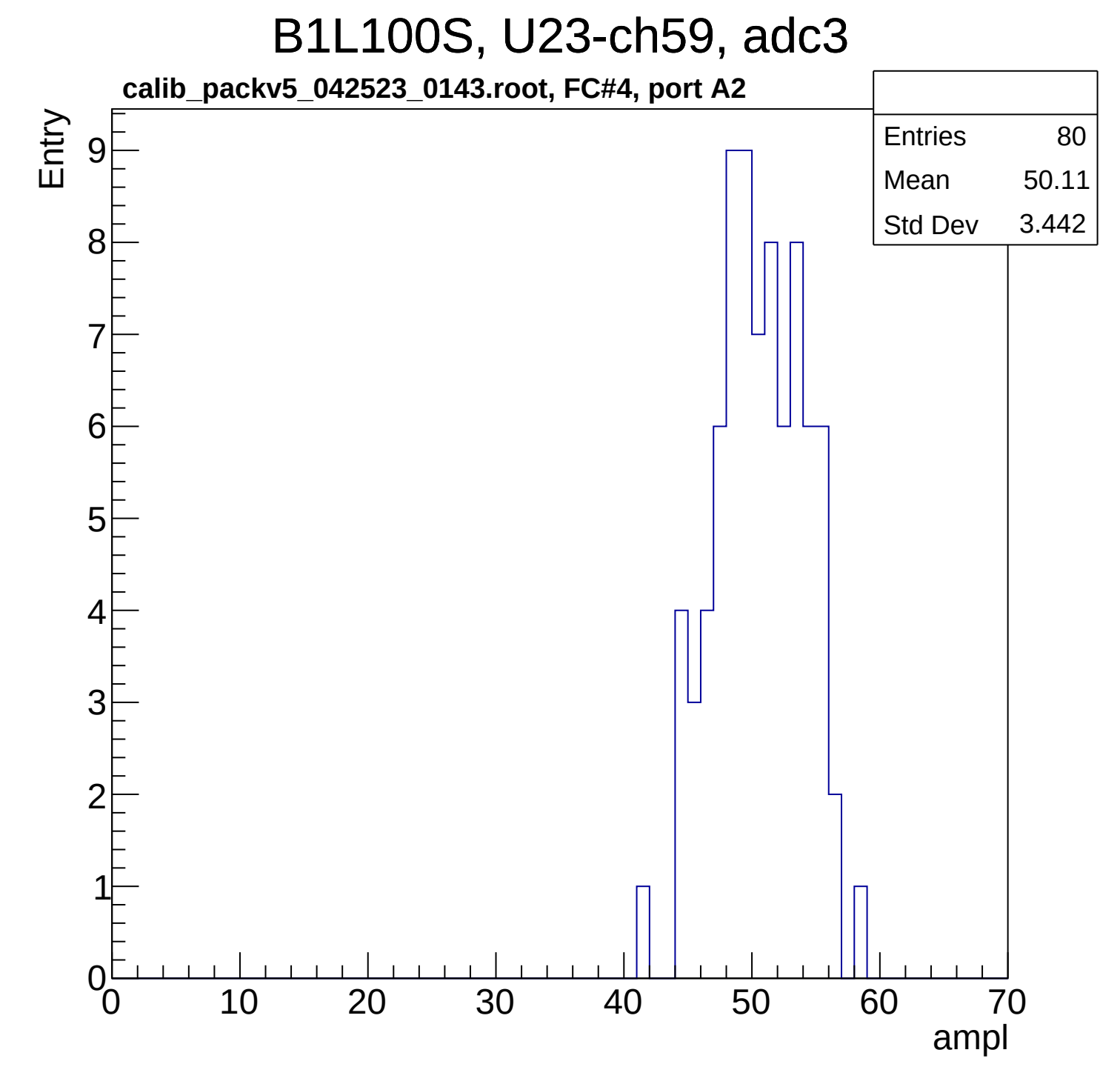
B1L100S, U23-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 50.11
Std Dev 3.442
Entry
ampl
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4
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0
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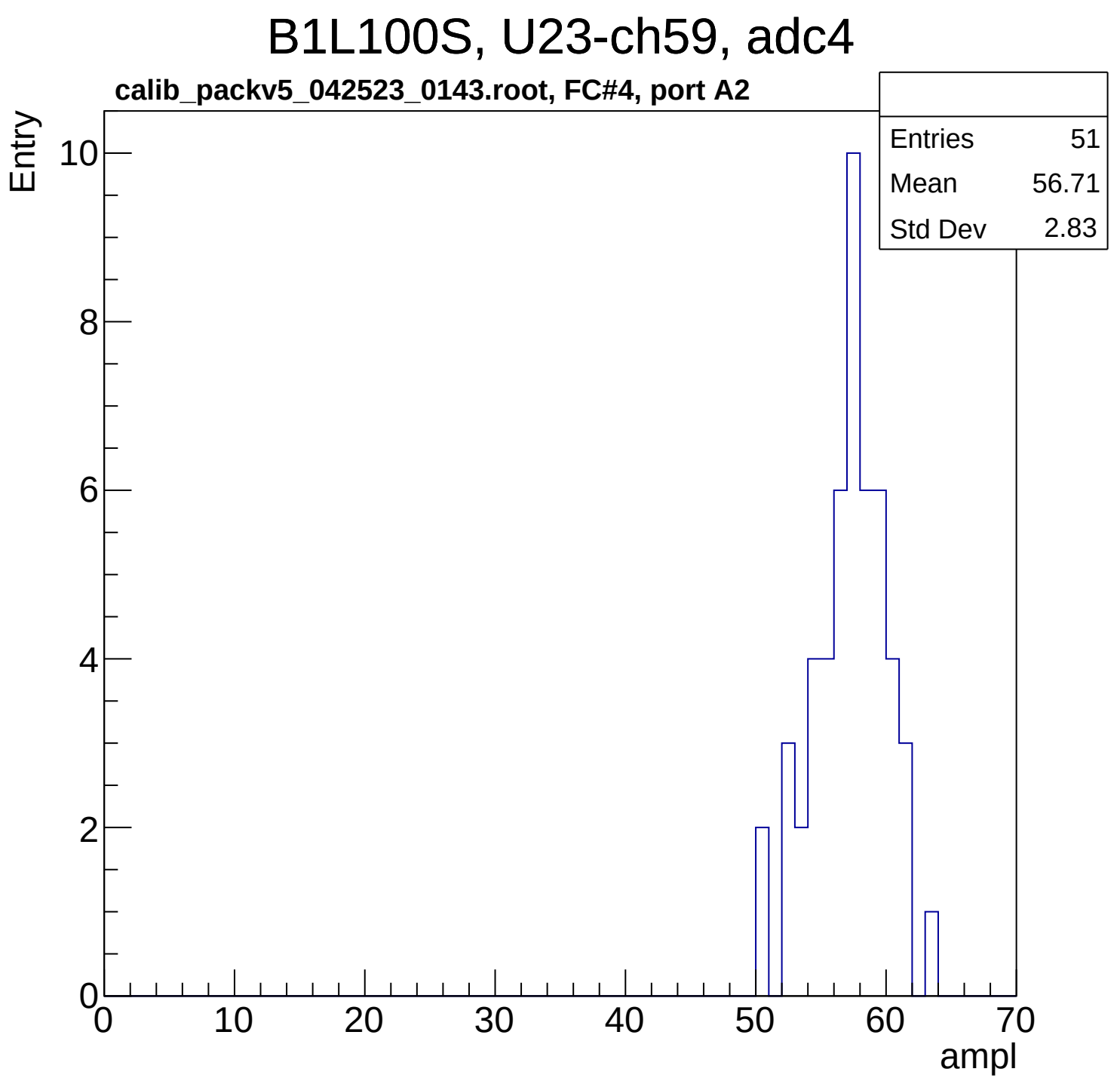
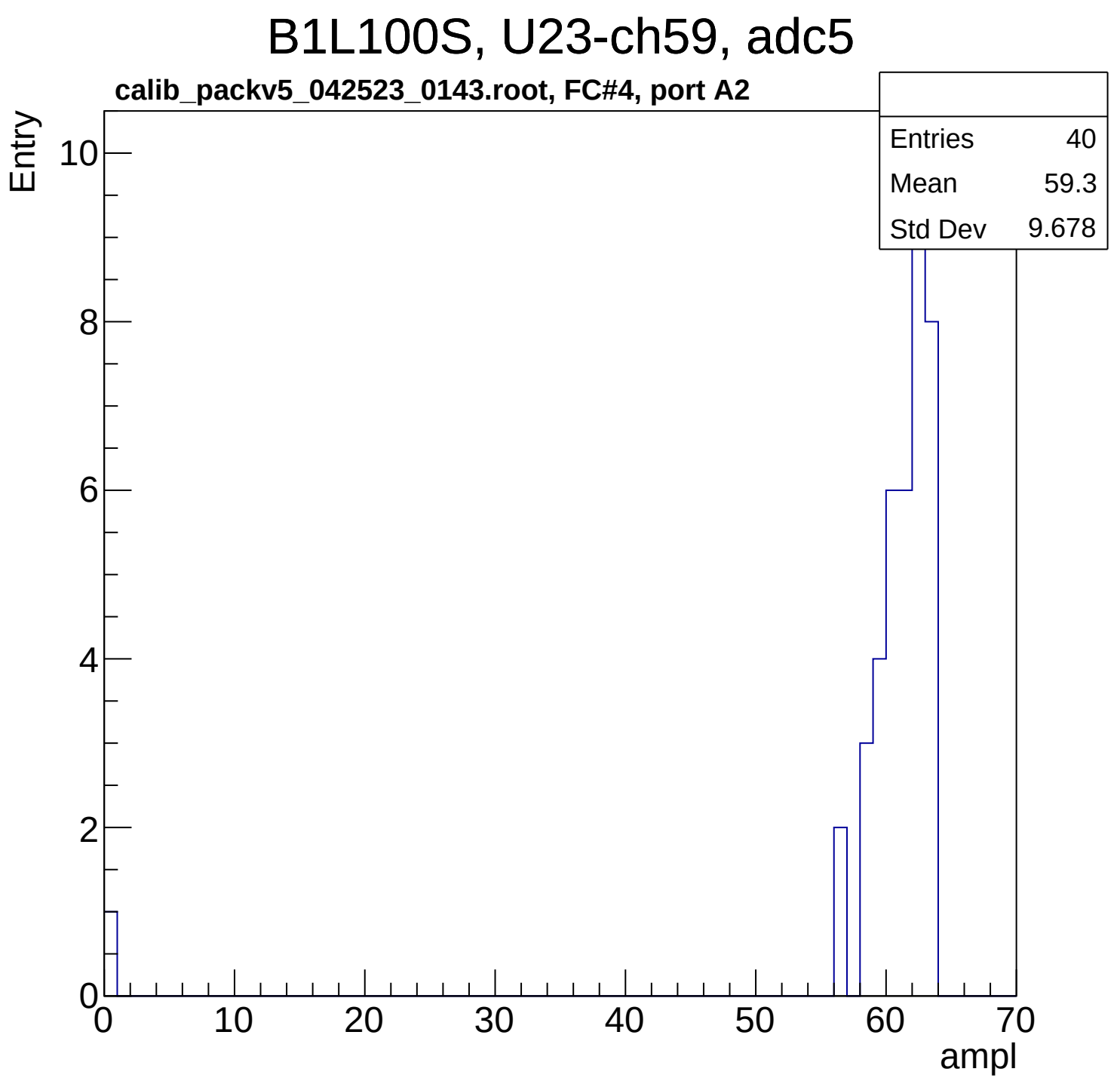
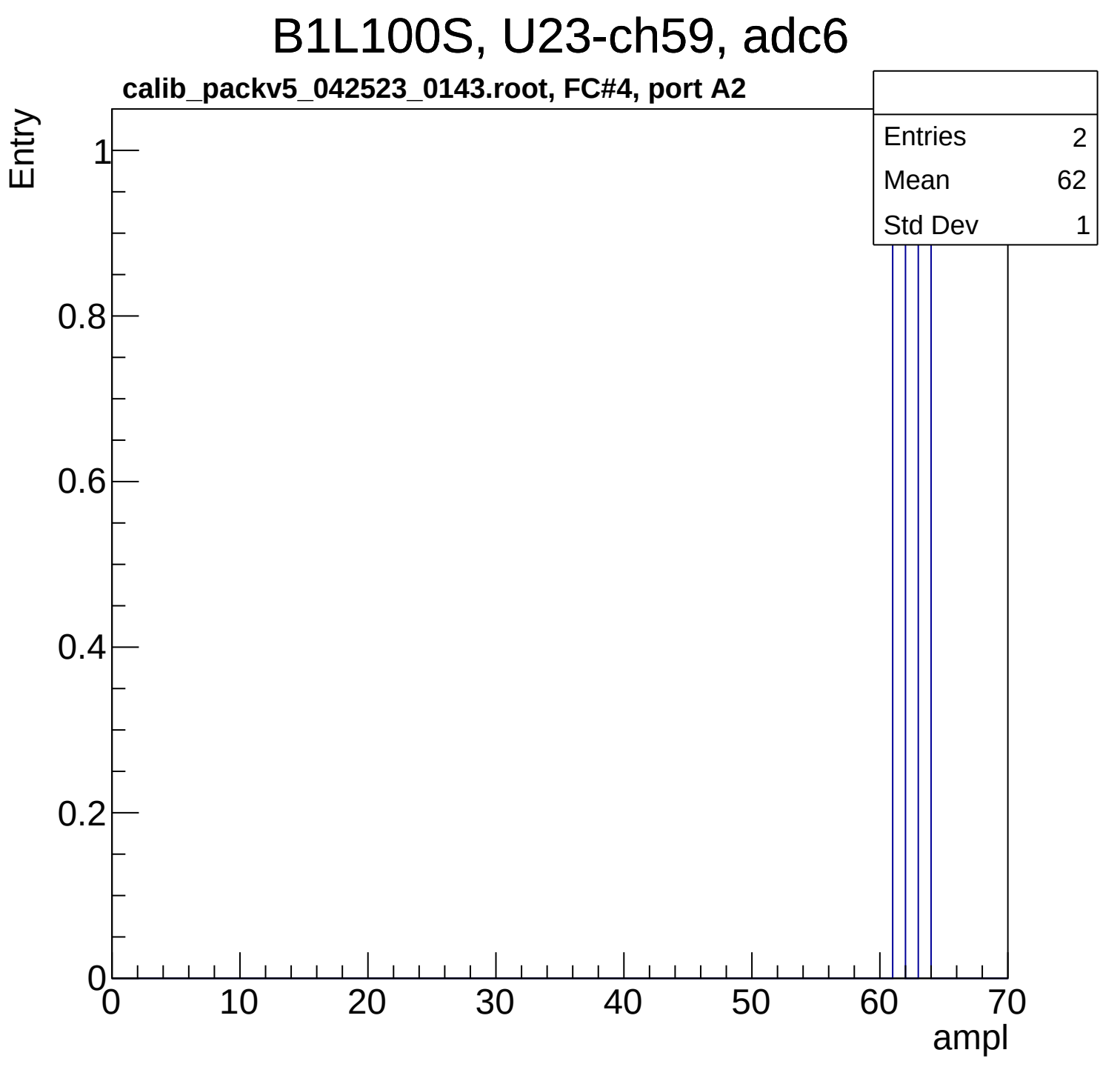
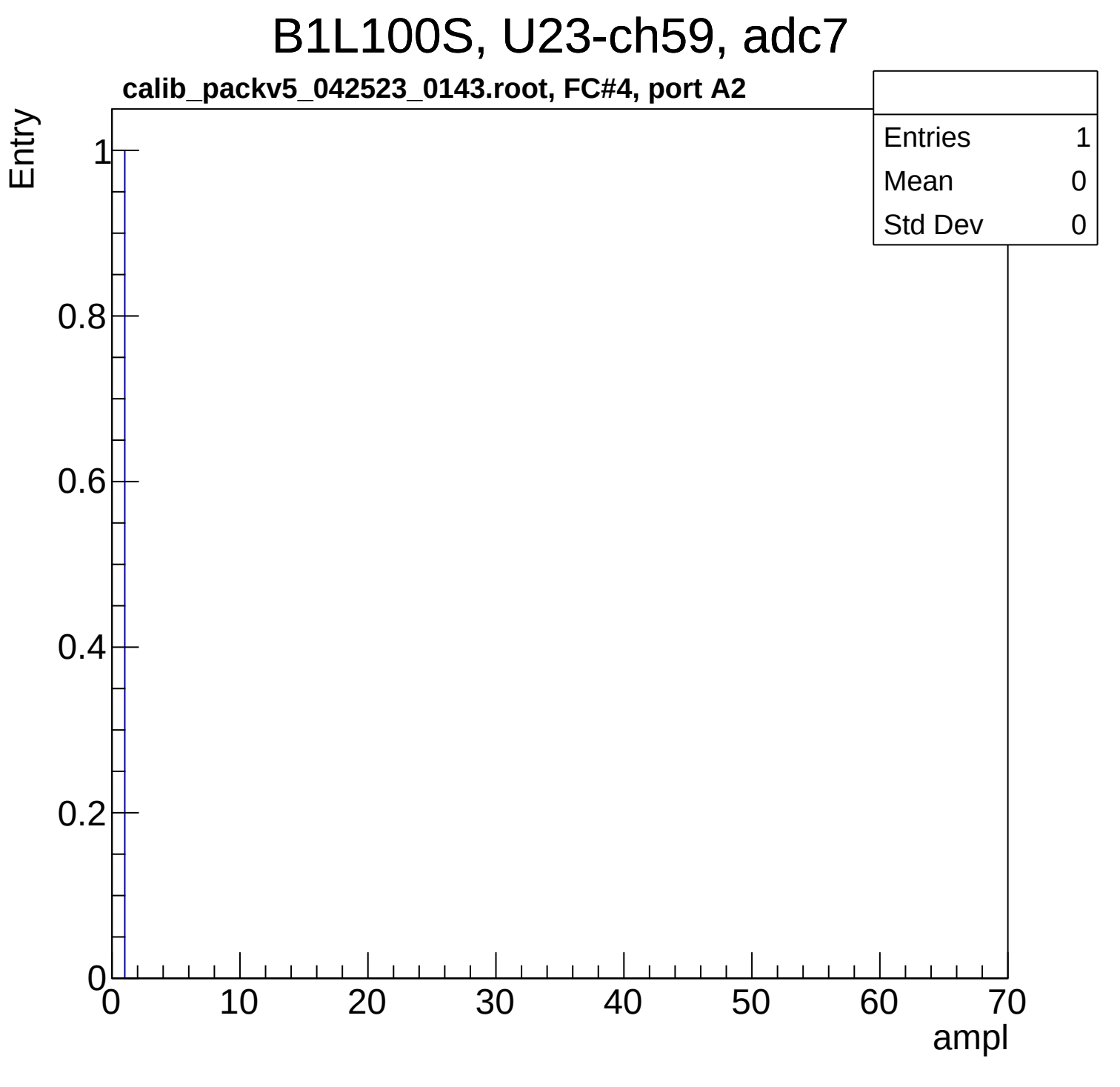

B1L100S, U23-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.71
Std Dev 2.83
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch59, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 59.3
Std Dev 9.678
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch59, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
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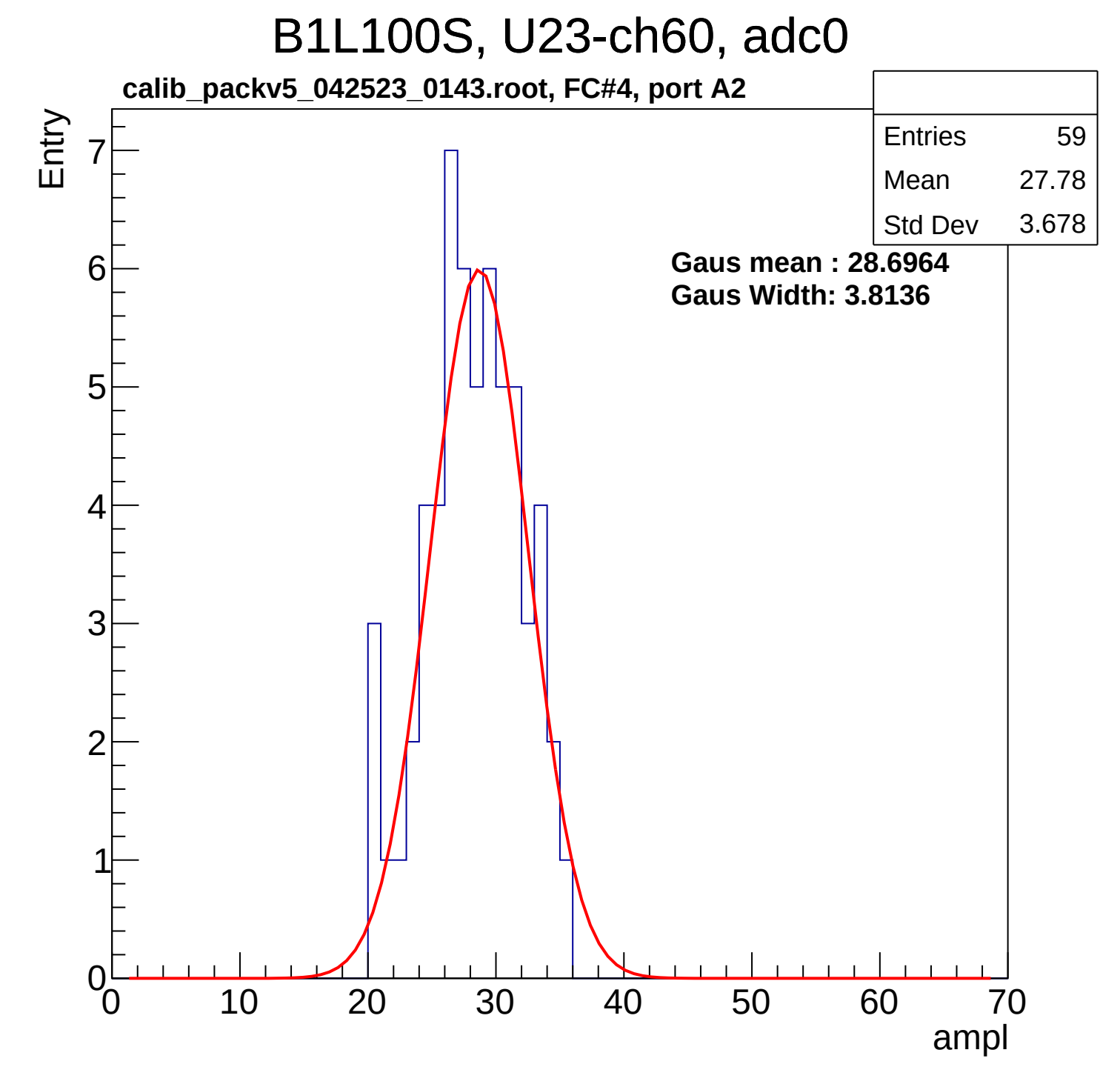
30
40
50
60
70
ampl


B1L100S, U23-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 27.78
Std Dev 3.678
Gaus mean : 28.6964
Gaus Width: 3.8136
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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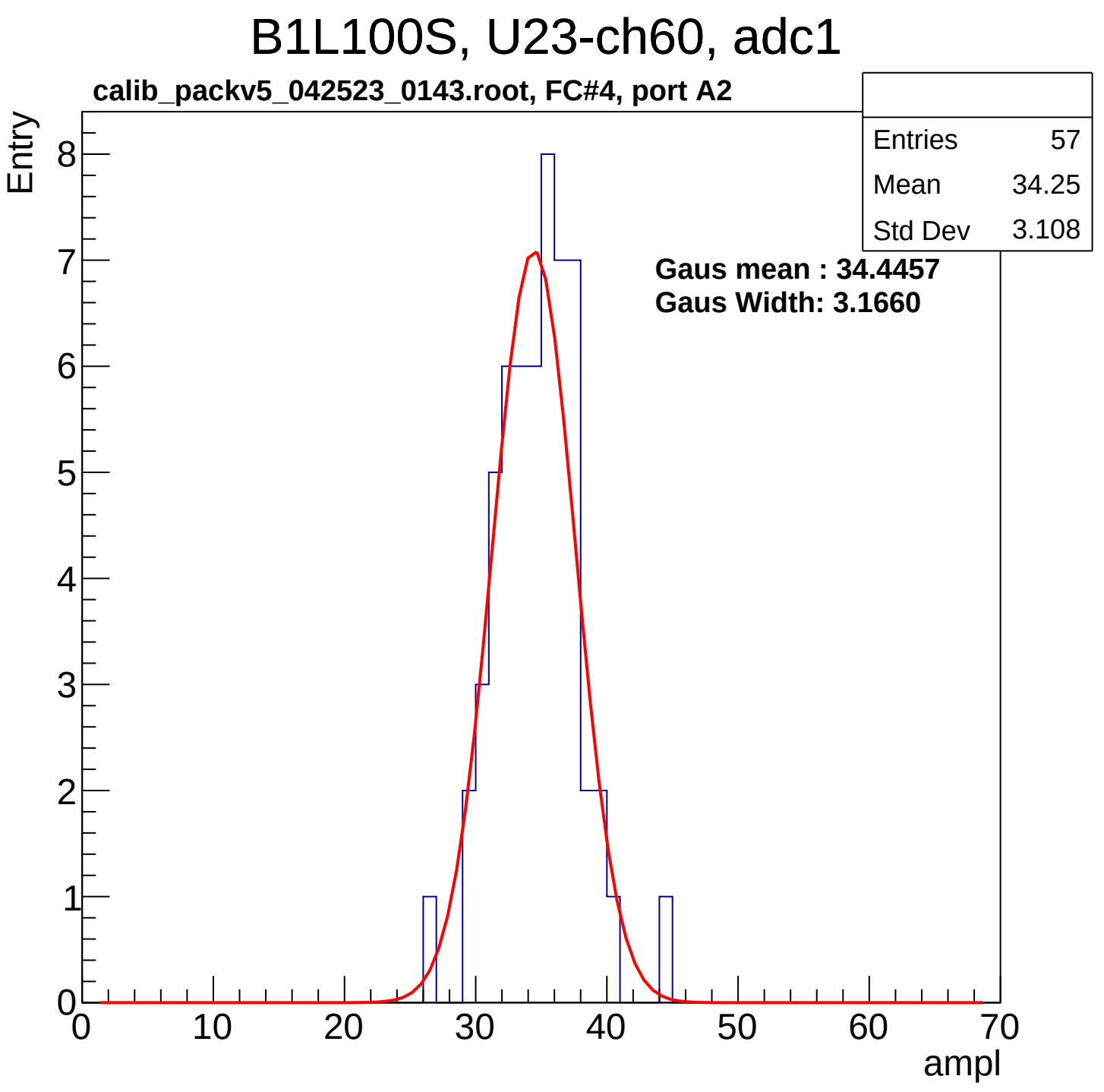
60
70


B1L100S, U23-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 34.25
Std Dev 3.108
Gaus mean : 34.4457
Gaus Width: 3.1660
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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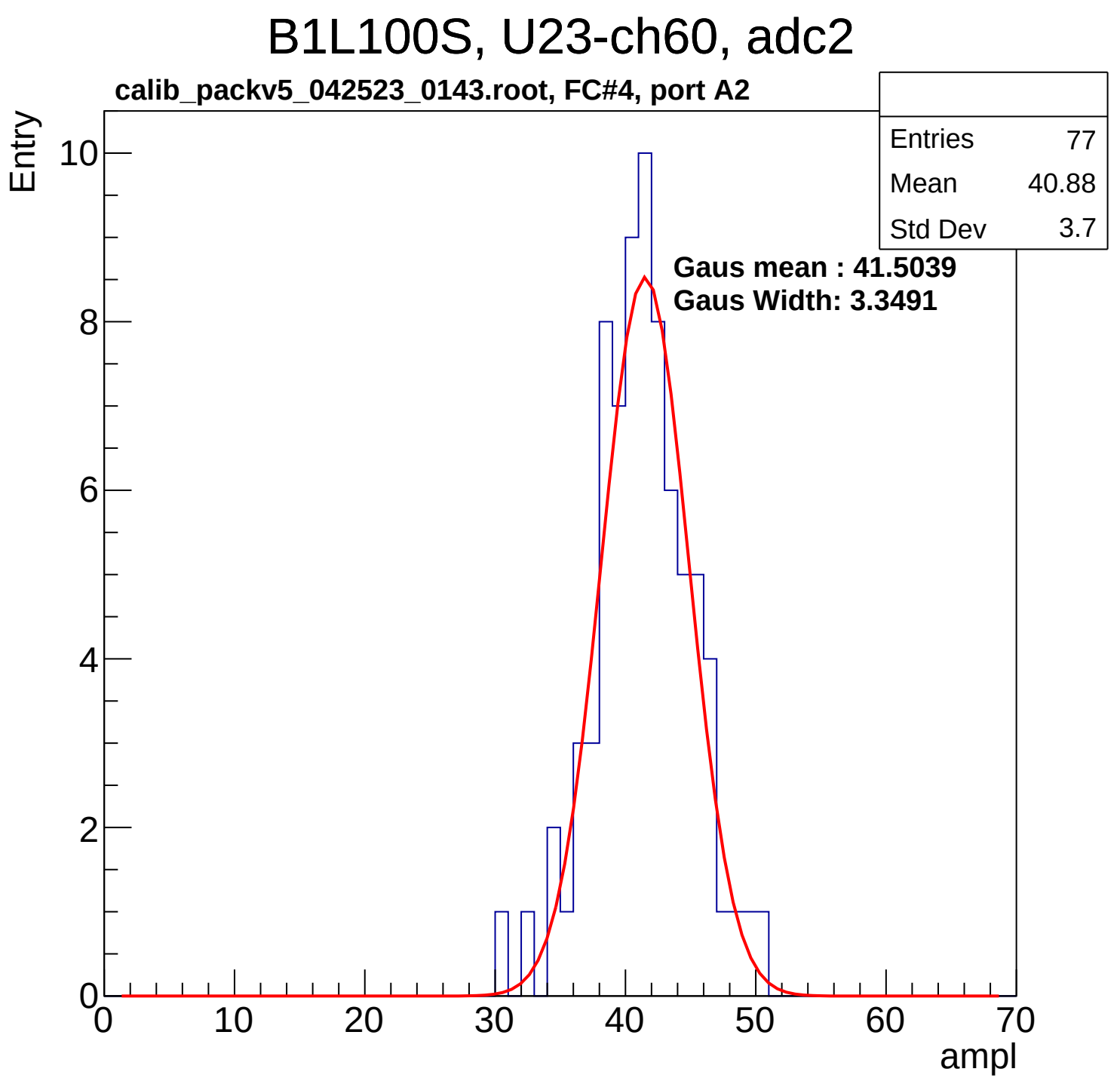
B1L100S, U23-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 40.88
Std Dev 3.7
Gaus mean : 41.5039
Gaus Width: 3.3491
Entry
ampl
0
2
4
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10
0
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40
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60
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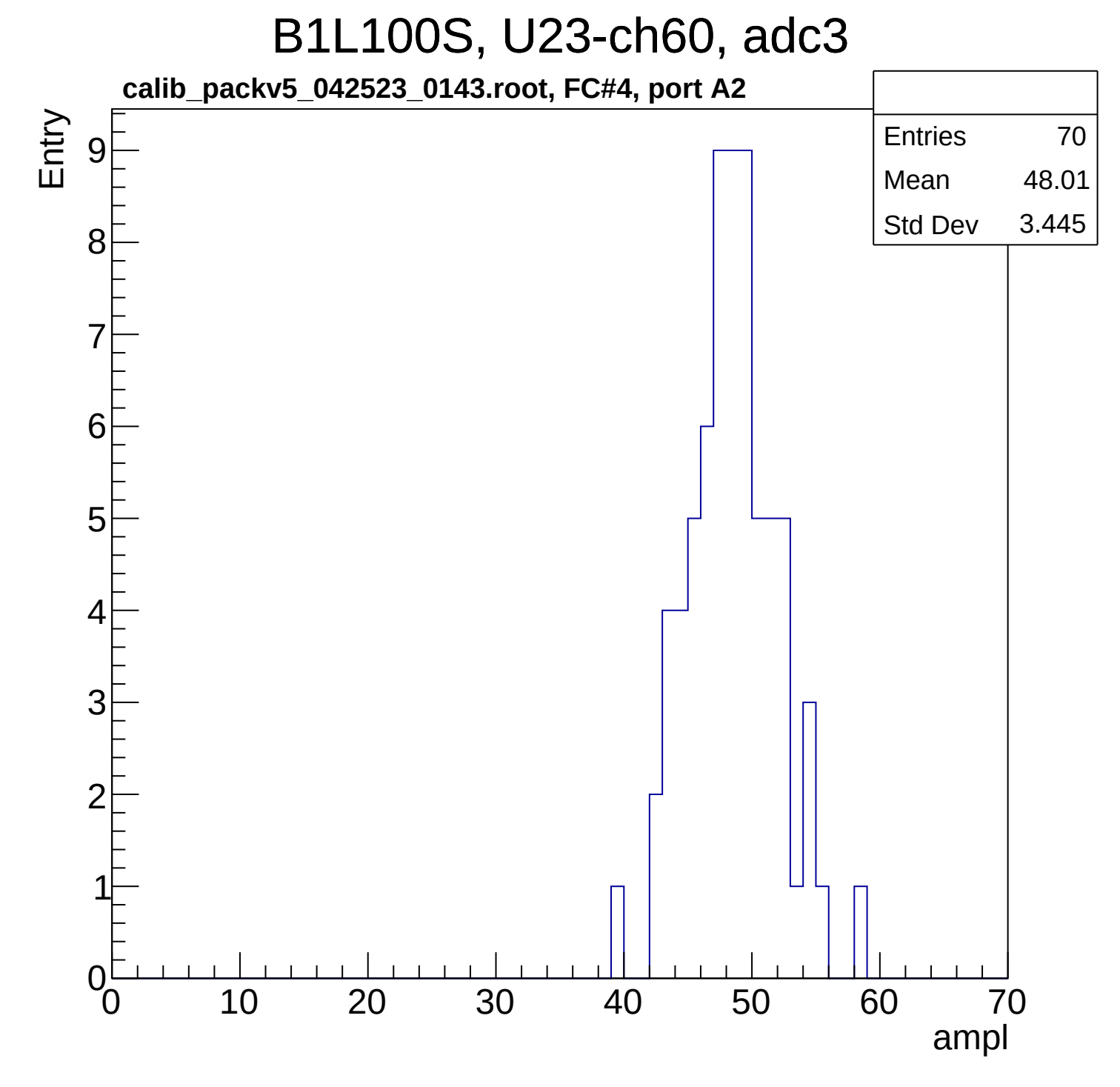
B1L100S, U23-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 48.01
Std Dev 3.445
Entry
ampl
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4
5
6
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8
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0
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60
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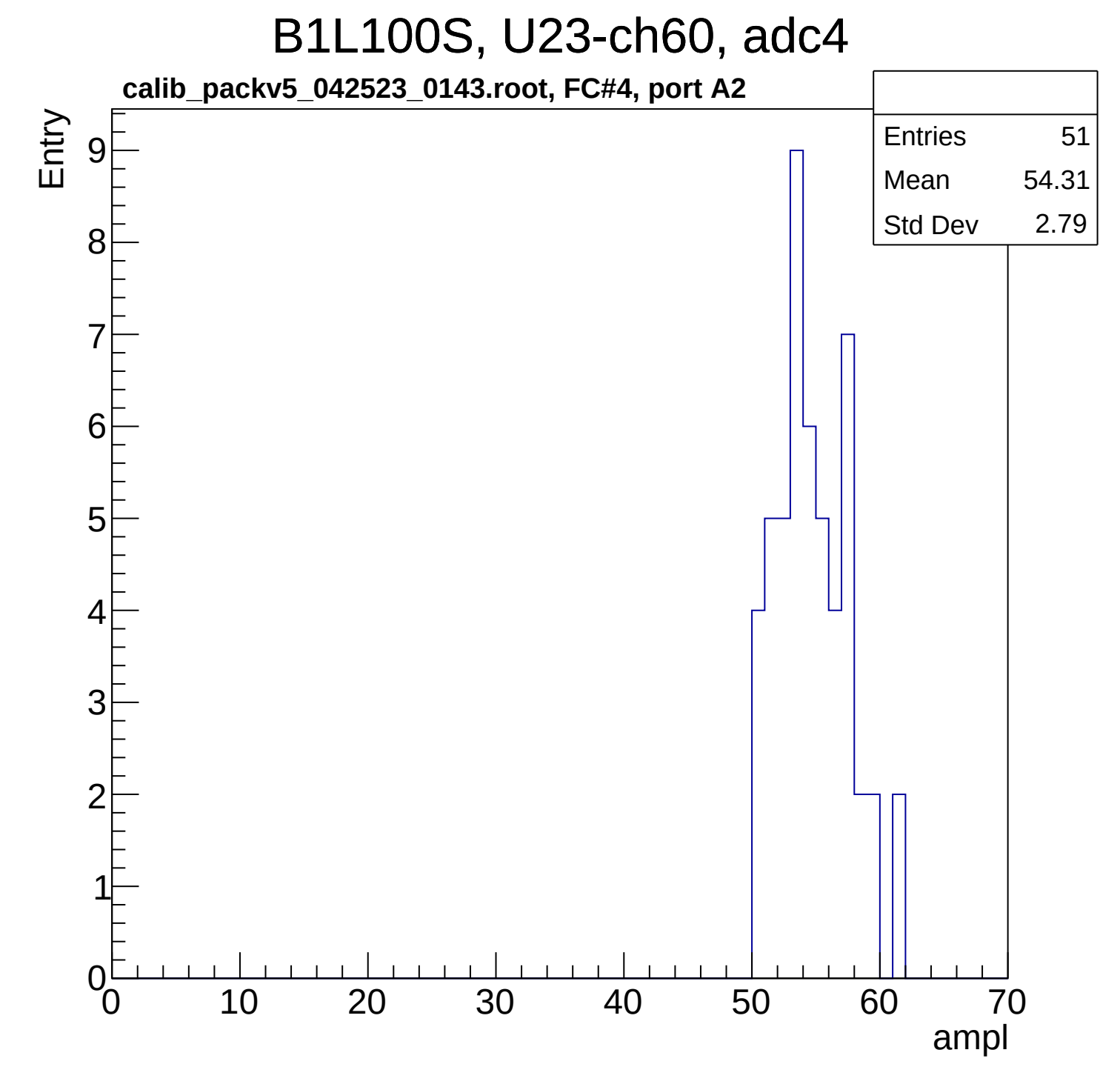
B1L100S, U23-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.31
Std Dev 2.79
Entry
ampl
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2
3
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6
7
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0
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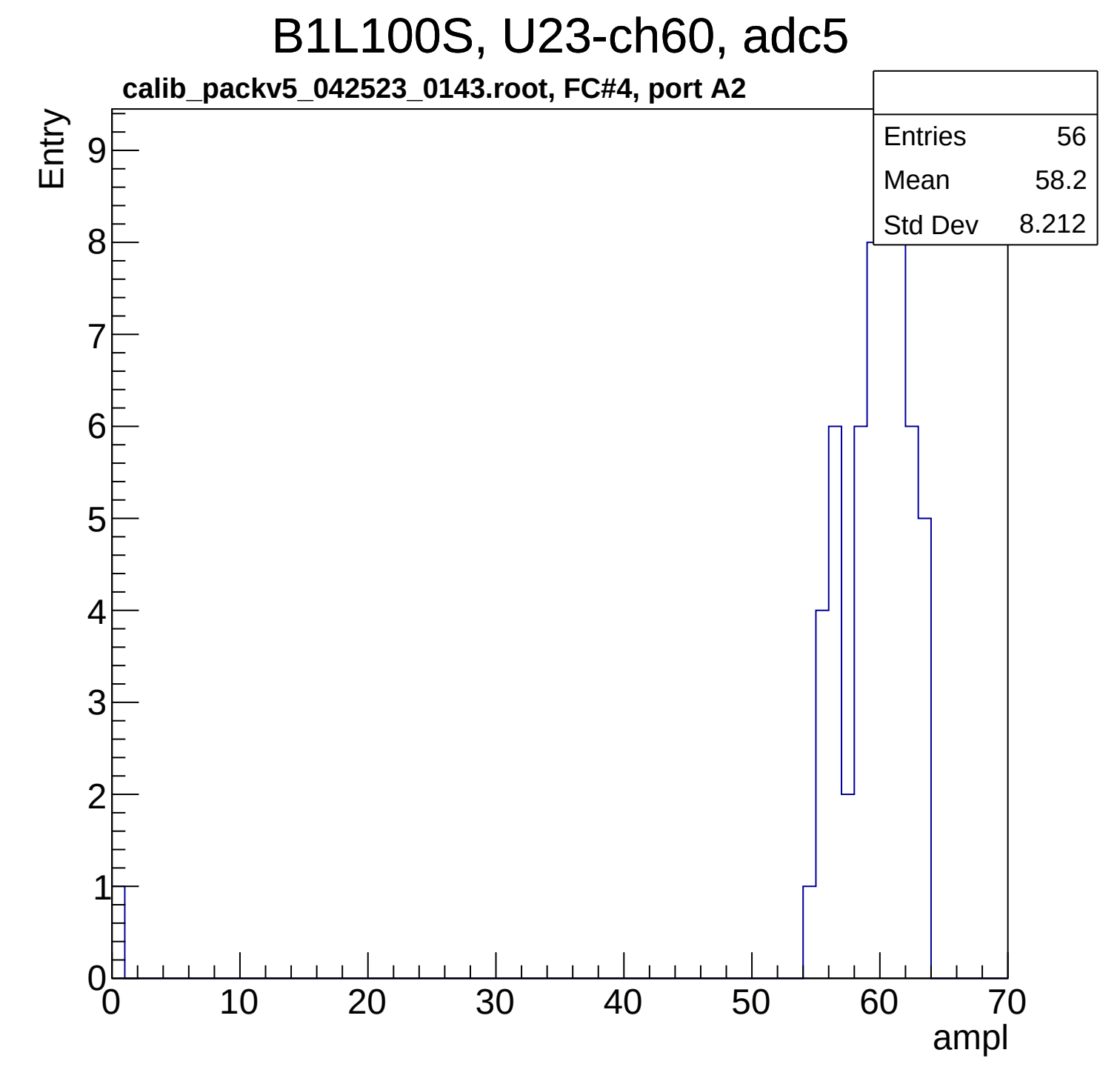
B1L100S, U23-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.2
Std Dev 8.212
Entry
ampl
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6
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0
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20
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60
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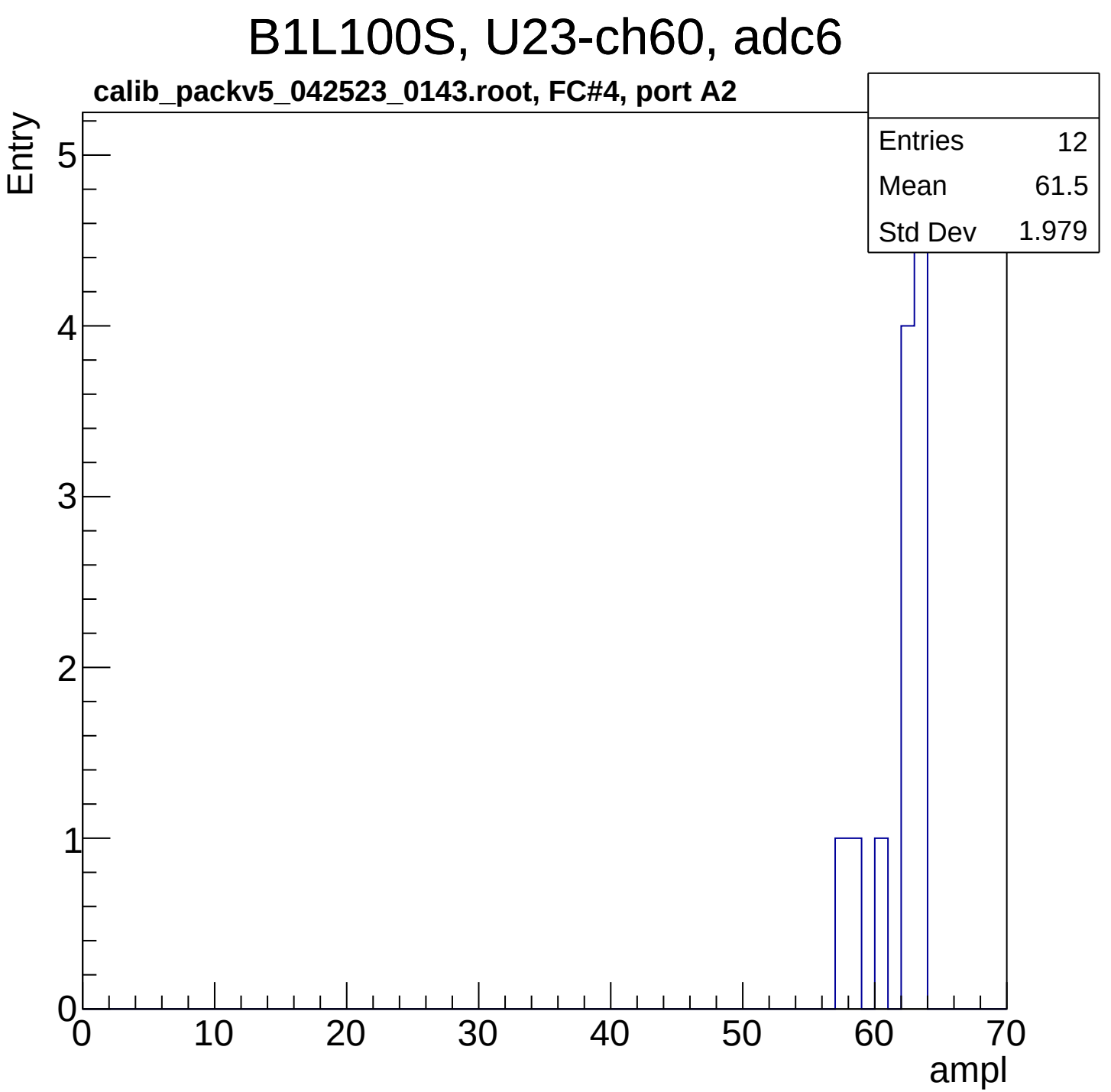
B1L100S, U23-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 61.5
Std Dev 1.979
Entry
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3
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5
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10
20
30
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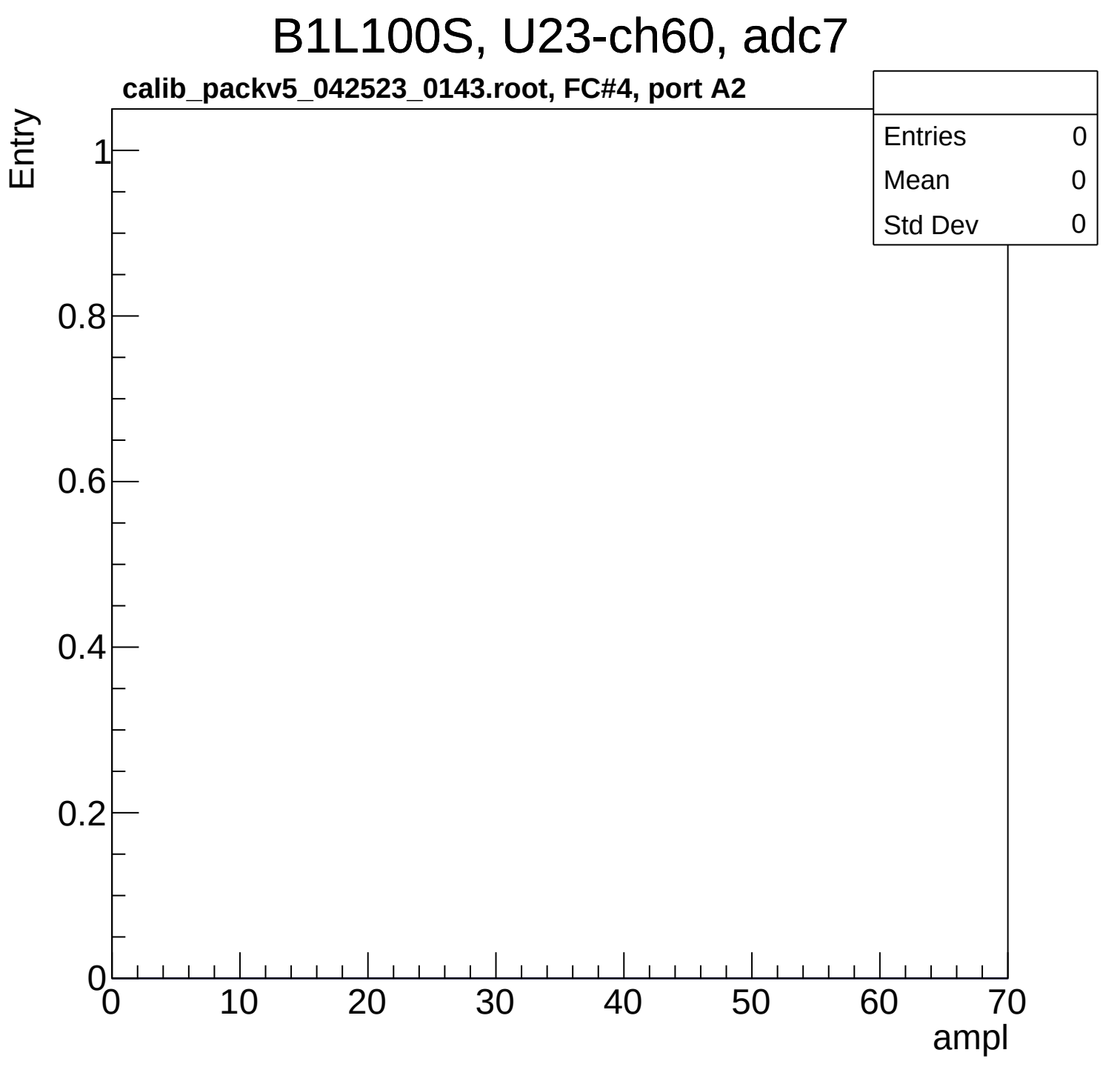
50
60
70
ampl


B1L100S, U23-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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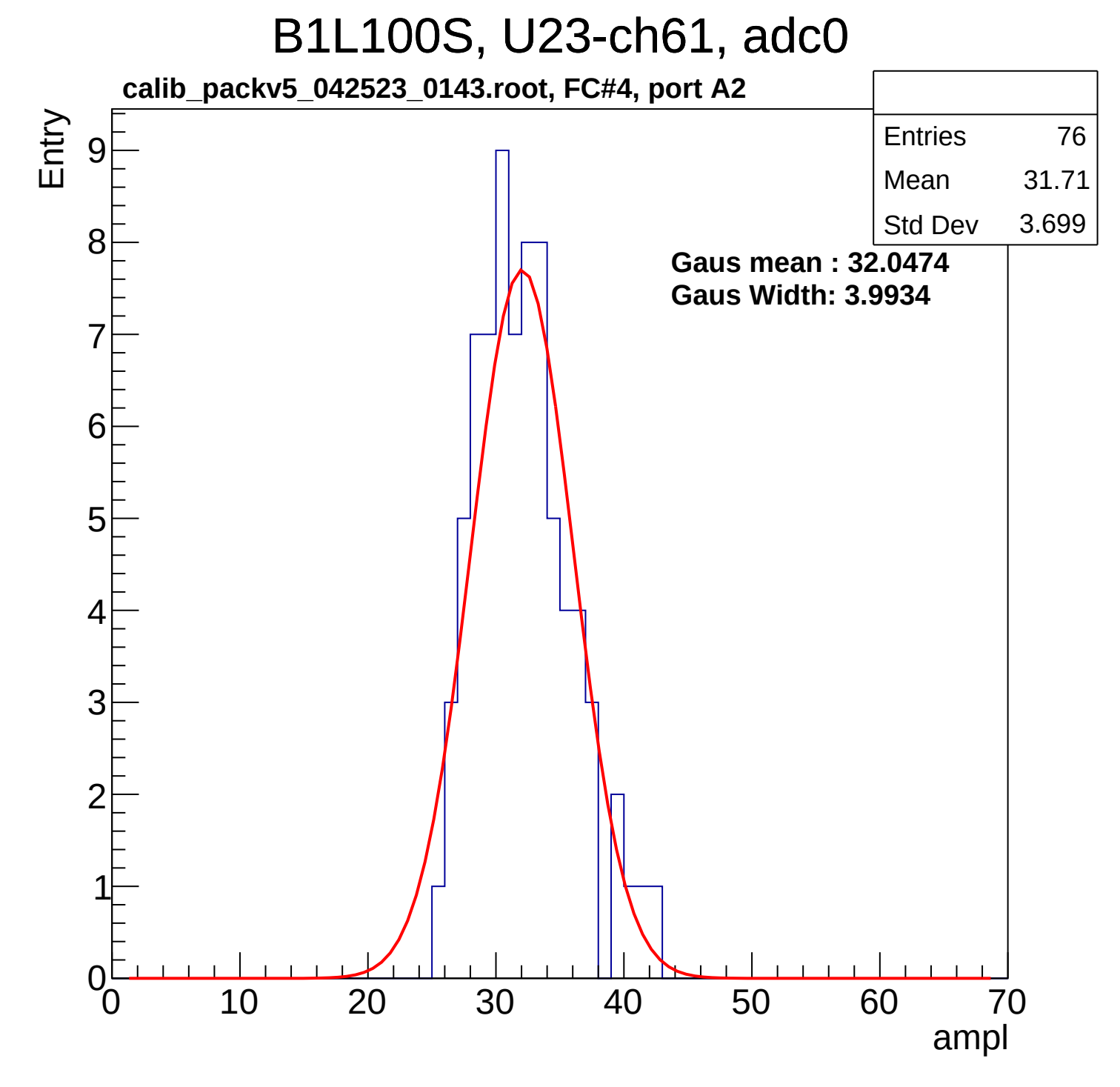
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 31.71
Std Dev 3.699
Gaus mean : 32.0474
Gaus Width: 3.9934
Entry
ampl
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3
4
5
6
7
8
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0
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60
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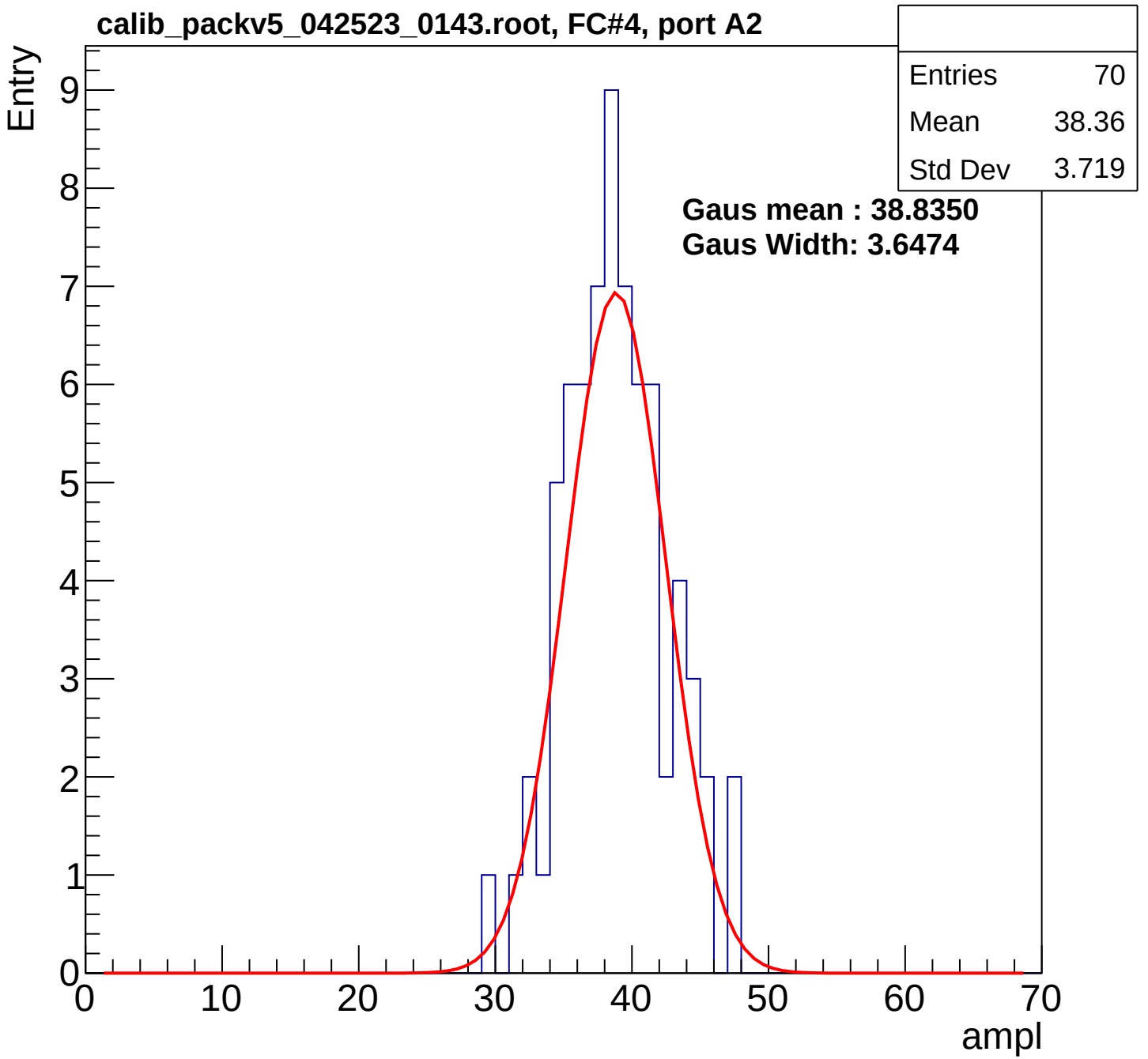
B1L100S, U23-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 38.36
Std Dev 3.719
Gaus mean : 38.8350
Gaus Width: 3.6474
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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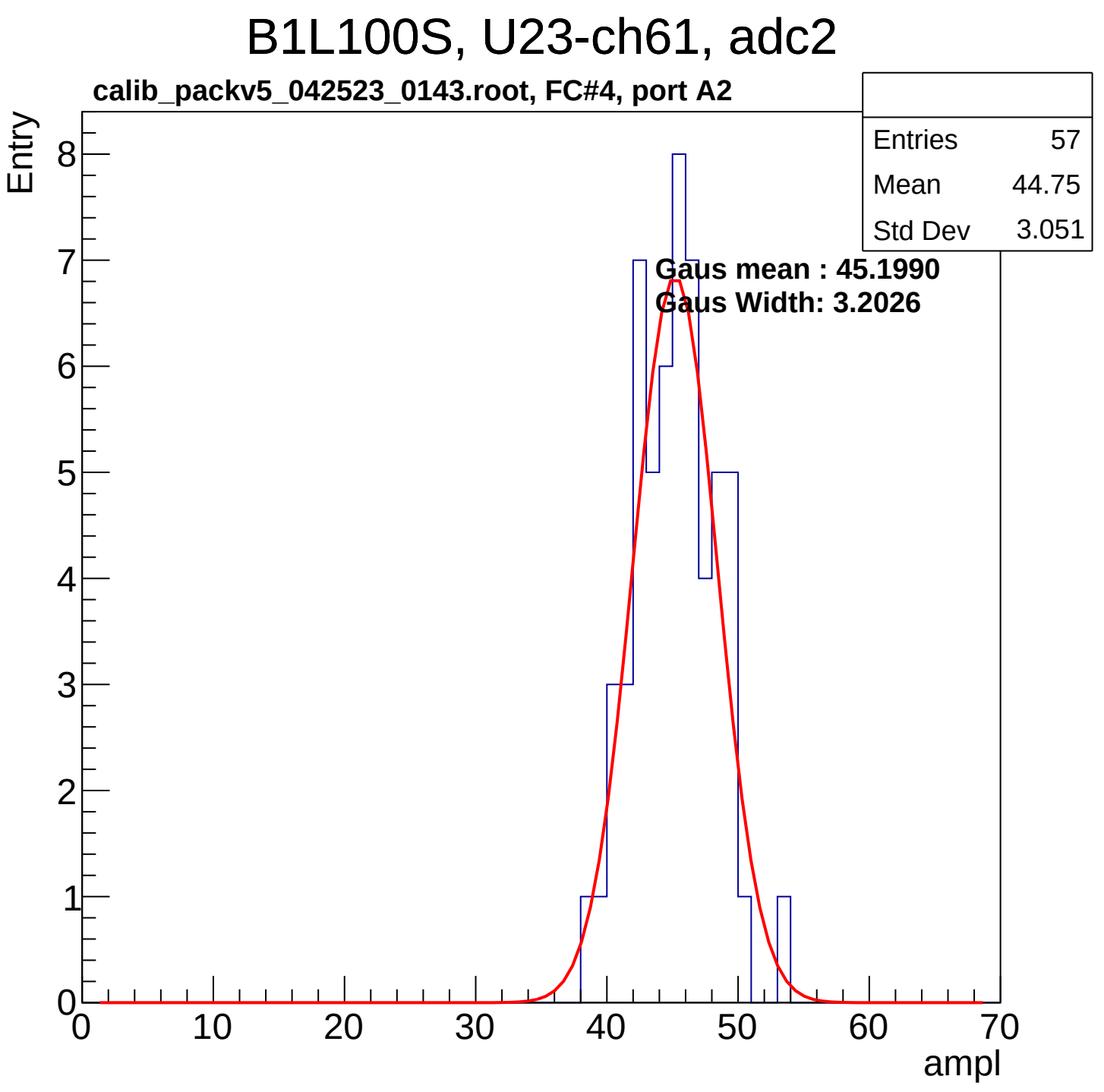
B1L100S, U23-ch61, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.75
Std Dev 3.051
Gaus mean : 45.1990
Gaus Width: 3.2026
Entry
ampl
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3
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5
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7
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0
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20
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40
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60
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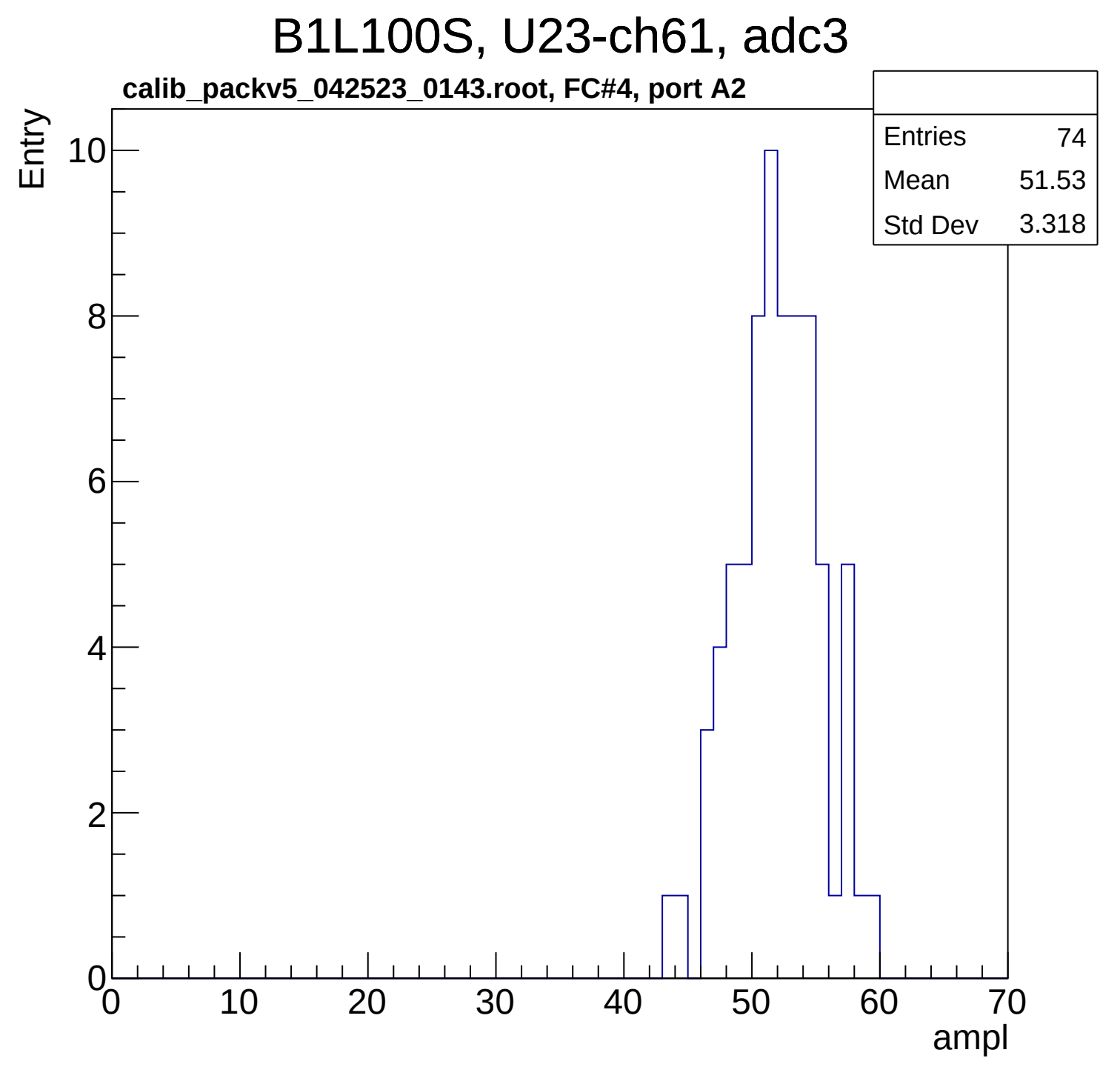
B1L100S, U23-ch61, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.53
Std Dev 3.318
Entry
ampl
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4
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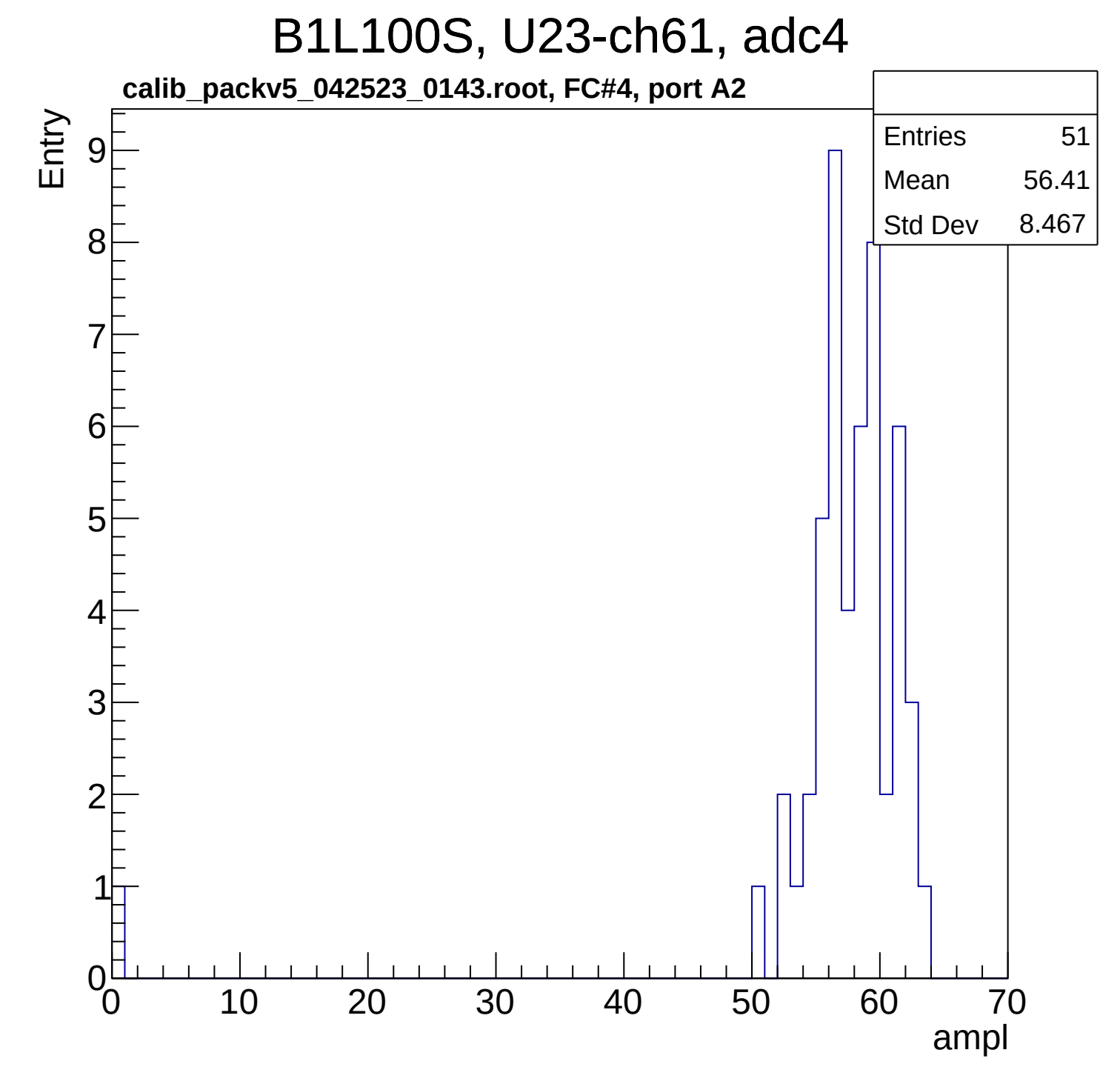
B1L100S, U23-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.41
Std Dev 8.467
Entry
ampl
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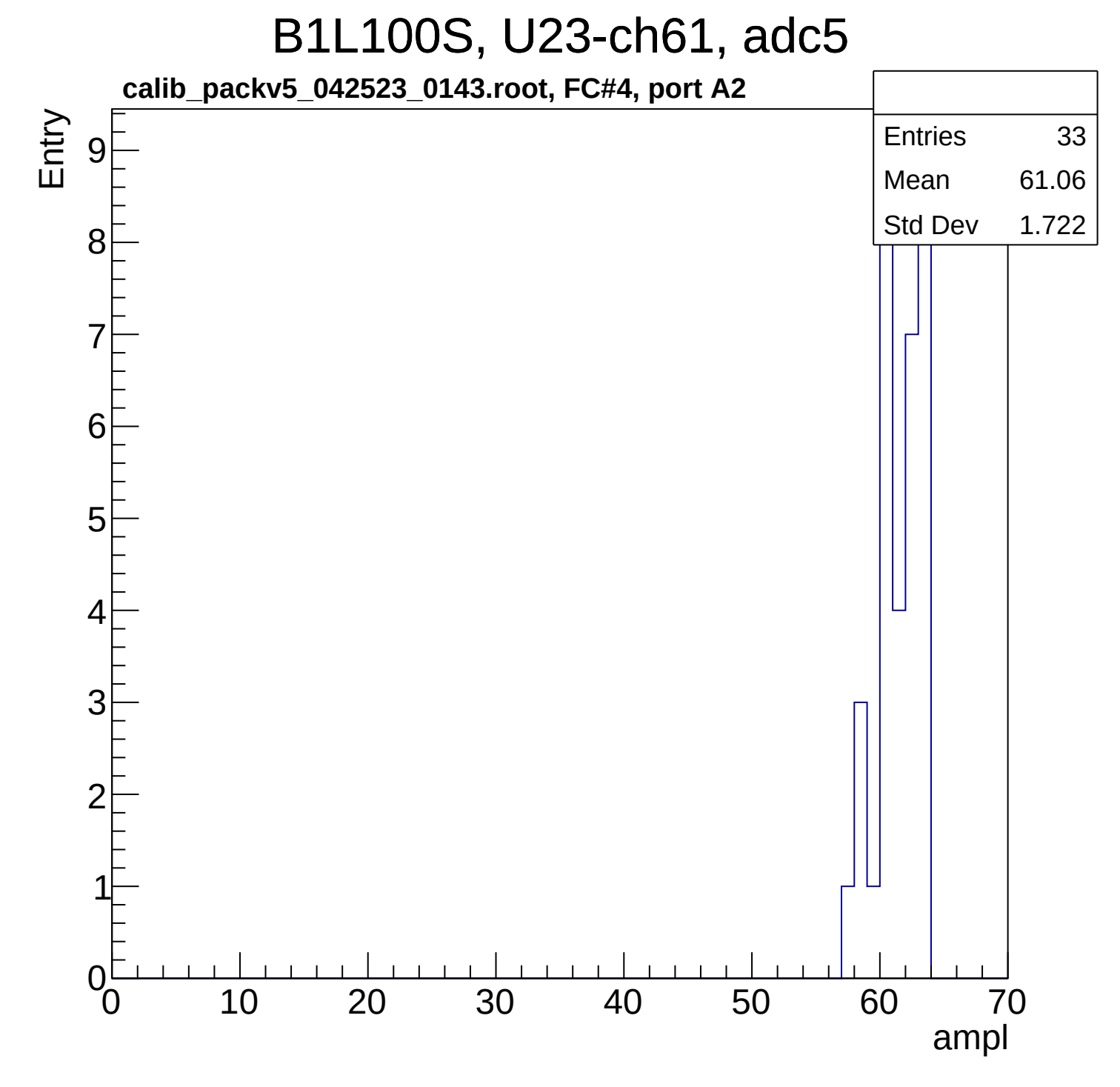
B1L100S, U23-ch61, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 61.06
Std Dev 1.722
Entry
ampl
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3
4
5
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7
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0
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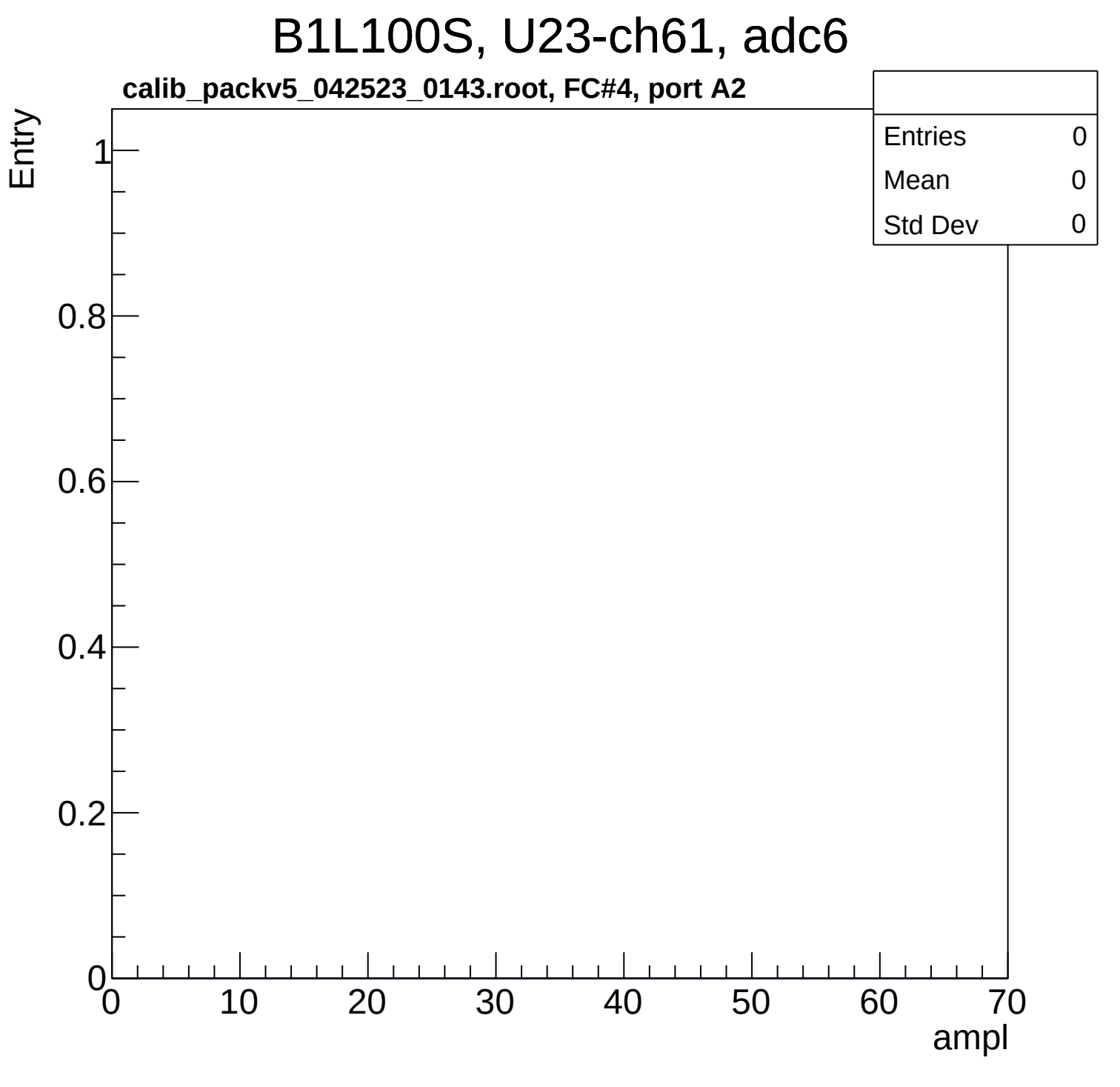
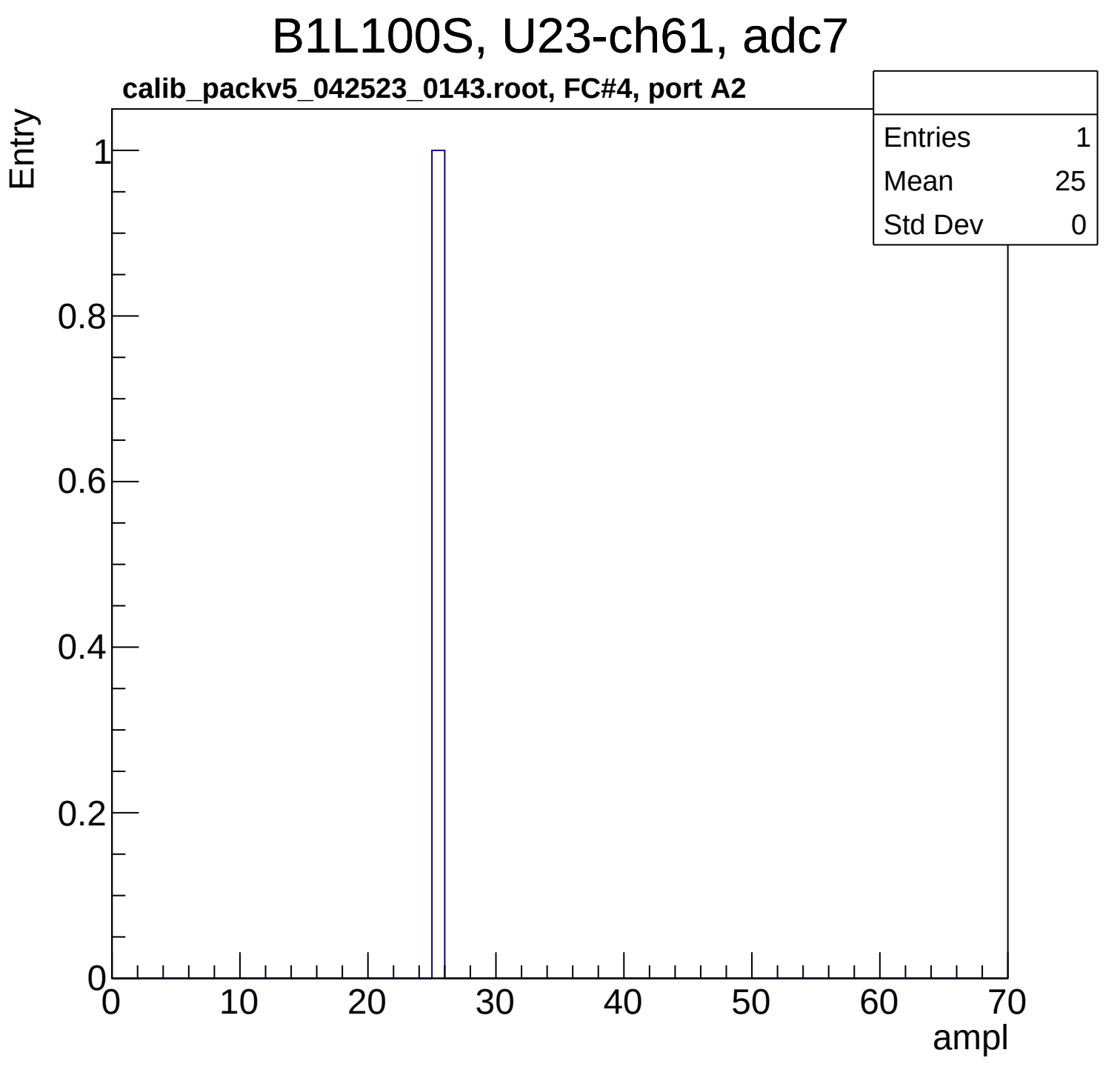

B1L100S, U23-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 25
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
20
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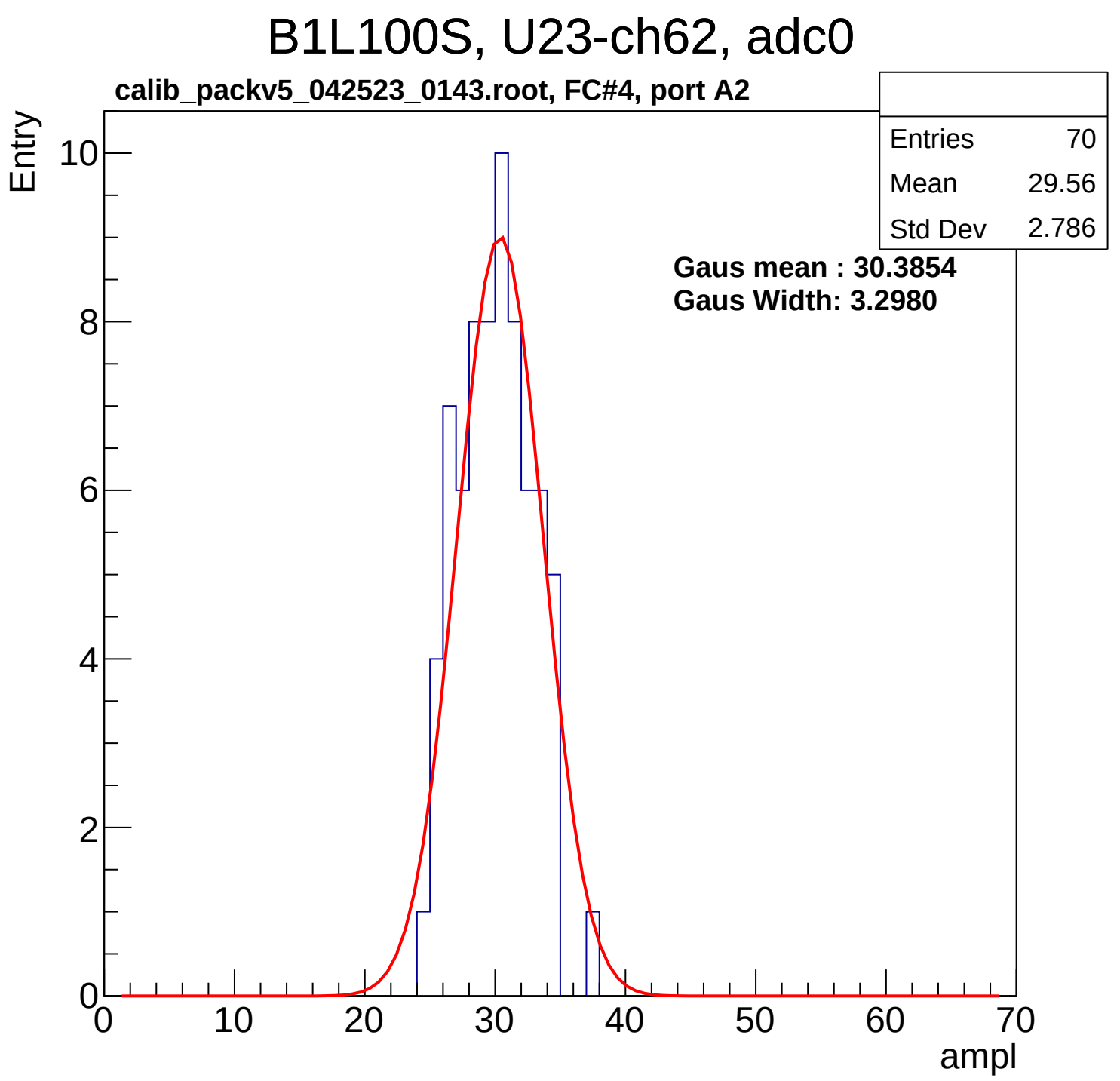
50
60
70
ampl


B1L100S, U23-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 29.56
Std Dev 2.786
Gaus mean : 30.3854
Gaus Width: 3.2980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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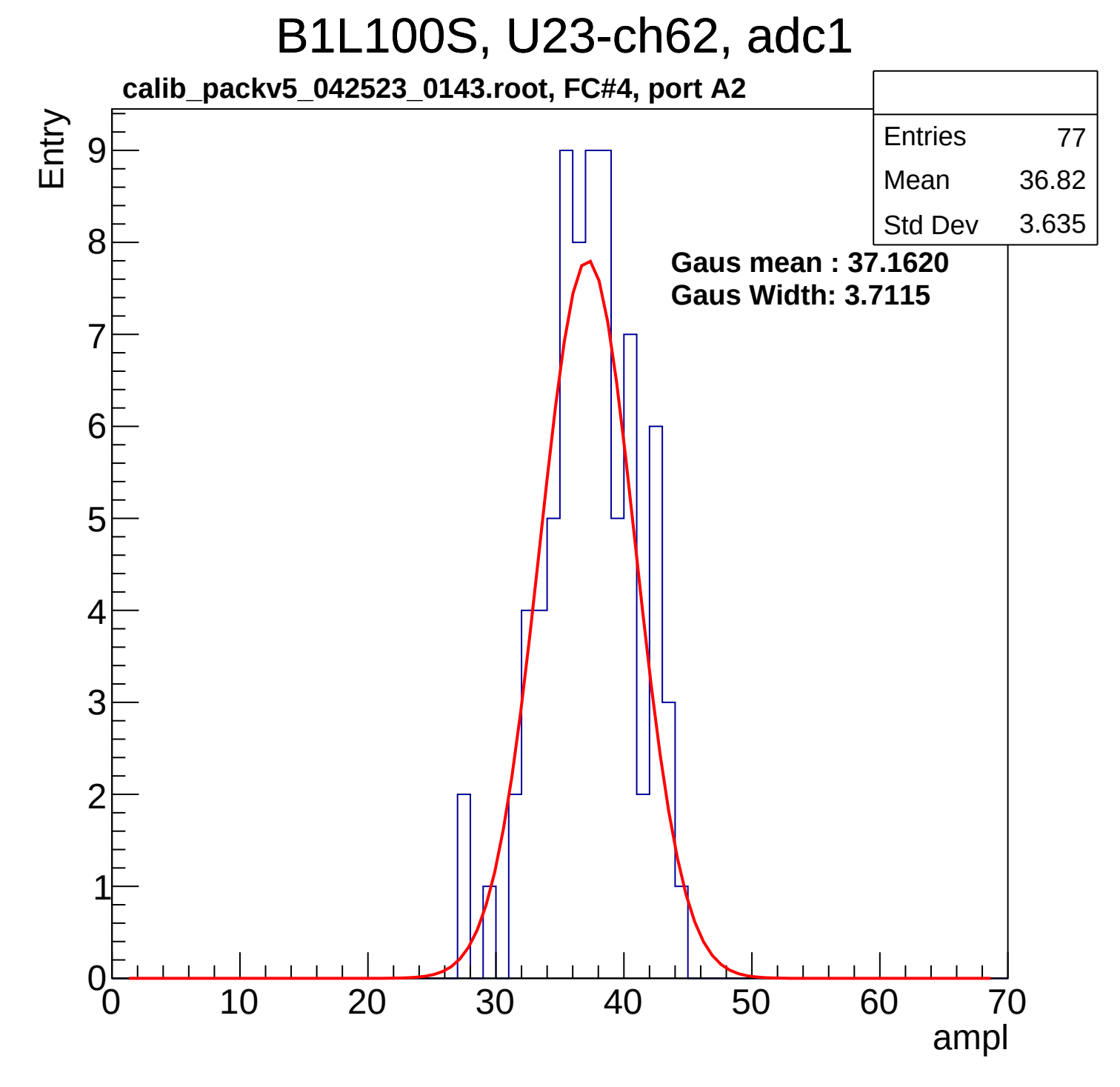
B1L100S, U23-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.82
Std Dev 3.635
Gaus mean : 37.1620
Gaus Width: 3.7115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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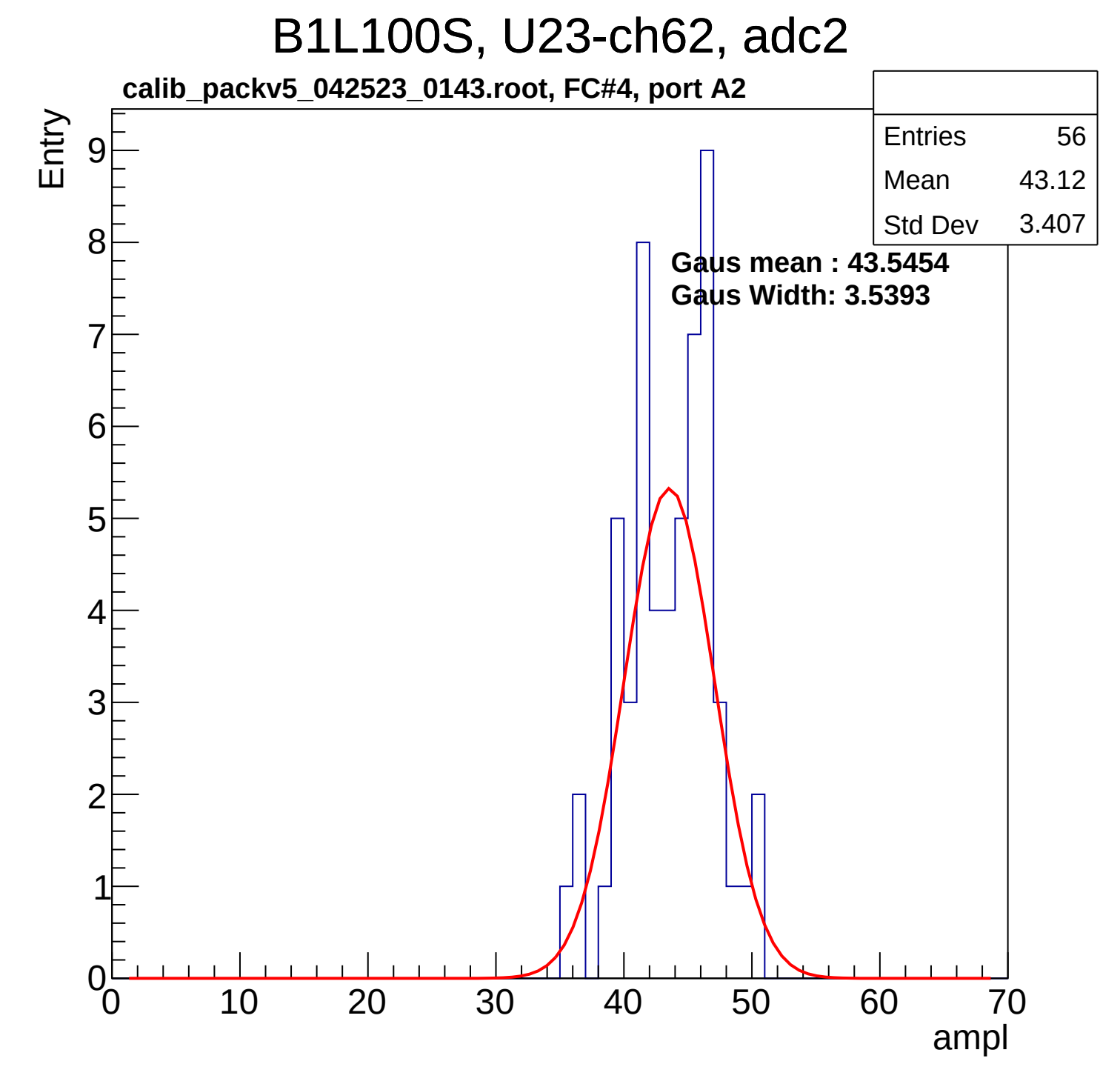
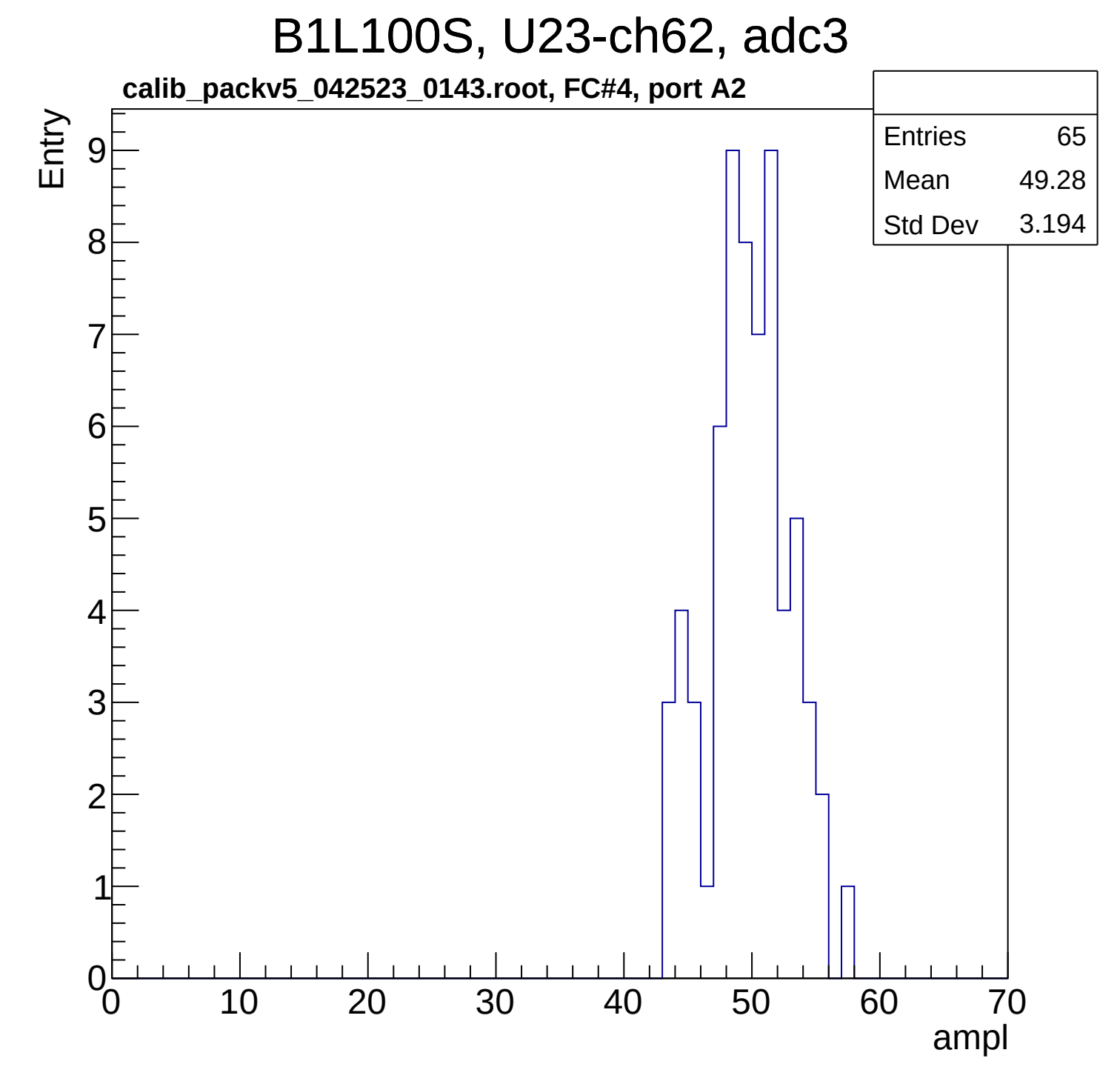

B1L100S, U23-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.12
Std Dev 3.407
Gaus mean : 43.5454
Gaus Width: 3.5393
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U23-ch62, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.28
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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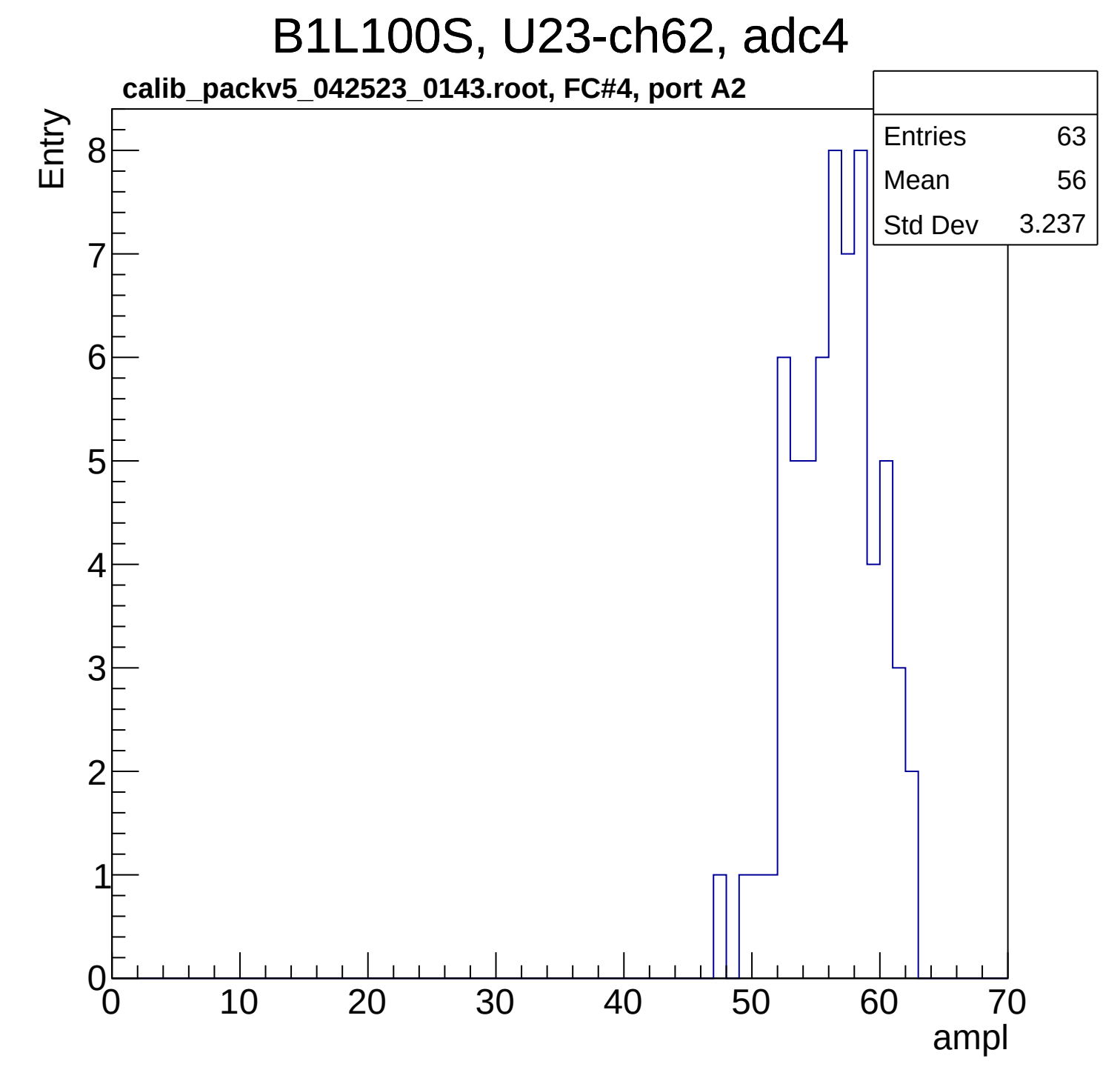
B1L100S, U23-ch62, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56
Std Dev 3.237
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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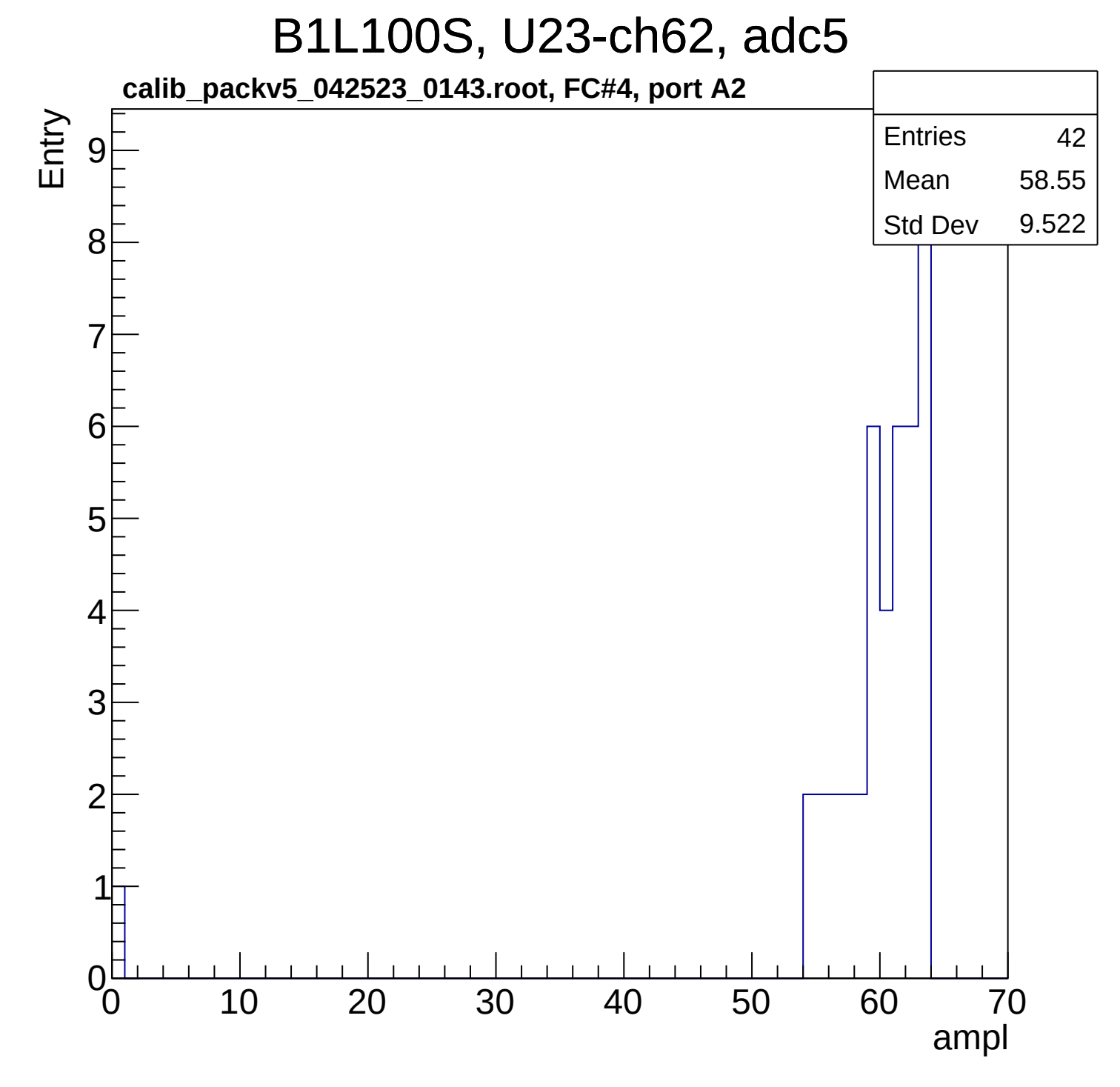
B1L100S, U23-ch62, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.55
Std Dev 9.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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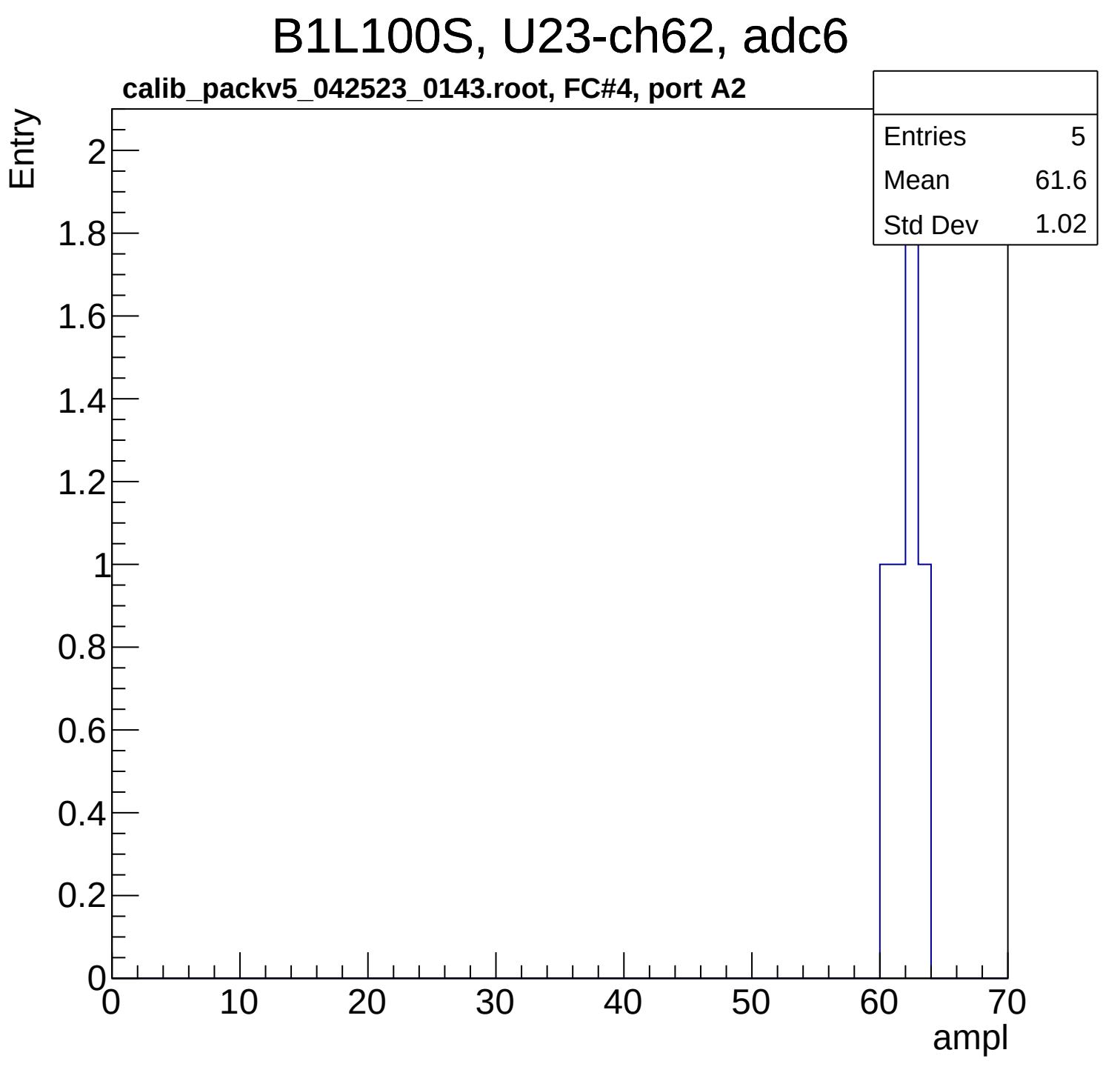
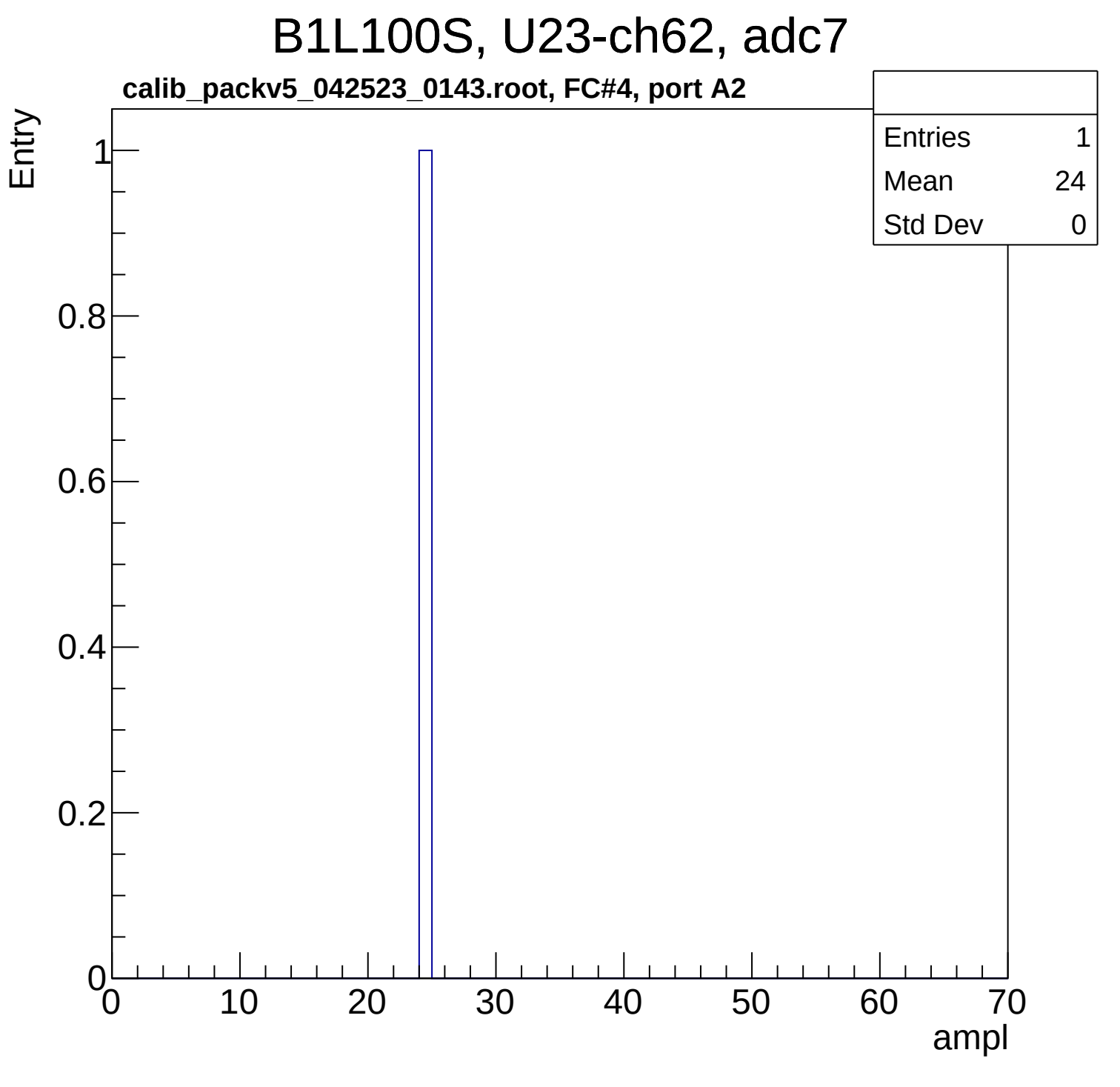
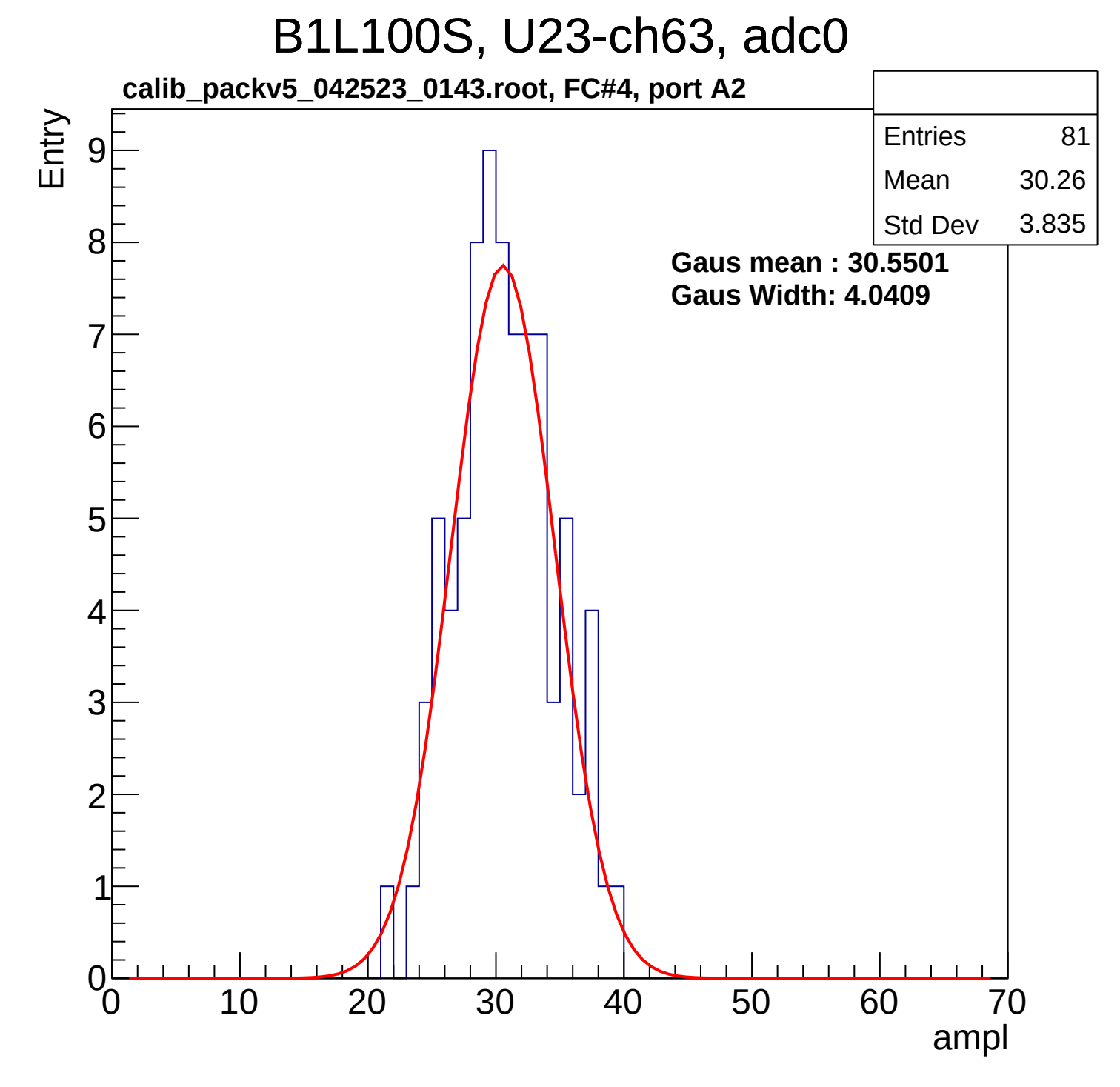
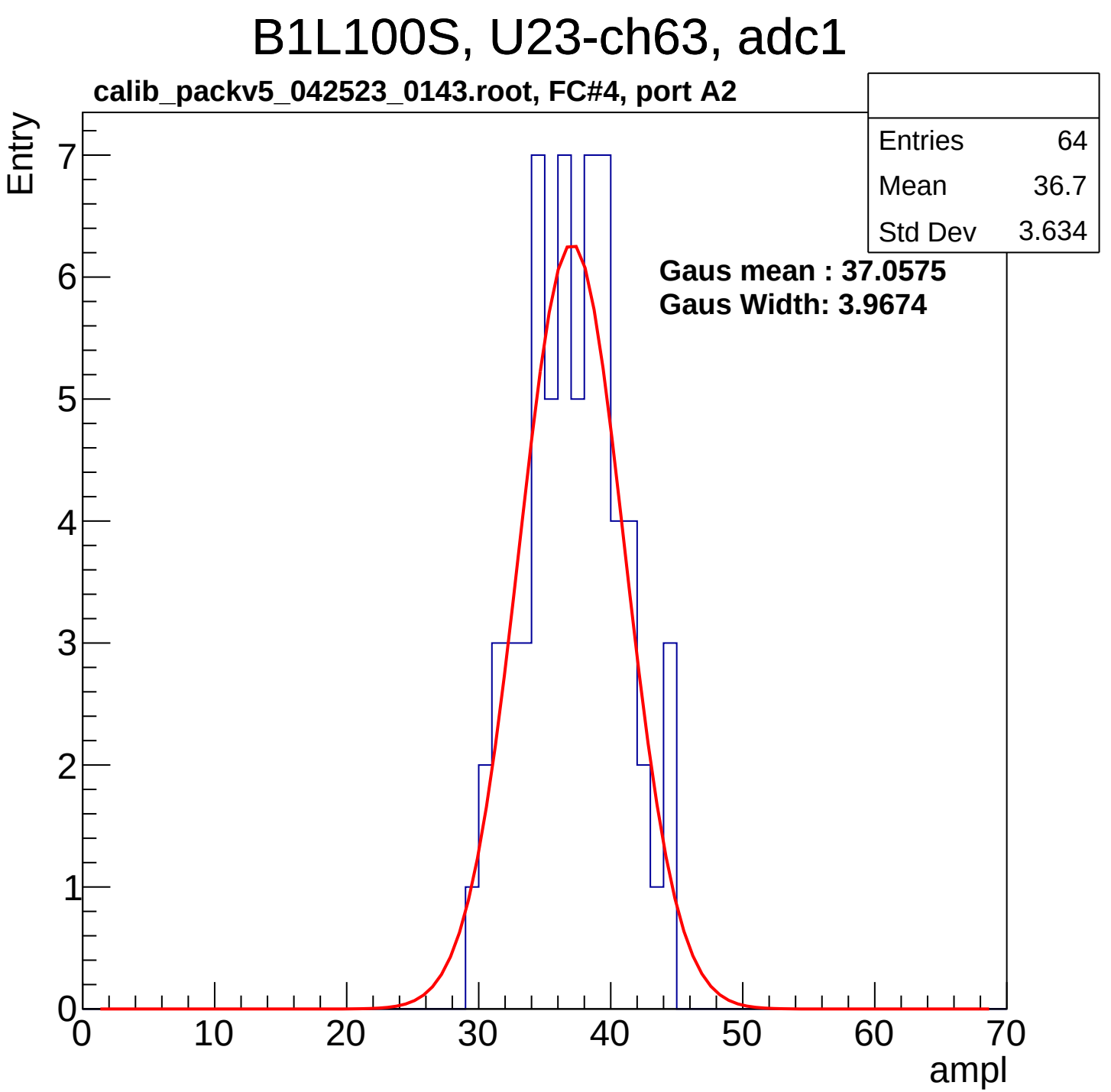

B1L100S, U23-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch62, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch63, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.26
Std Dev 3.835
Gaus mean : 30.5501
Gaus Width: 4.0409
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.7
Std Dev 3.634
Gaus mean : 37.0575
Gaus Width: 3.9674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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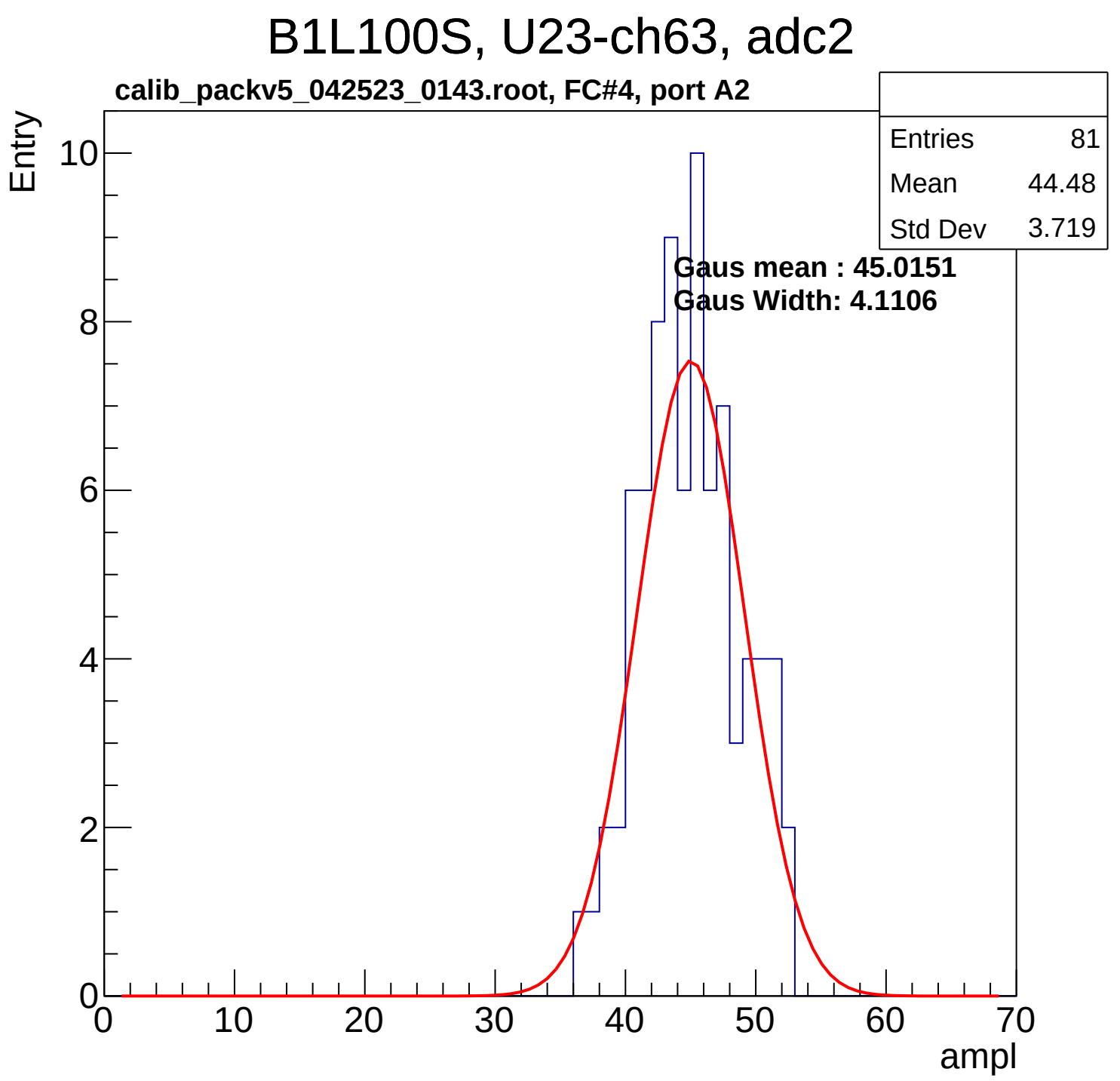
B1L100S, U23-ch63, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 44.48
Std Dev 3.719
Gaus mean : 45.0151
Gaus Width: 4.1106
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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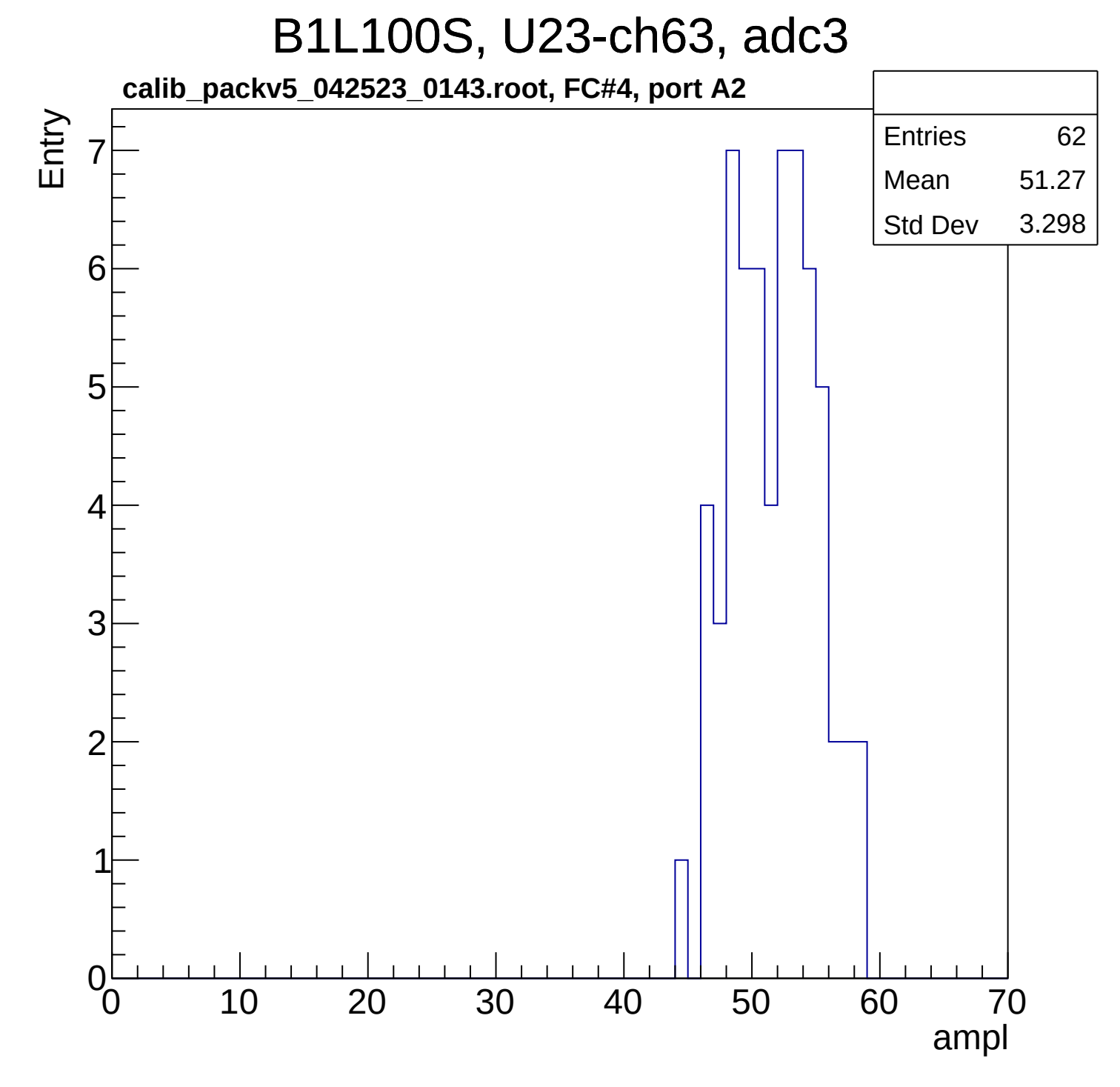
B1L100S, U23-ch63, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.27
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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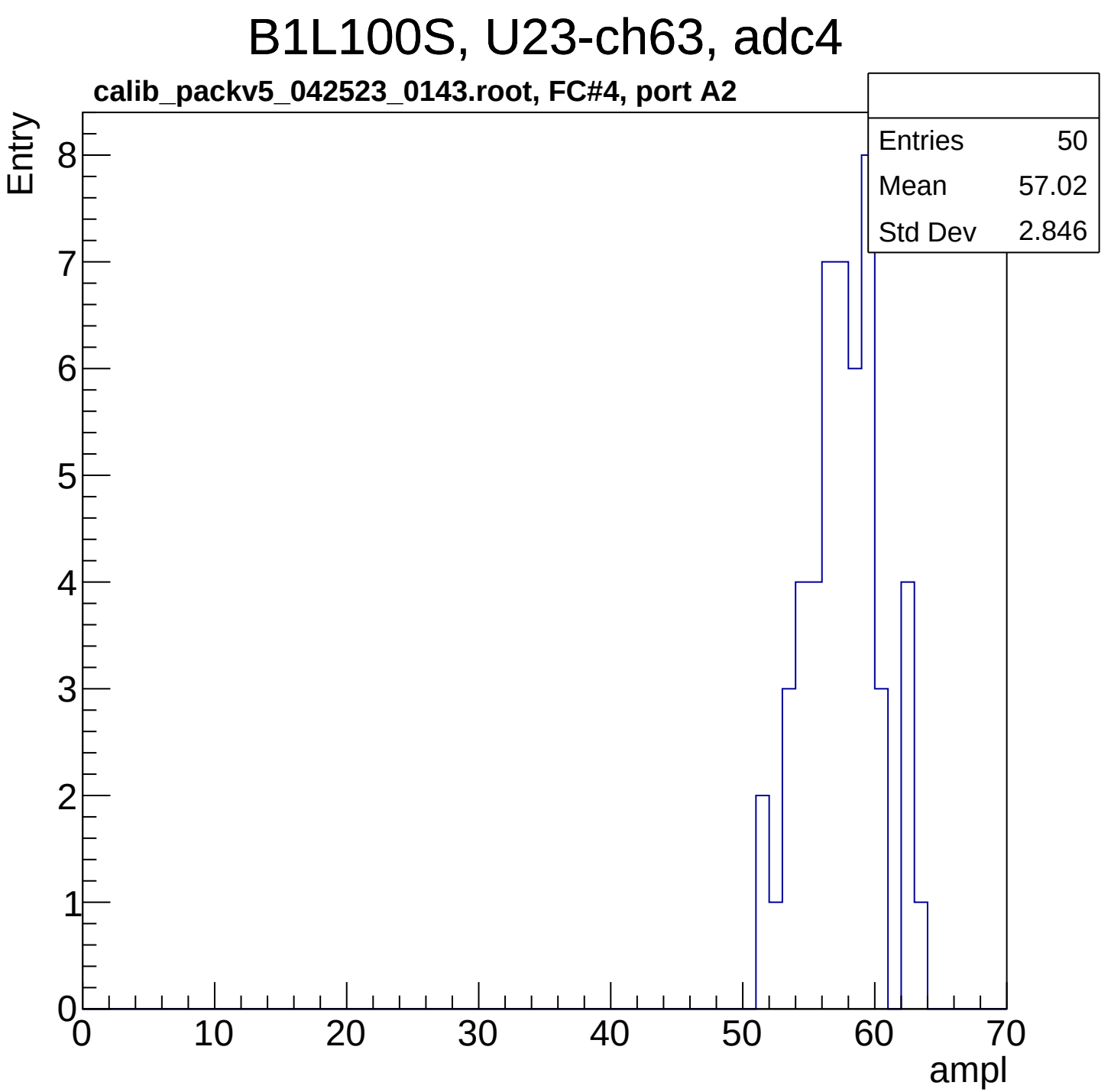
B1L100S, U23-ch63, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.02
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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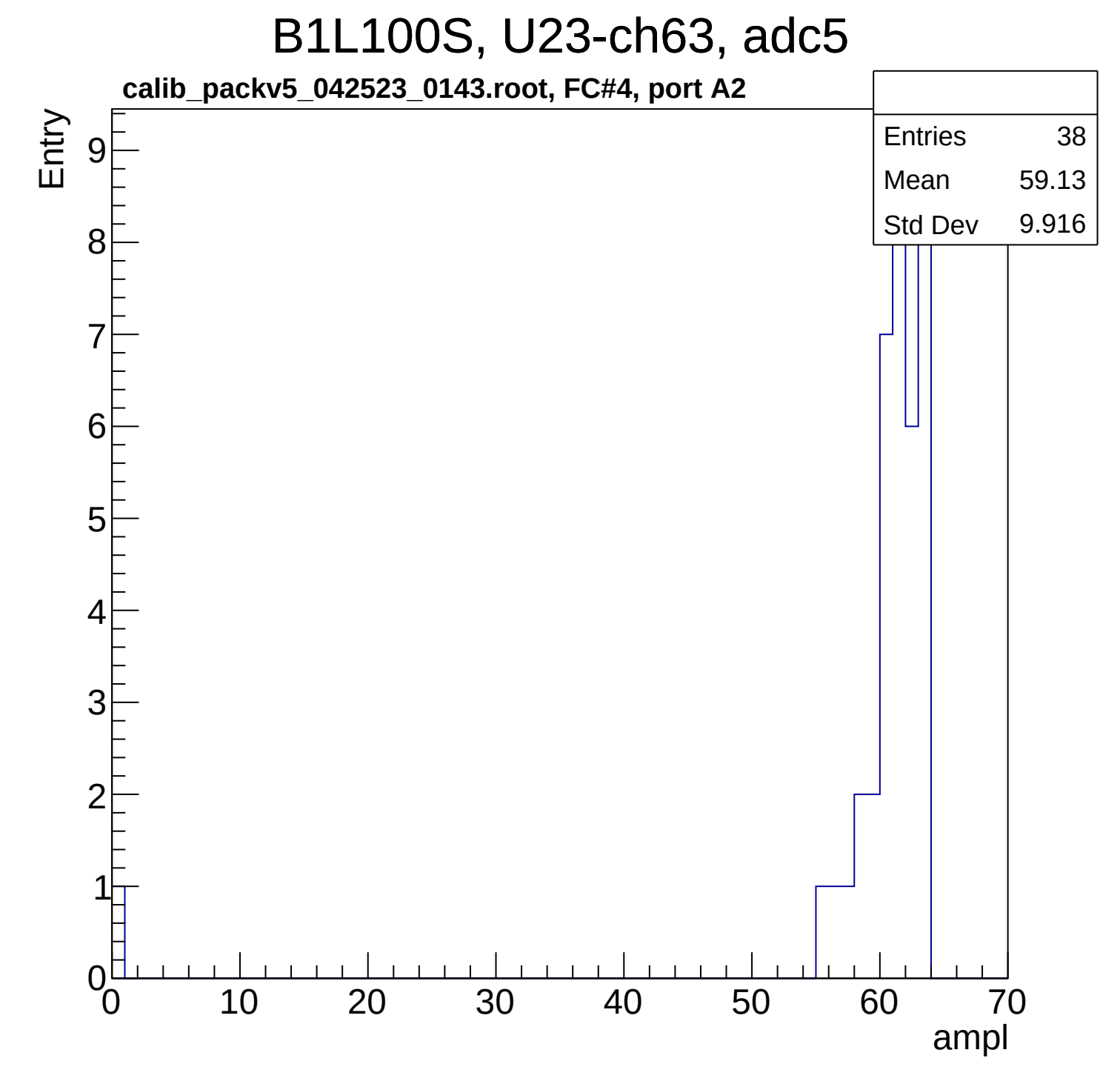
B1L100S, U23-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.13
Std Dev 9.916
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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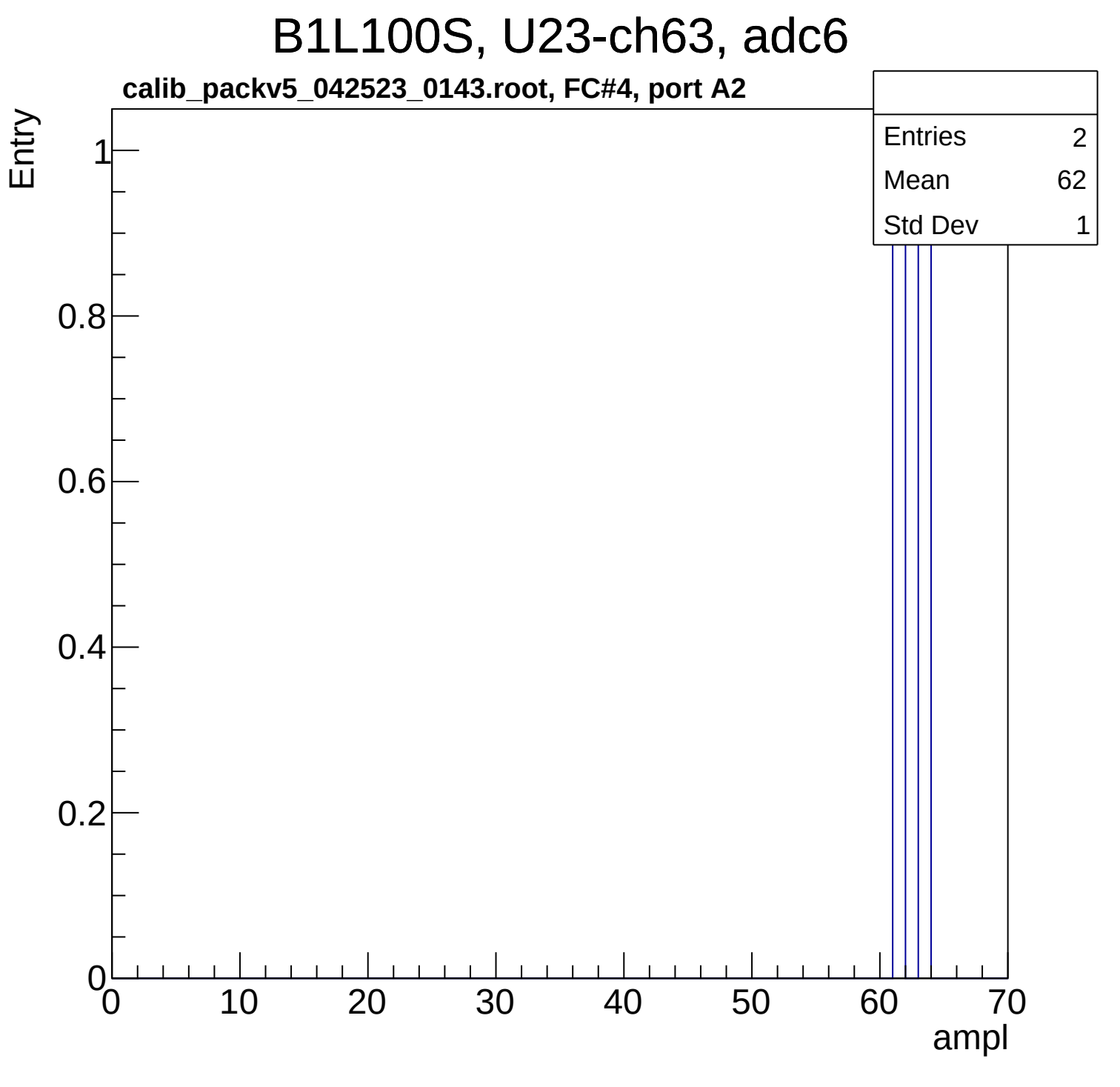
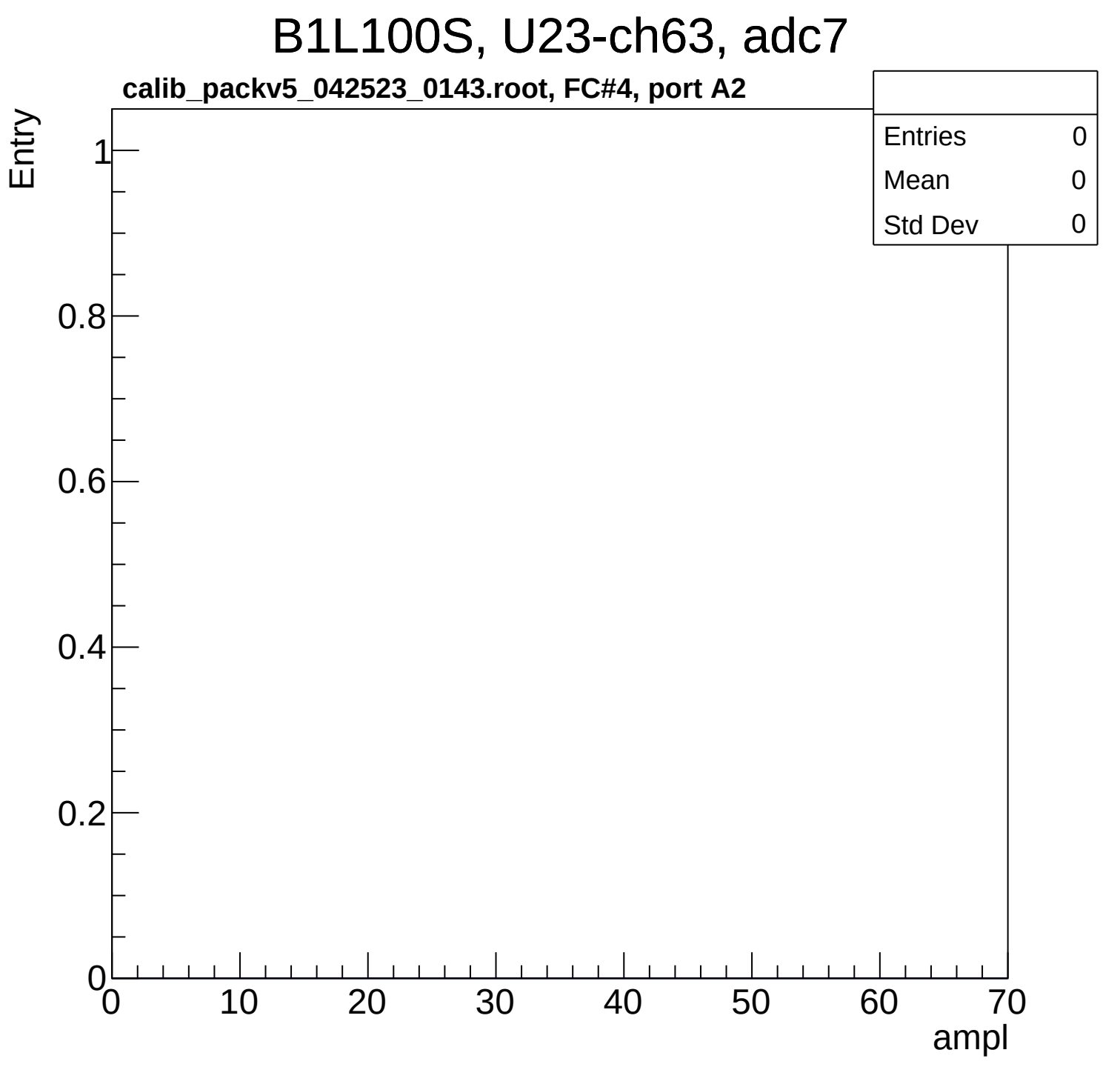
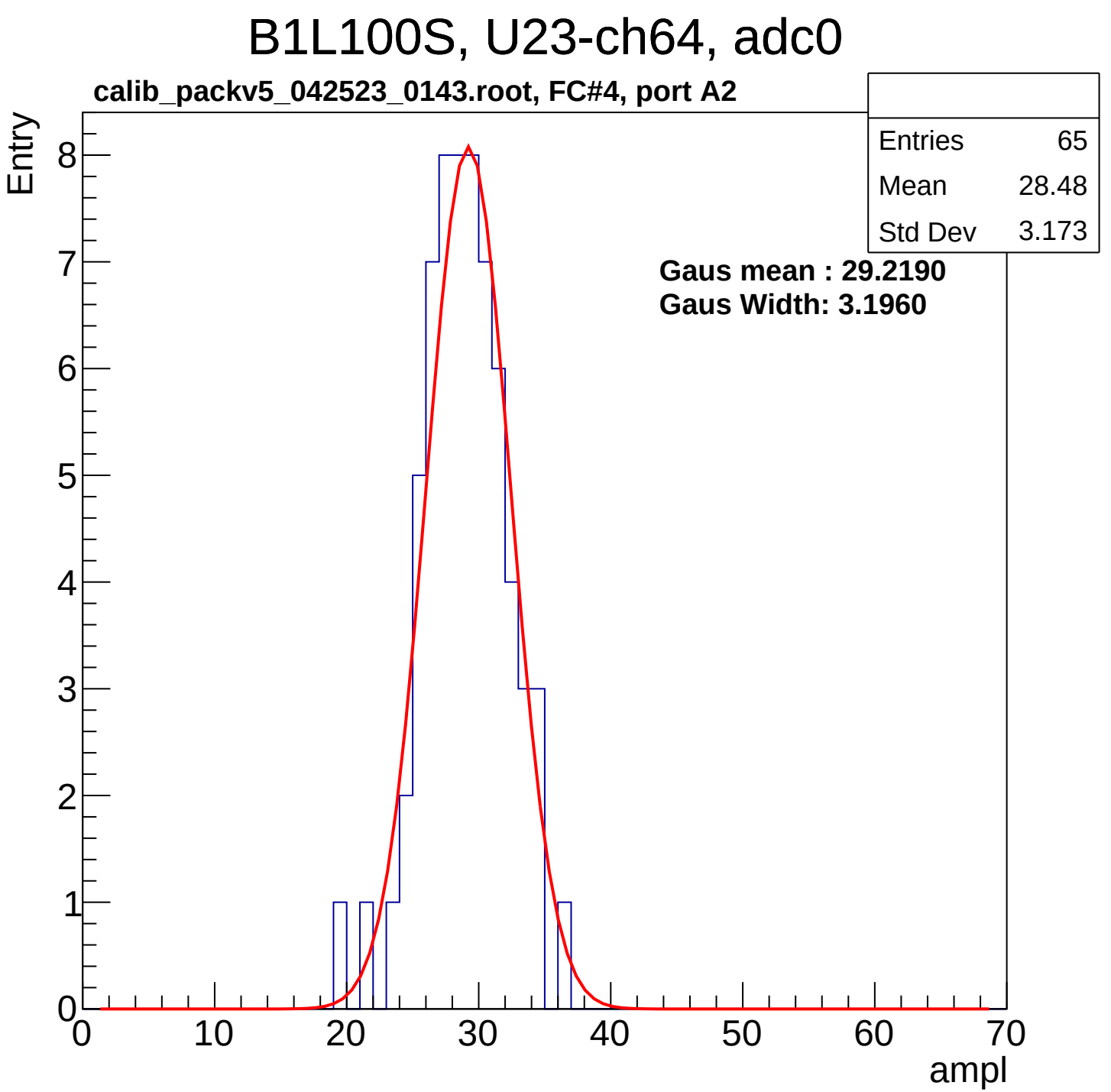

B1L100S, U23-ch63, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch63, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.48
Std Dev 3.173
Gaus mean : 29.2190
Gaus Width: 3.1960
Entry
ampl
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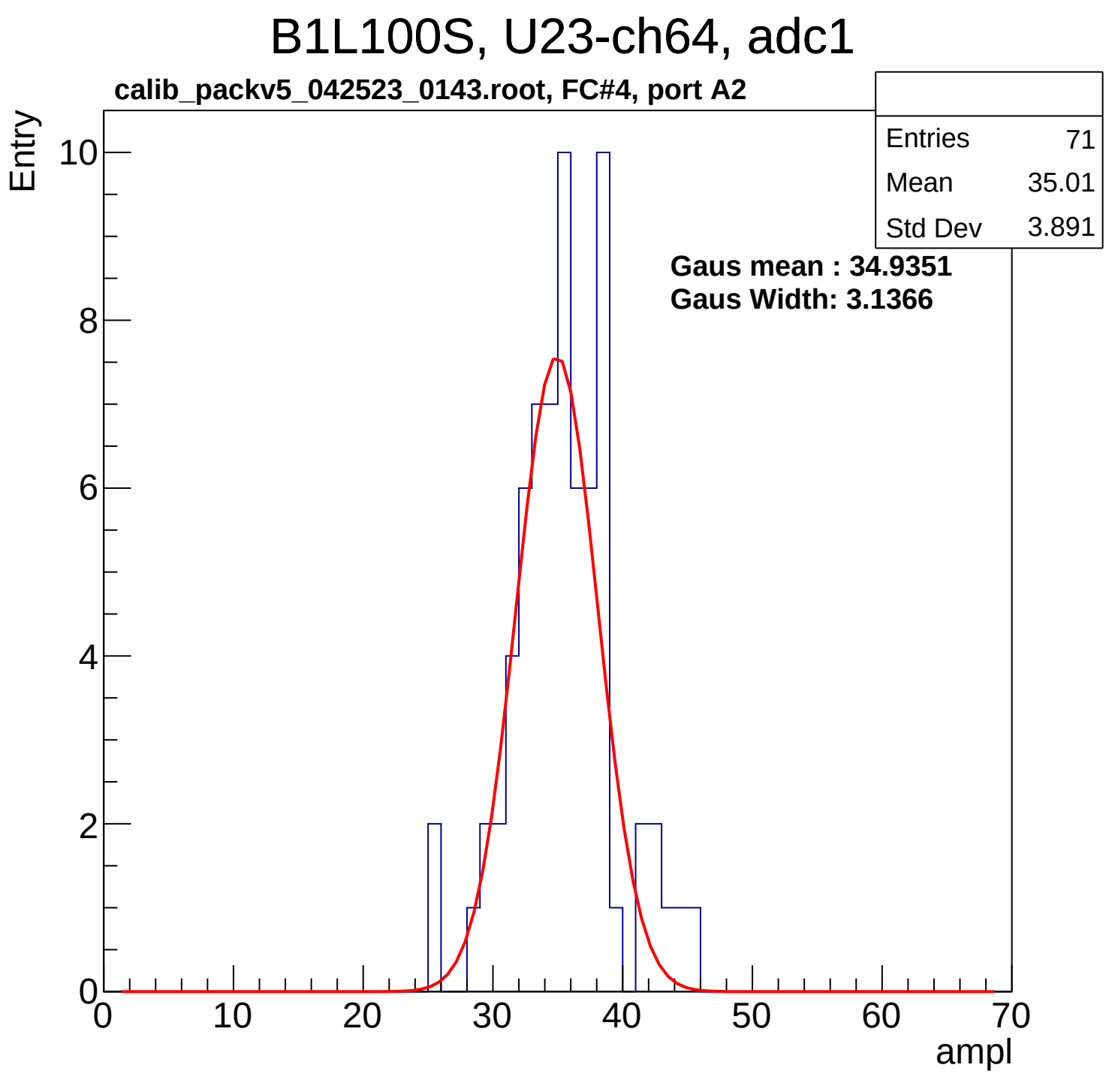
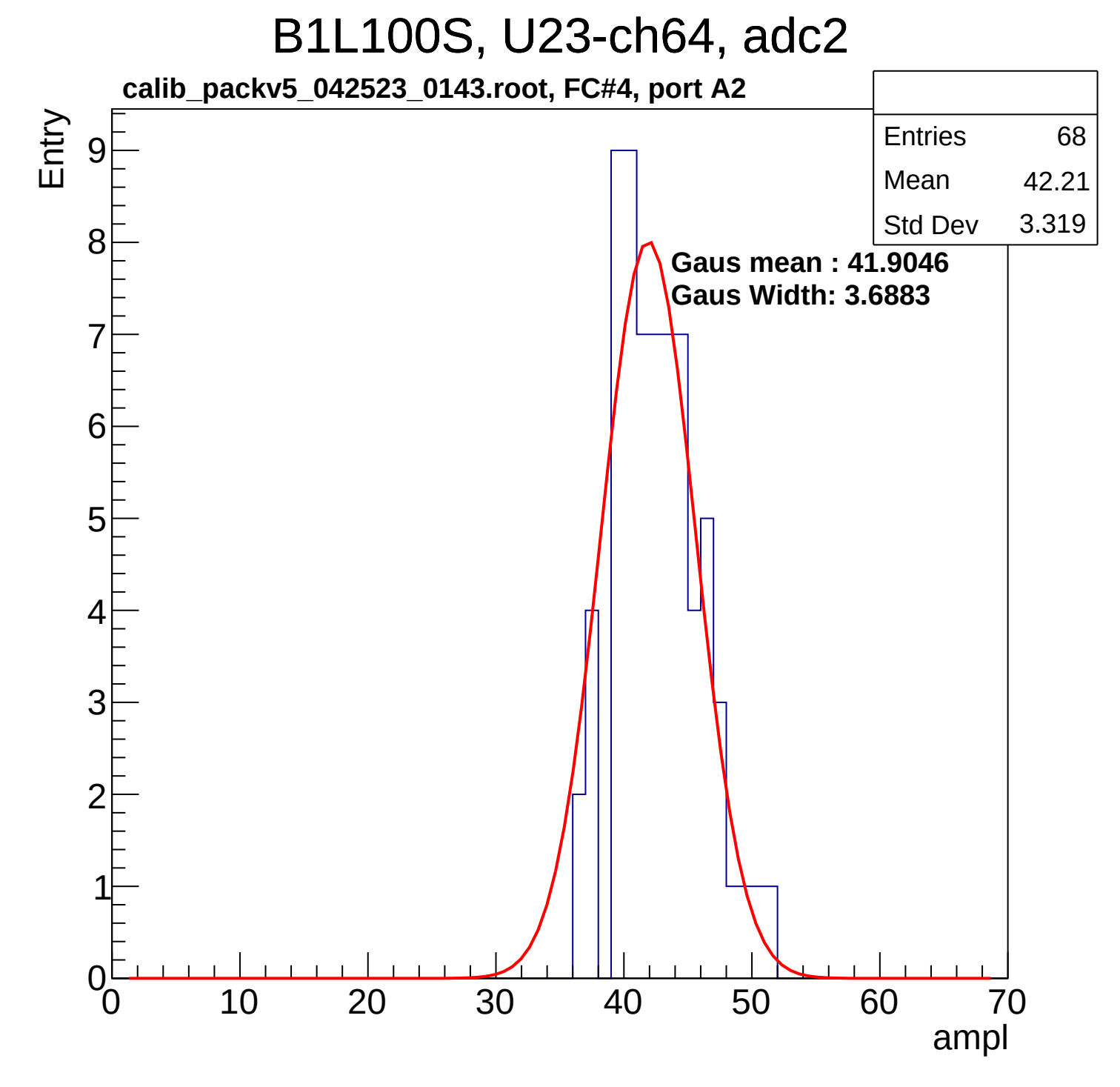
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 35.01
Std Dev 3.891
Gaus mean : 34.9351
Gaus Width: 3.1366
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.21
Std Dev 3.319
Gaus mean : 41.9046
Gaus Width: 3.6883
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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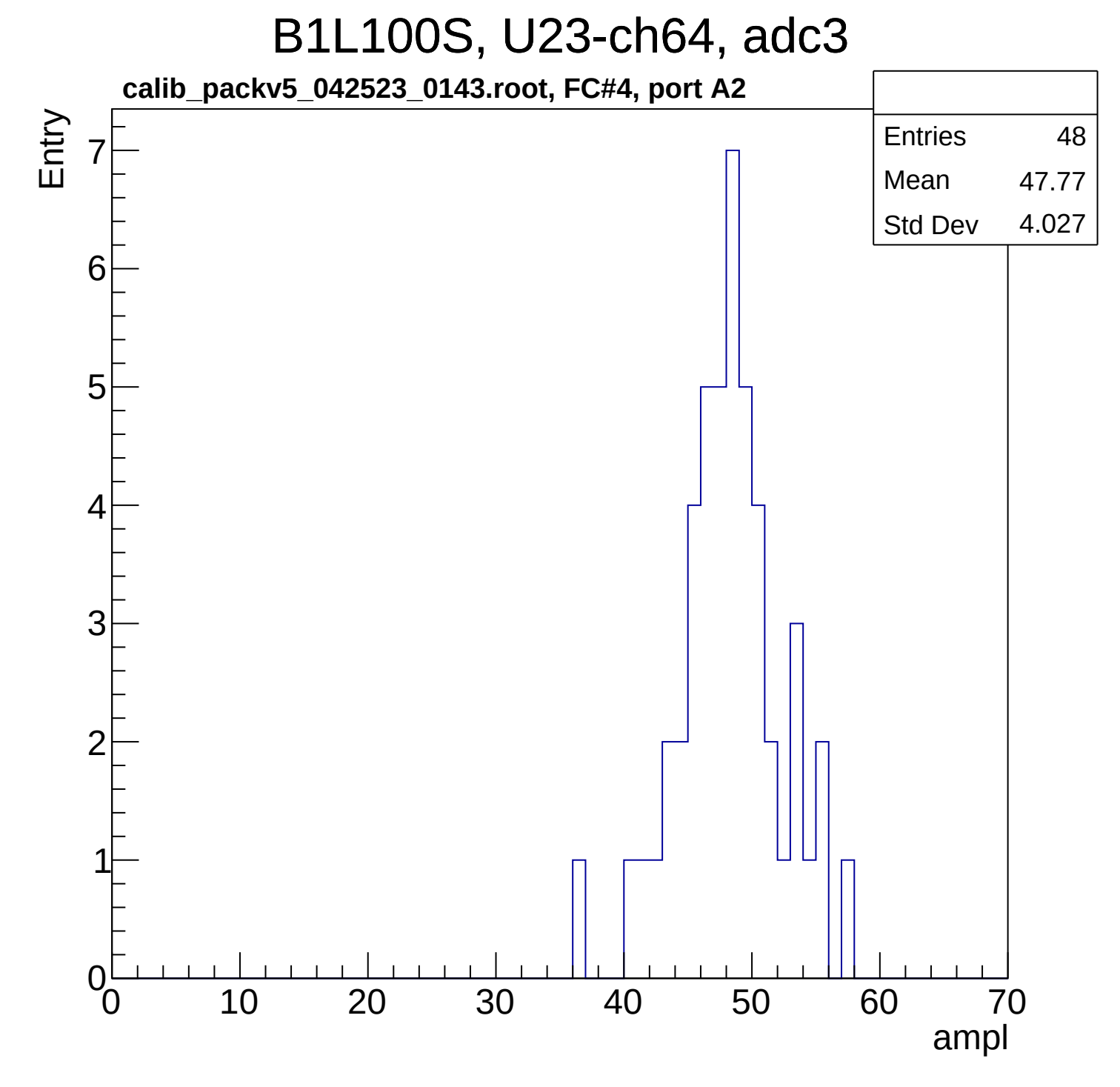
B1L100S, U23-ch64, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 47.77
Std Dev 4.027
Entry
ampl
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2
3
4
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6
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0
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20
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60
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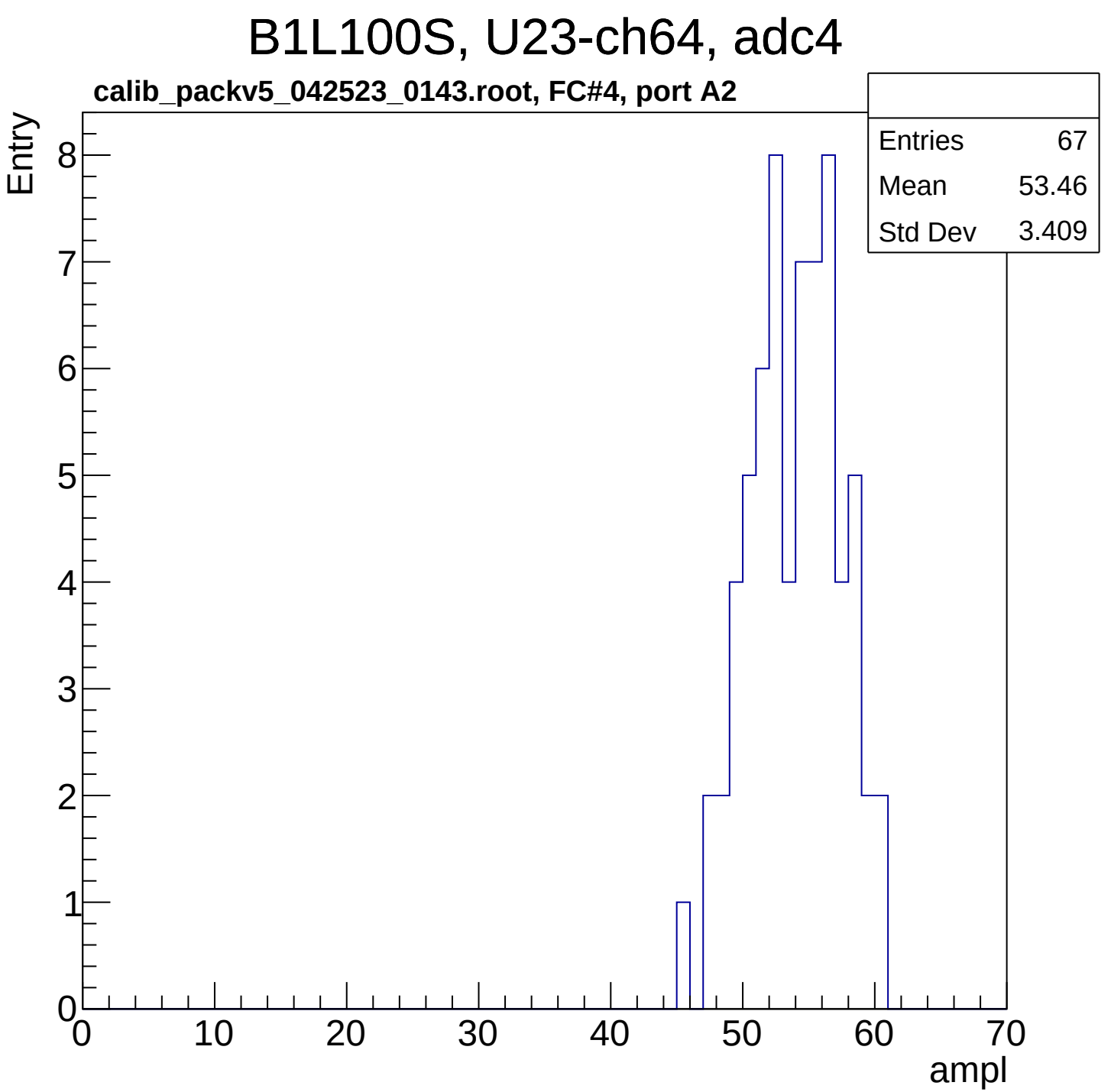
B1L100S, U23-ch64, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 53.46
Std Dev 3.409
Entry
ampl
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3
4
5
6
7
8
0
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20
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60
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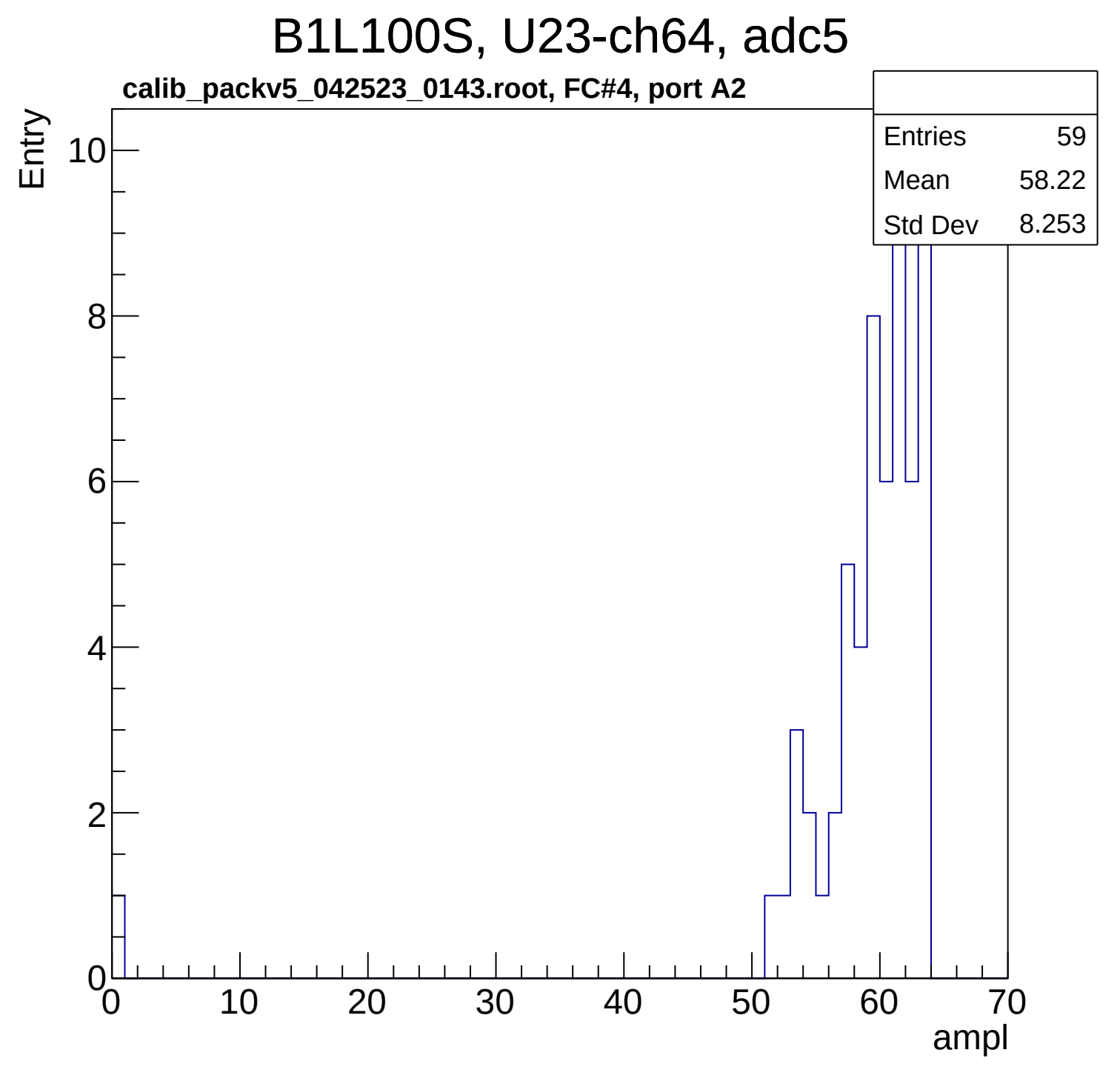
B1L100S, U23-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.22
Std Dev 8.253
Entry
ampl
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2
4
6
8
10
0
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20
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40
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60
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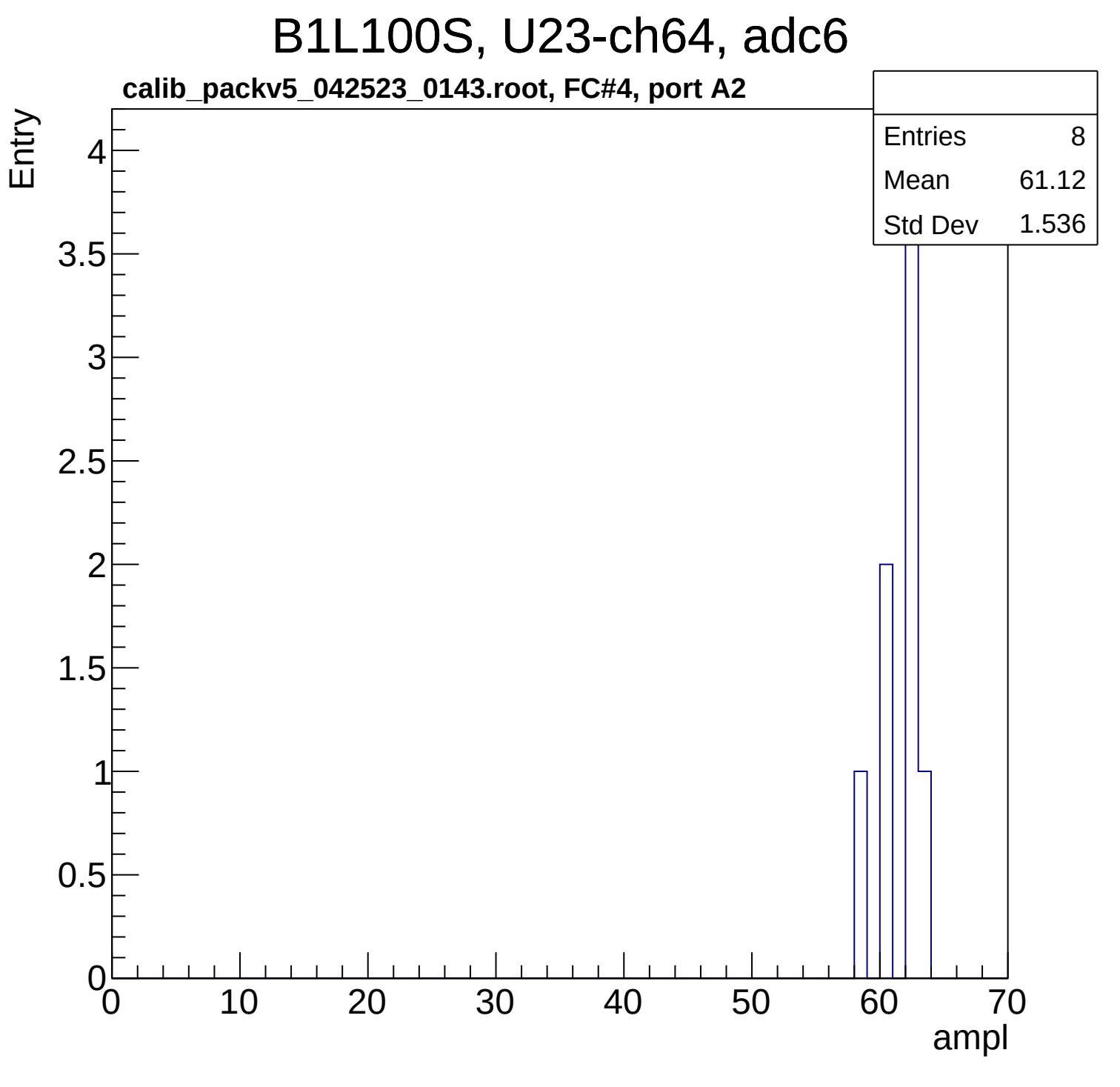
B1L100S, U23-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.12
Std Dev 1.536
Entry
ampl
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1.5
2
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3
3.5
4
0
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30
40
50
60
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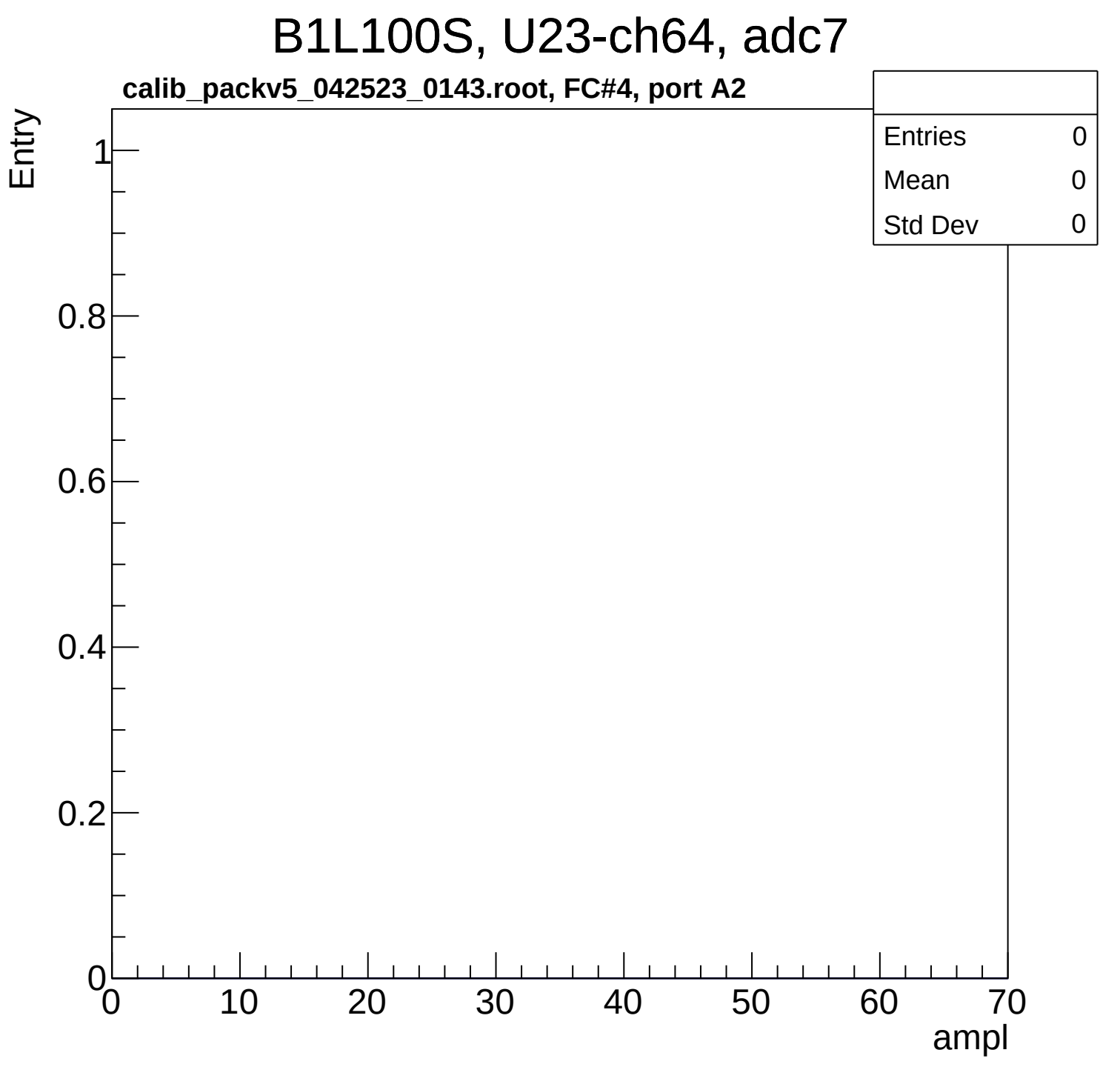
B1L100S, U23-ch64, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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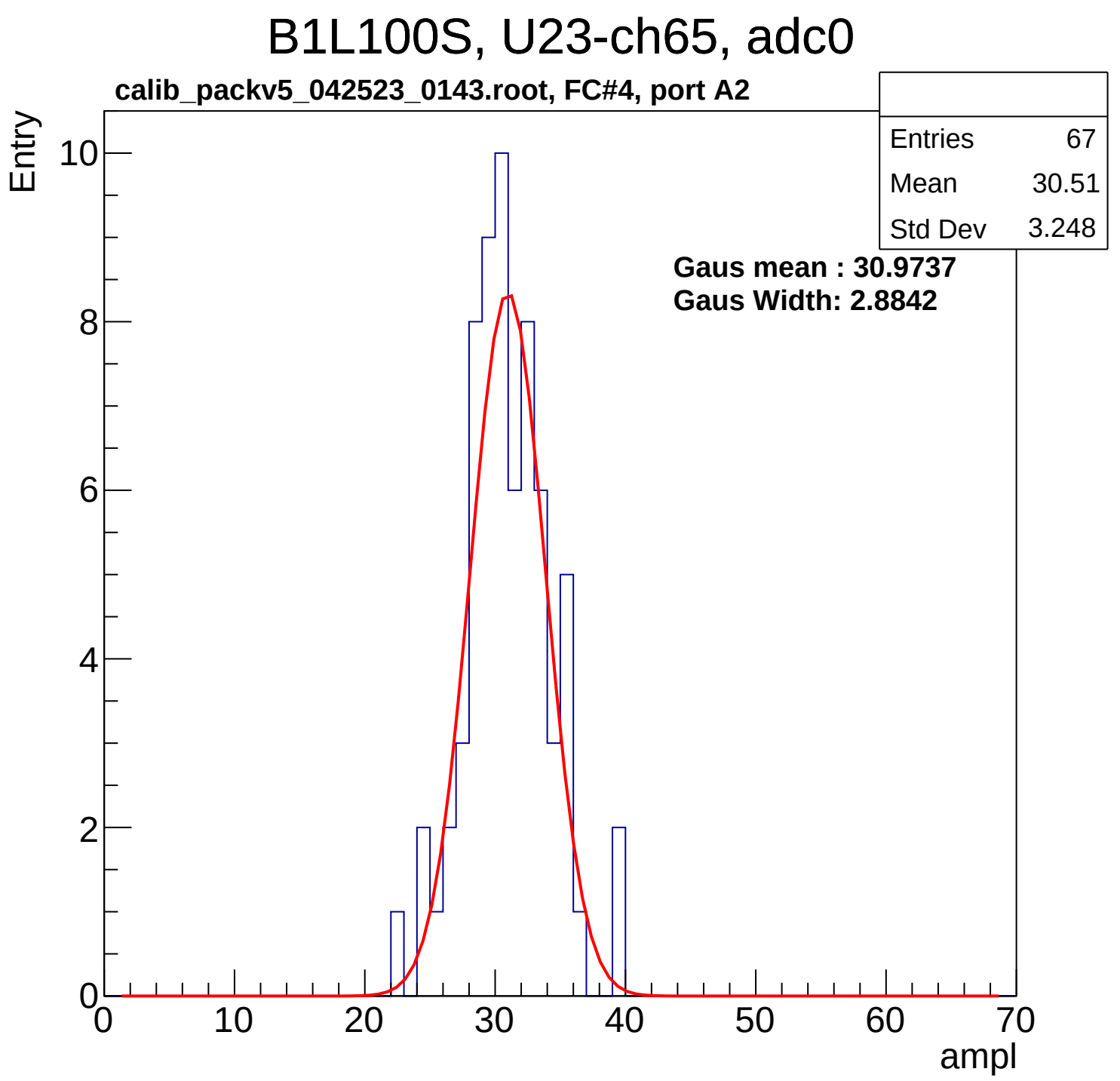
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.51
Std Dev 3.248
Gaus mean : 30.9737
Gaus Width: 2.8842
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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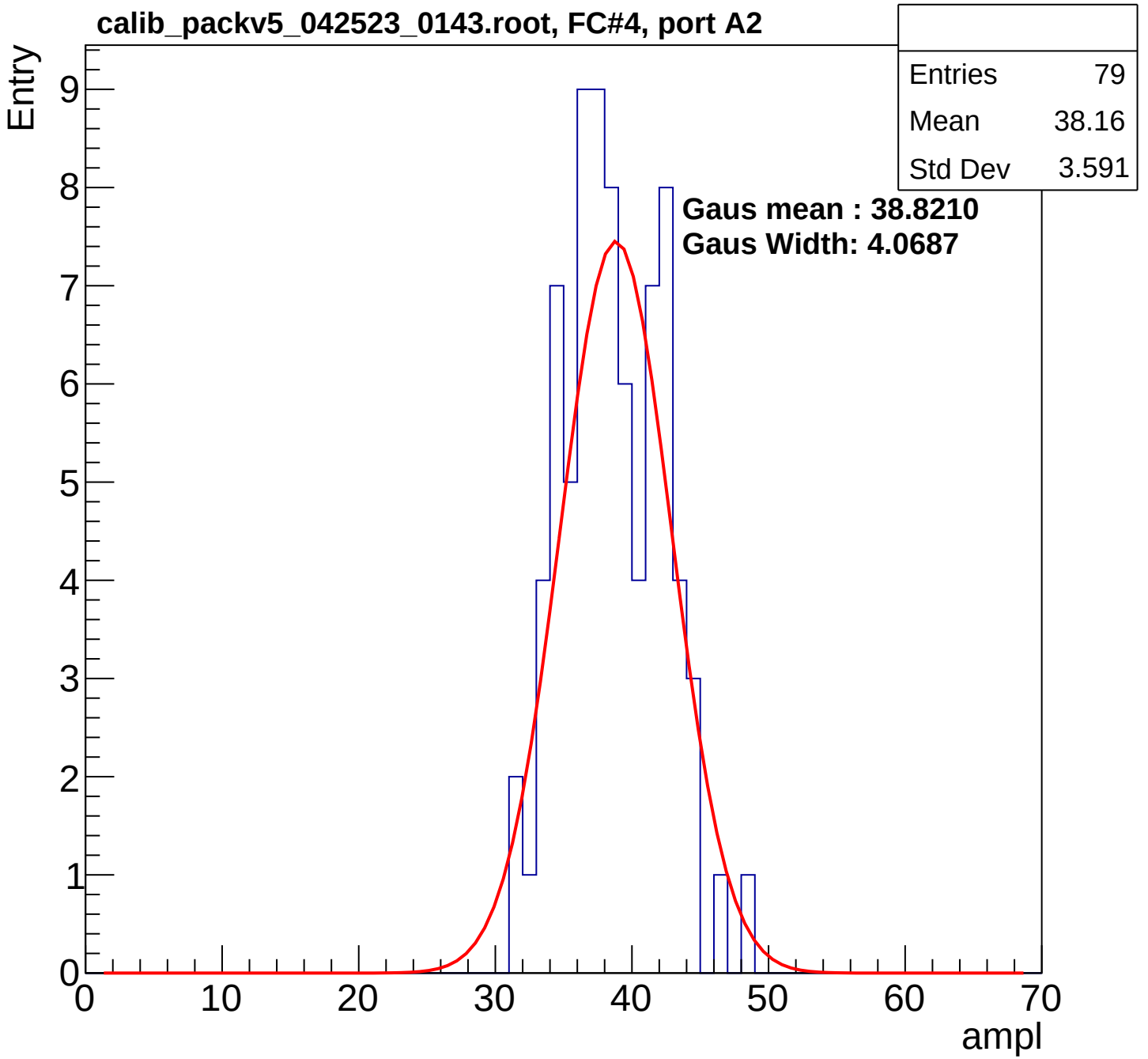
B1L100S, U23-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 38.16
Std Dev 3.591
Gaus mean : 38.8210
Gaus Width: 4.0687
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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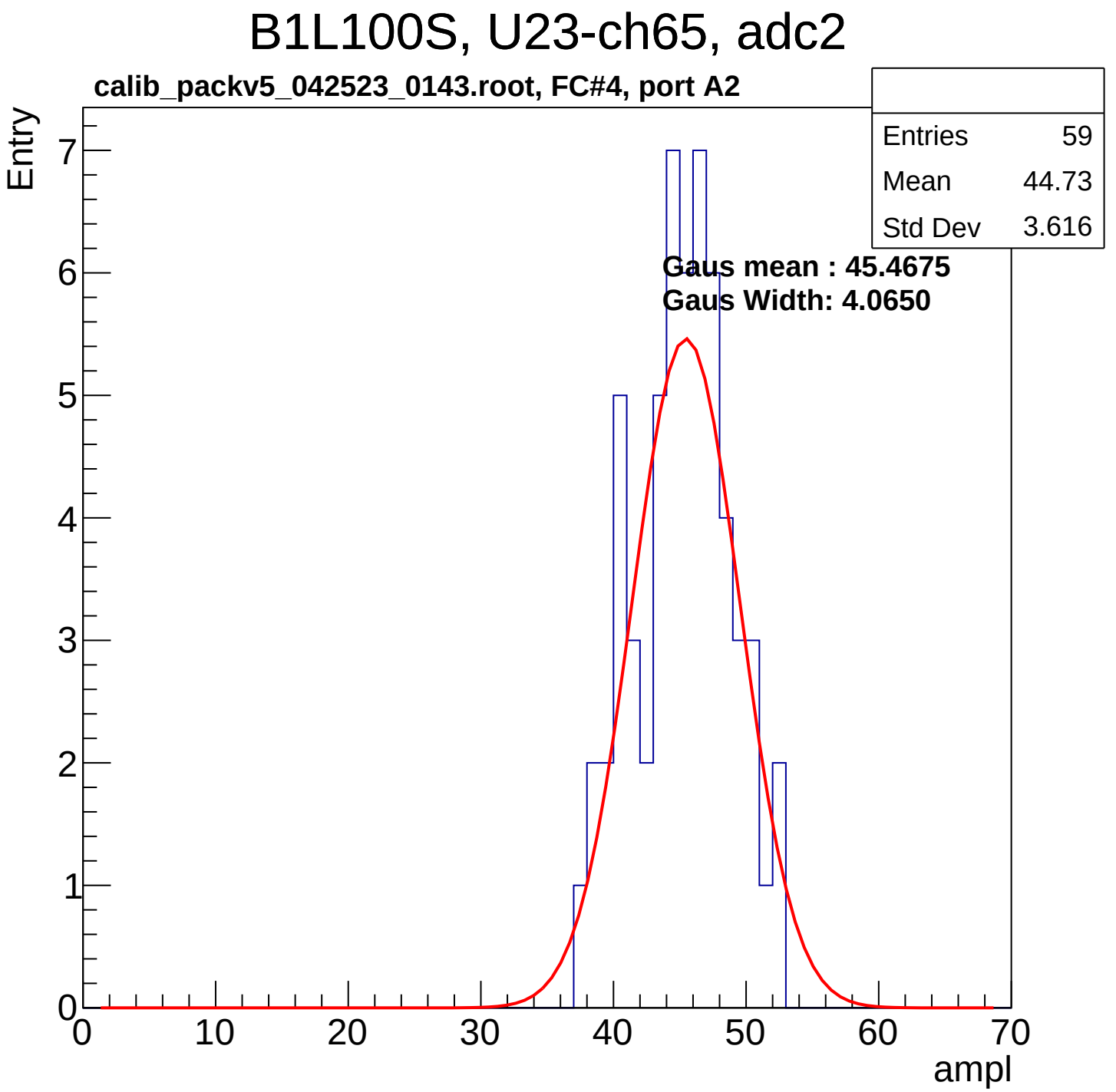
B1L100S, U23-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44.73
Std Dev 3.616
Gaus mean : 45.4675
Gaus Width: 4.0650
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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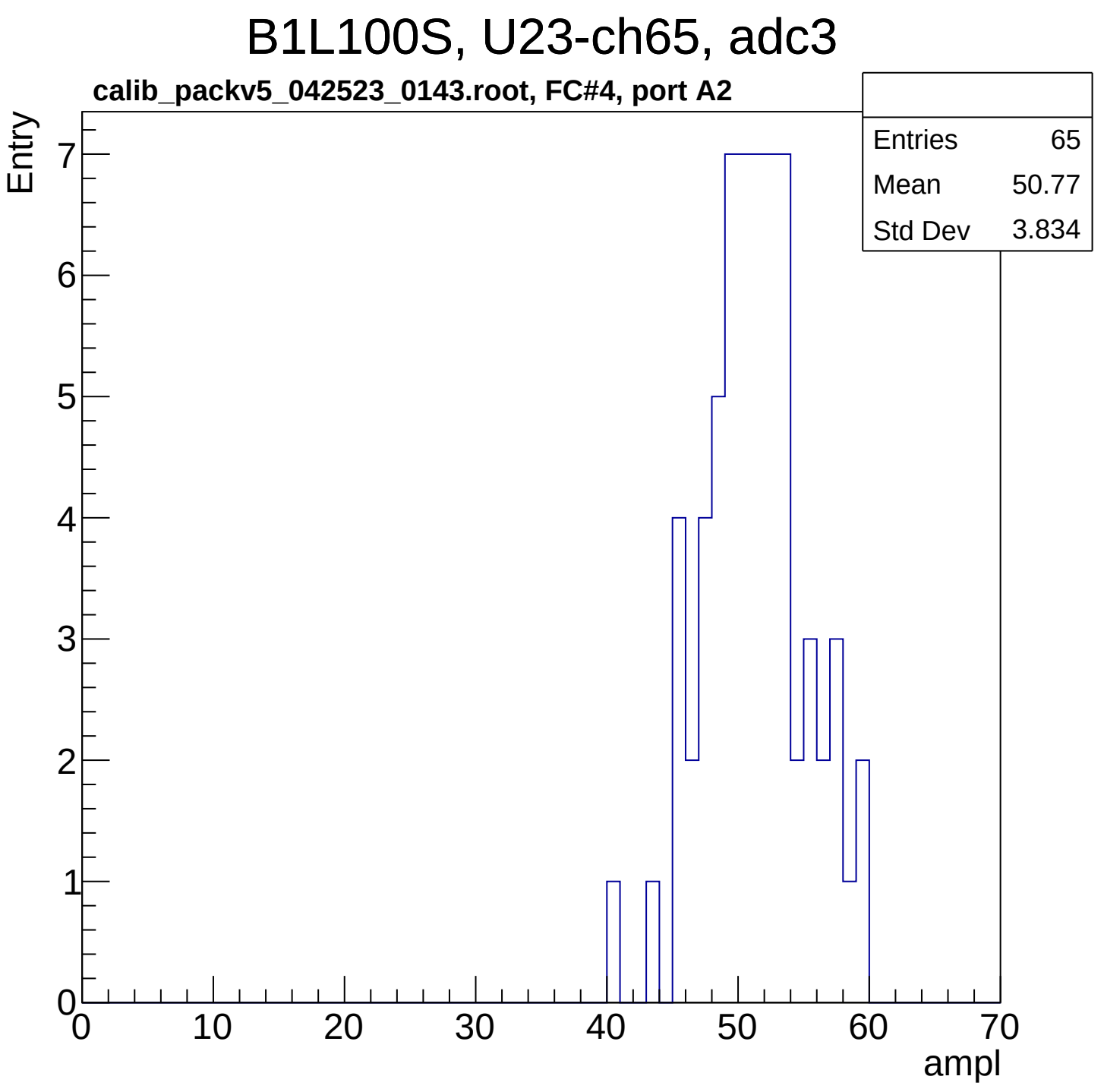
B1L100S, U23-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.77
Std Dev 3.834
Entry
ampl
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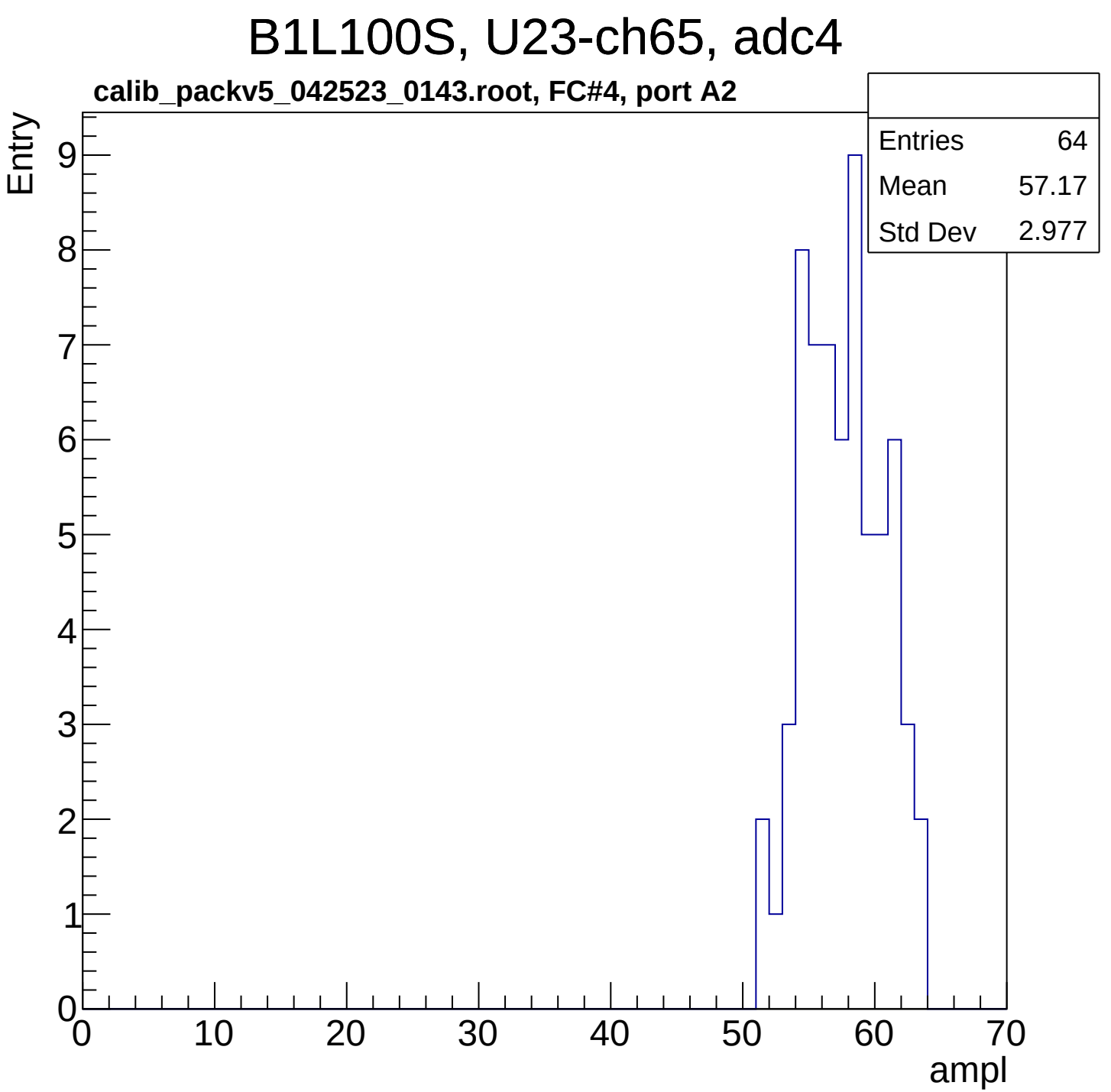
B1L100S, U23-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.17
Std Dev 2.977
Entry
ampl
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0
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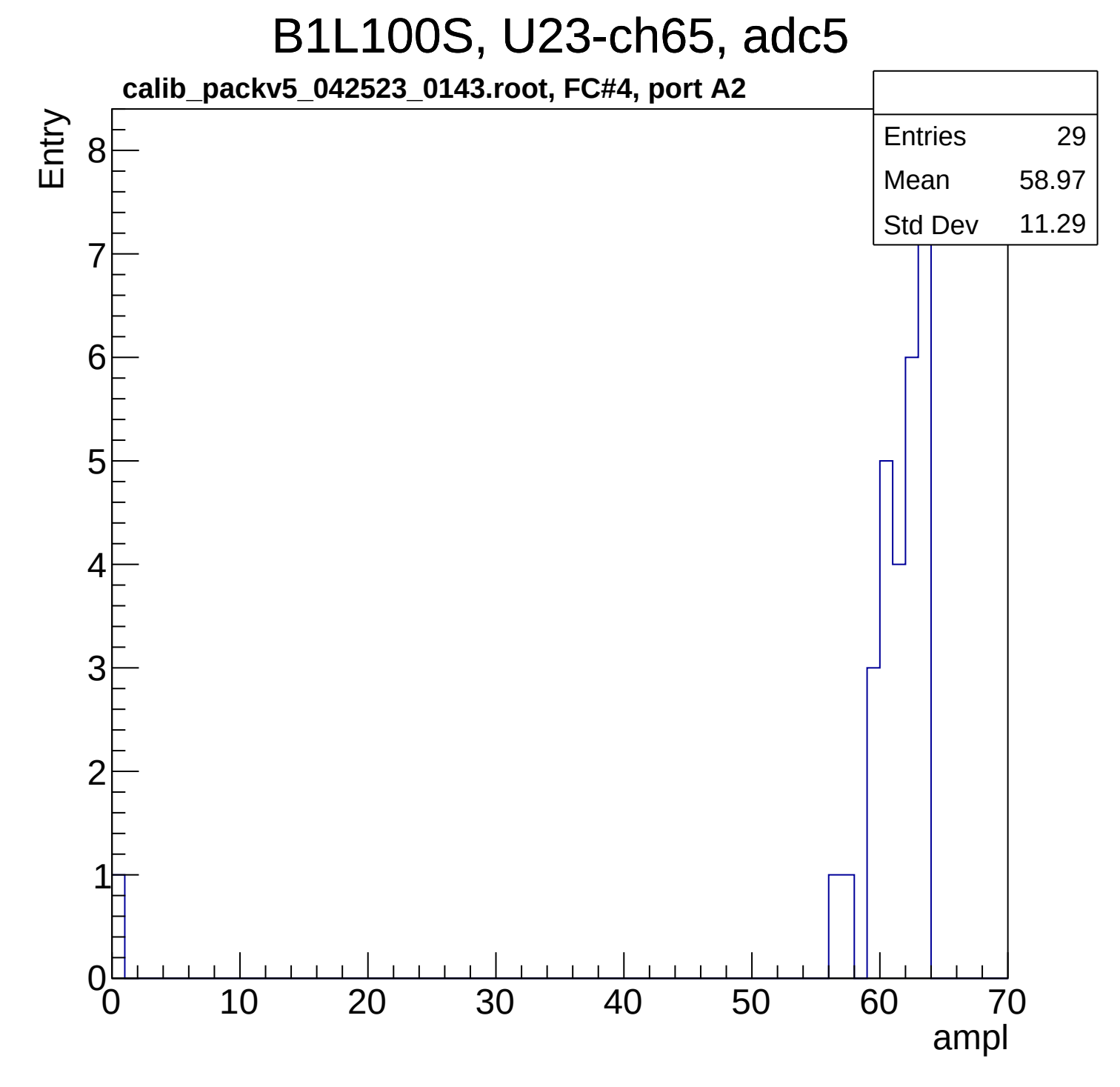
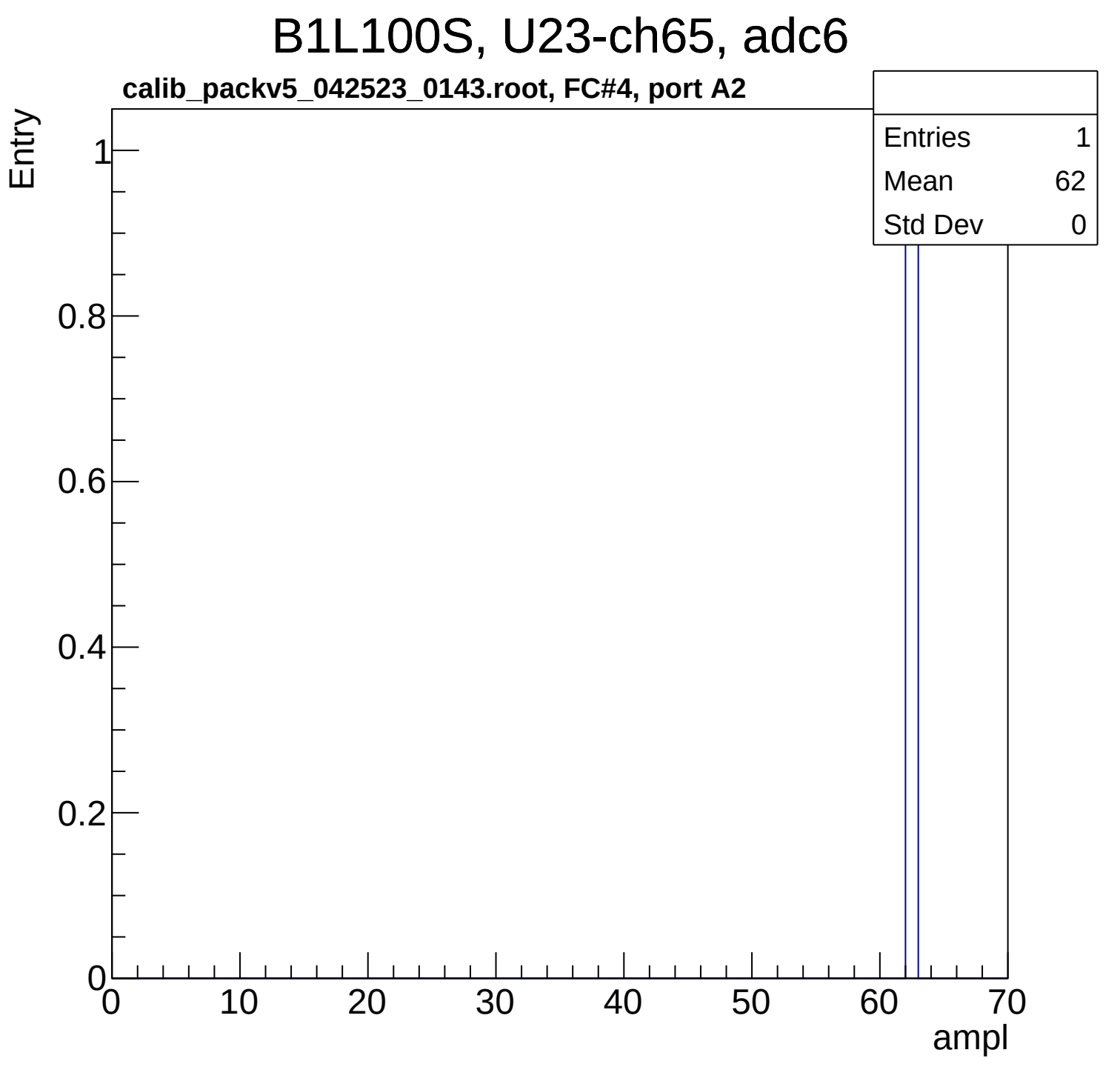

B1L100S, U23-ch65, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.97
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch65, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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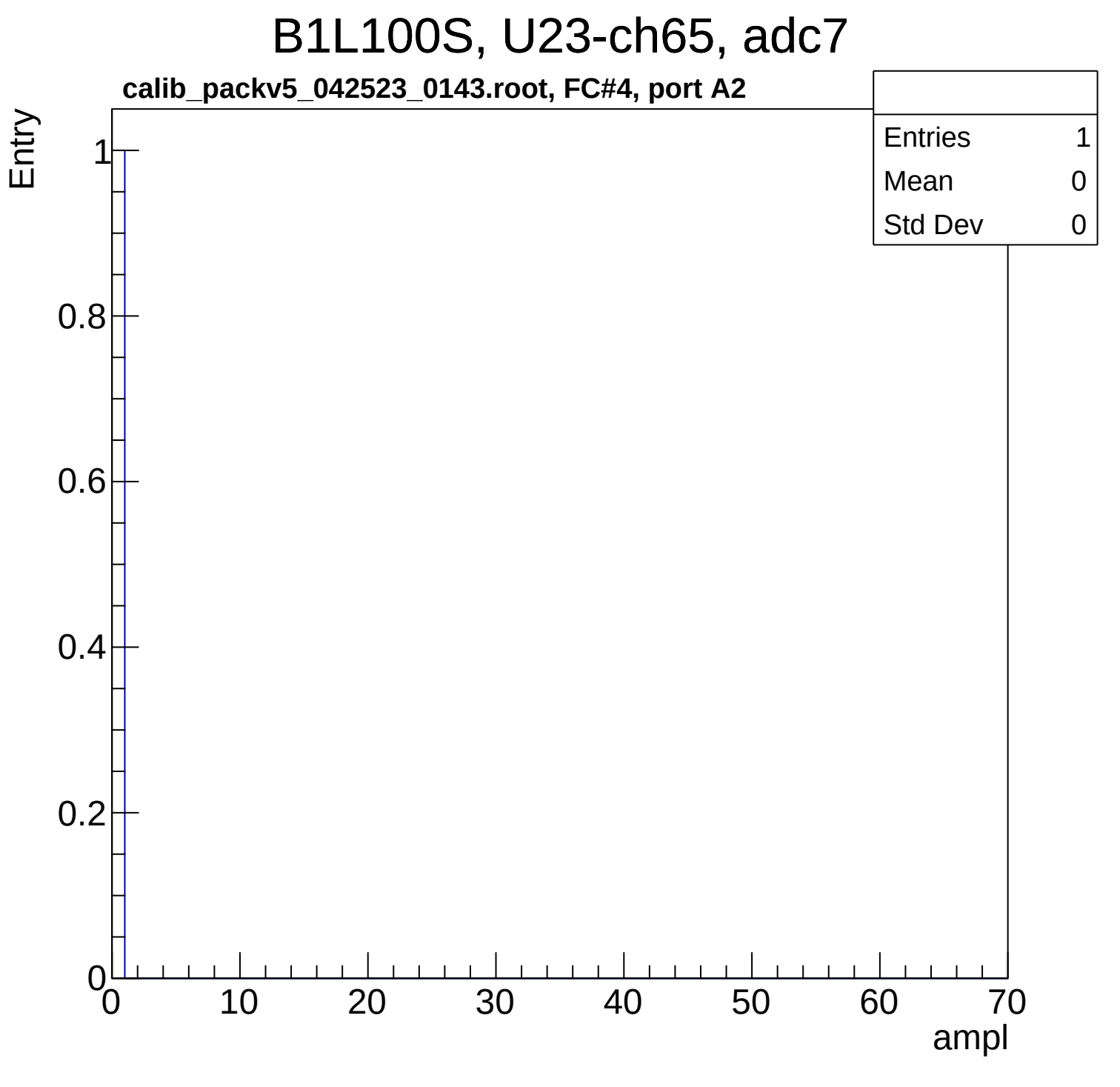
70
ampl


B1L100S, U23-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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0
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10
20
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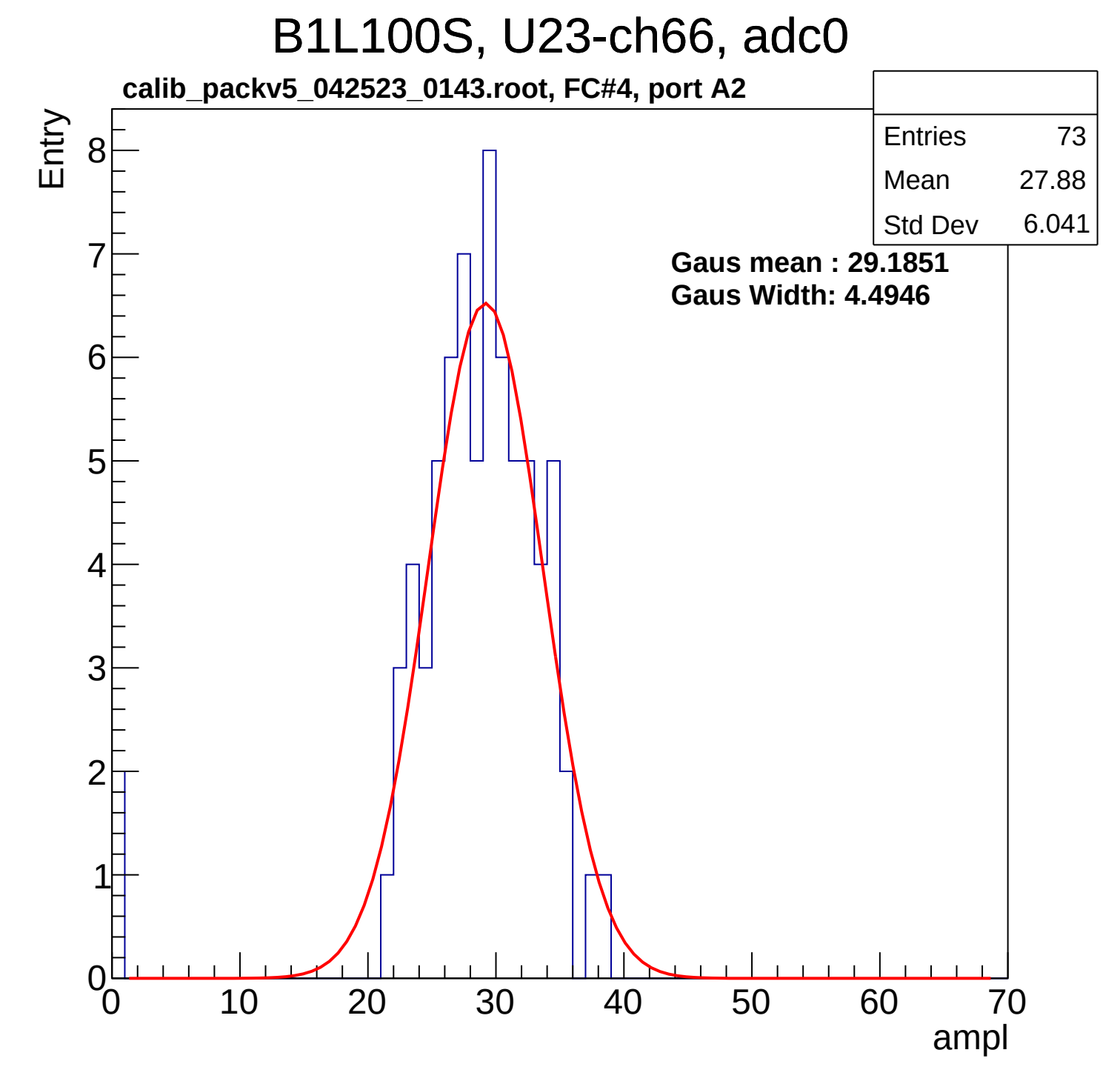
50
60
70
ampl


B1L100S, U23-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 27.88
Std Dev 6.041
Gaus mean : 29.1851
Gaus Width: 4.4946
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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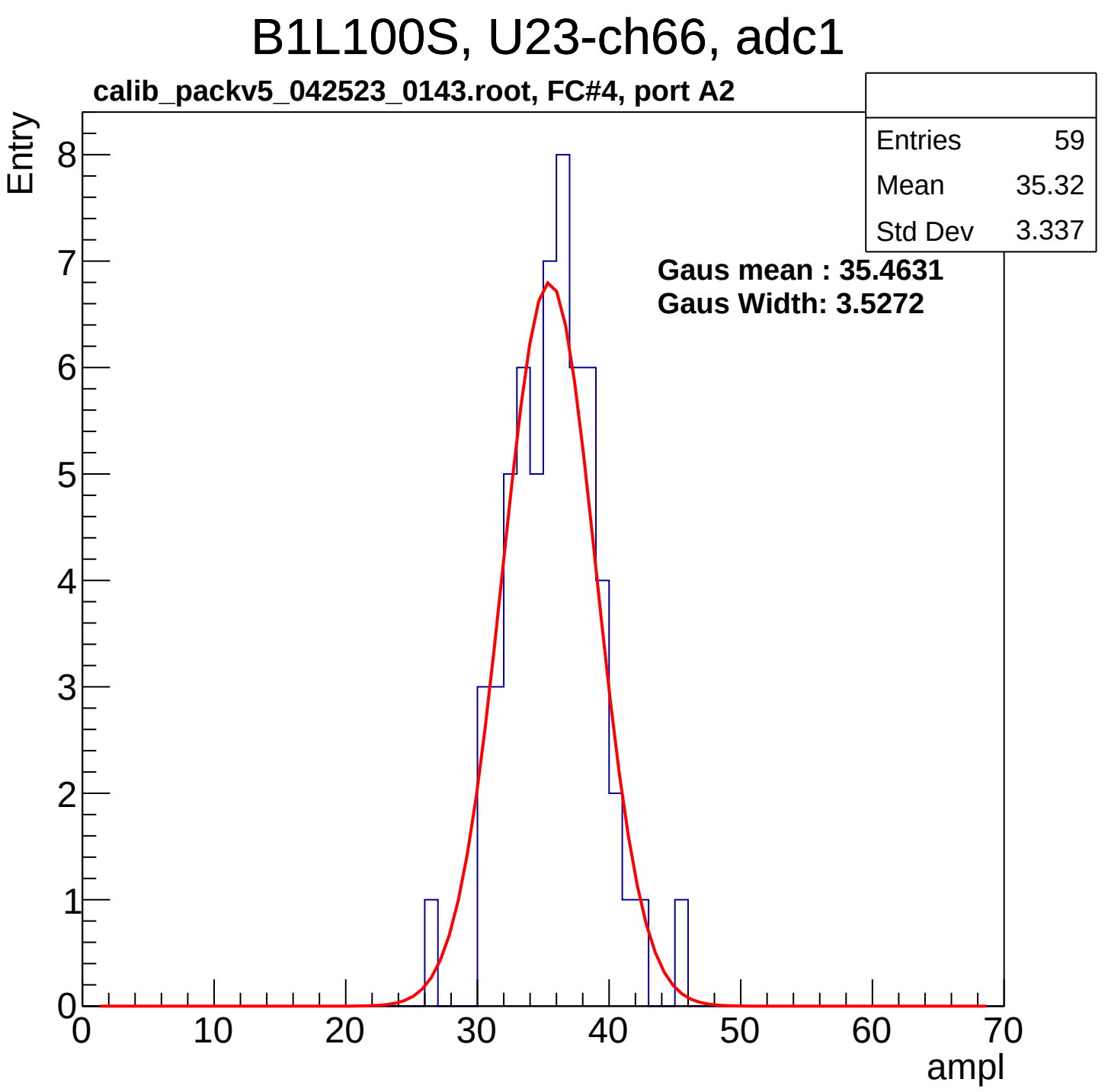
B1L100S, U23-ch66, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.32
Std Dev 3.337
Gaus mean : 35.4631
Gaus Width: 3.5272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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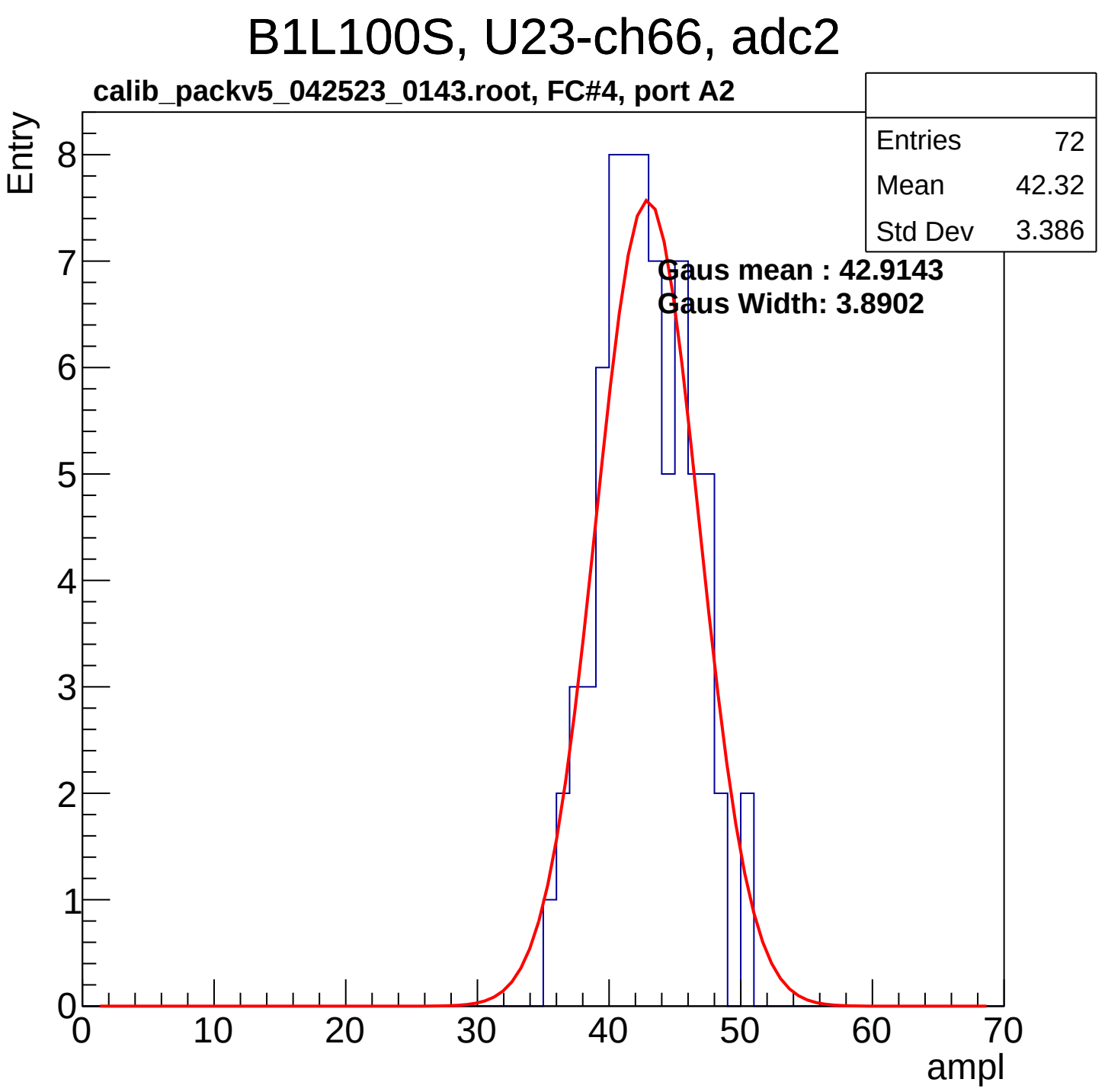
B1L100S, U23-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 42.32
Std Dev 3.386
Gaus mean : 42.9143
Gaus Width: 3.8902
Entry
ampl
0
1
2
3
4
5
6
7
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0
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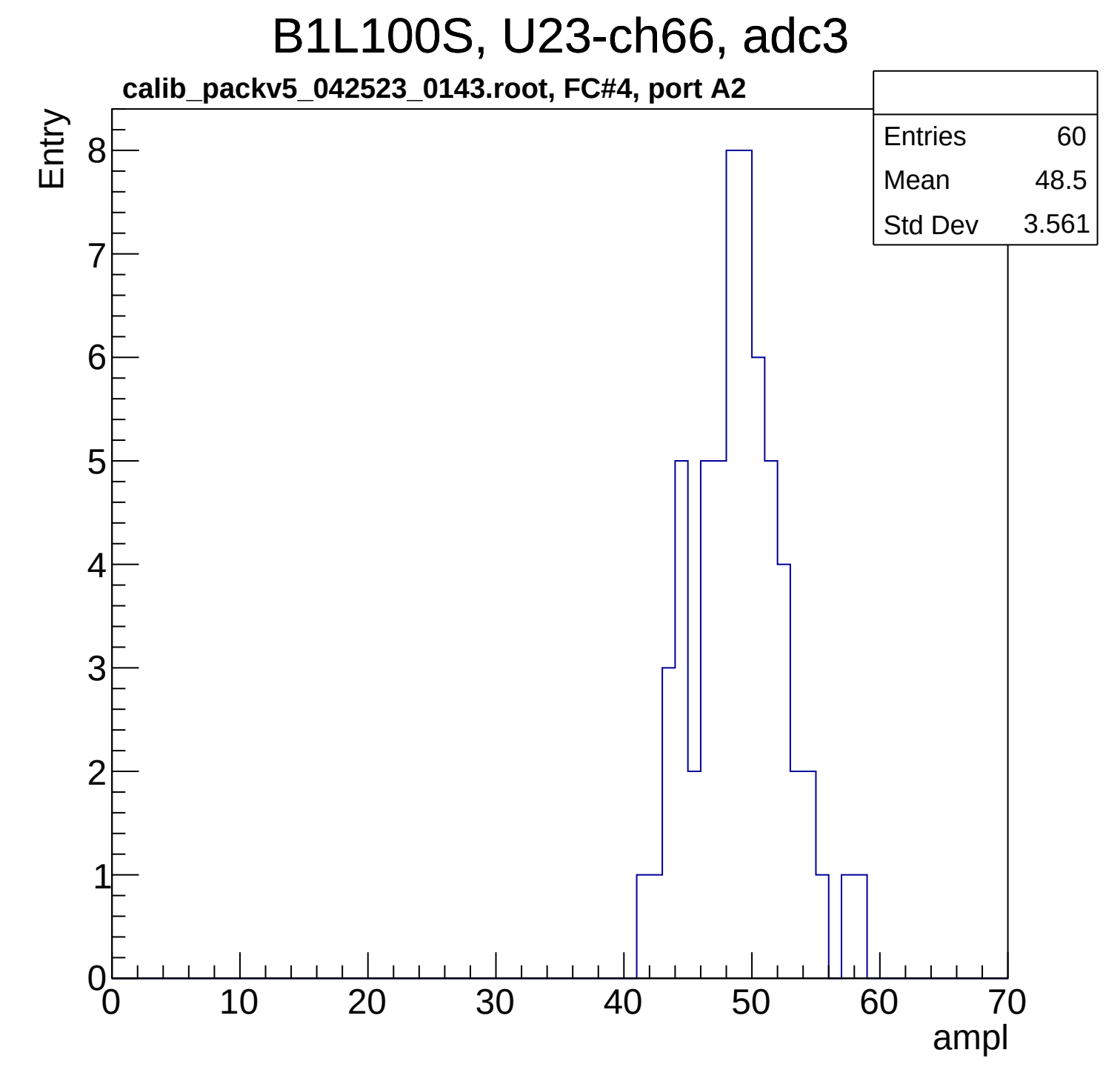
B1L100S, U23-ch66, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 48.5
Std Dev 3.561
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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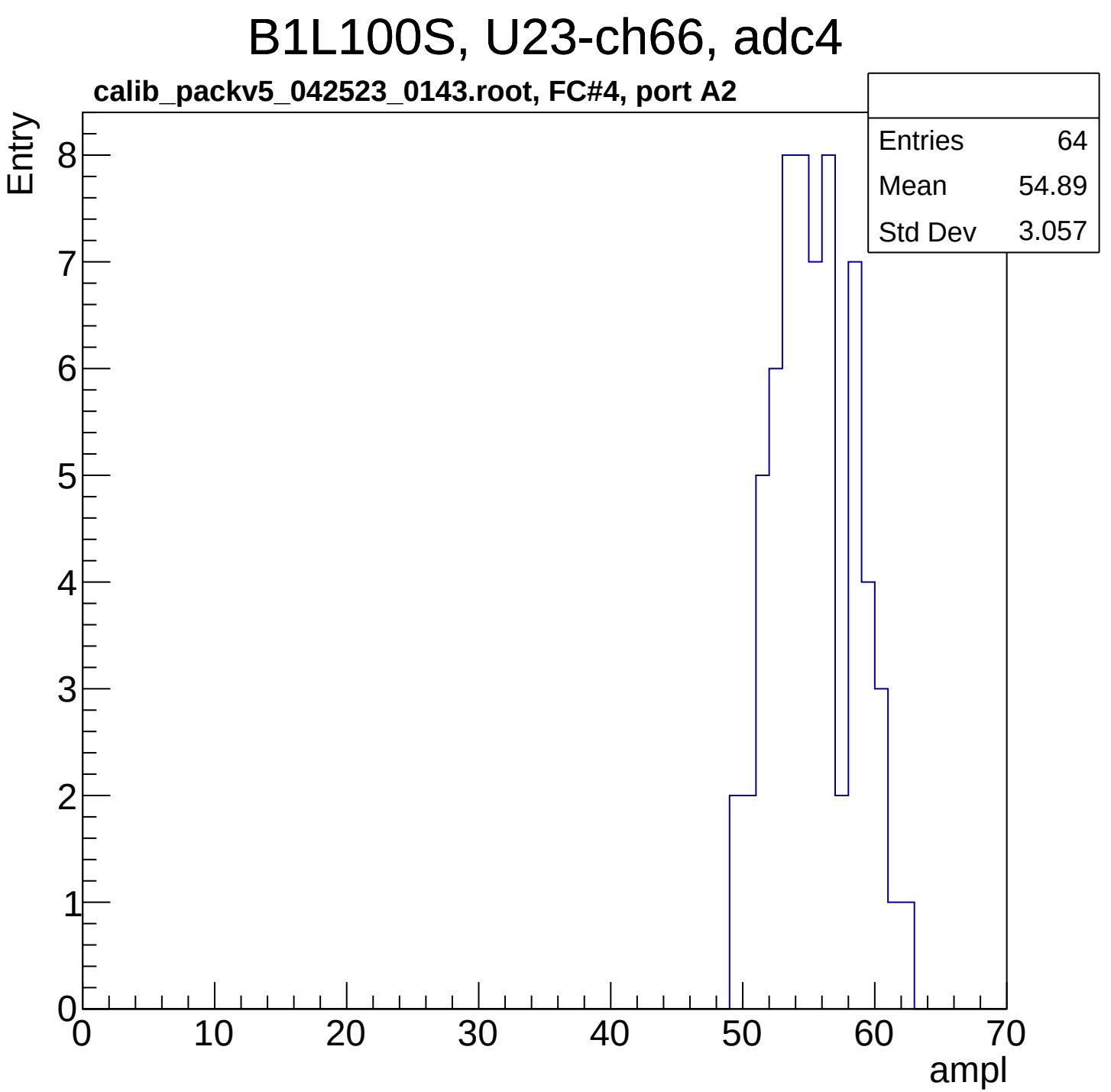
B1L100S, U23-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.89
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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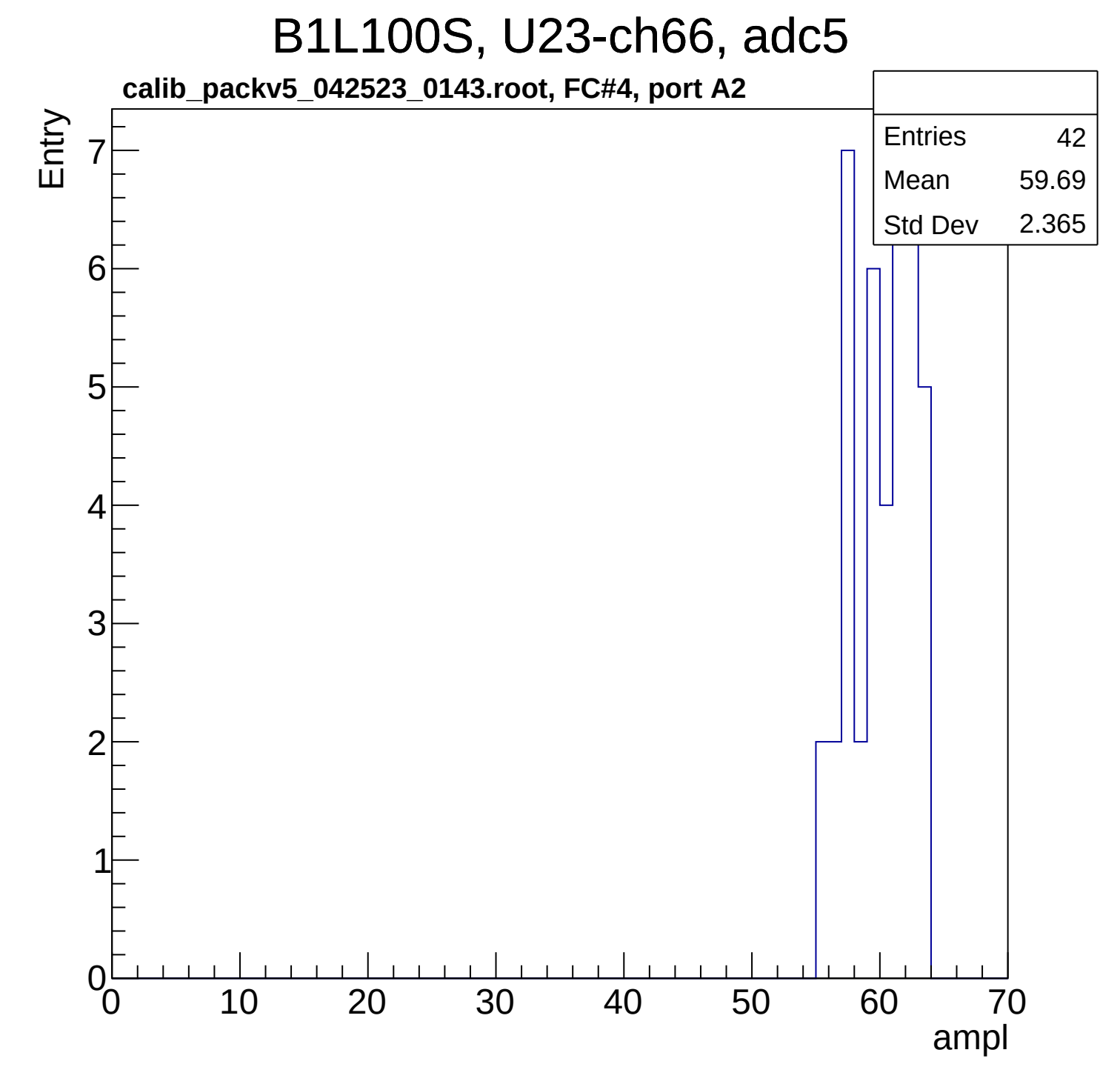
B1L100S, U23-ch66, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.69
Std Dev 2.365
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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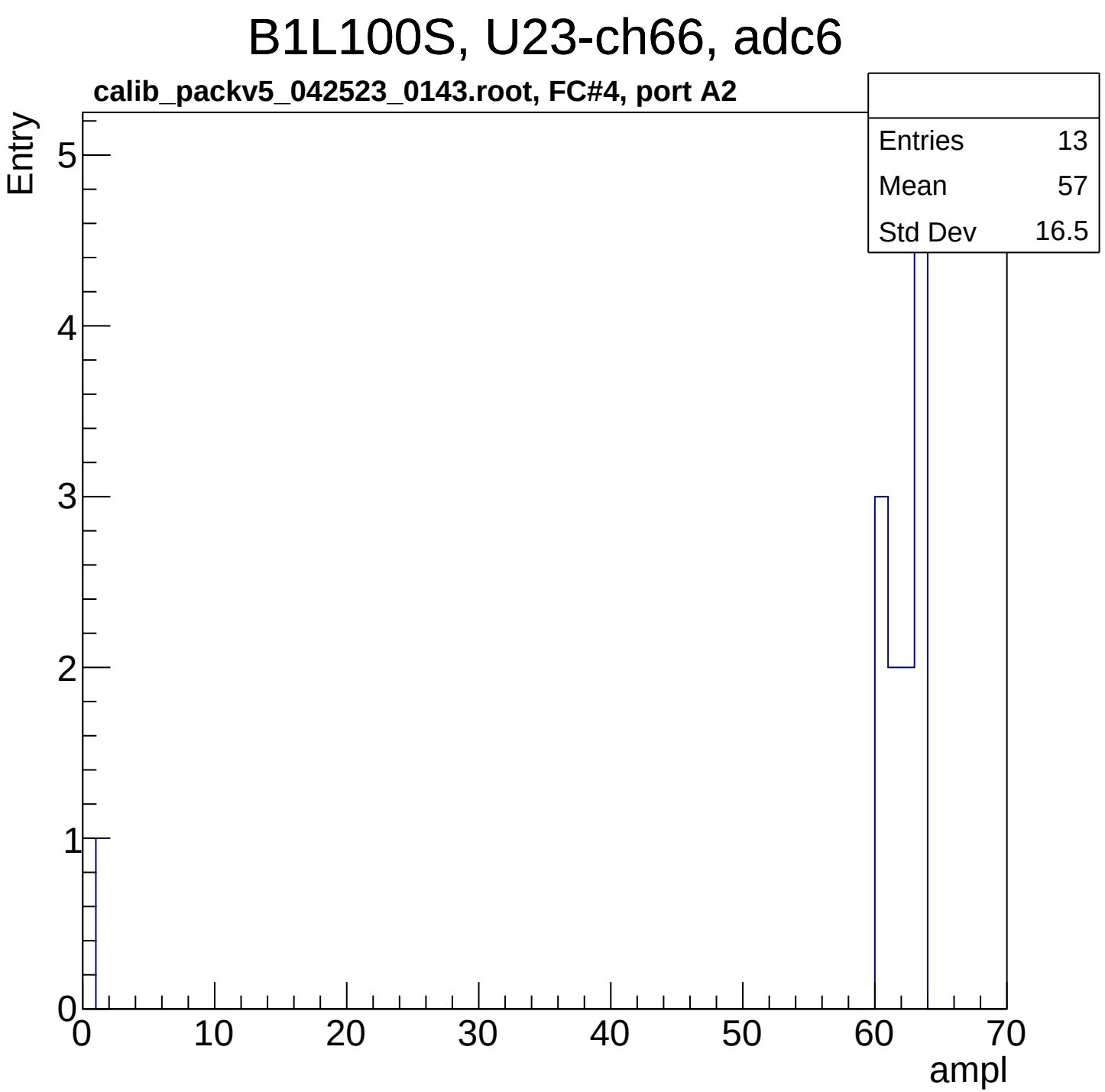
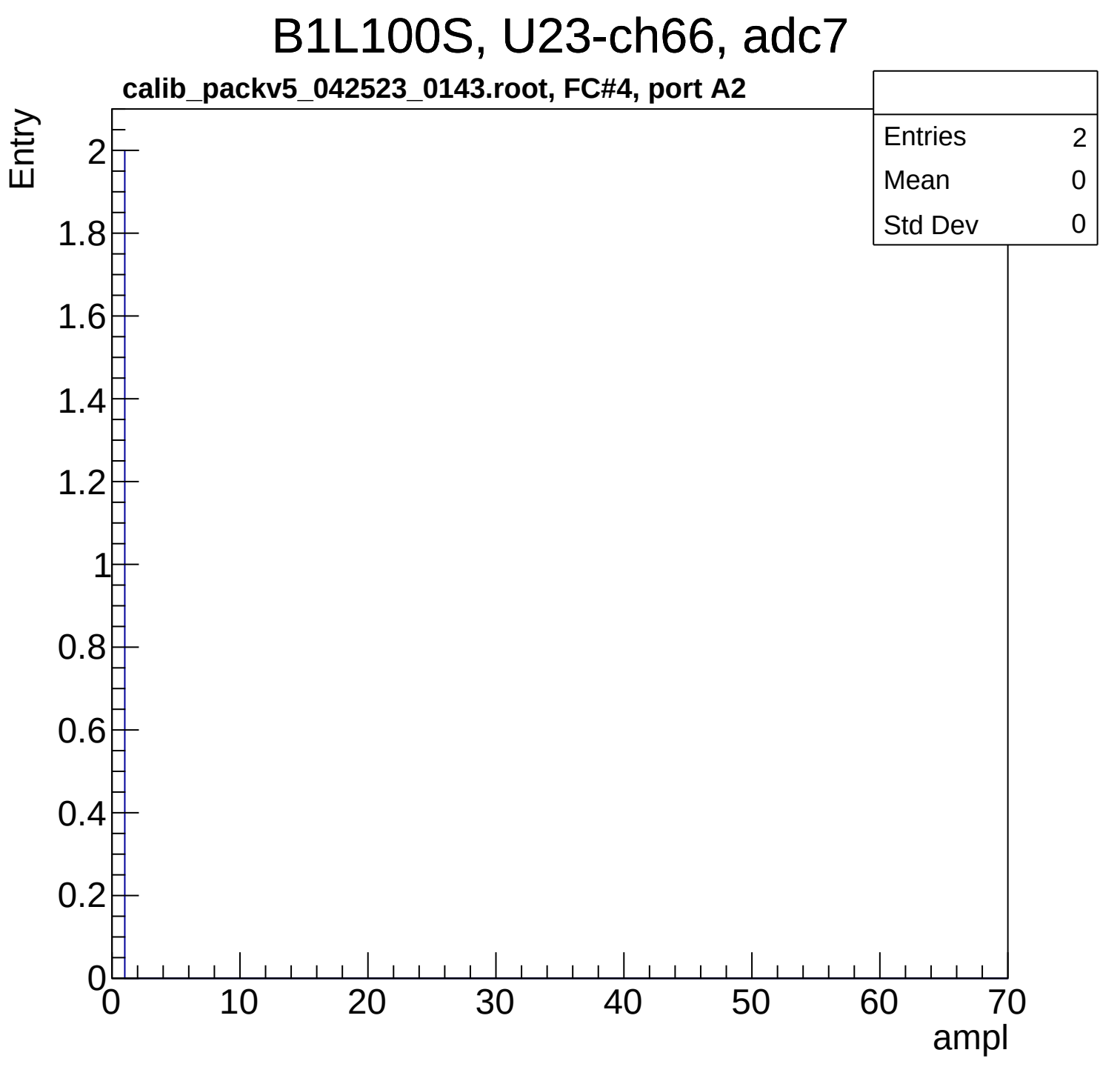
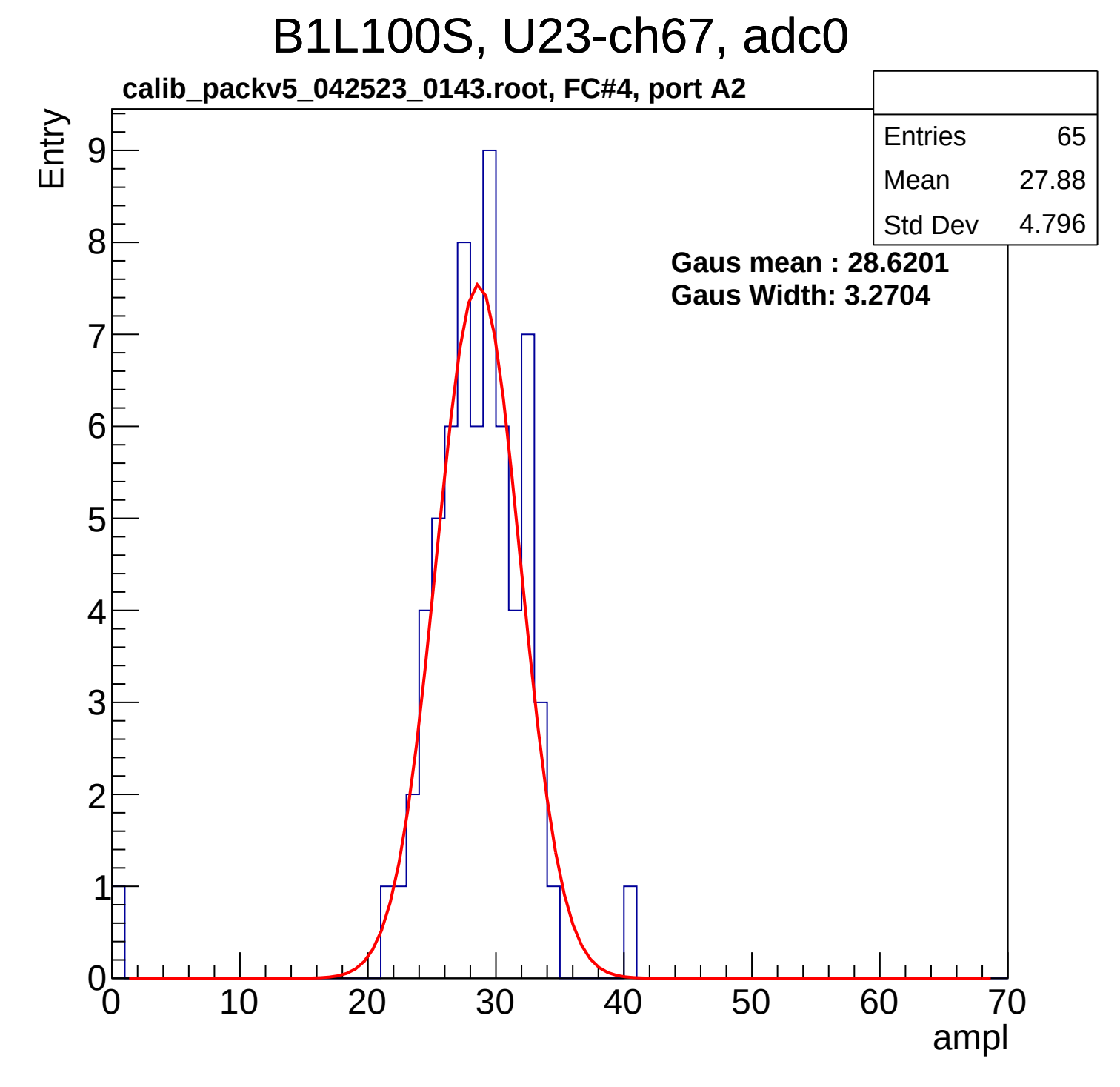
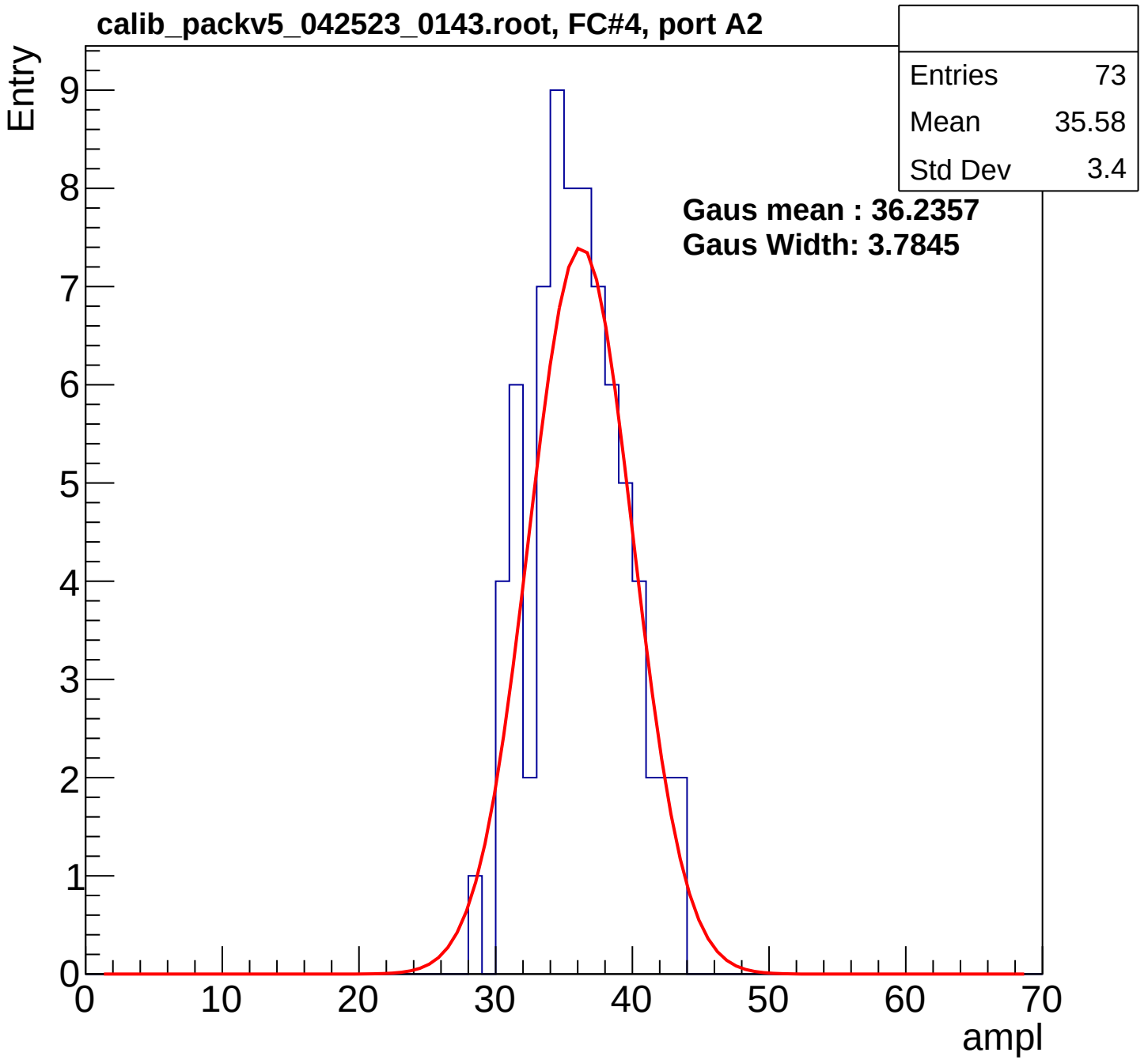
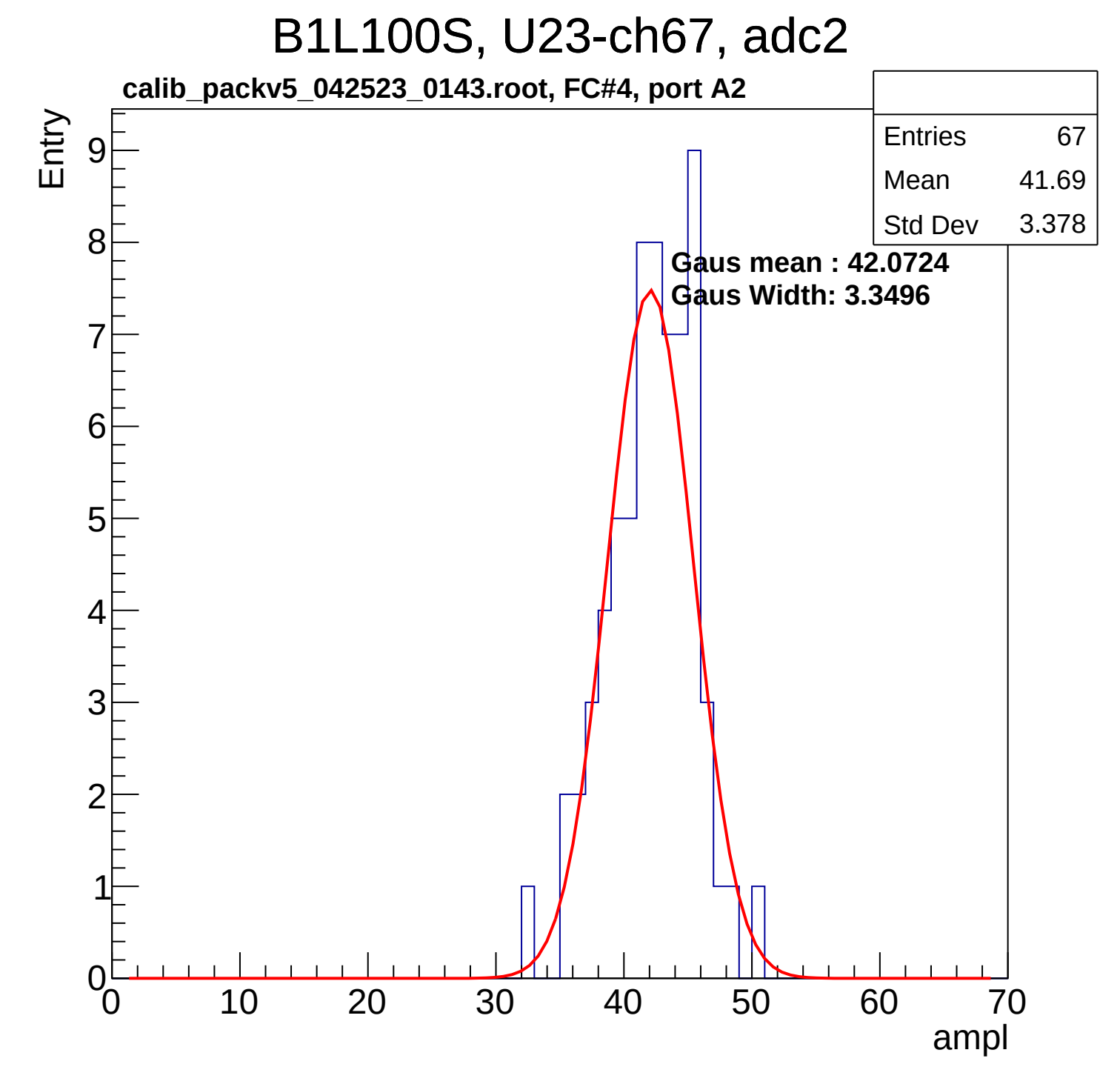
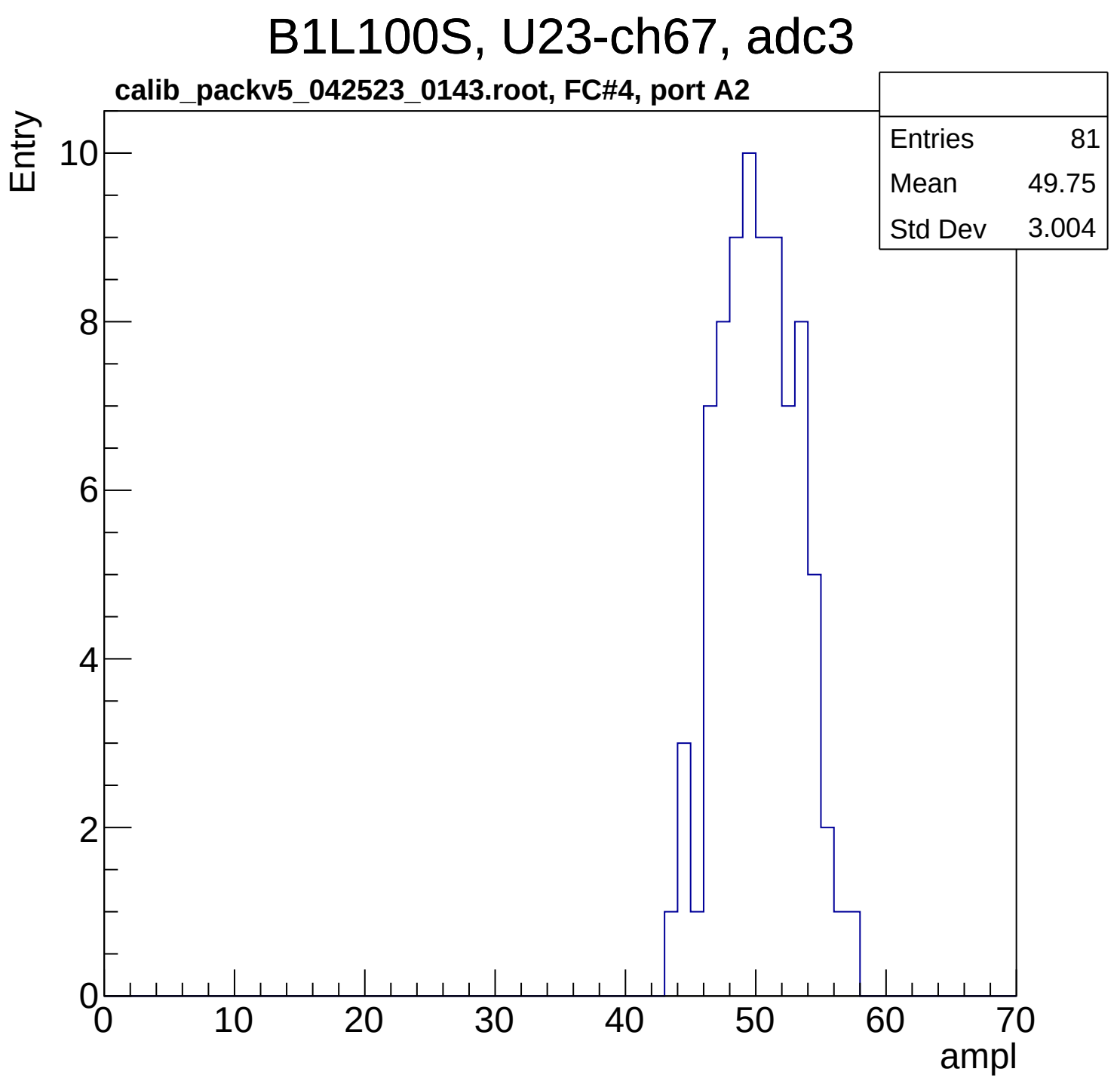

B1L100S, U23-ch66, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 57
Std Dev 16.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

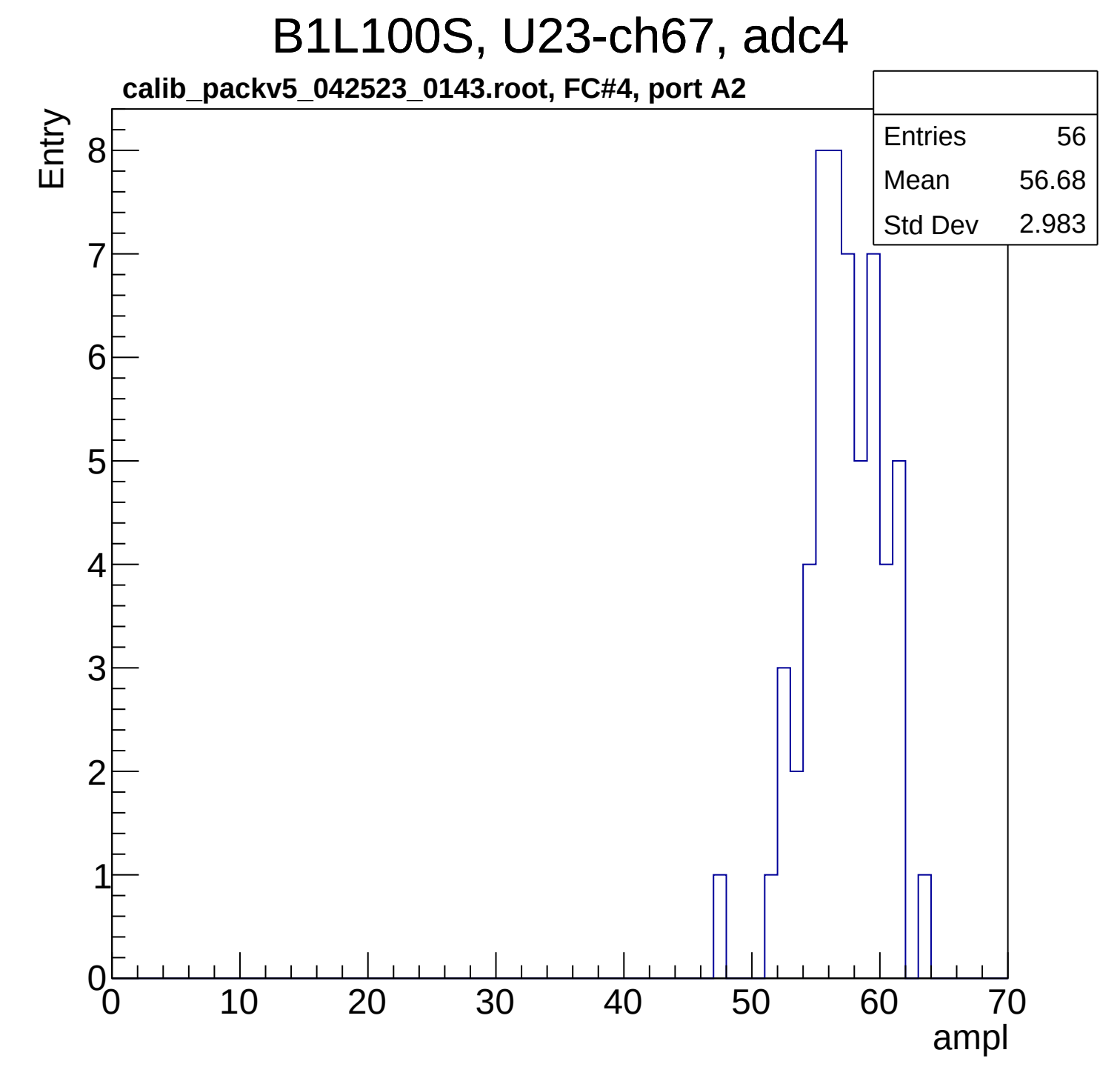

B1L100S, U23-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

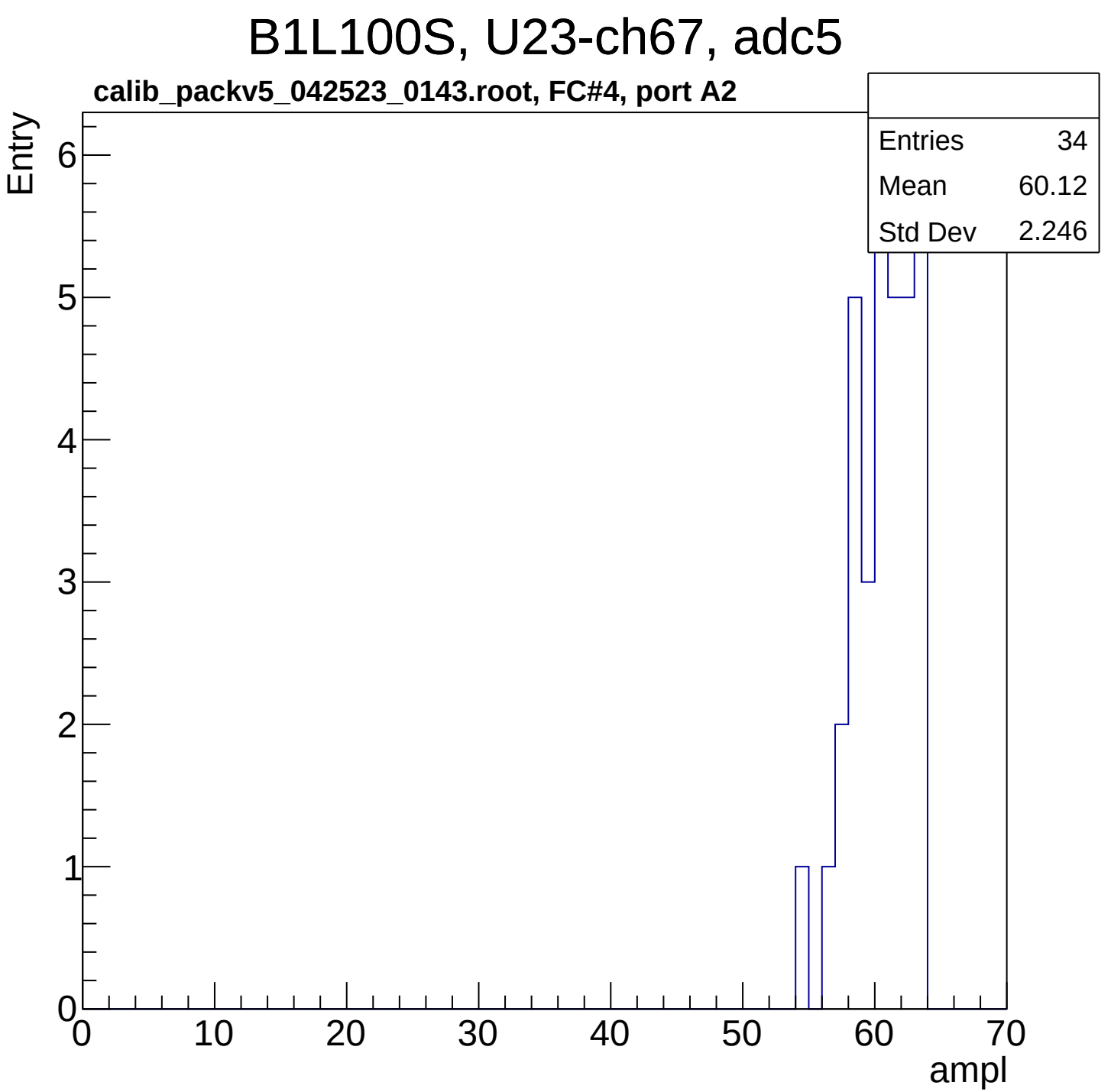

B1L100S, U23-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 27.88
Std Dev 4.796
Gaus mean : 28.6201
Gaus Width: 3.2704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

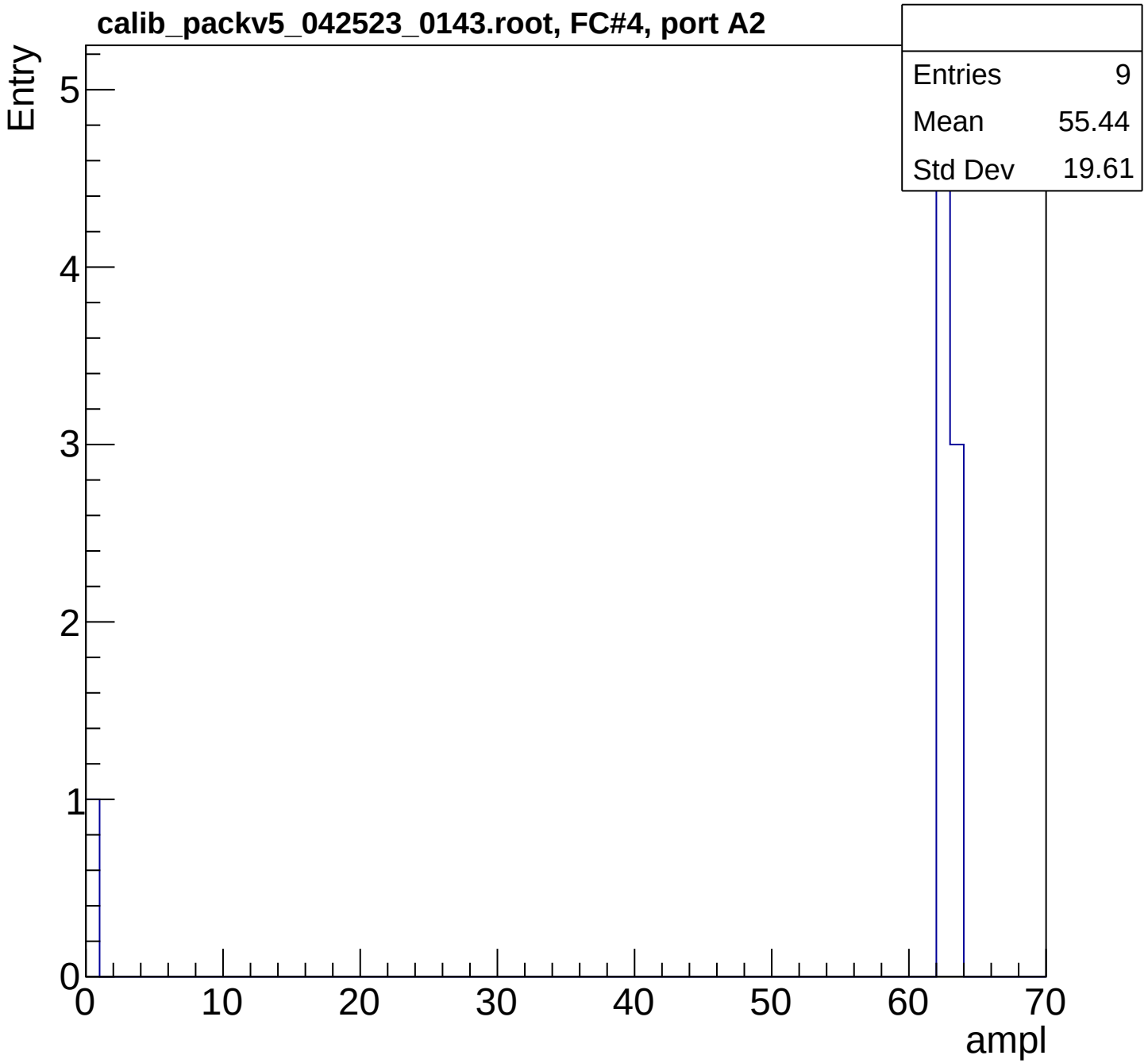

B1L100S, U23-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 35.58
Std Dev 3.4
Gaus mean : 36.2357
Gaus Width: 3.7845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

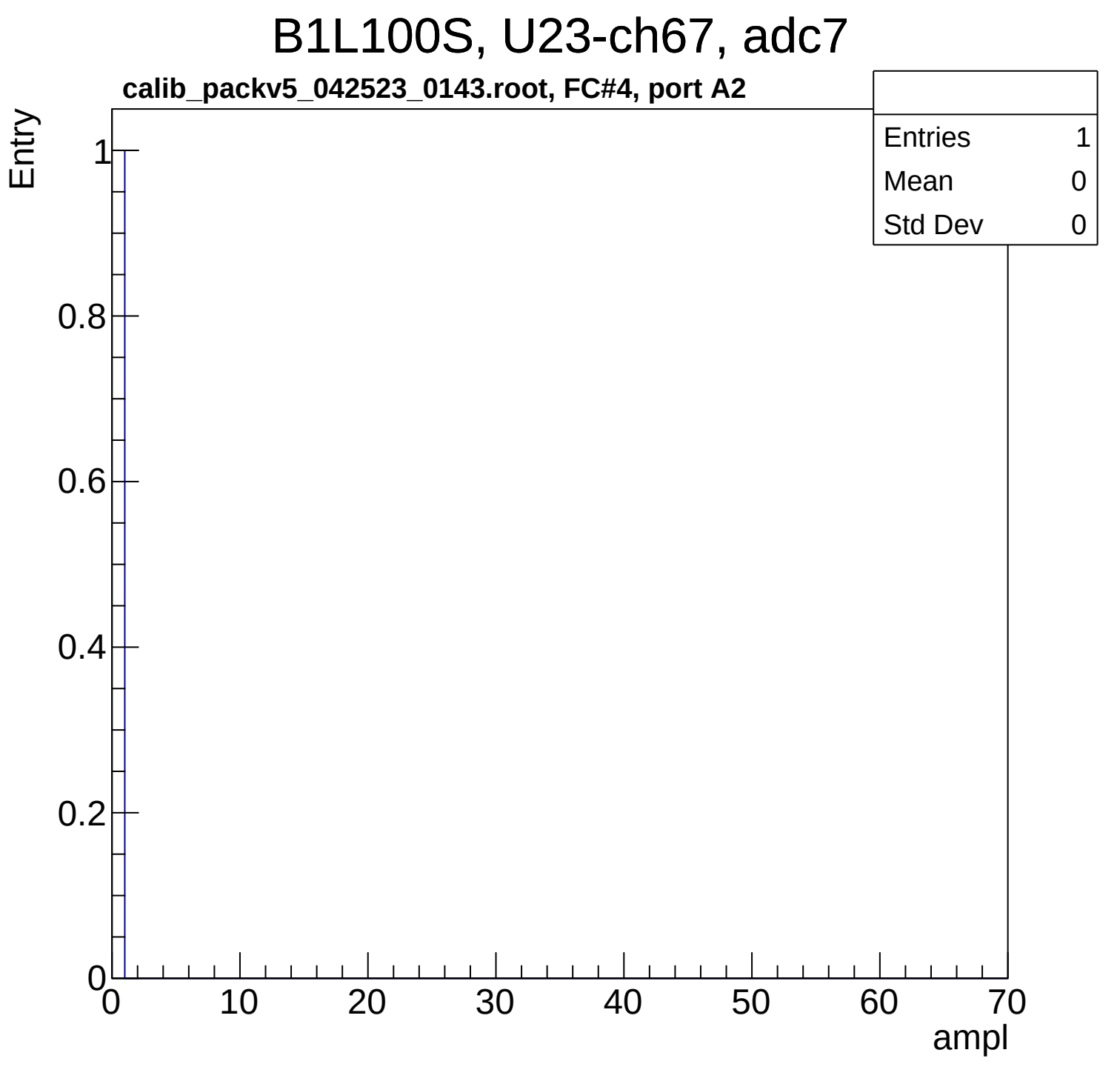

B1L100S, U23-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 41.69
Std Dev 3.378
Gaus mean : 42.0724
Gaus Width: 3.3496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

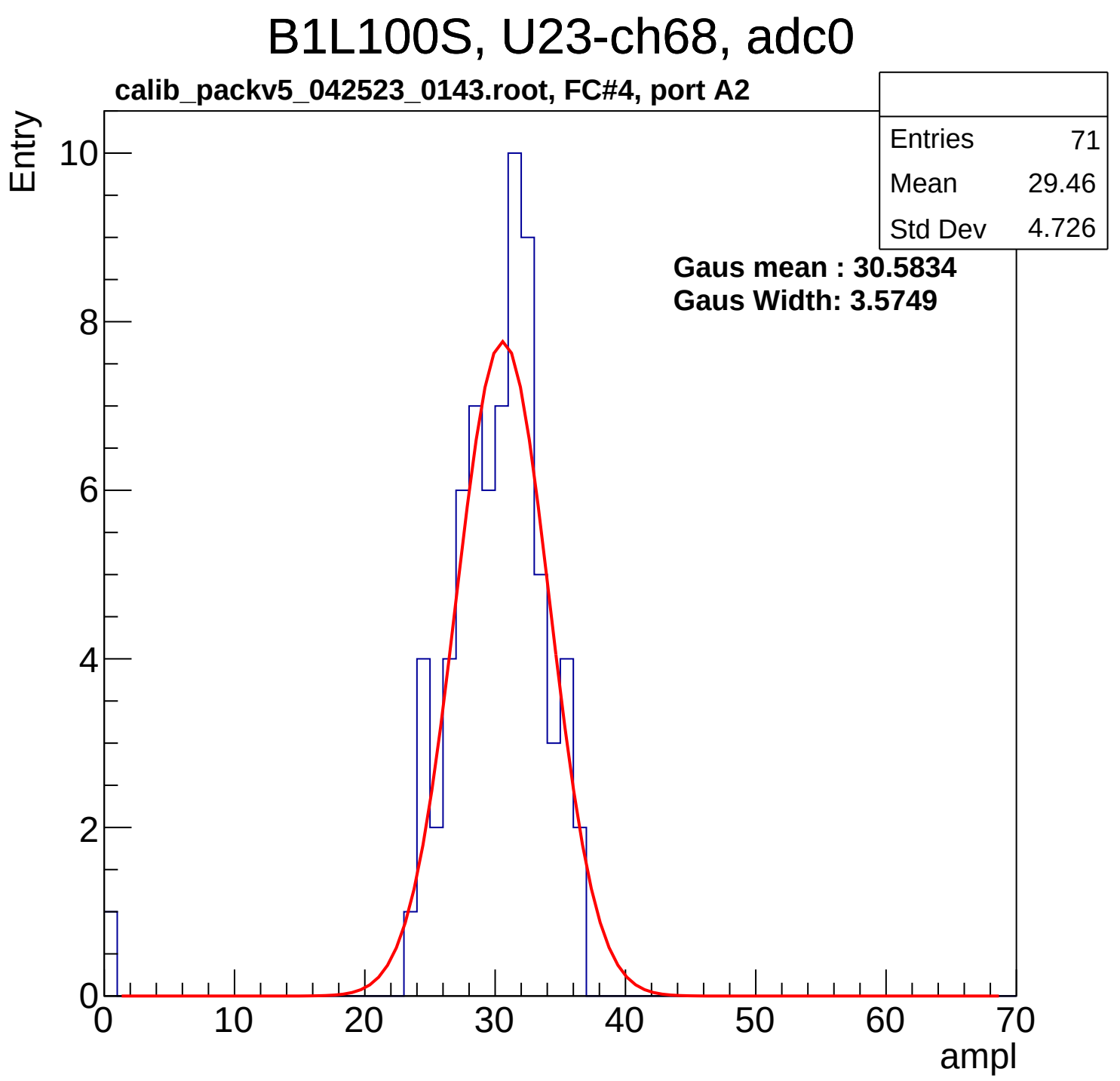

B1L100S, U23-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 49.75
Std Dev 3.004
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

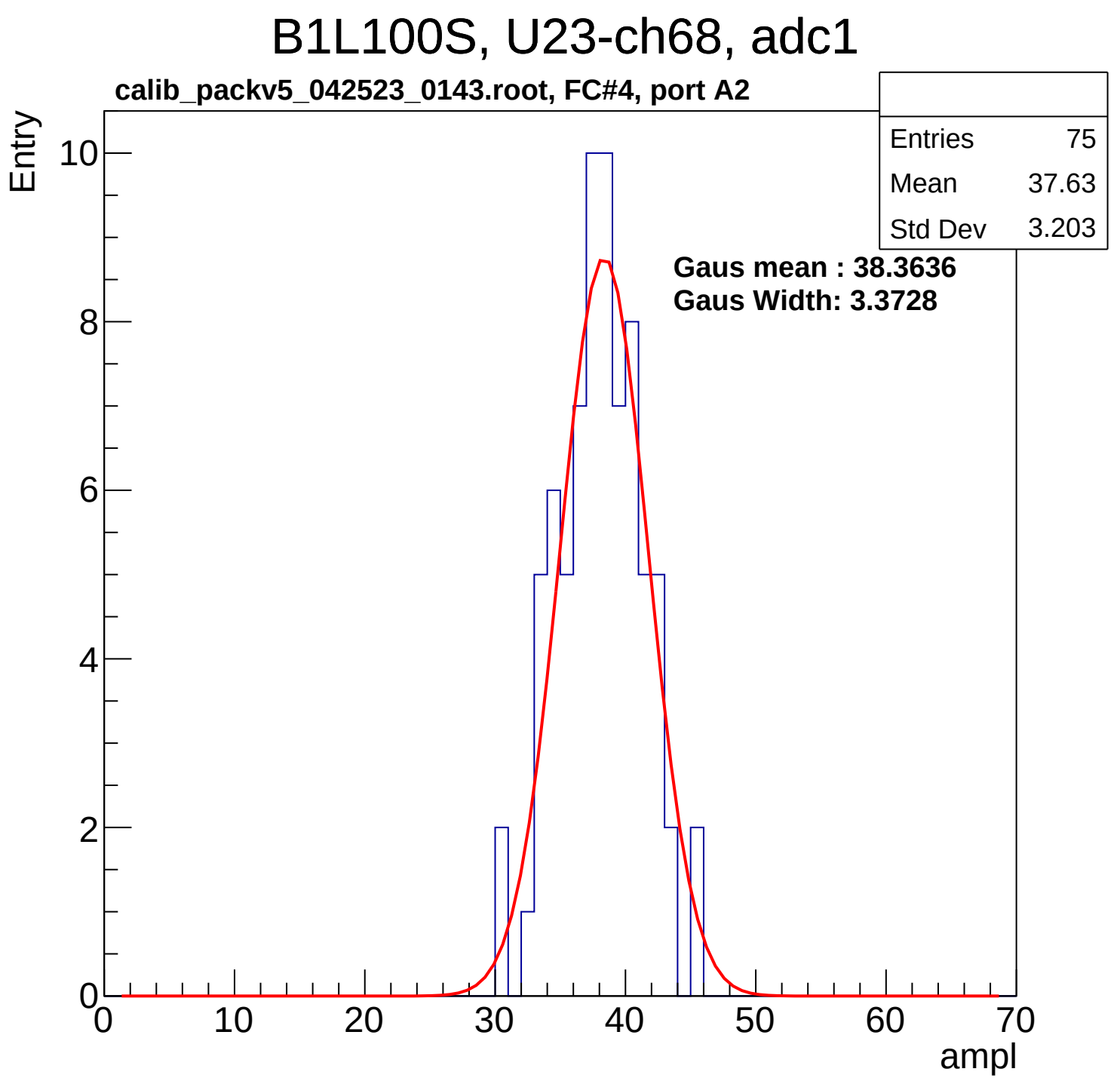

B1L100S, U23-ch67, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.68
Std Dev 2.983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

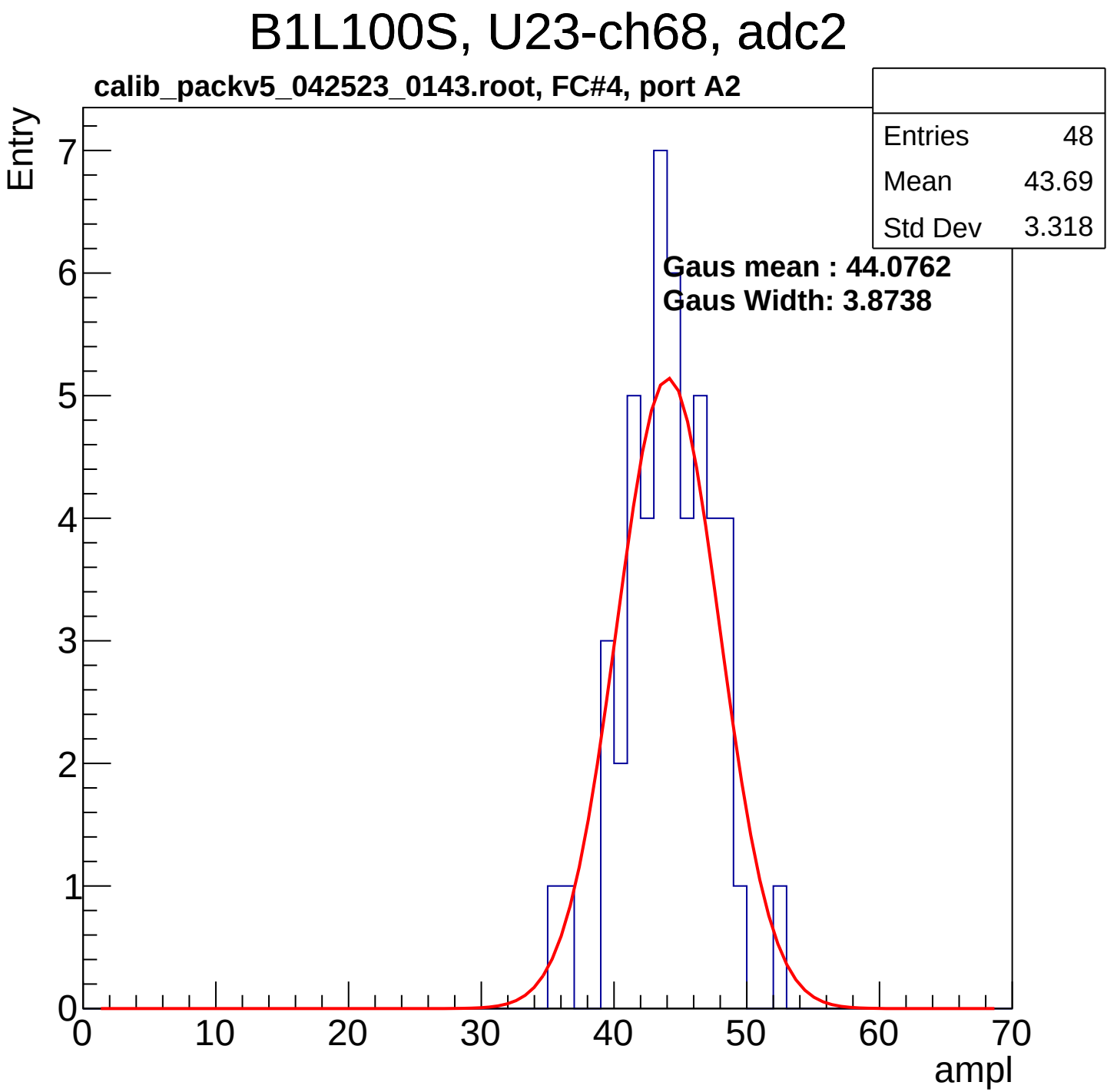

B1L100S, U23-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 60.12
Std Dev 2.246
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch67, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 55.44
Std Dev 19.61
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch67, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.46
Std Dev 4.726
Gaus mean : 30.5834
Gaus Width: 3.5749
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.63
Std Dev 3.203
Gaus mean : 38.3636
Gaus Width: 3.3728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 43.69
Std Dev 3.318
Gaus mean : 44.0762
Gaus Width: 3.8738
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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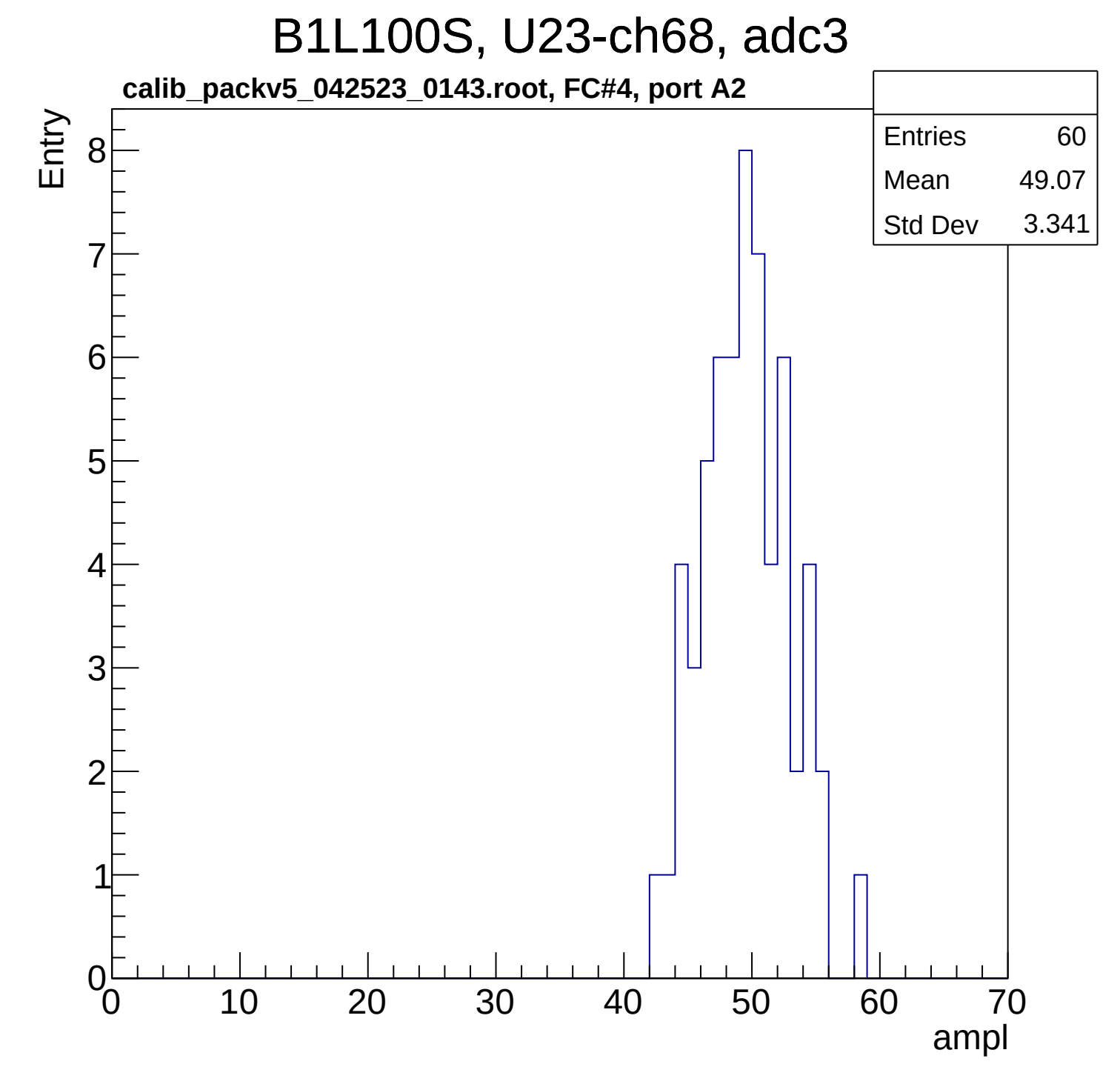
40
50
60
70


B1L100S, U23-ch68, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.07
Std Dev 3.341
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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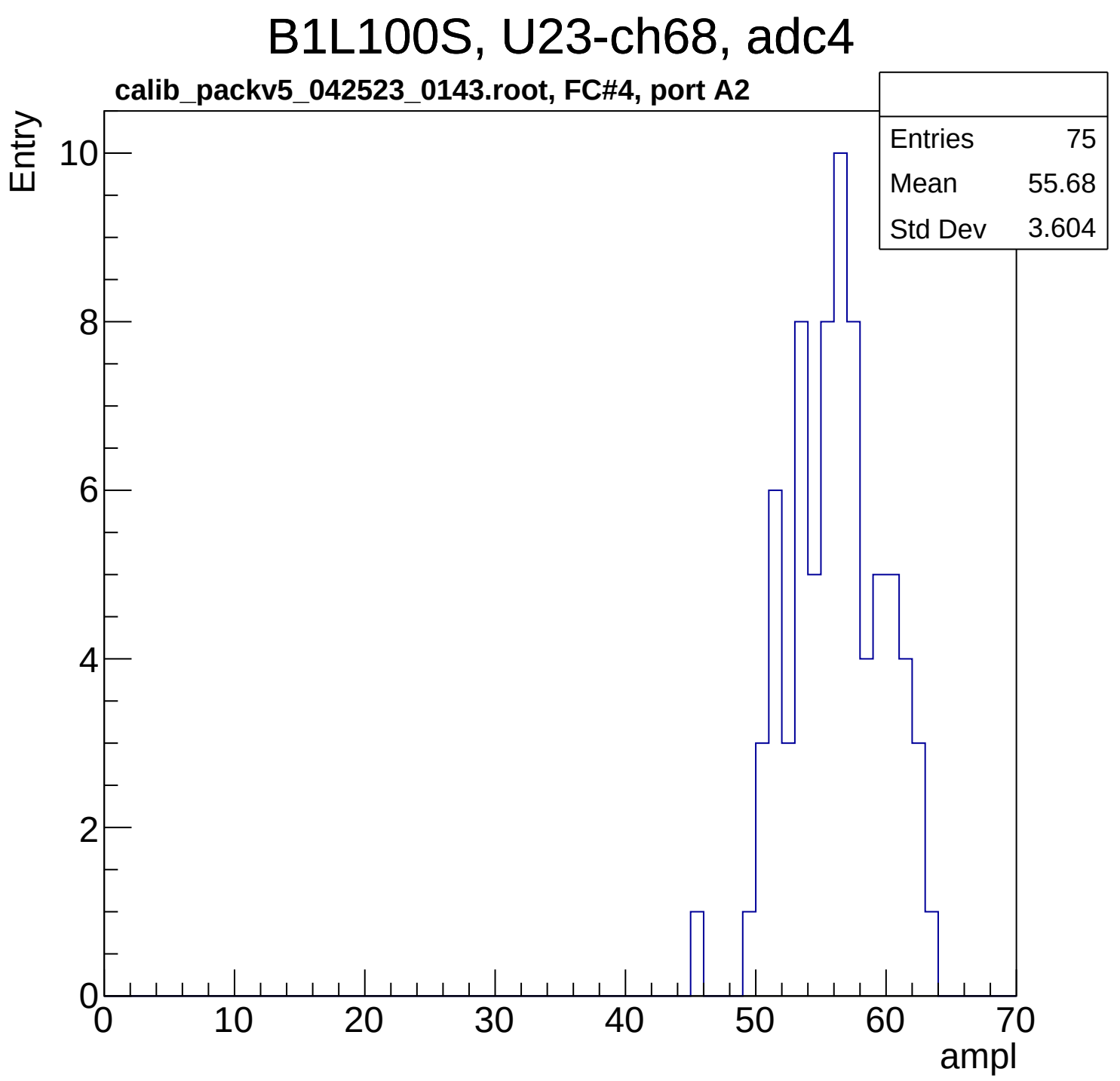
B1L100S, U23-ch68, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 55.68
Std Dev 3.604
Entry
ampl
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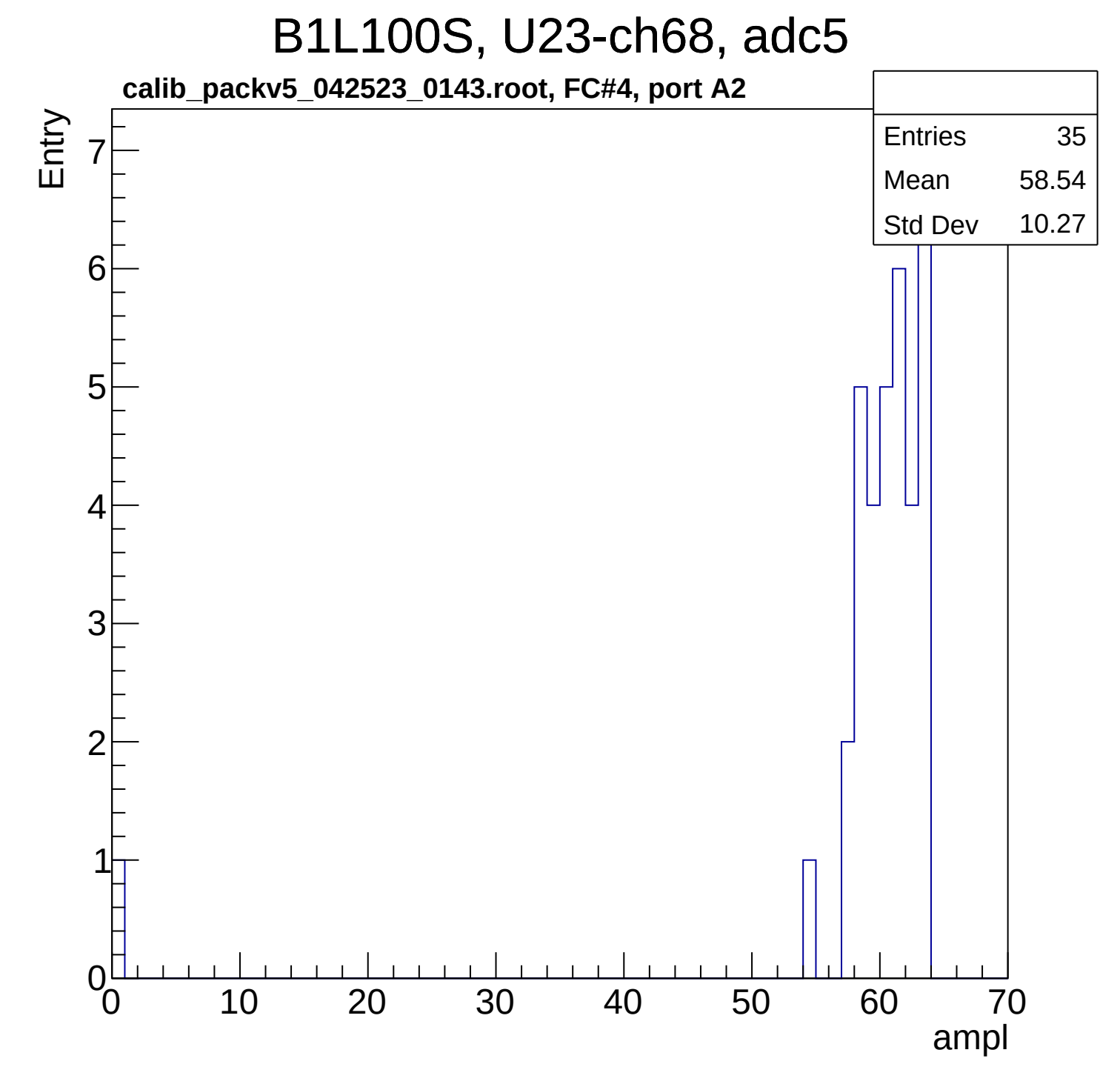
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch68, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.54
Std Dev 10.27
Entry
ampl
0
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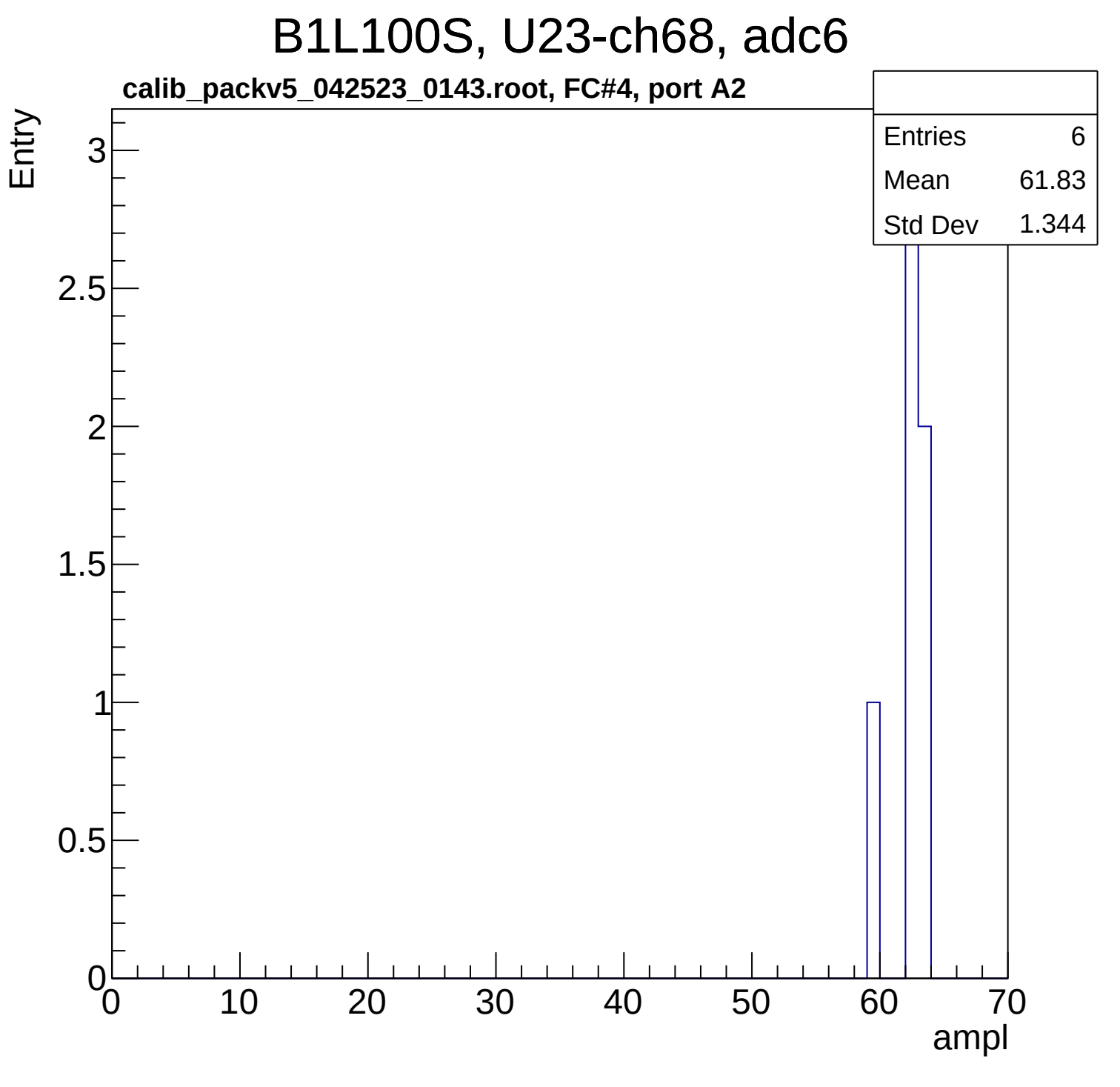
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.83
Std Dev 1.344
Entry
ampl
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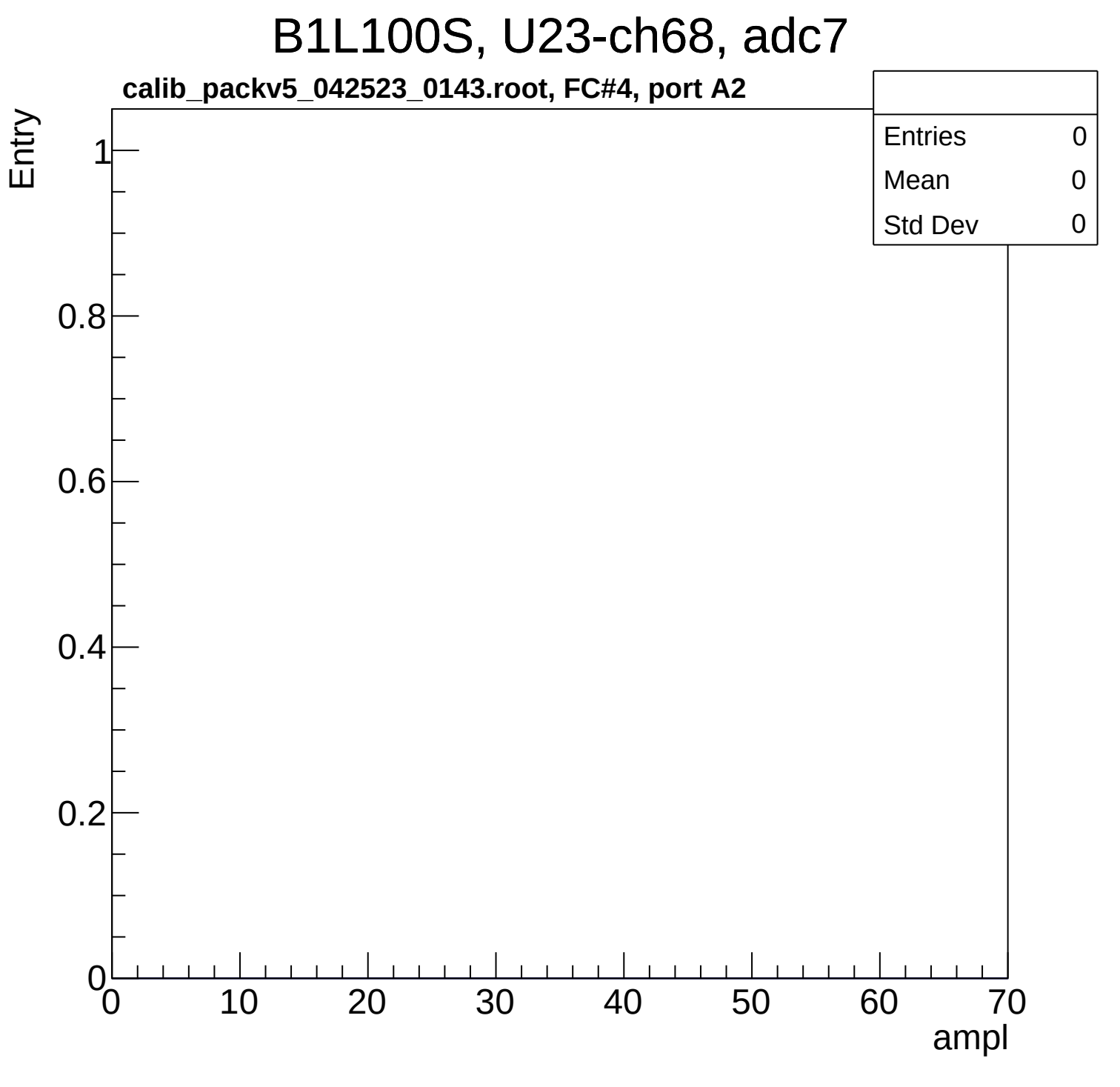
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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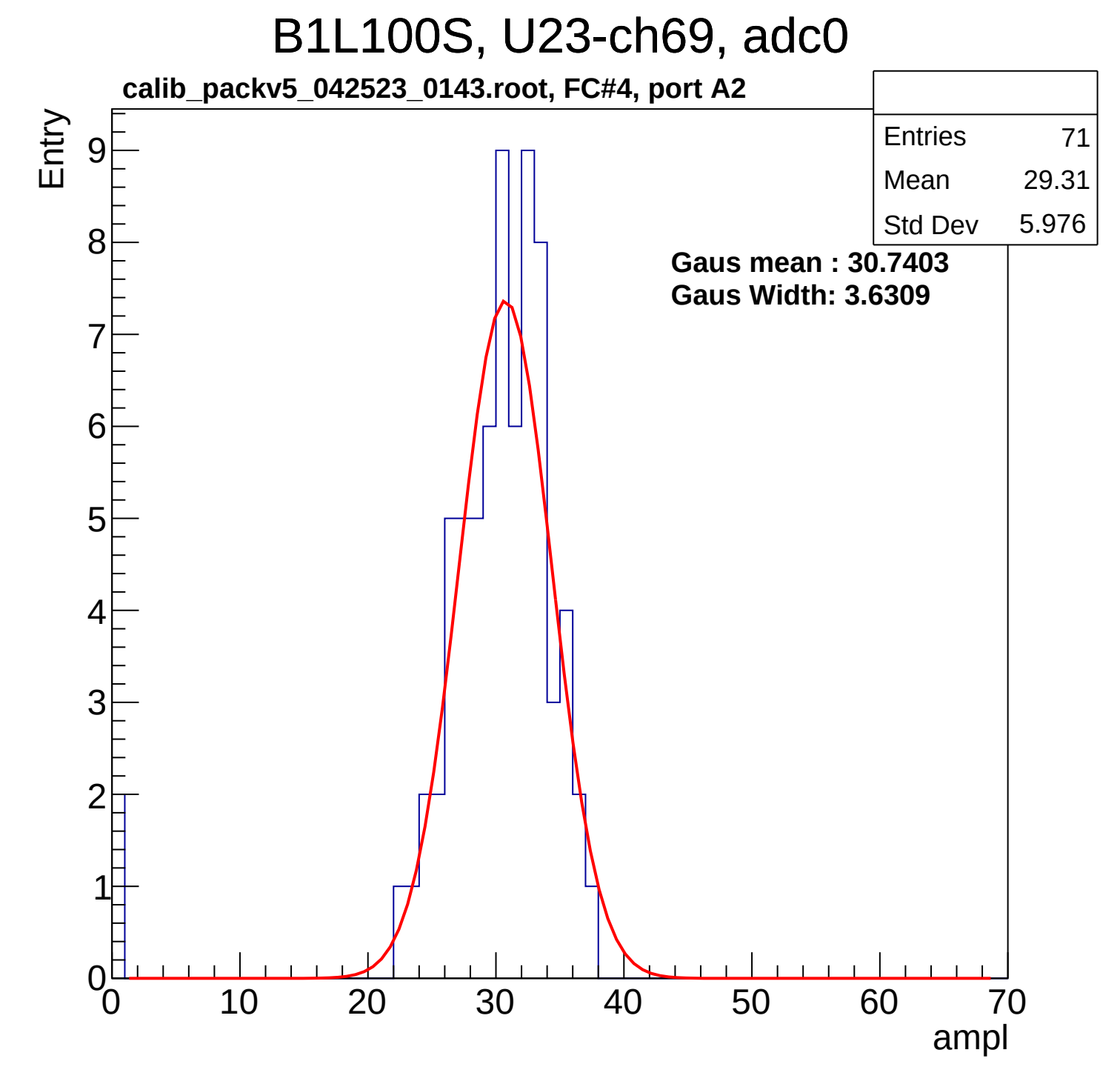
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.31
Std Dev 5.976
Gaus mean : 30.7403
Gaus Width: 3.6309
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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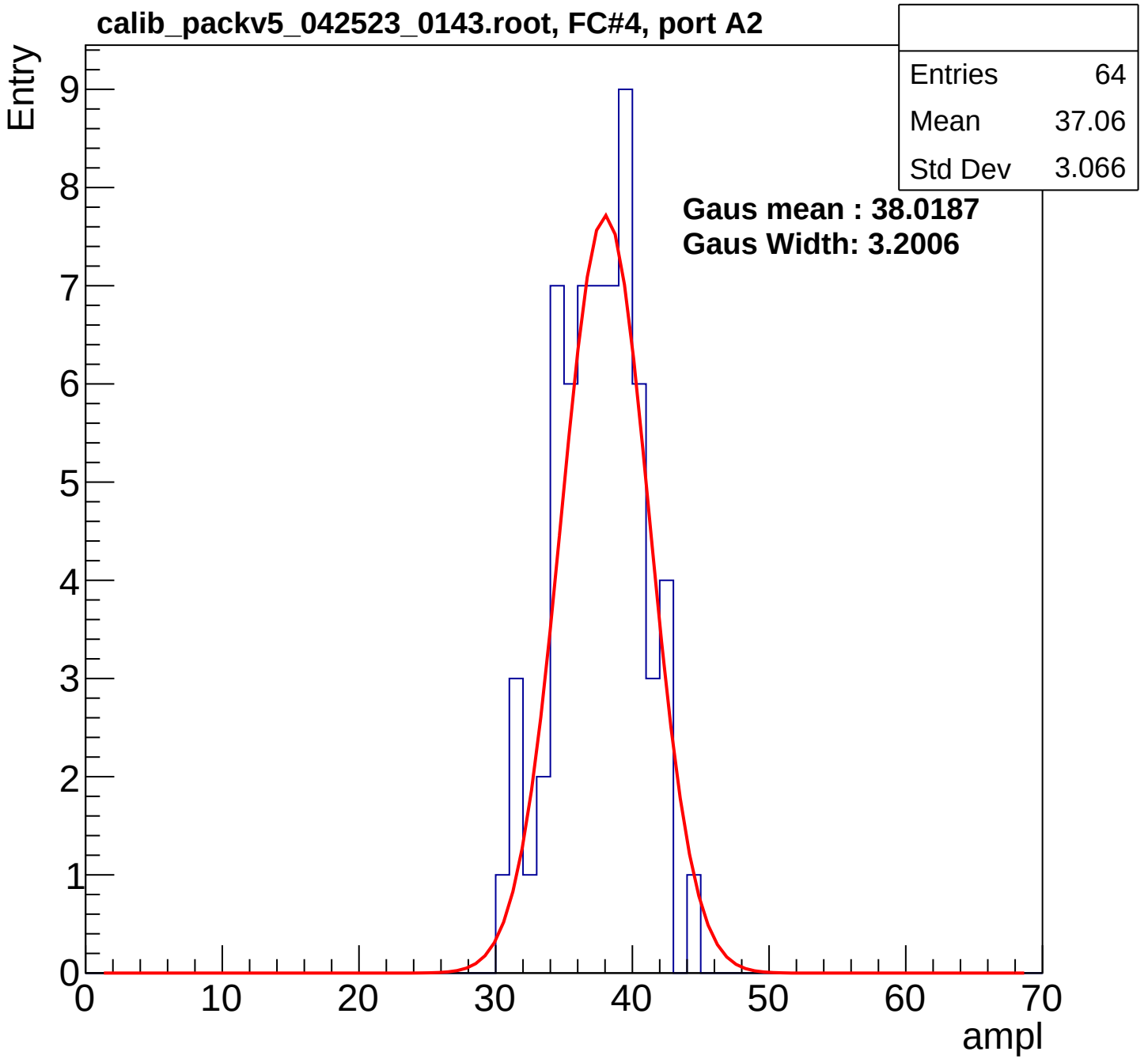
B1L100S, U23-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.06
Std Dev 3.066
Gaus mean : 38.0187
Gaus Width: 3.2006
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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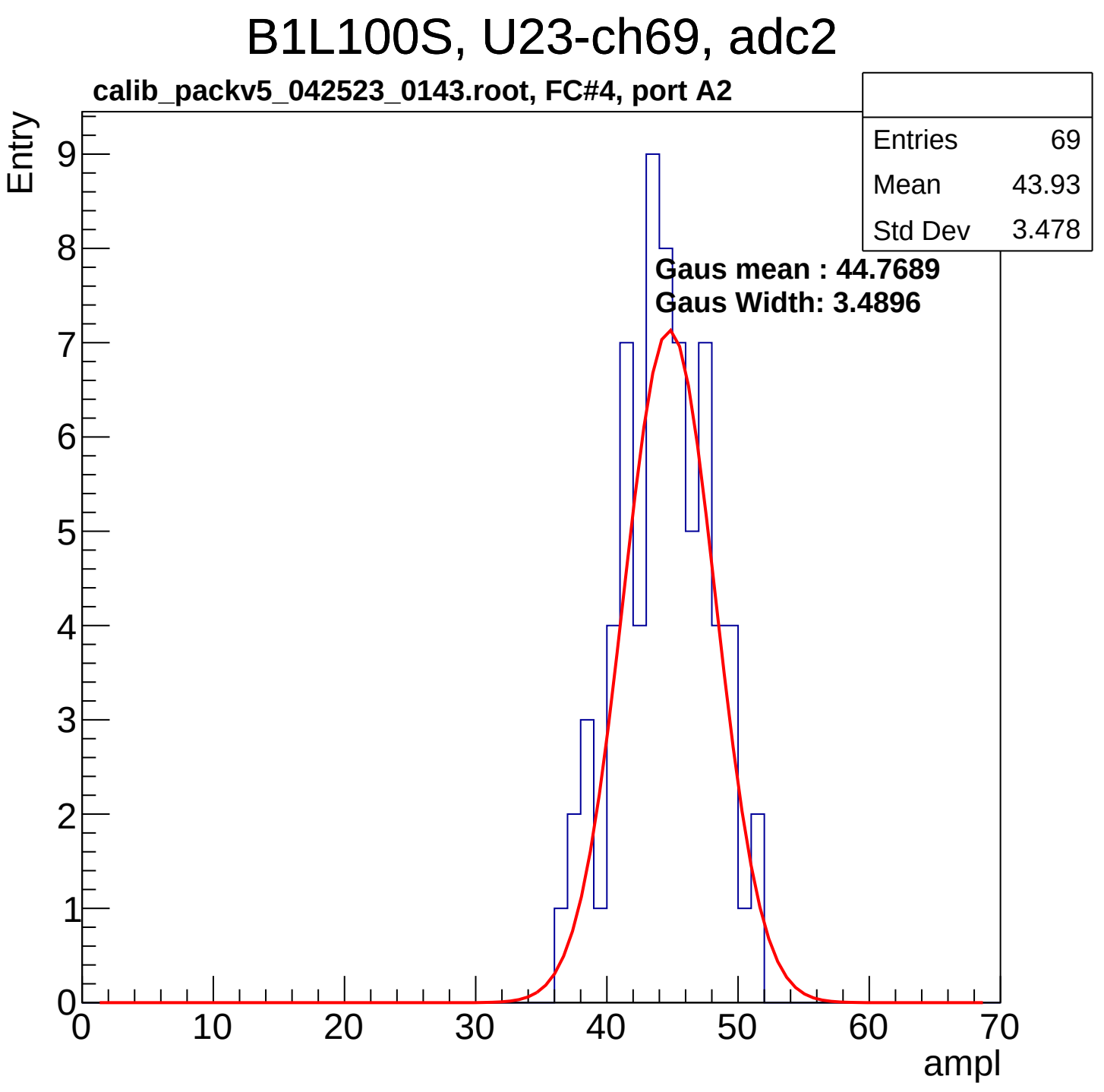
B1L100S, U23-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.93
Std Dev 3.478
Gaus mean : 44.7689
Gaus Width: 3.4896
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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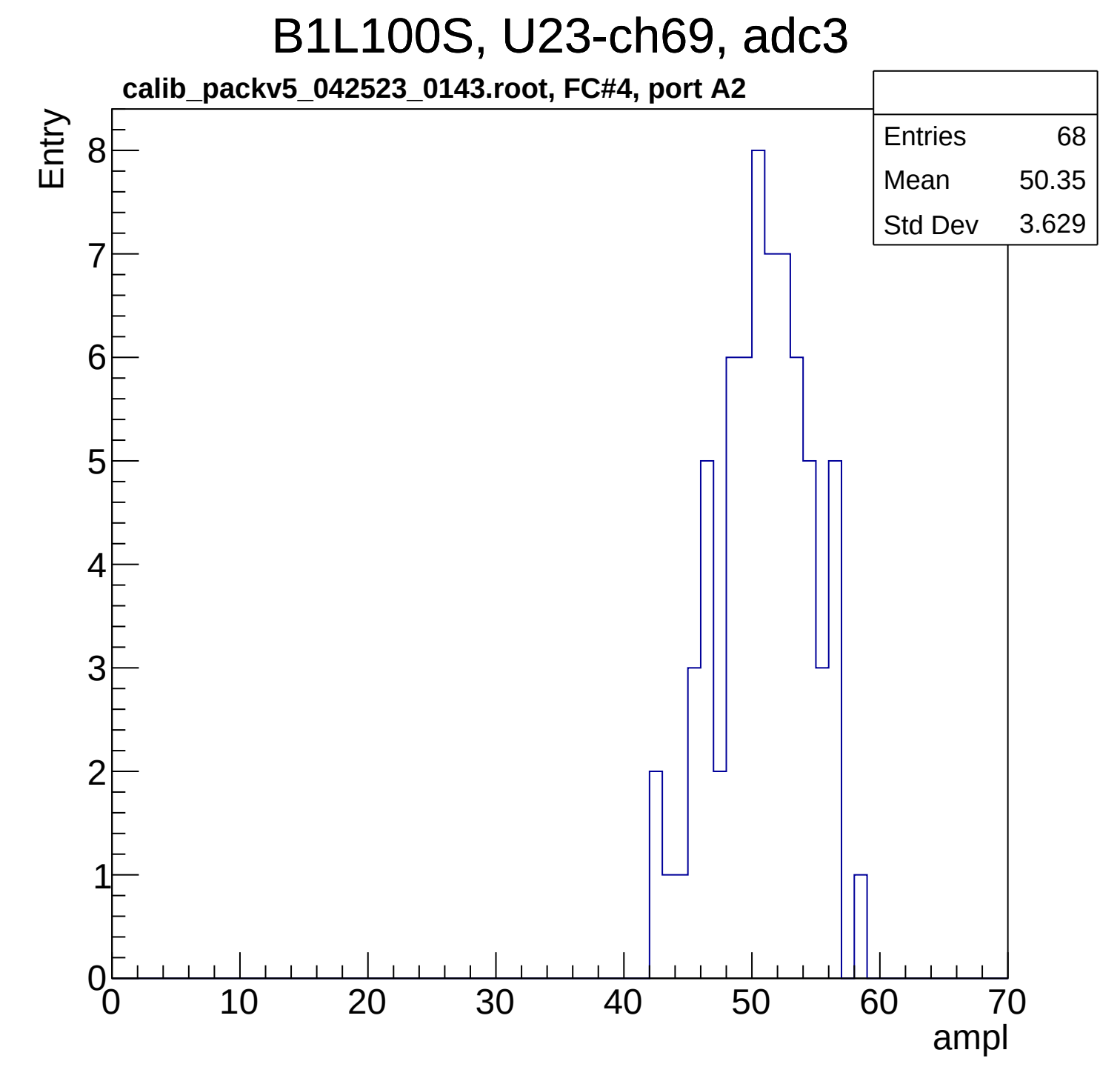
B1L100S, U23-ch69, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.35
Std Dev 3.629
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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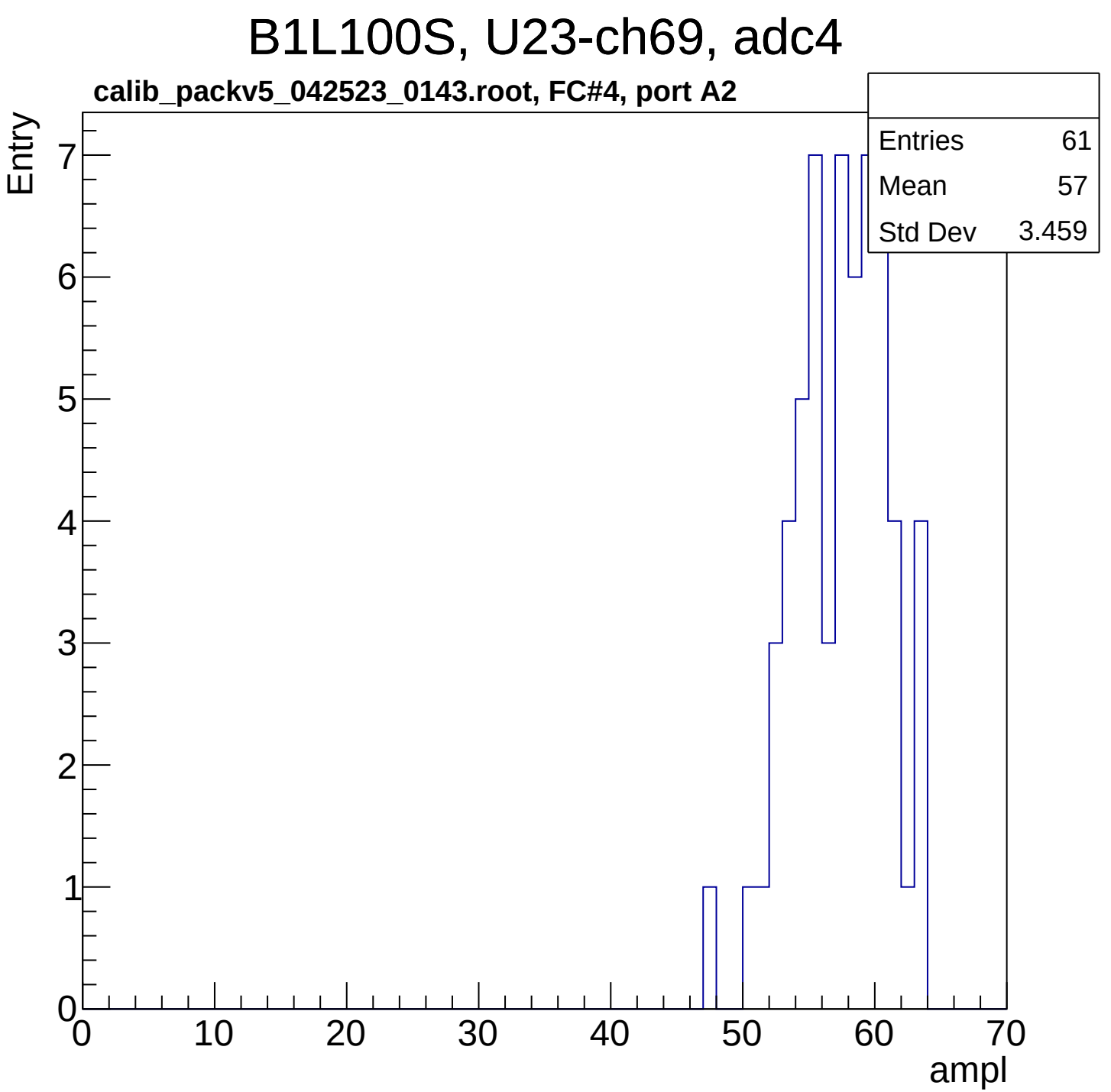
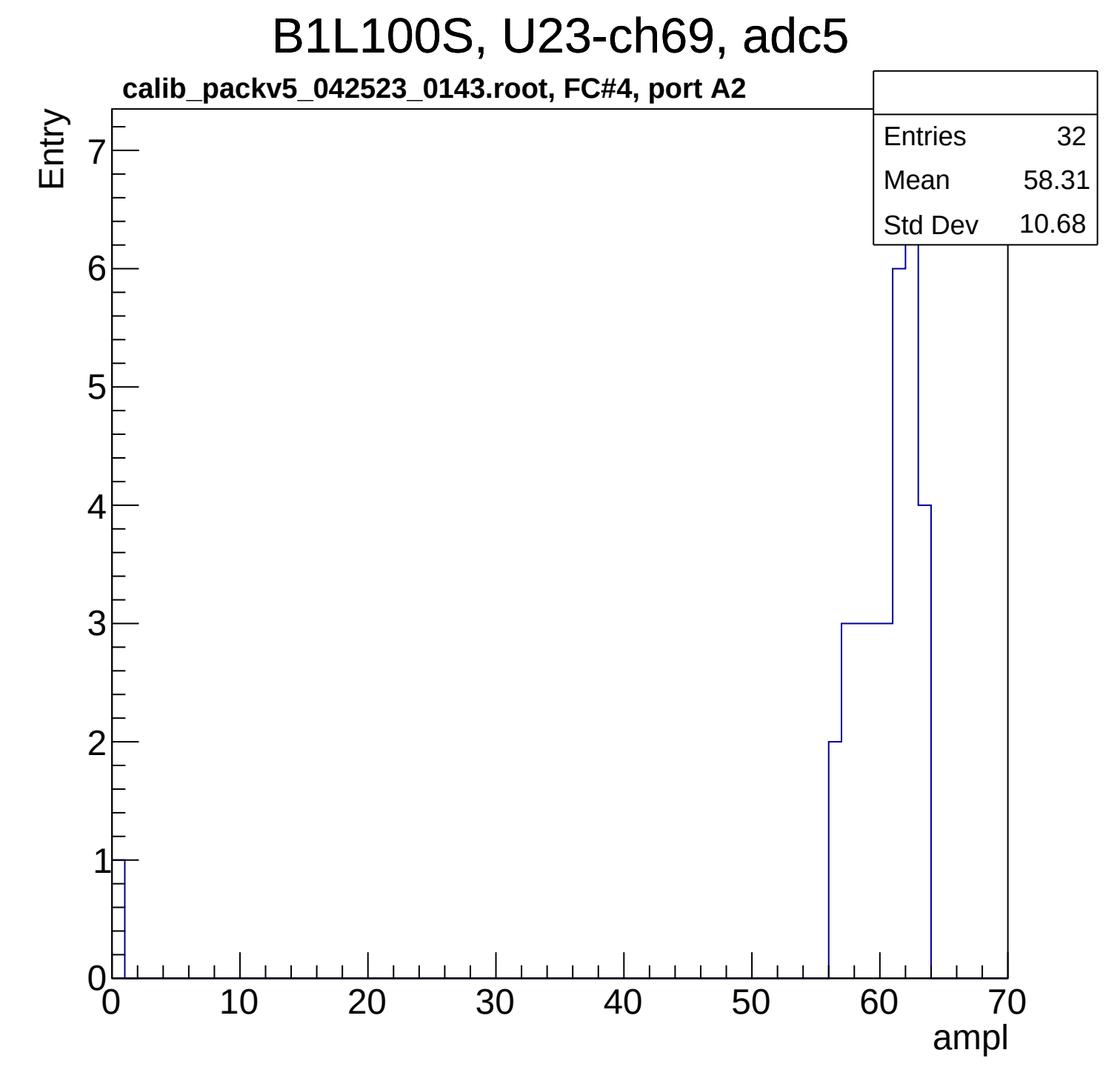
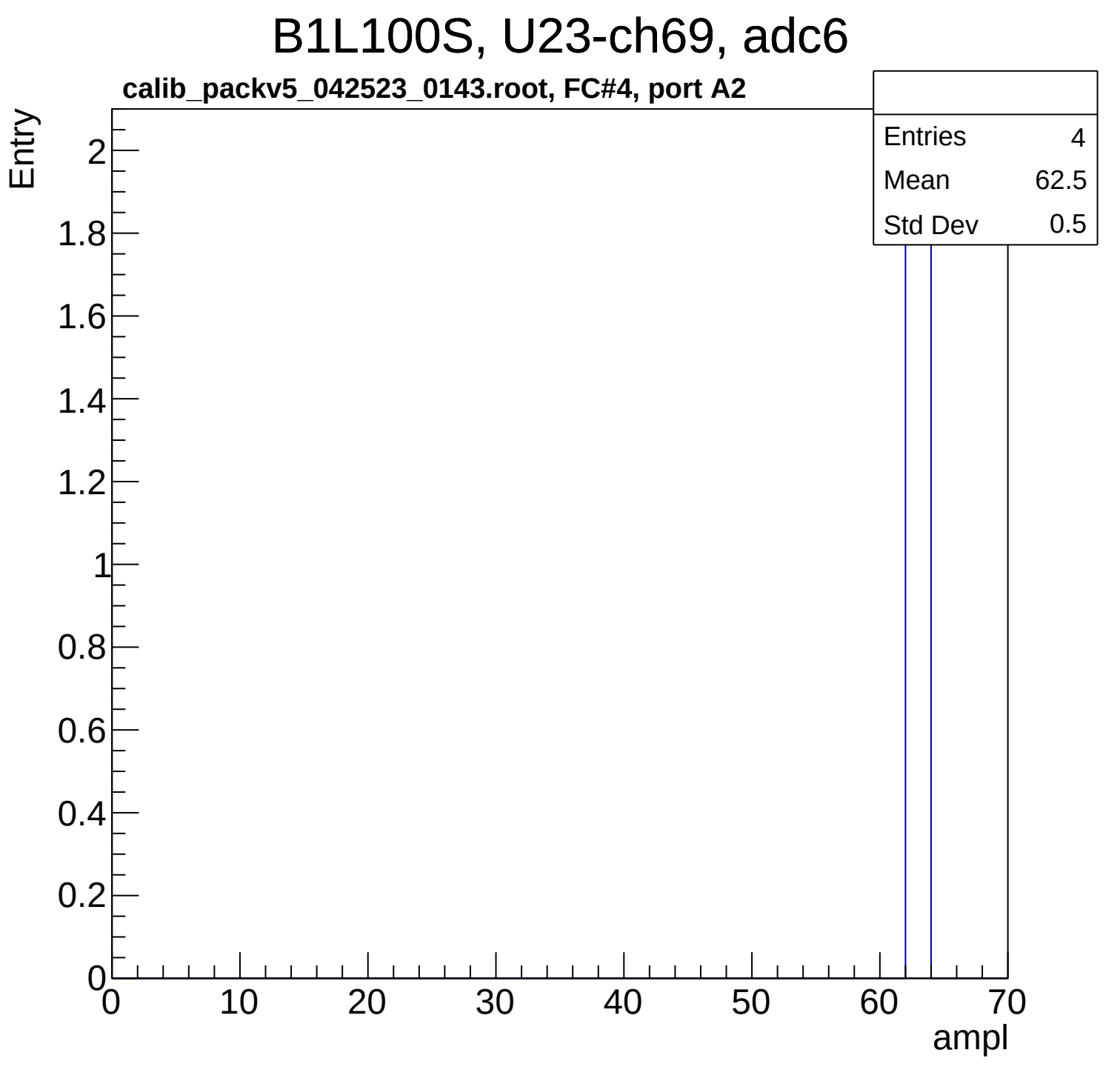

B1L100S, U23-ch69, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57
Std Dev 3.459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.31
Std Dev 10.68
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch69, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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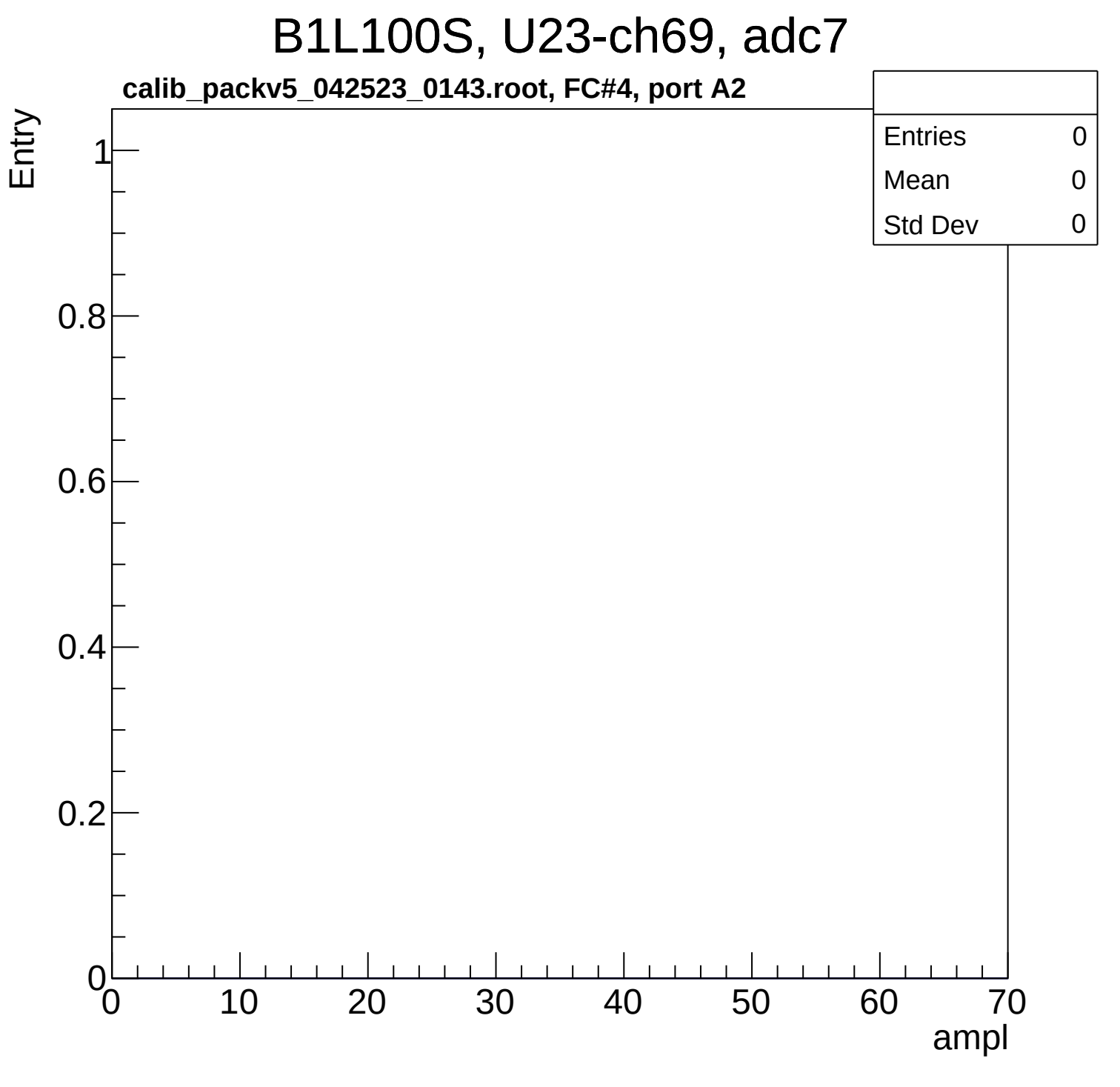
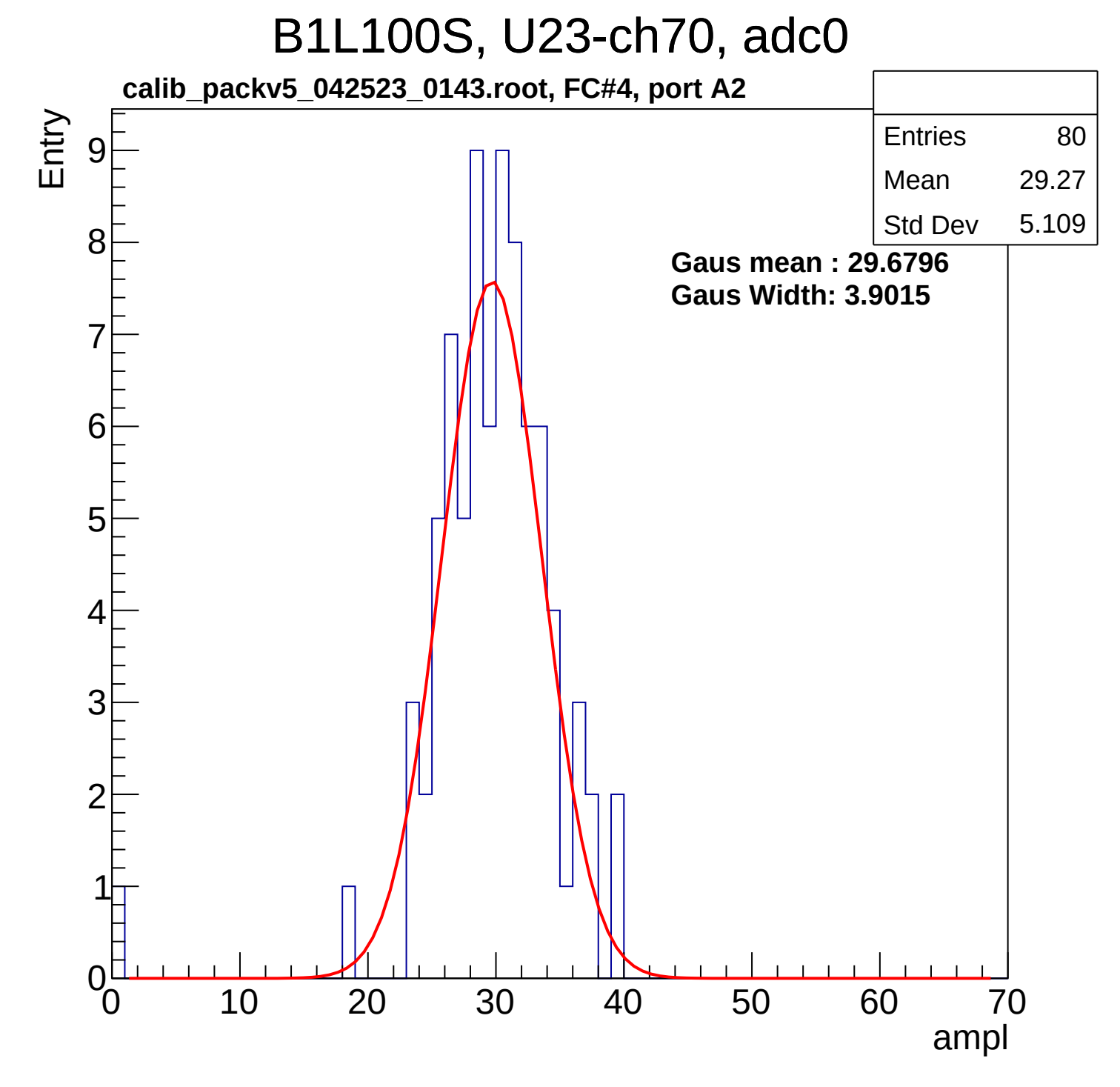
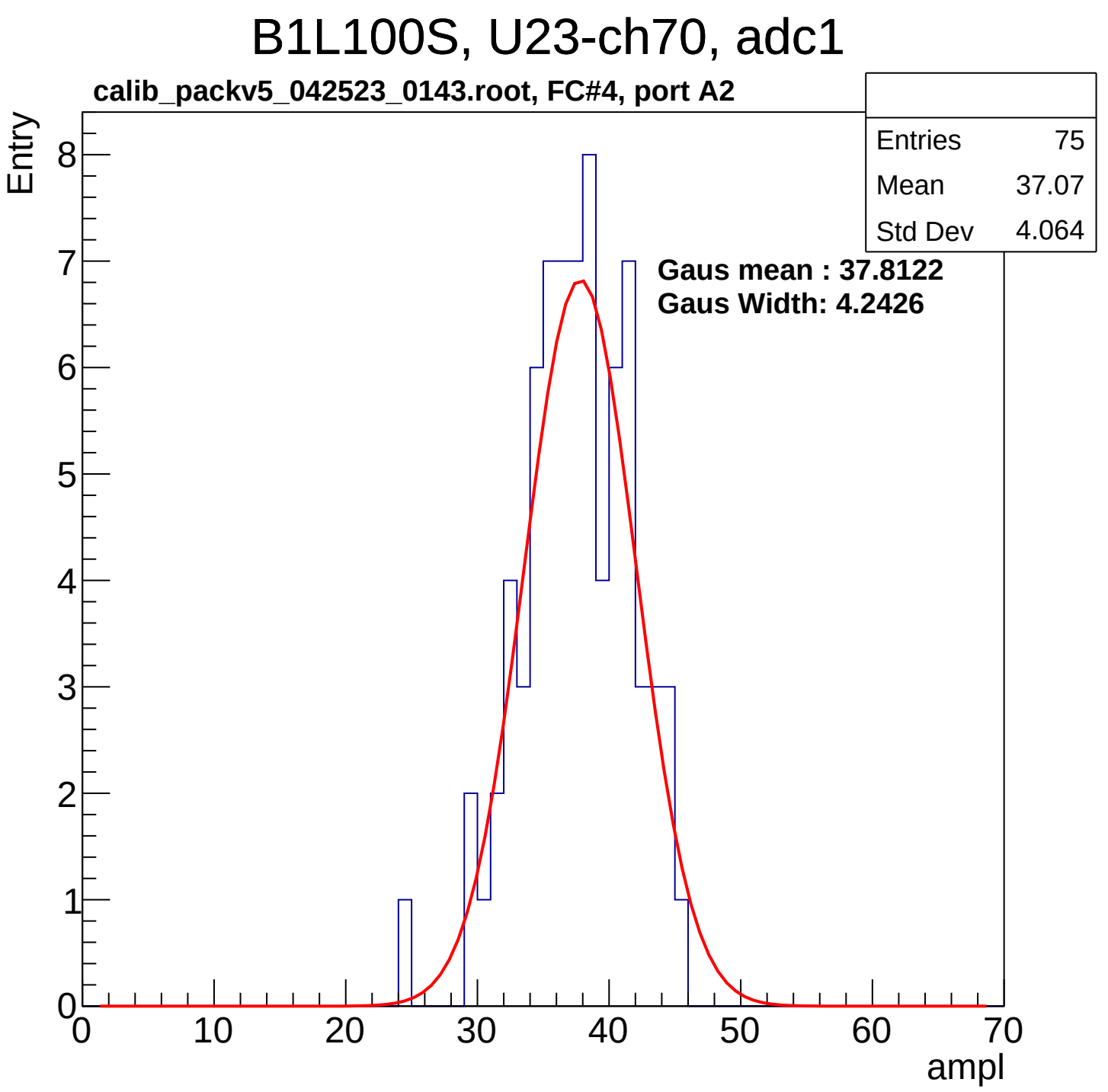

B1L100S, U23-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.27
Std Dev 5.109
Gaus mean : 29.6796
Gaus Width: 3.9015
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.07
Std Dev 4.064
Gaus mean : 37.8122
Gaus Width: 4.2426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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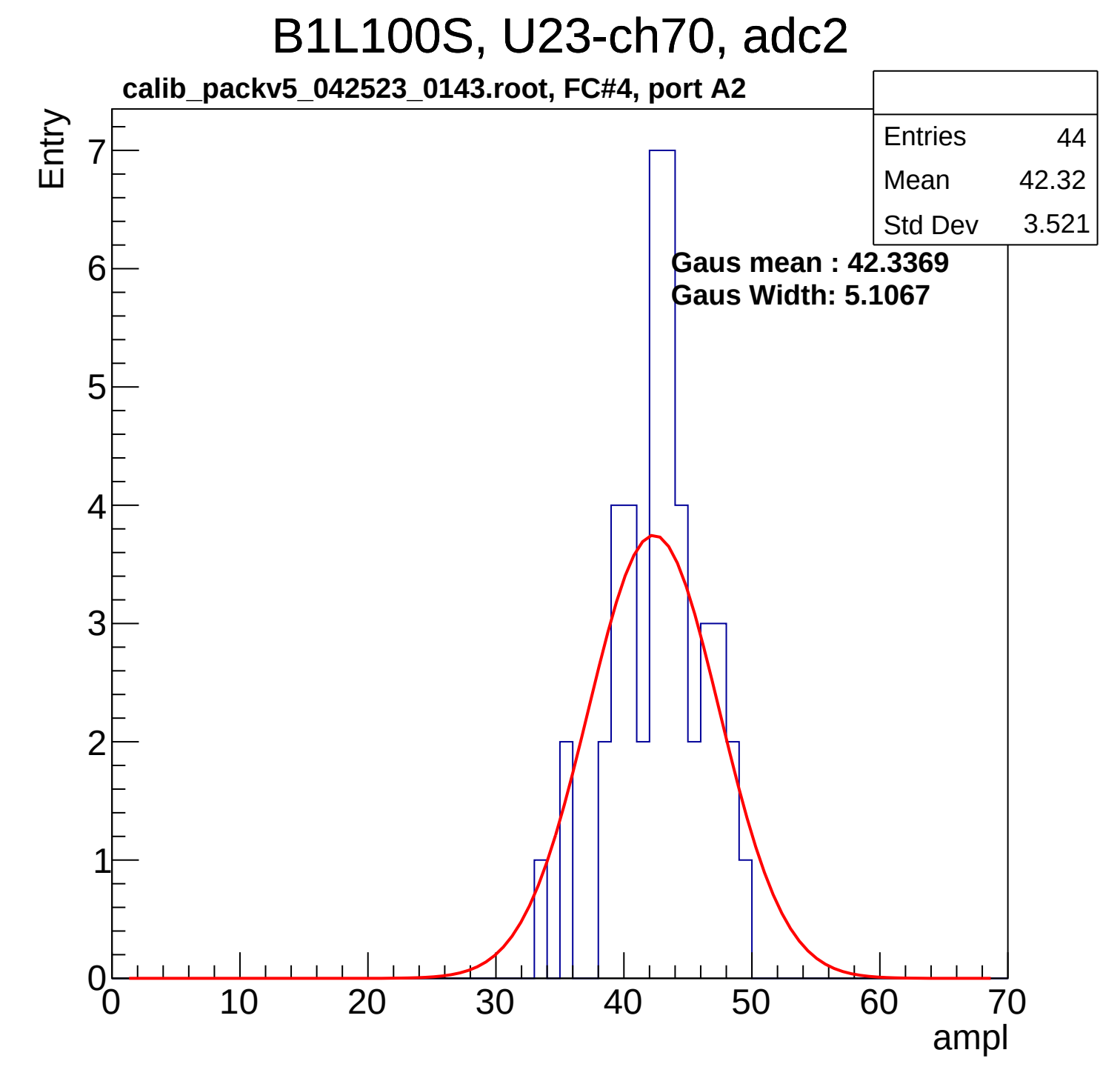
B1L100S, U23-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 42.32
Std Dev 3.521
Gaus mean : 42.3369
Gaus Width: 5.1067
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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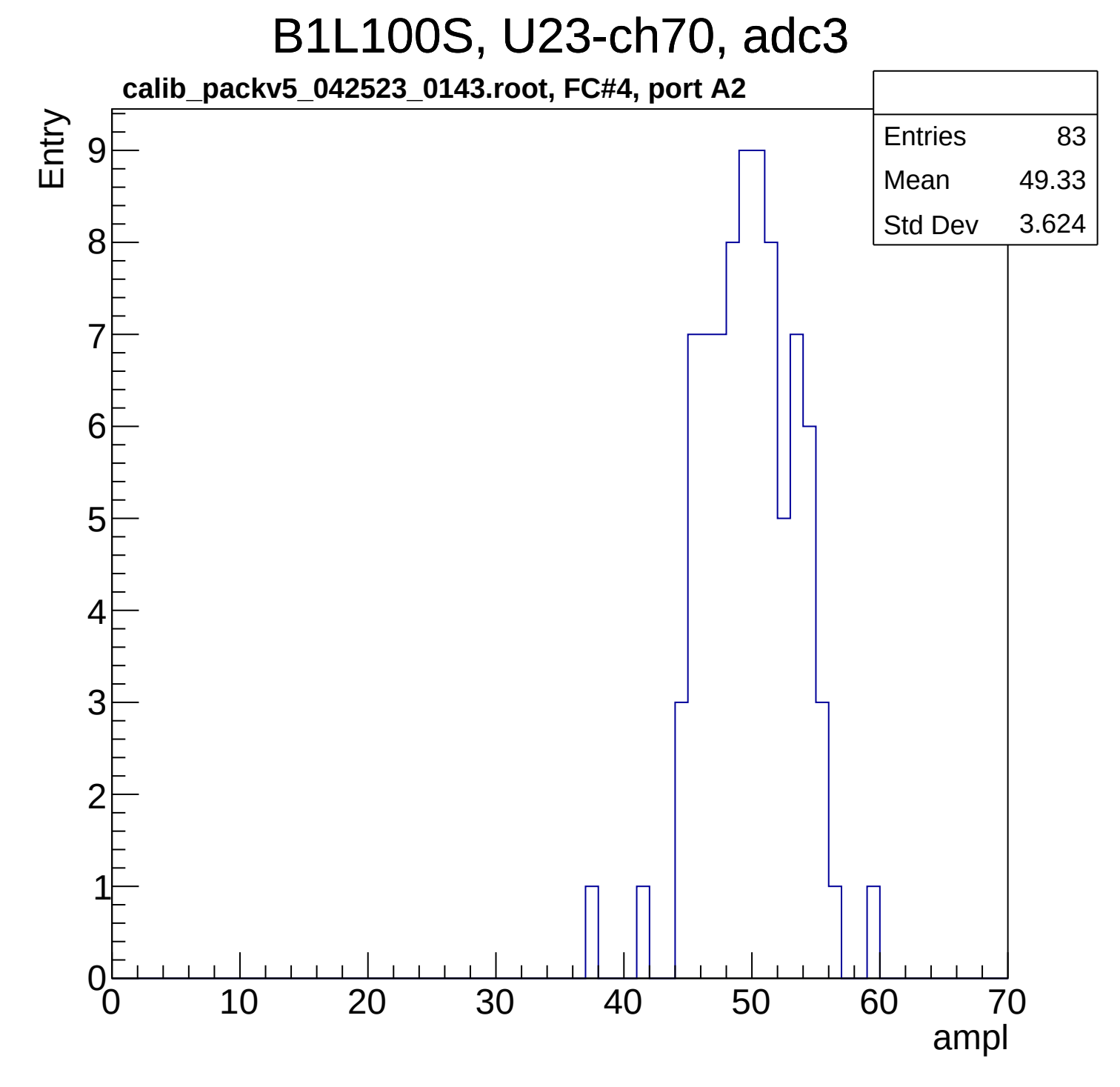
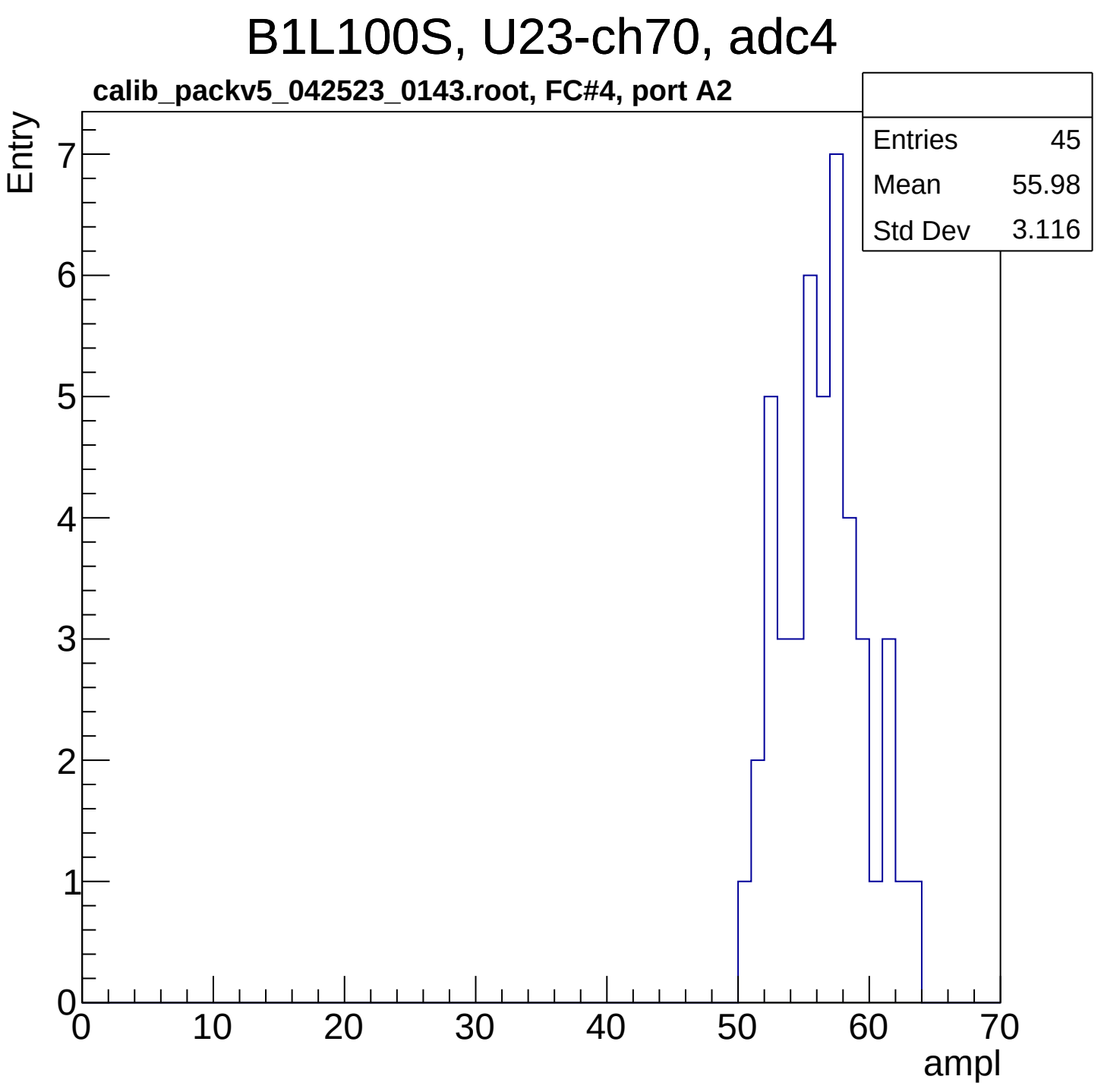
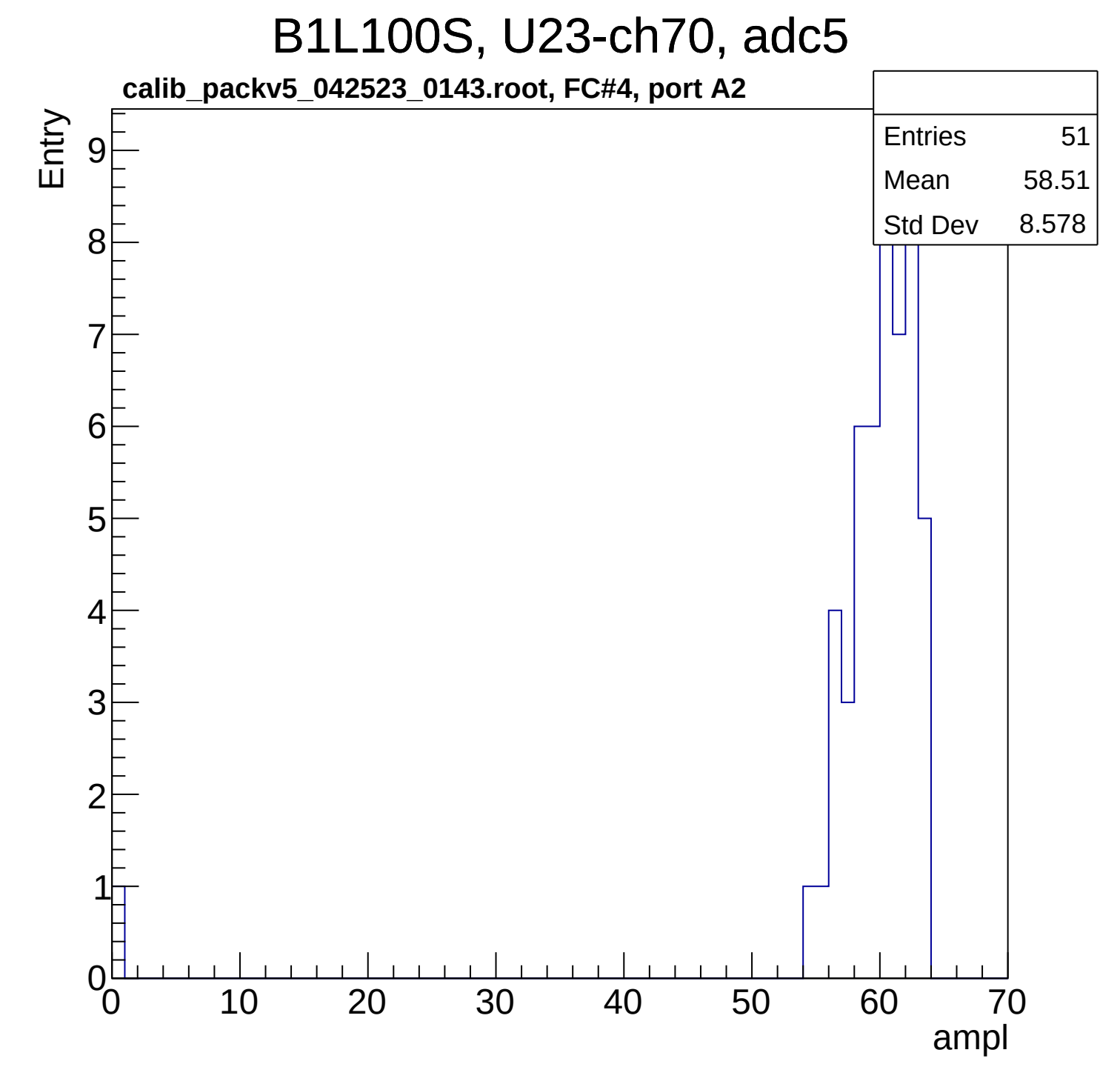
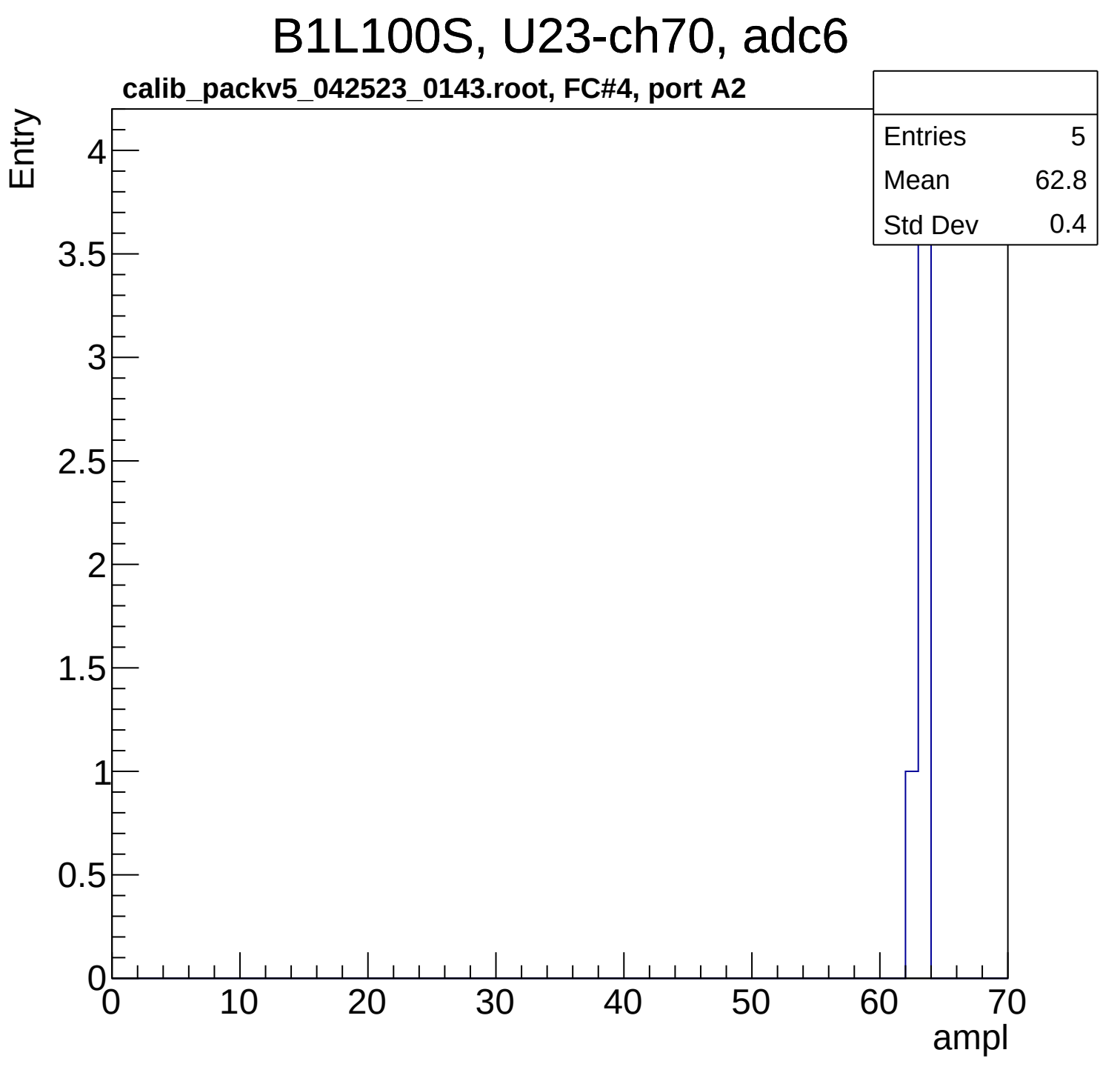
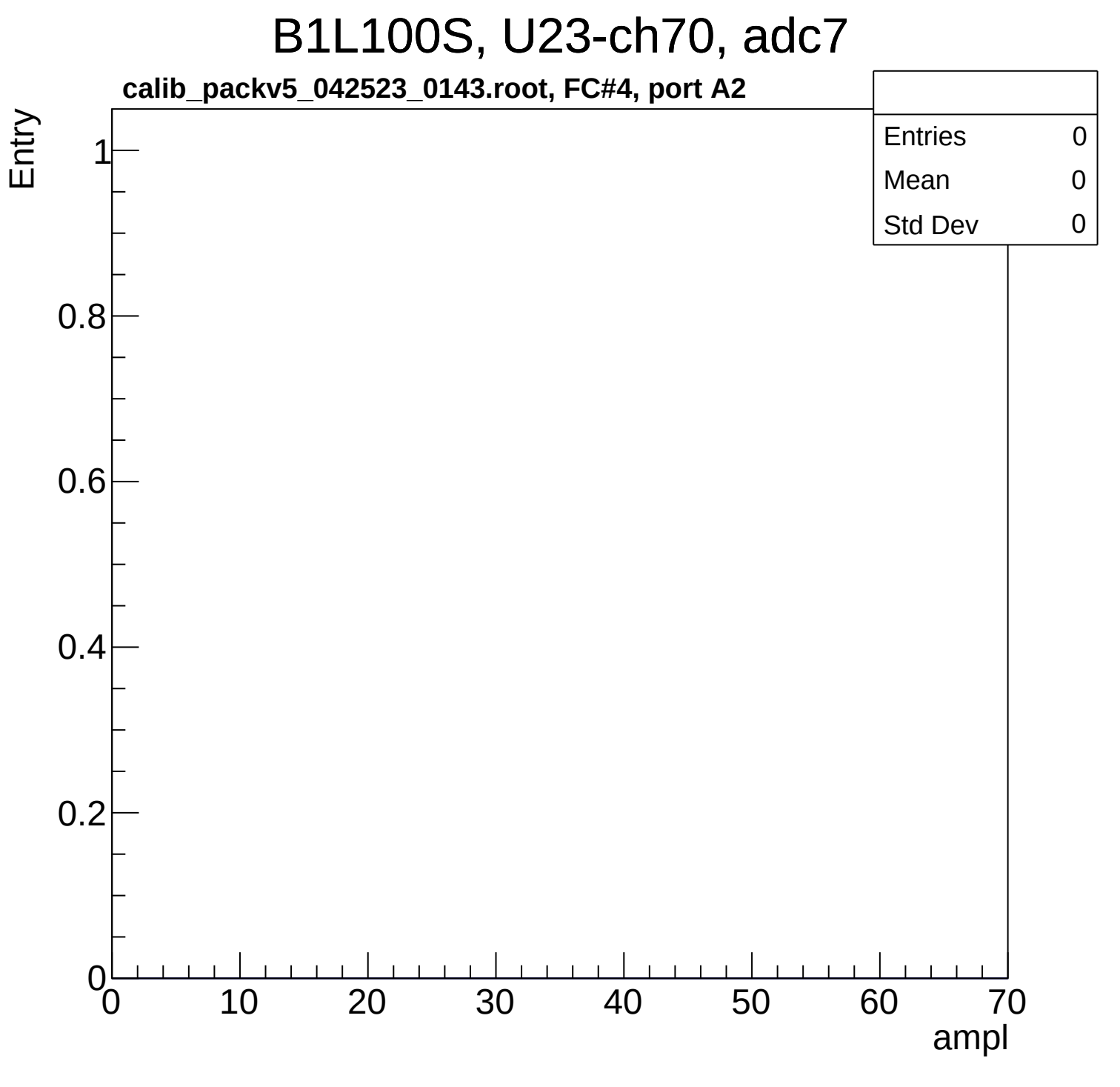
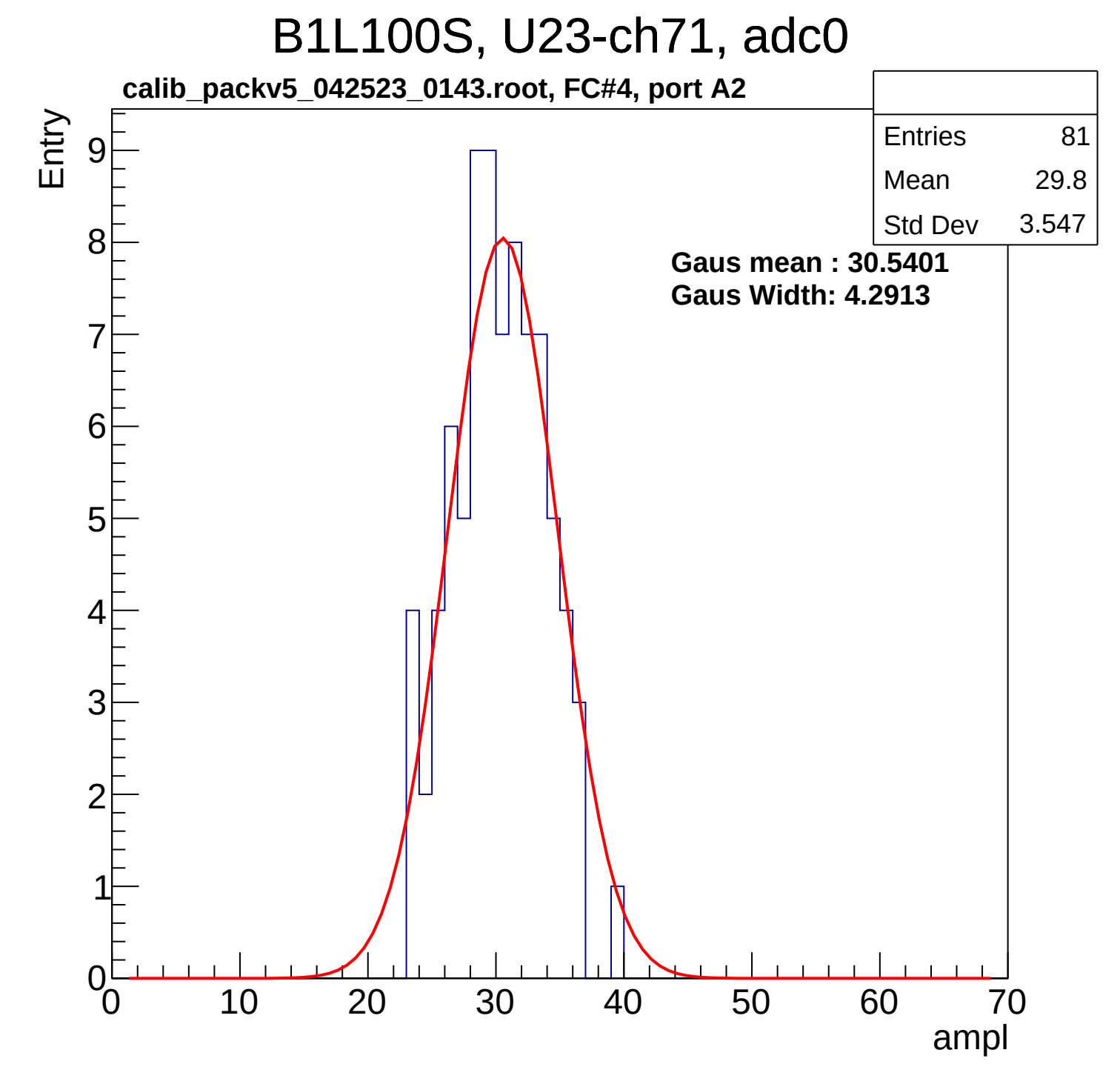

B1L100S, U23-ch70, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 49.33
Std Dev 3.624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

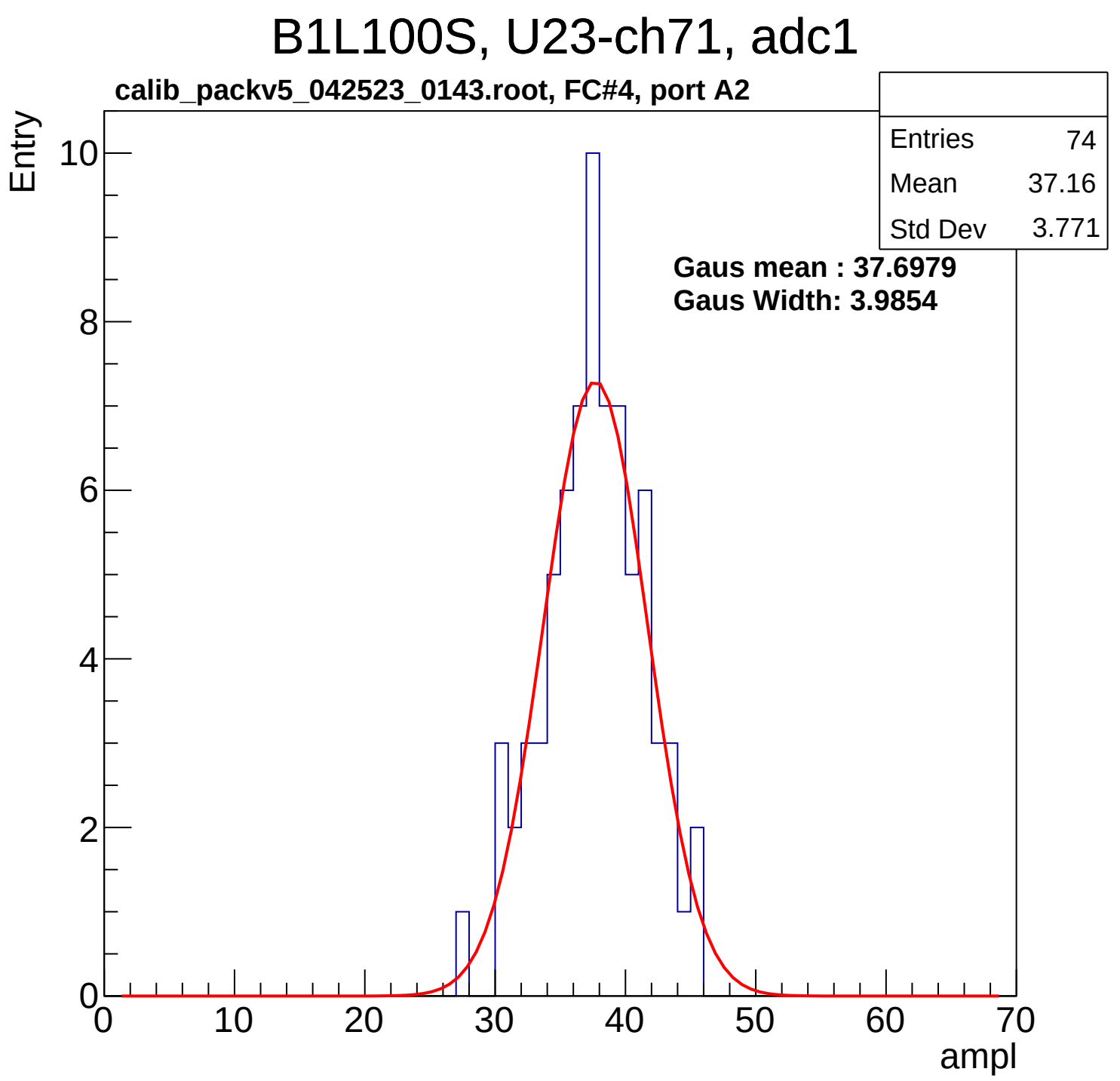

B1L100S, U23-ch70, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 55.98
Std Dev 3.116
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

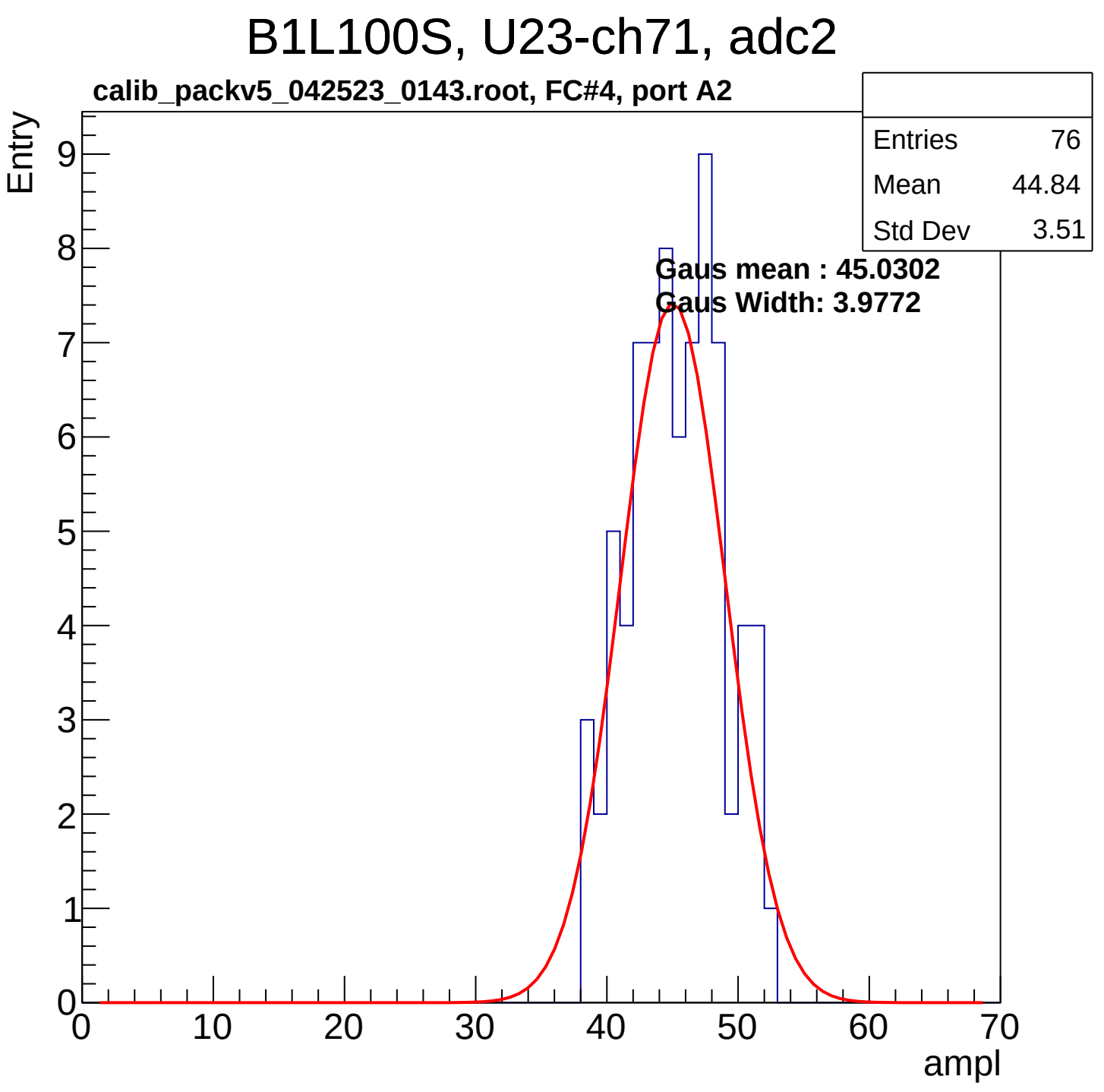

B1L100S, U23-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.51
Std Dev 8.578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

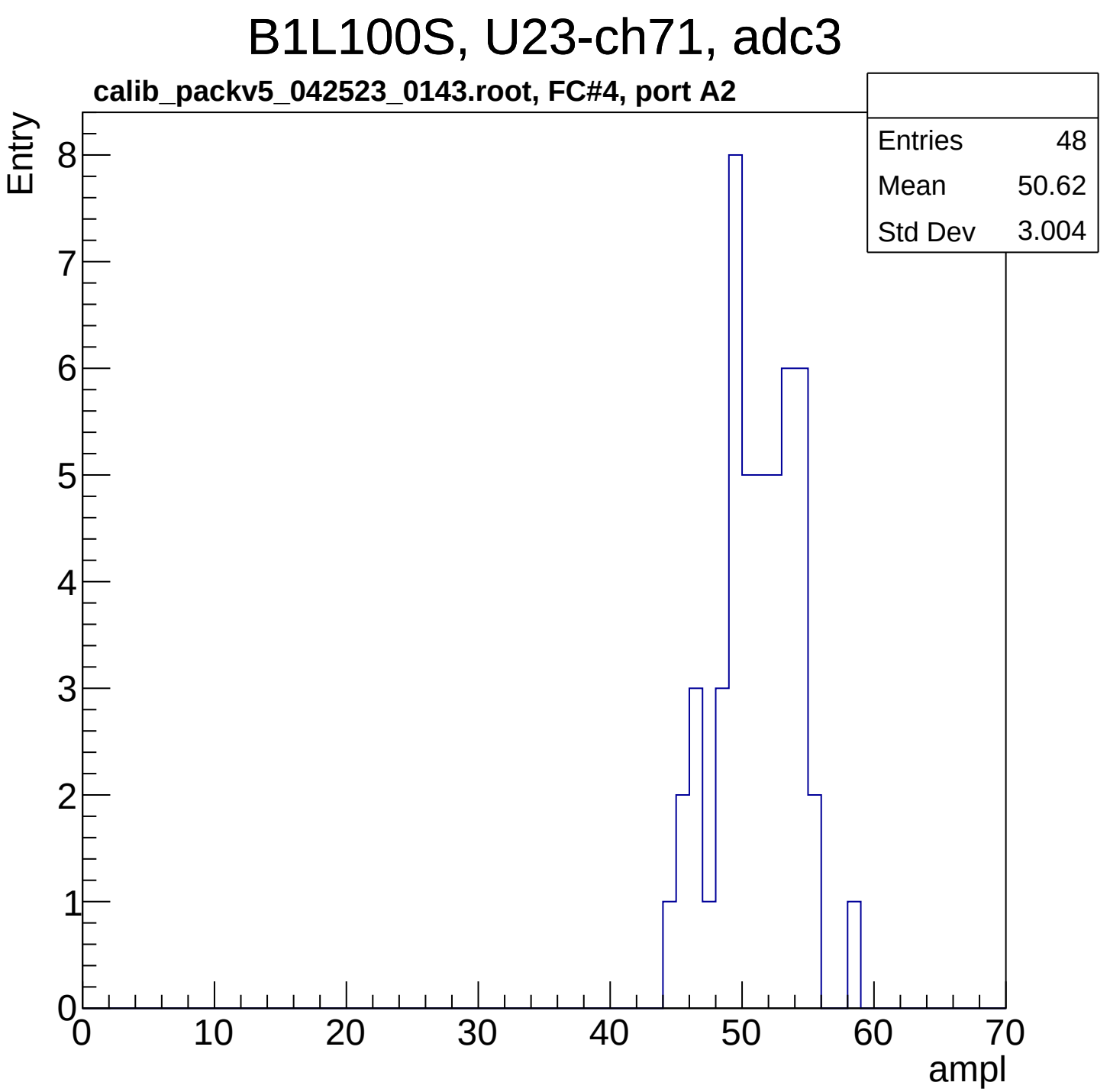

B1L100S, U23-ch70, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

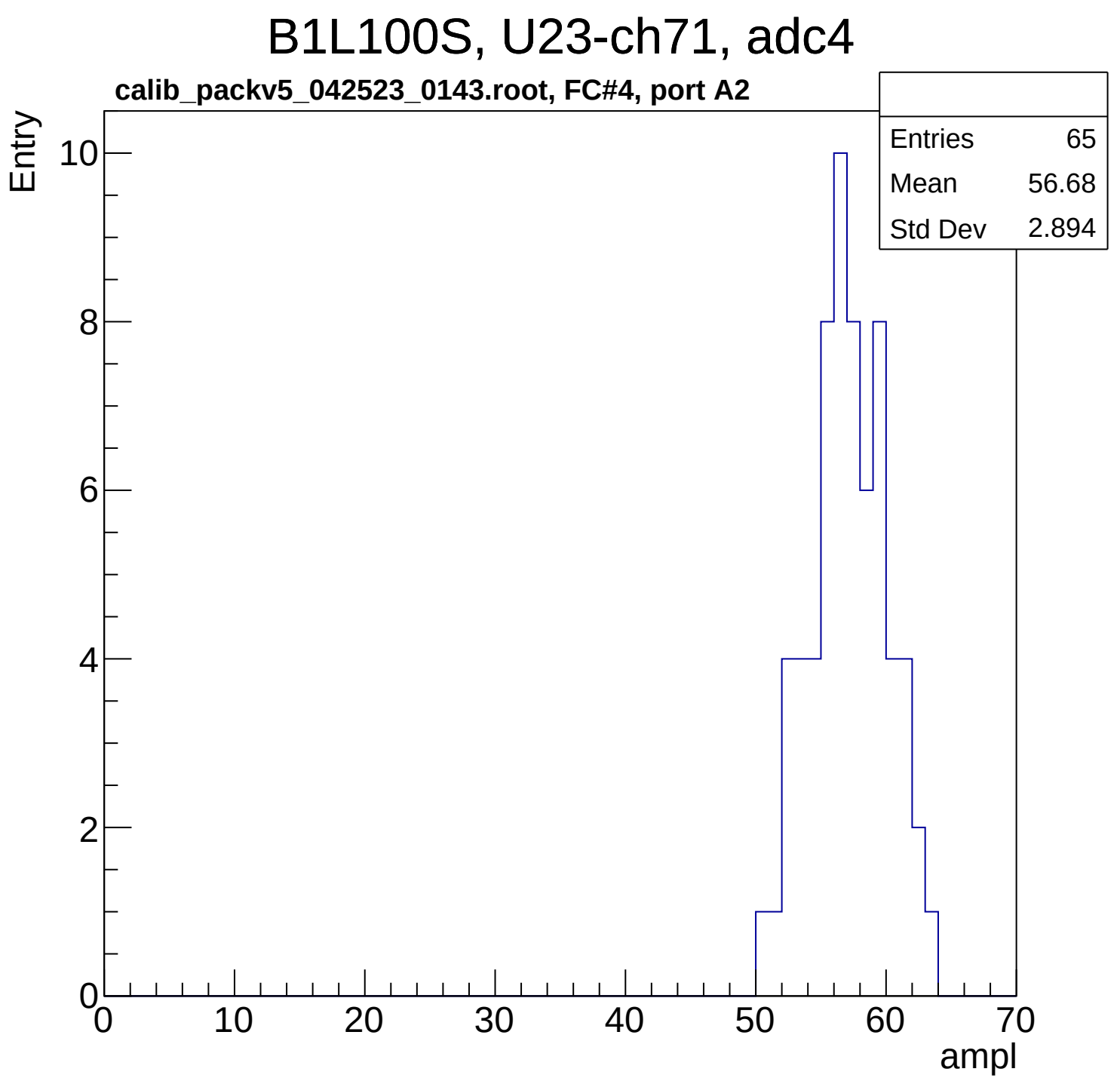

B1L100S, U23-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

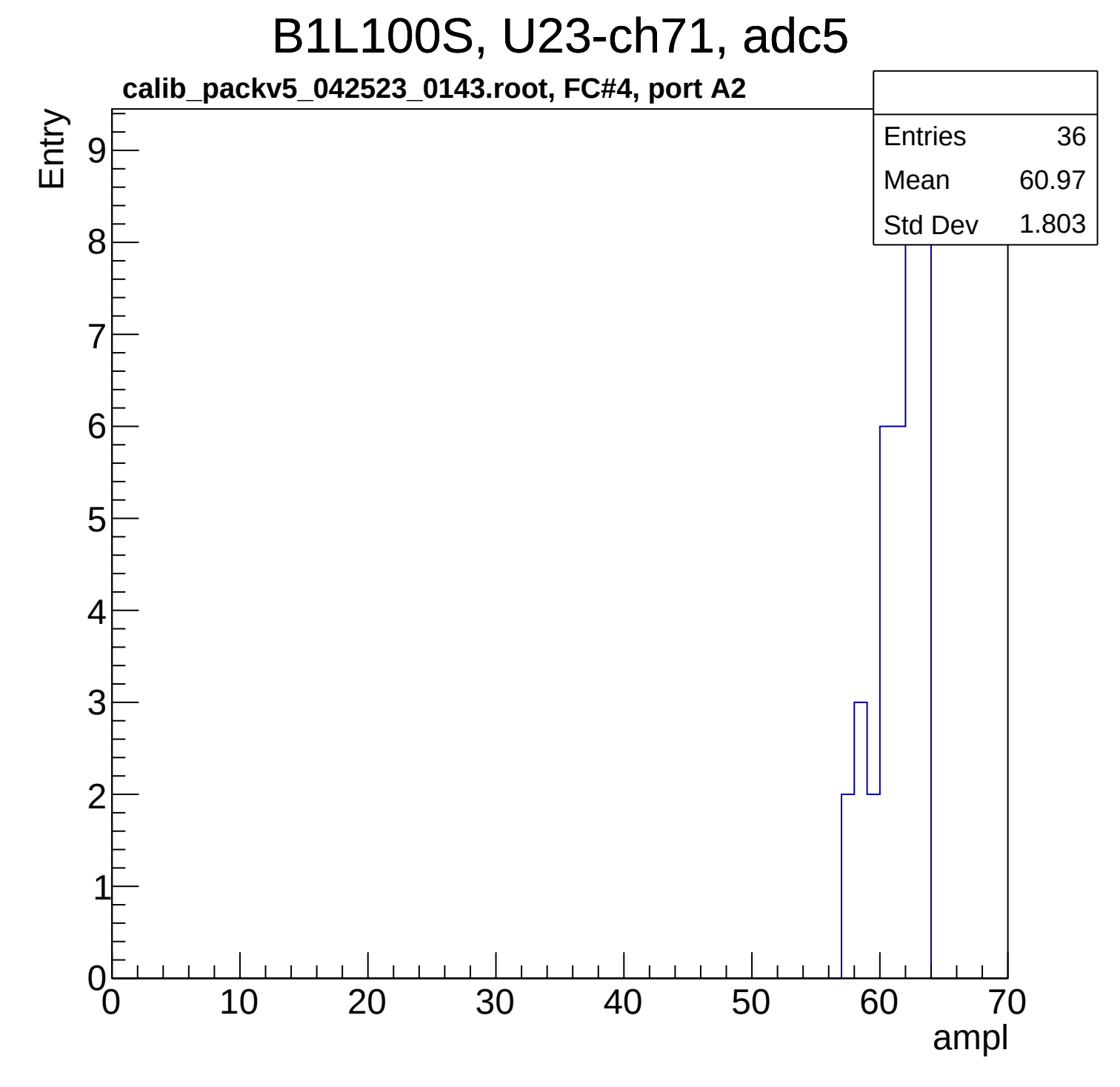

B1L100S, U23-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.8
Std Dev 3.547
Gaus mean : 30.5401
Gaus Width: 4.2913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

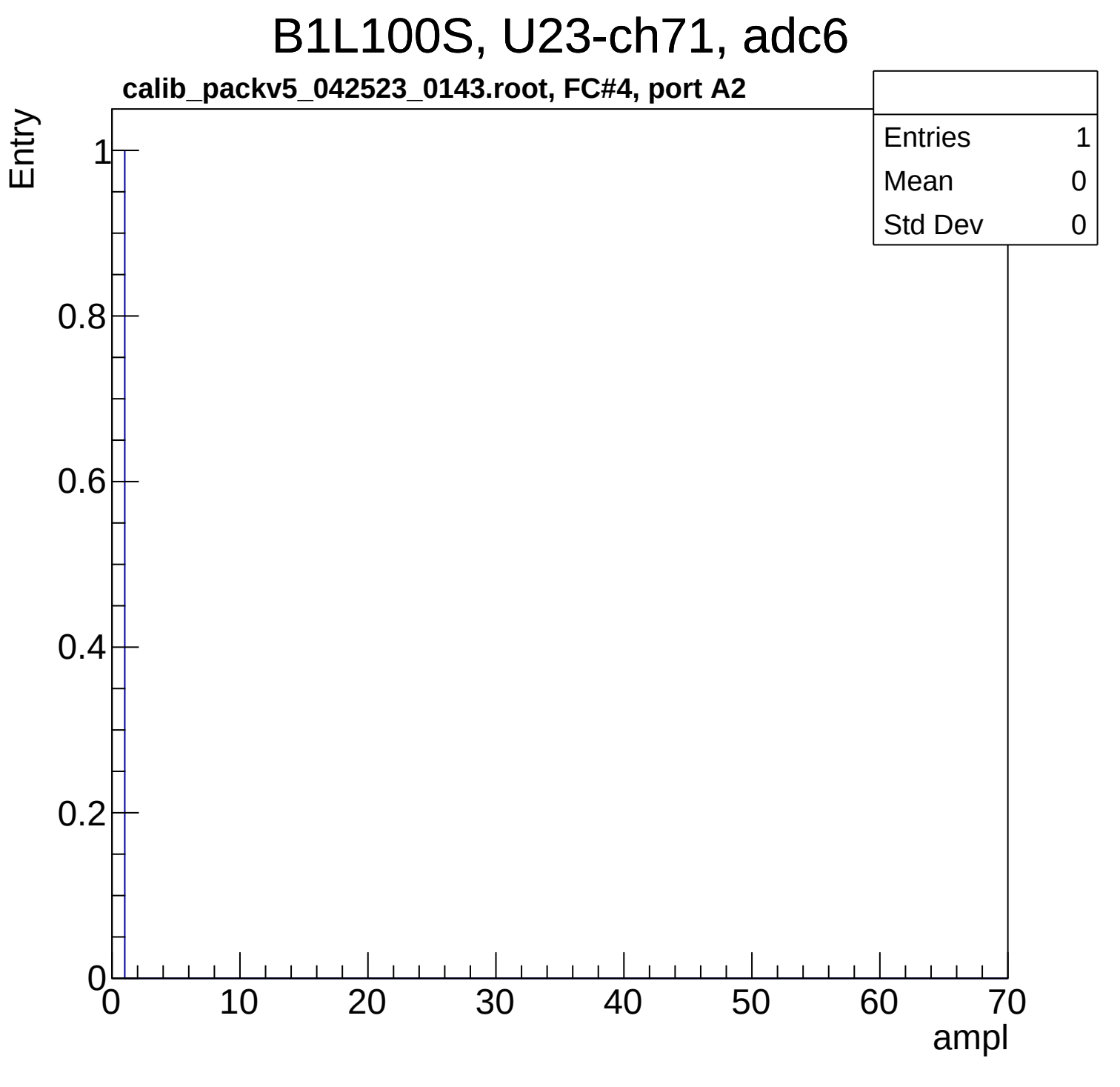

B1L100S, U23-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.16
Std Dev 3.771
Gaus mean : 37.6979
Gaus Width: 3.9854
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

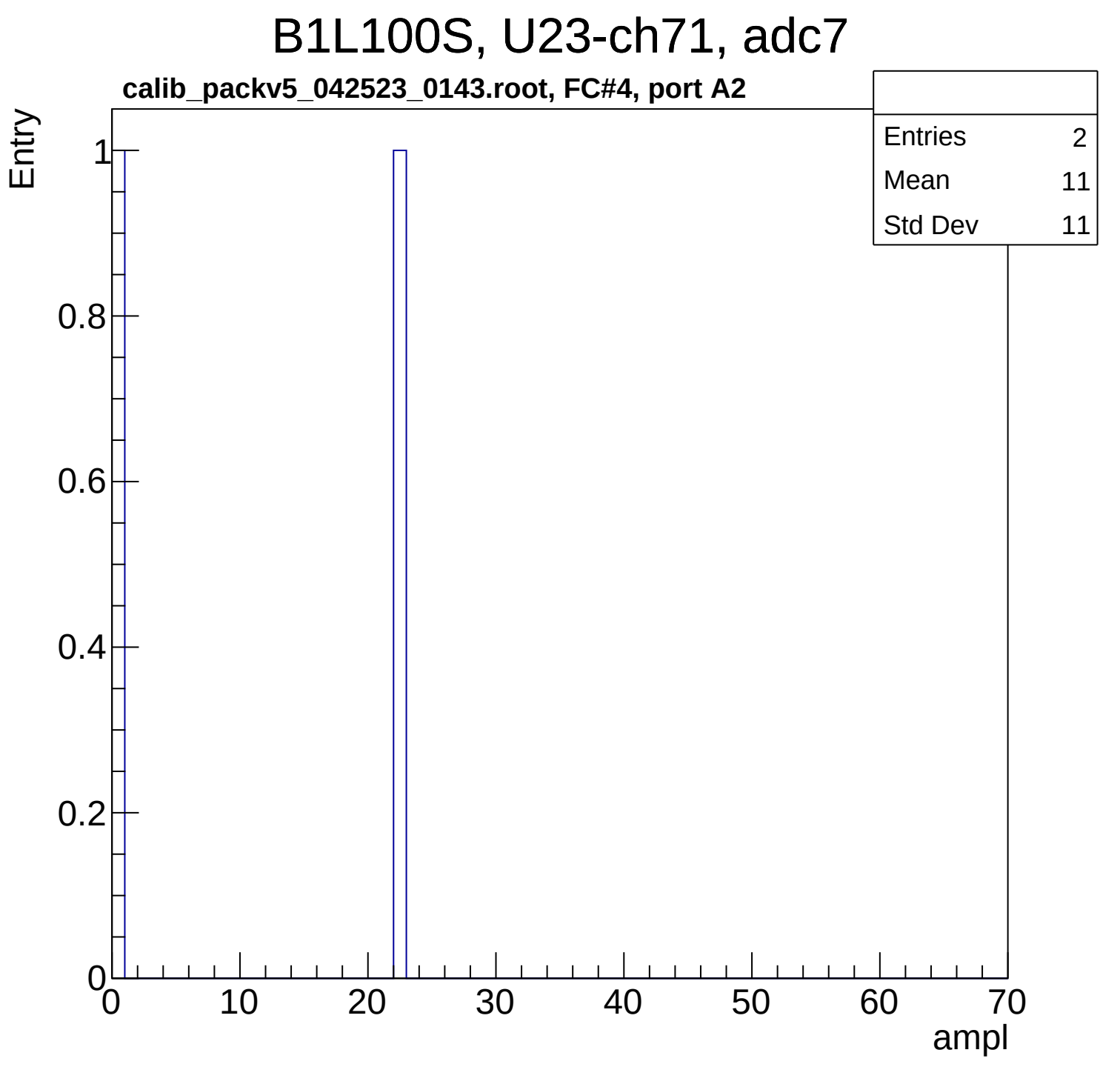

B1L100S, U23-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.84
Std Dev 3.51
Gaus mean : 45.0302
Gaus Width: 3.9772
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 50.62
Std Dev 3.004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch71, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.68
Std Dev 2.894
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.97
Std Dev 1.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch71, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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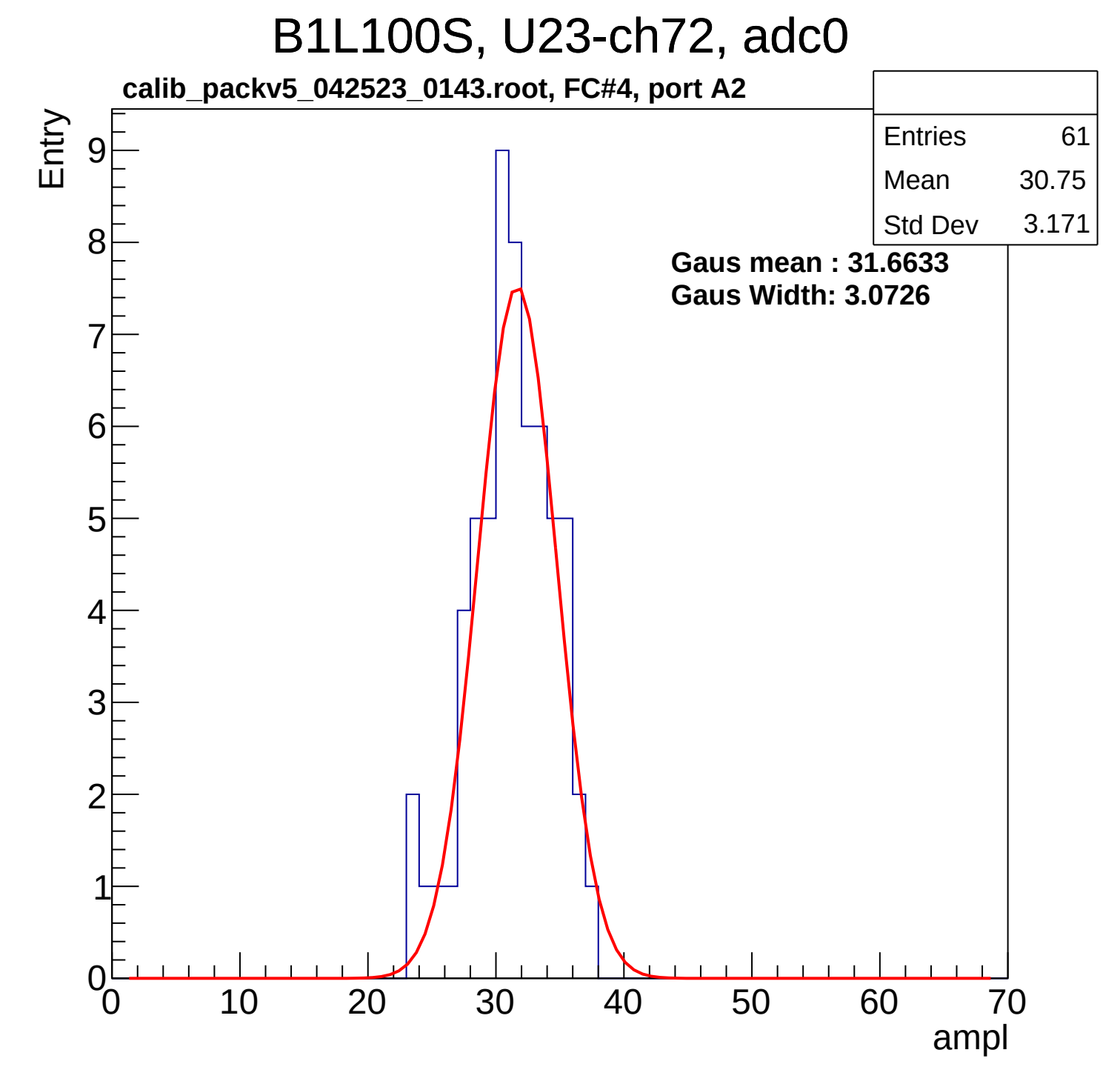
50
60
70
ampl


B1L100S, U23-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.75
Std Dev 3.171
Gaus mean : 31.6633
Gaus Width: 3.0726
Entry
ampl
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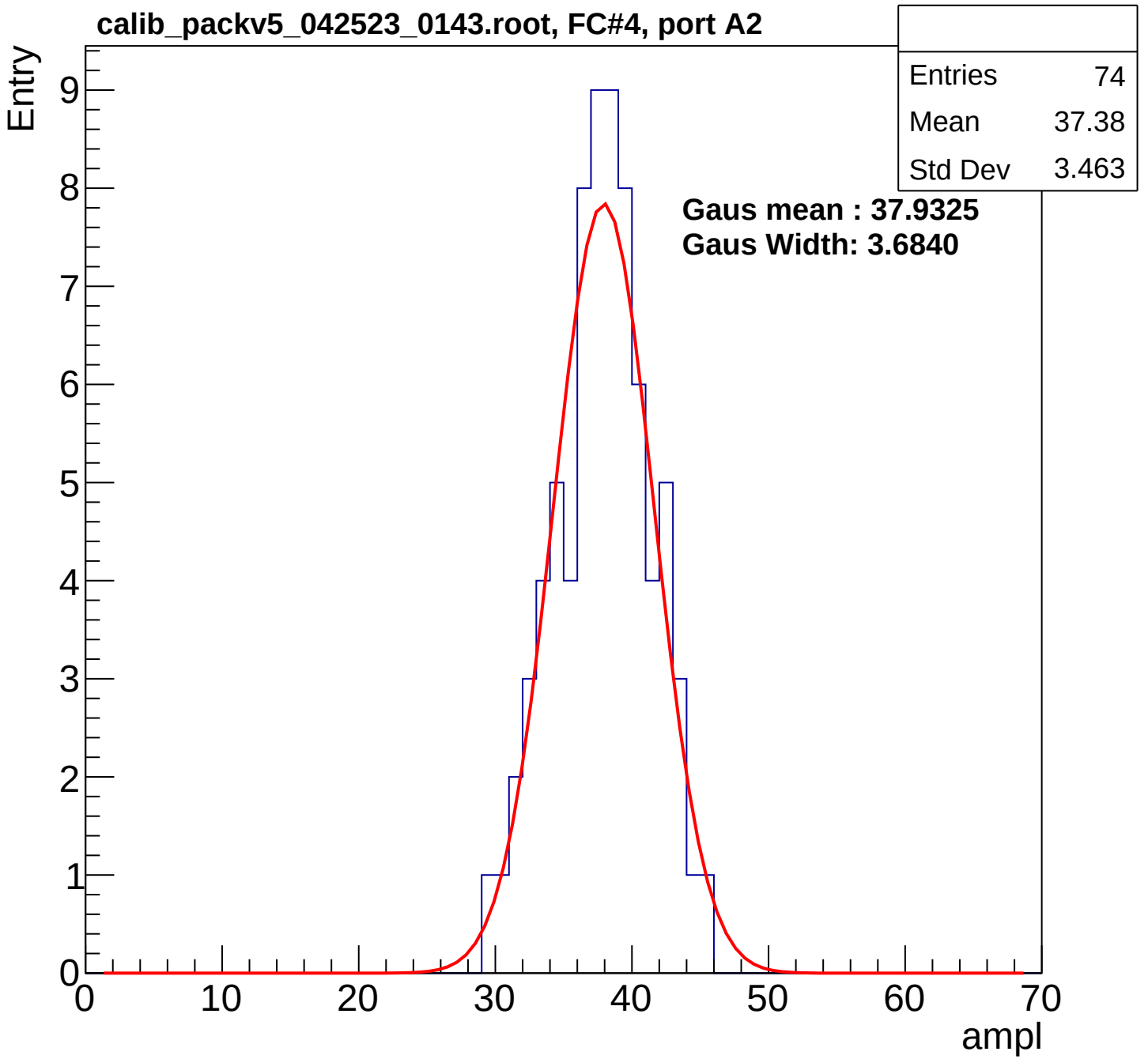
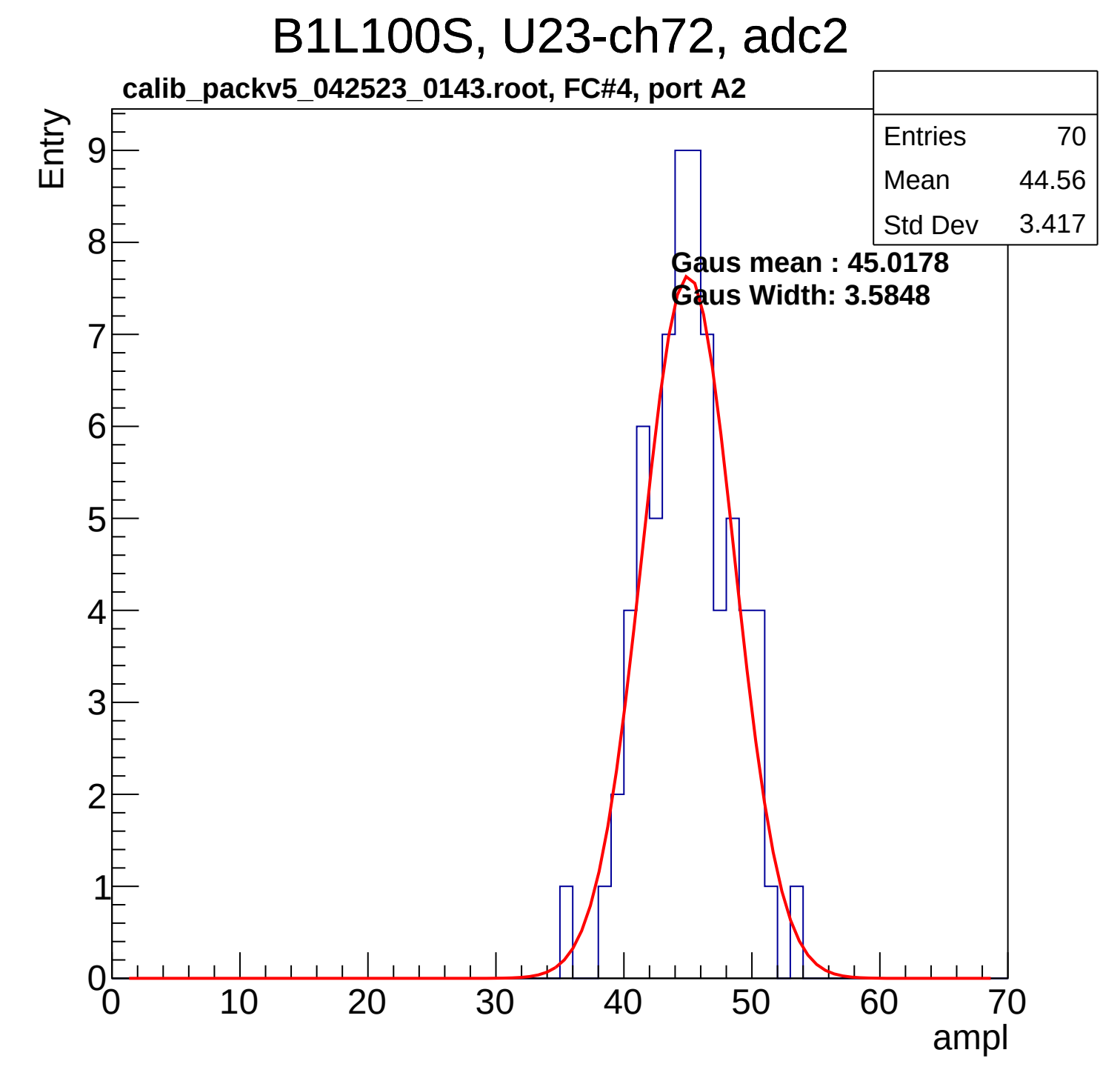
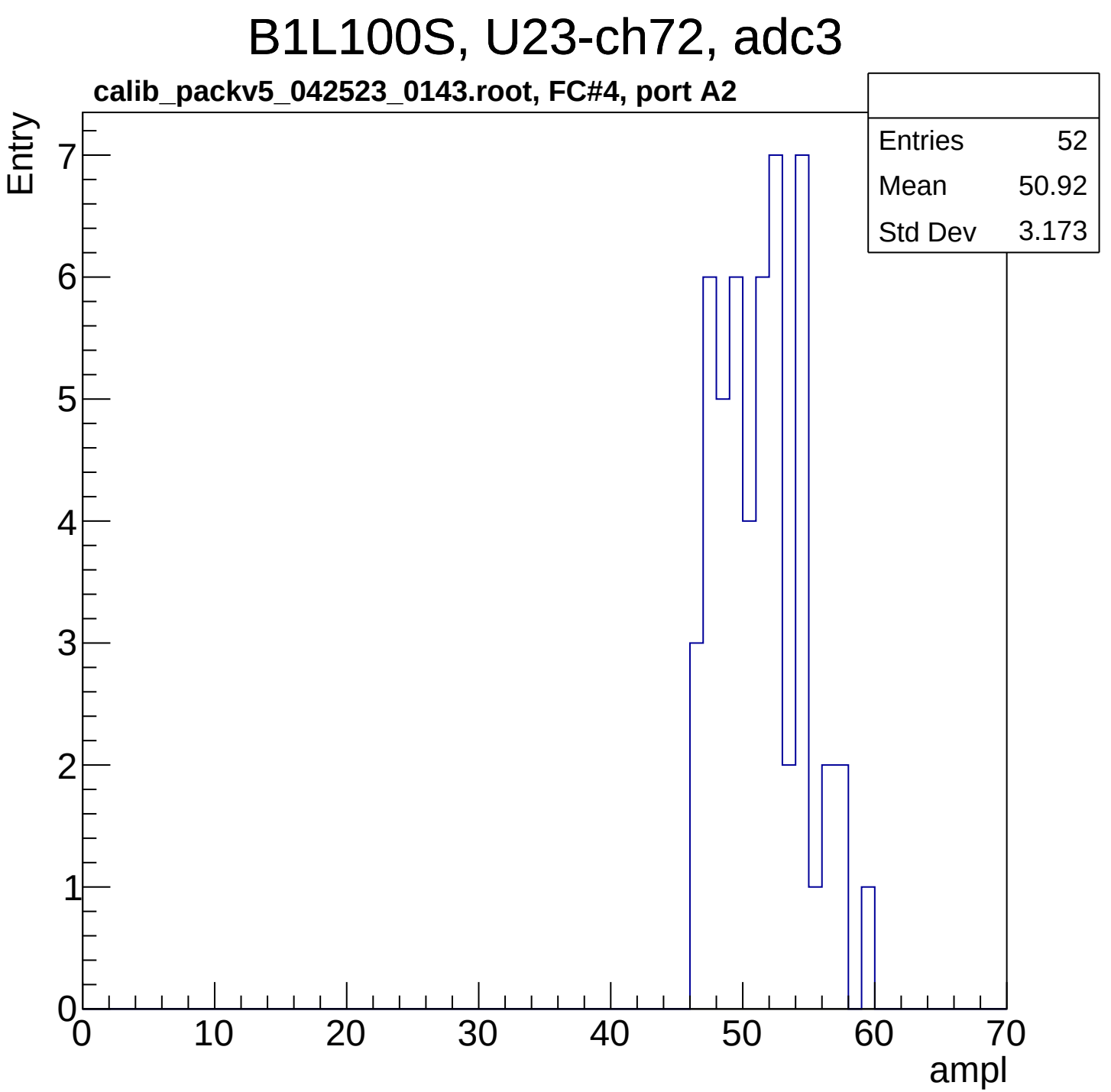
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.38
Std Dev 3.463
Gaus mean : 37.9325
Gaus Width: 3.6840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.56
Std Dev 3.417
Gaus mean : 45.0178
Gaus Width: 3.5848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch72, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 50.92
Std Dev 3.173
Entry
ampl
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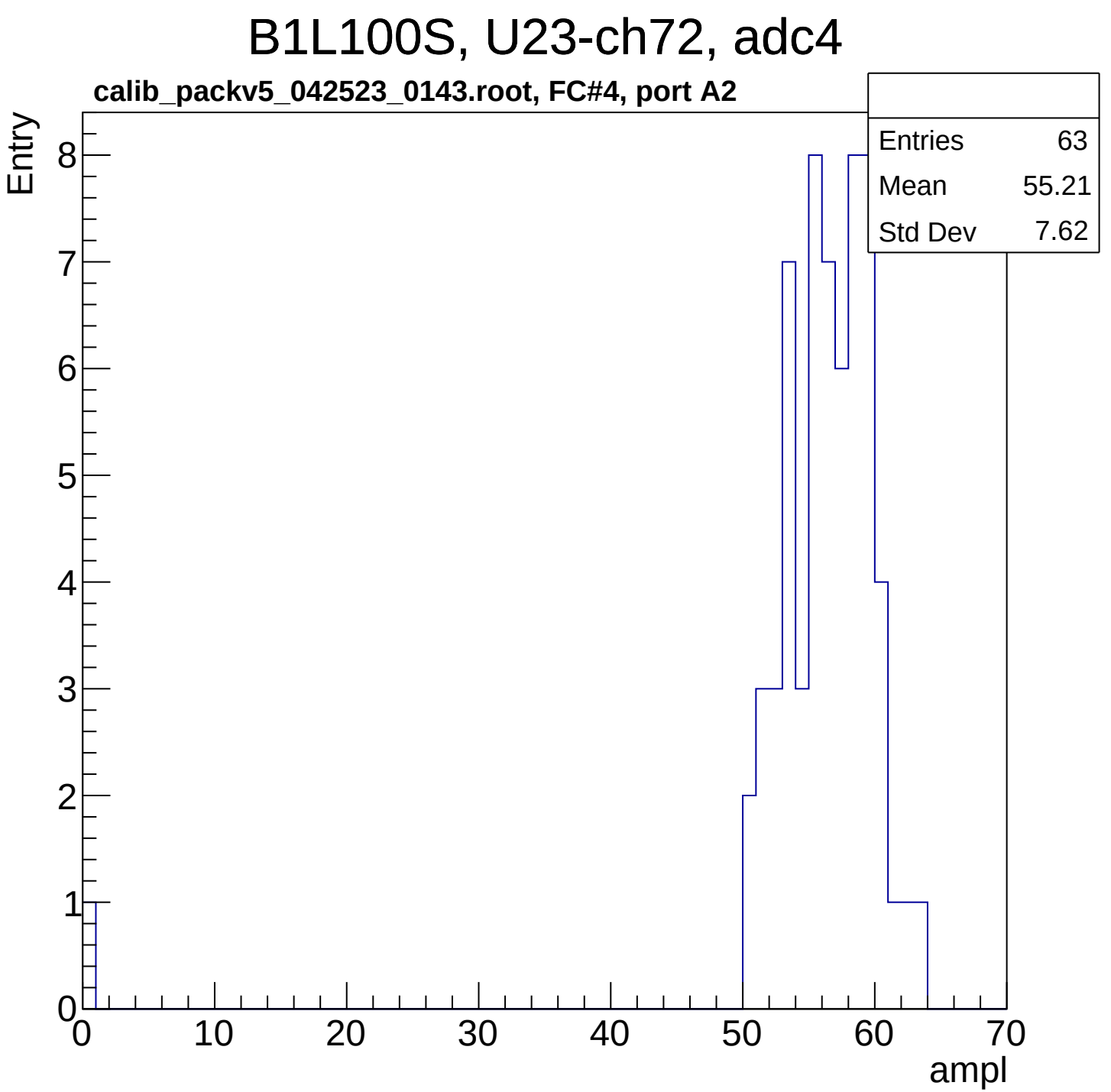
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.21
Std Dev 7.62
Entry
ampl
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1
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3
4
5
6
7
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0
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20
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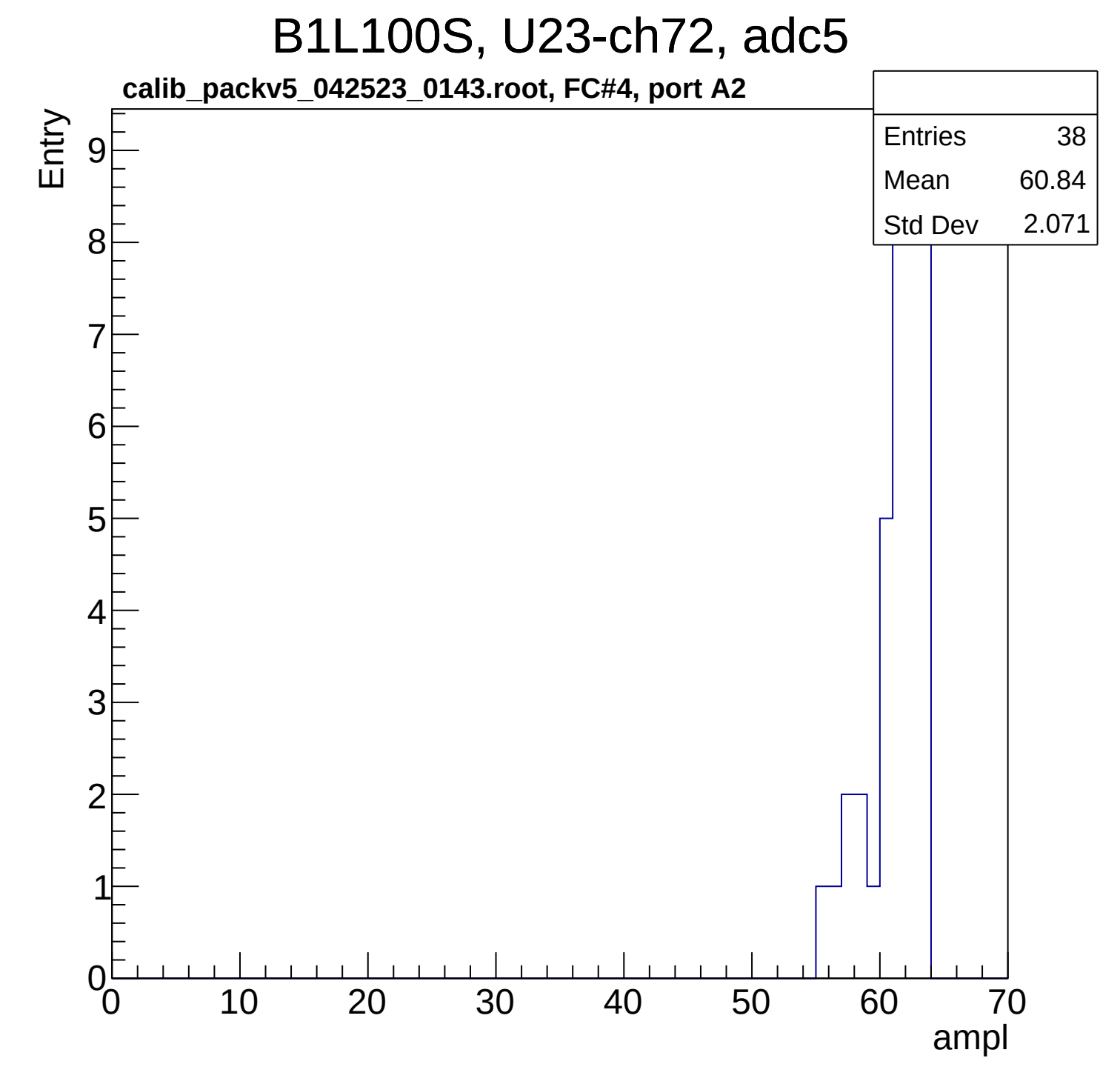
40
50
60
70


B1L100S, U23-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.84
Std Dev 2.071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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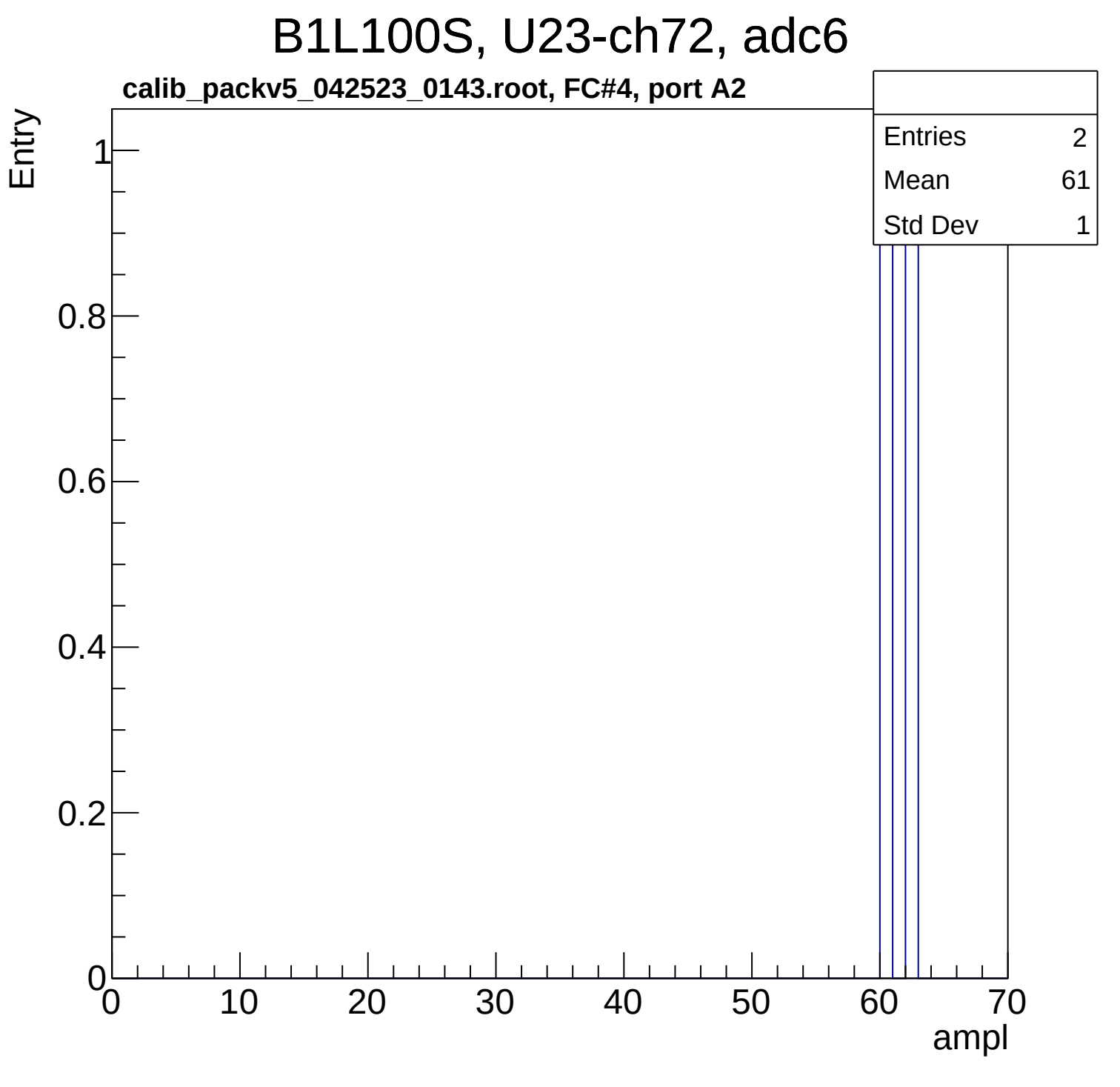
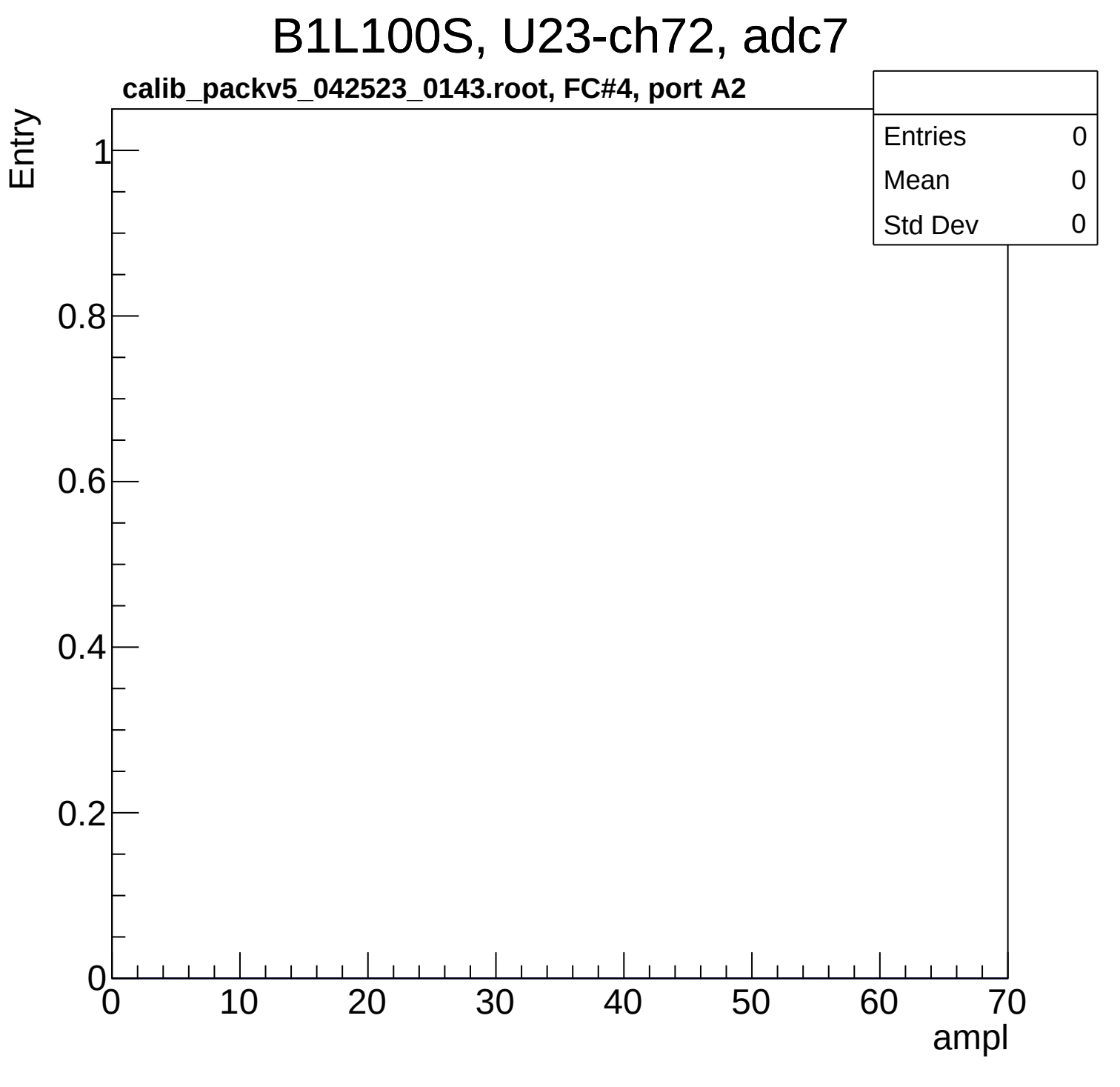
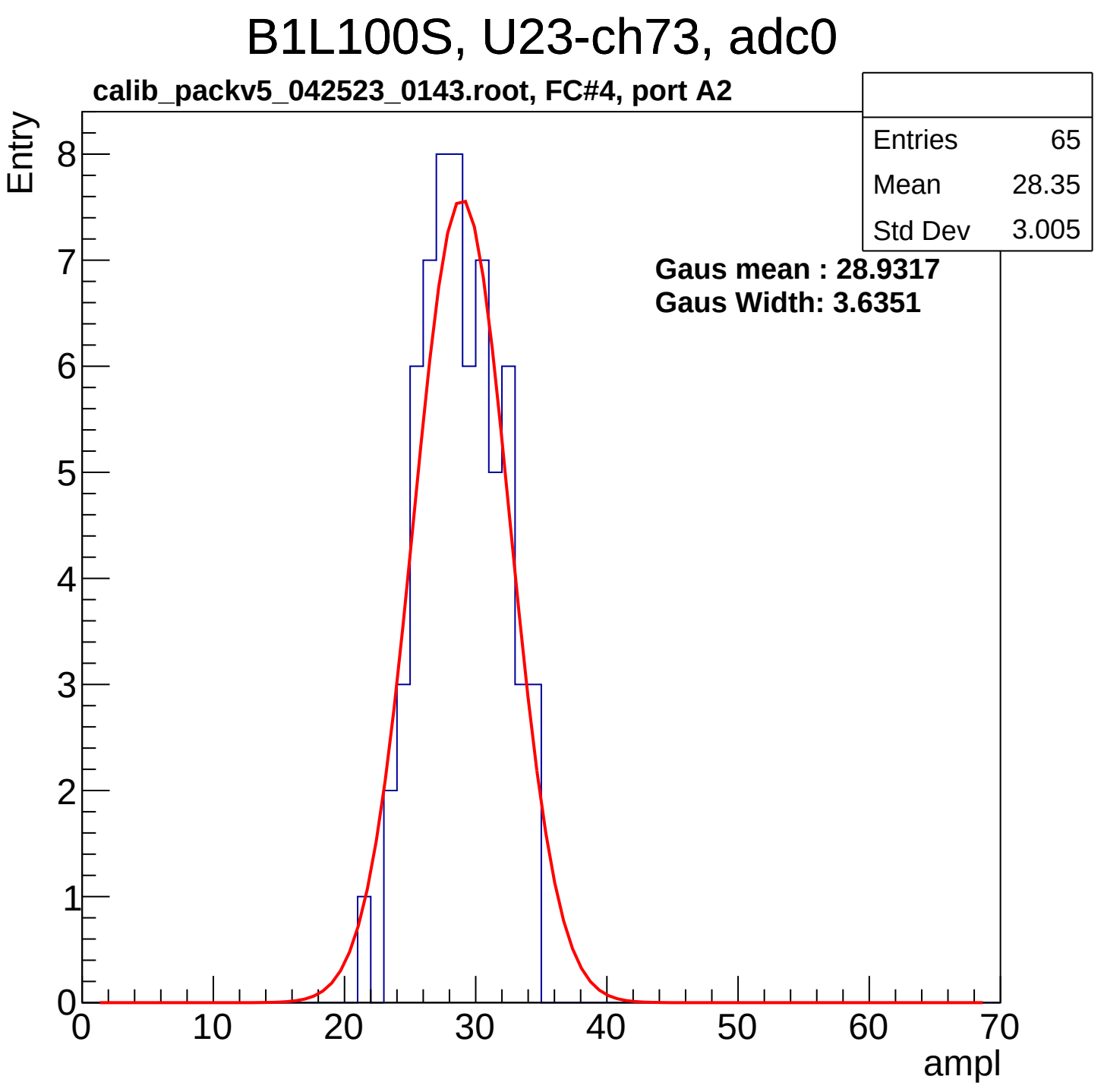
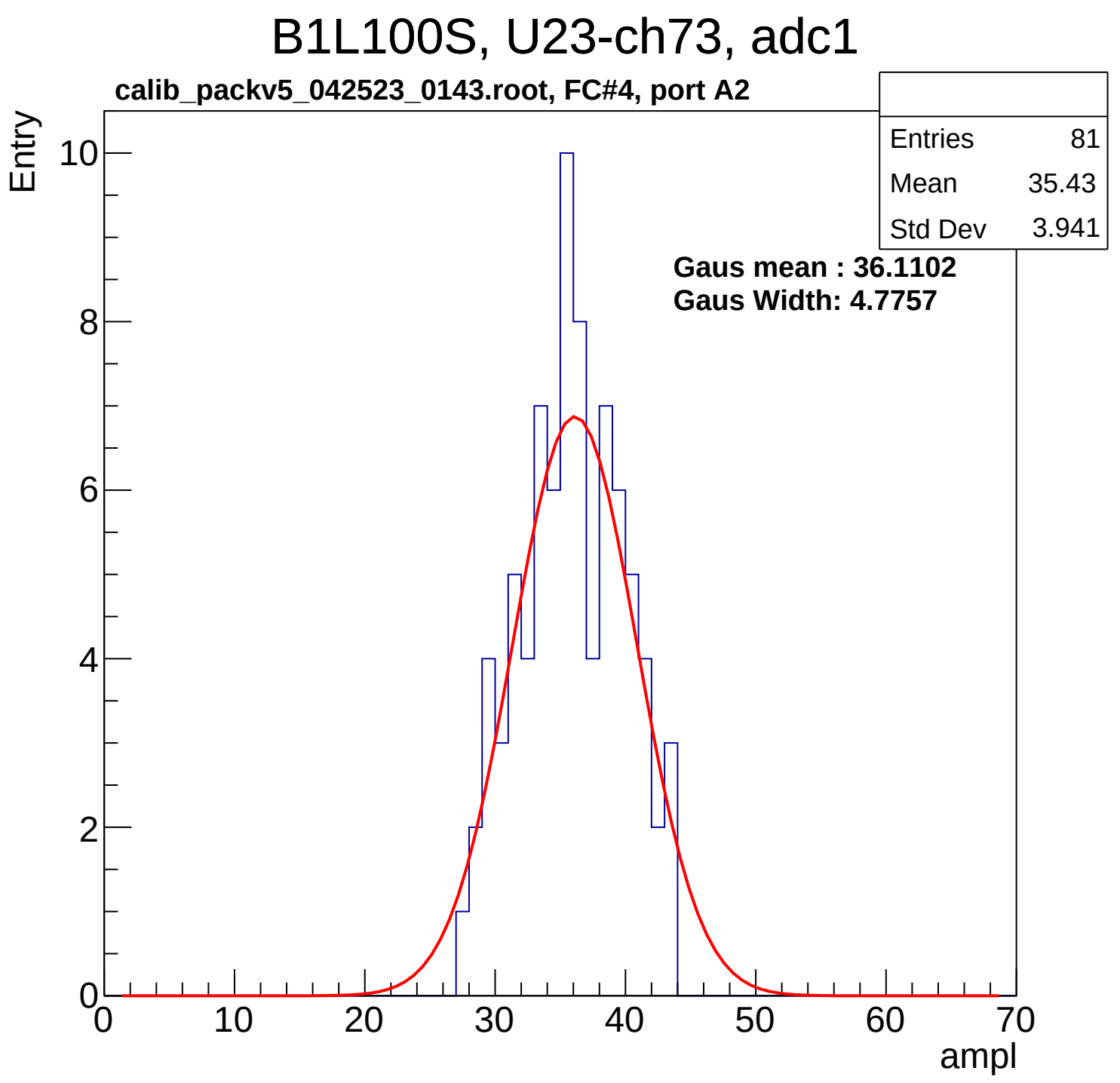

B1L100S, U23-ch72, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch72, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.35
Std Dev 3.005
Gaus mean : 28.9317
Gaus Width: 3.6351
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.43
Std Dev 3.941
Gaus mean : 36.1102
Gaus Width: 4.7757
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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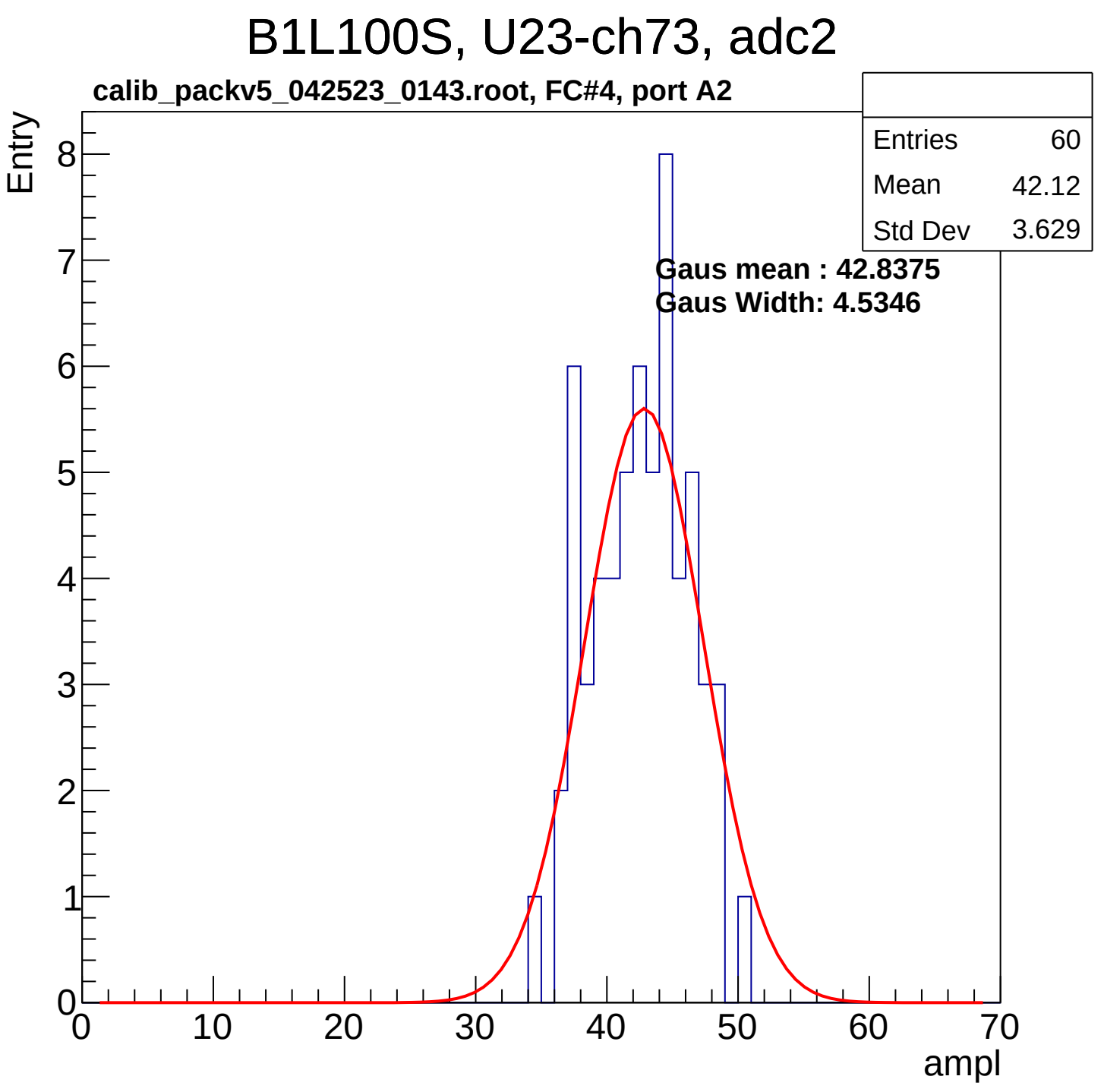
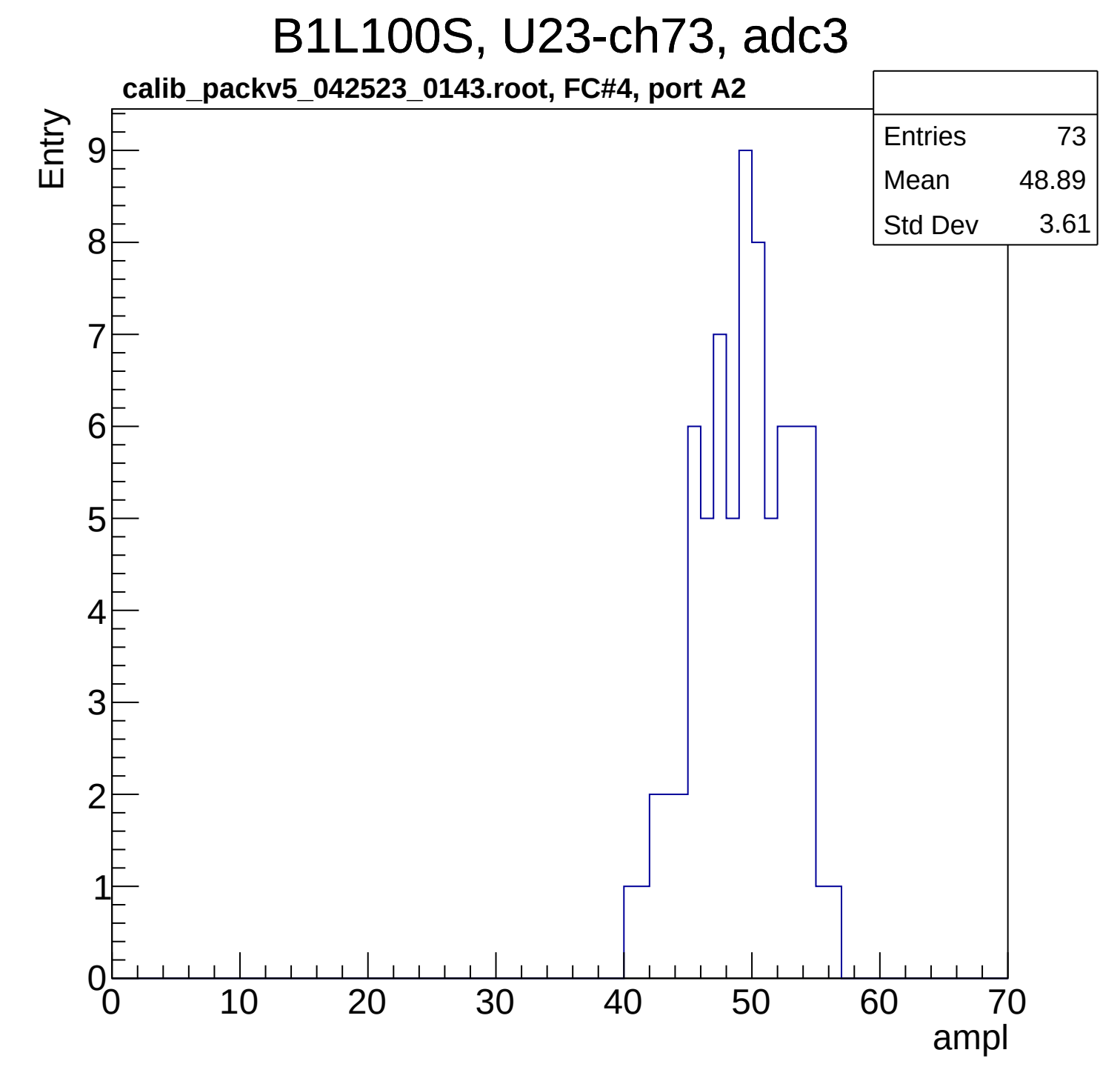

B1L100S, U23-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.12
Std Dev 3.629
Gaus mean : 42.8375
Gaus Width: 4.5346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 48.89
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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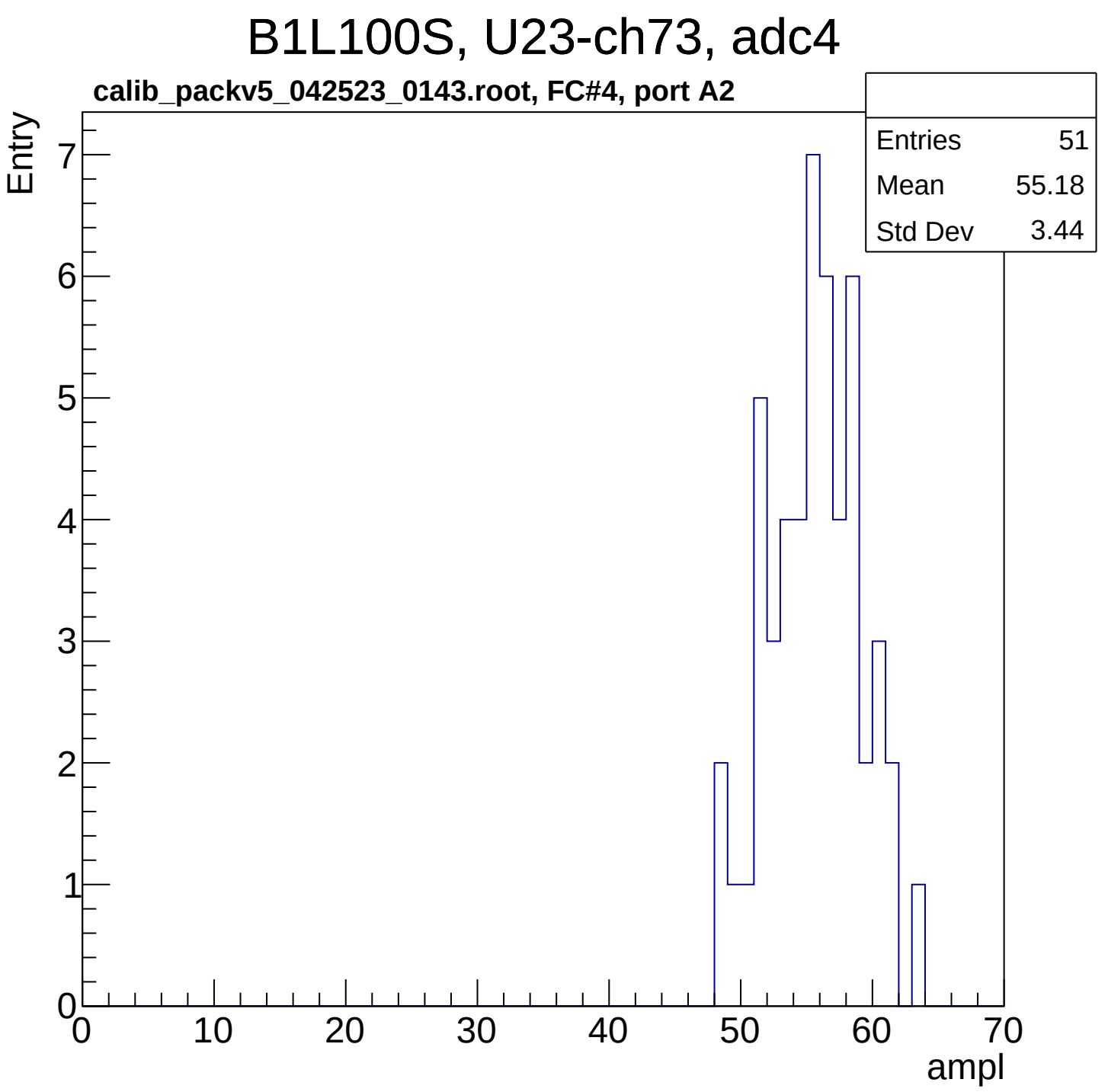
40
50
60
70


B1L100S, U23-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.18
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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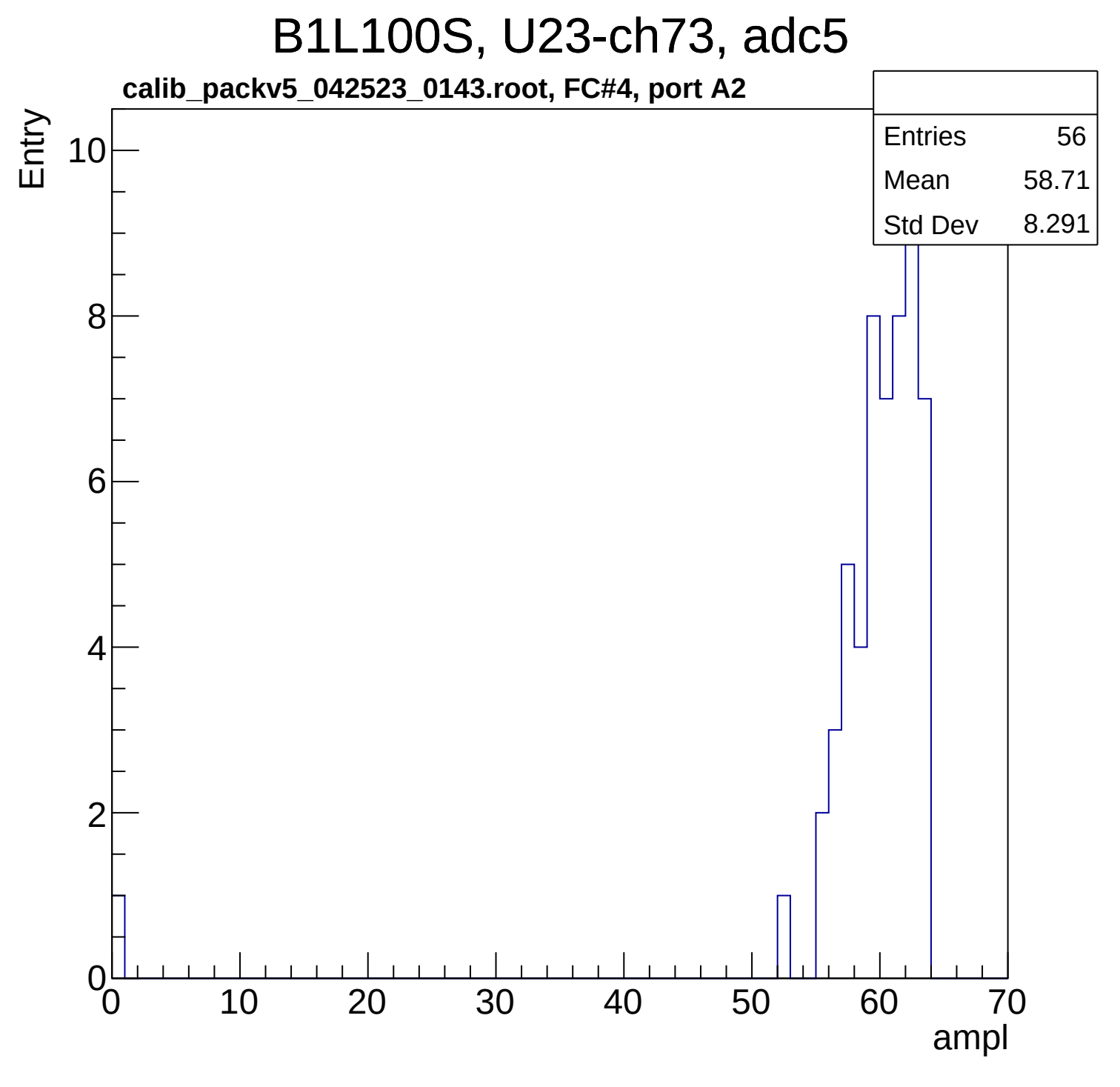
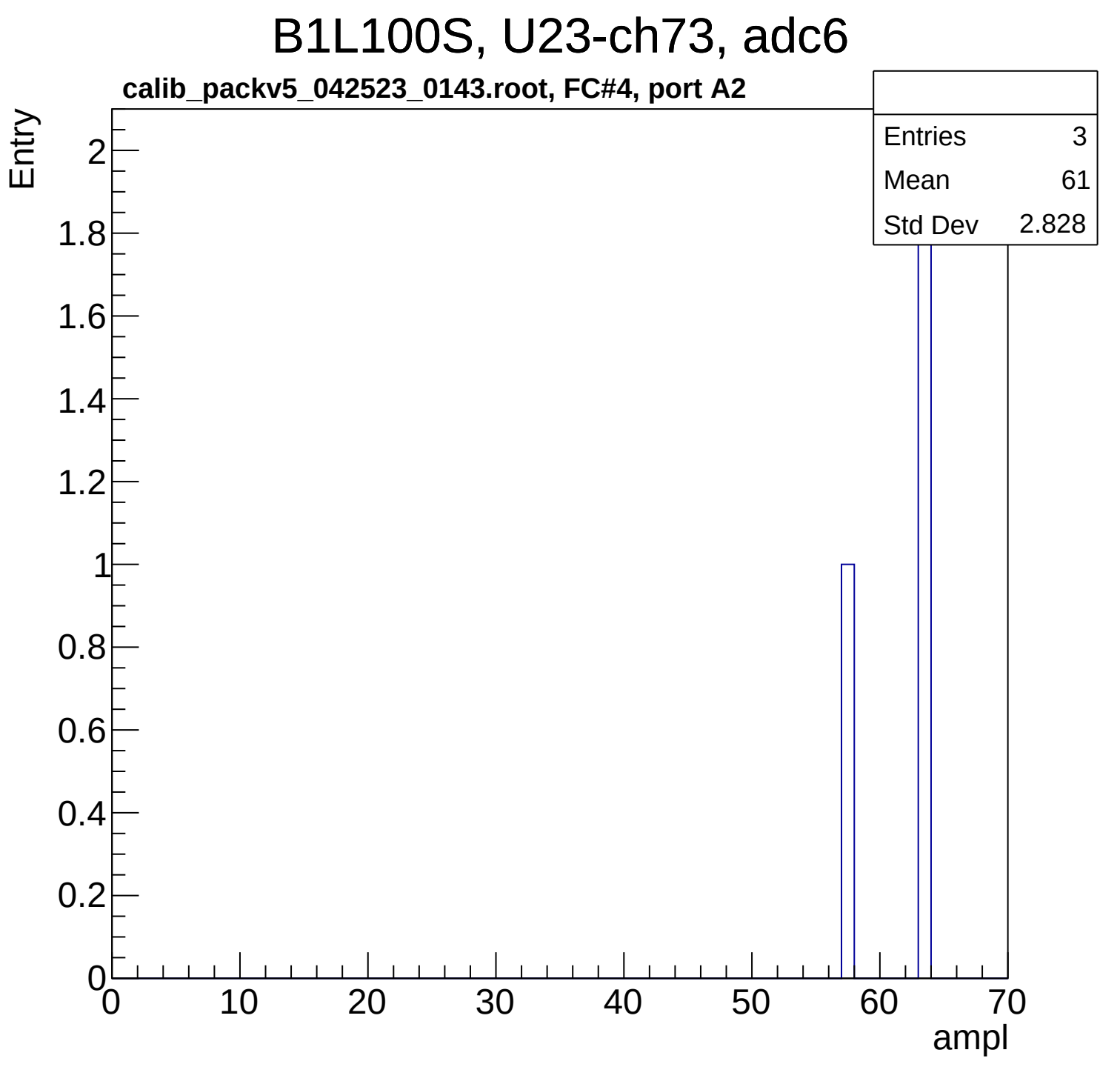

B1L100S, U23-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.71
Std Dev 8.291
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61
Std Dev 2.828
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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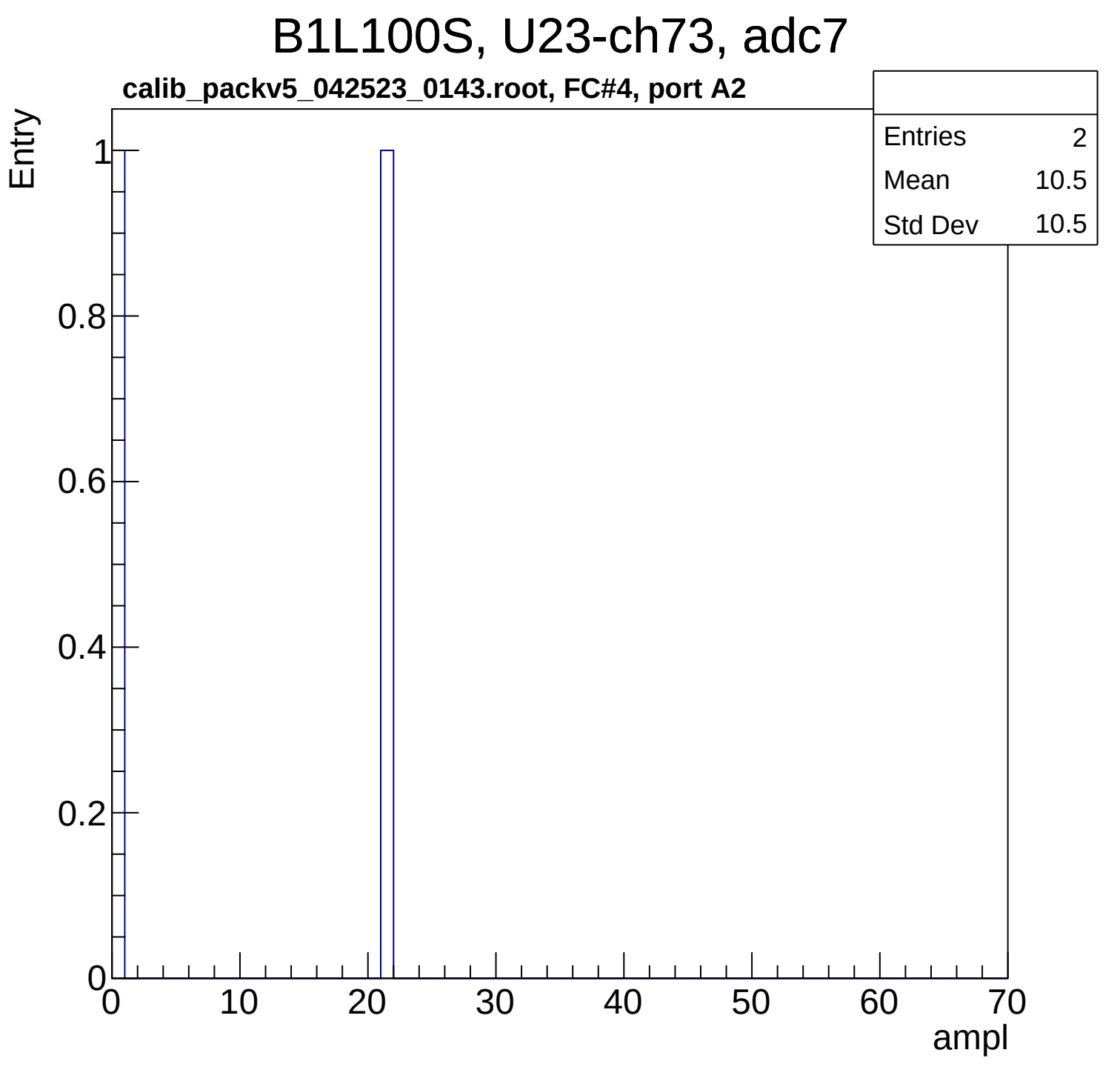
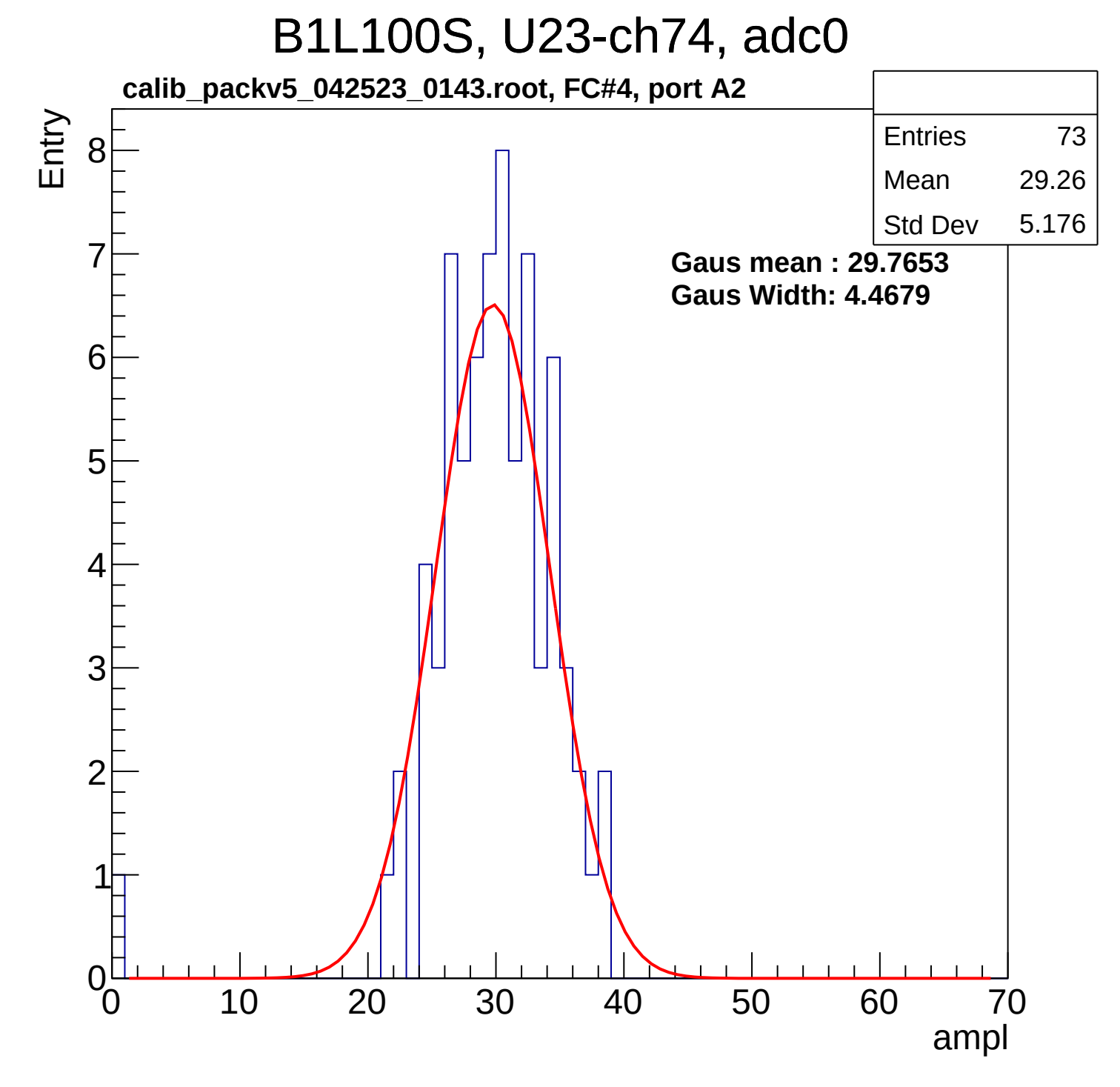

B1L100S, U23-ch73, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 29.26
Std Dev 5.176
Gaus mean : 29.7653
Gaus Width: 4.4679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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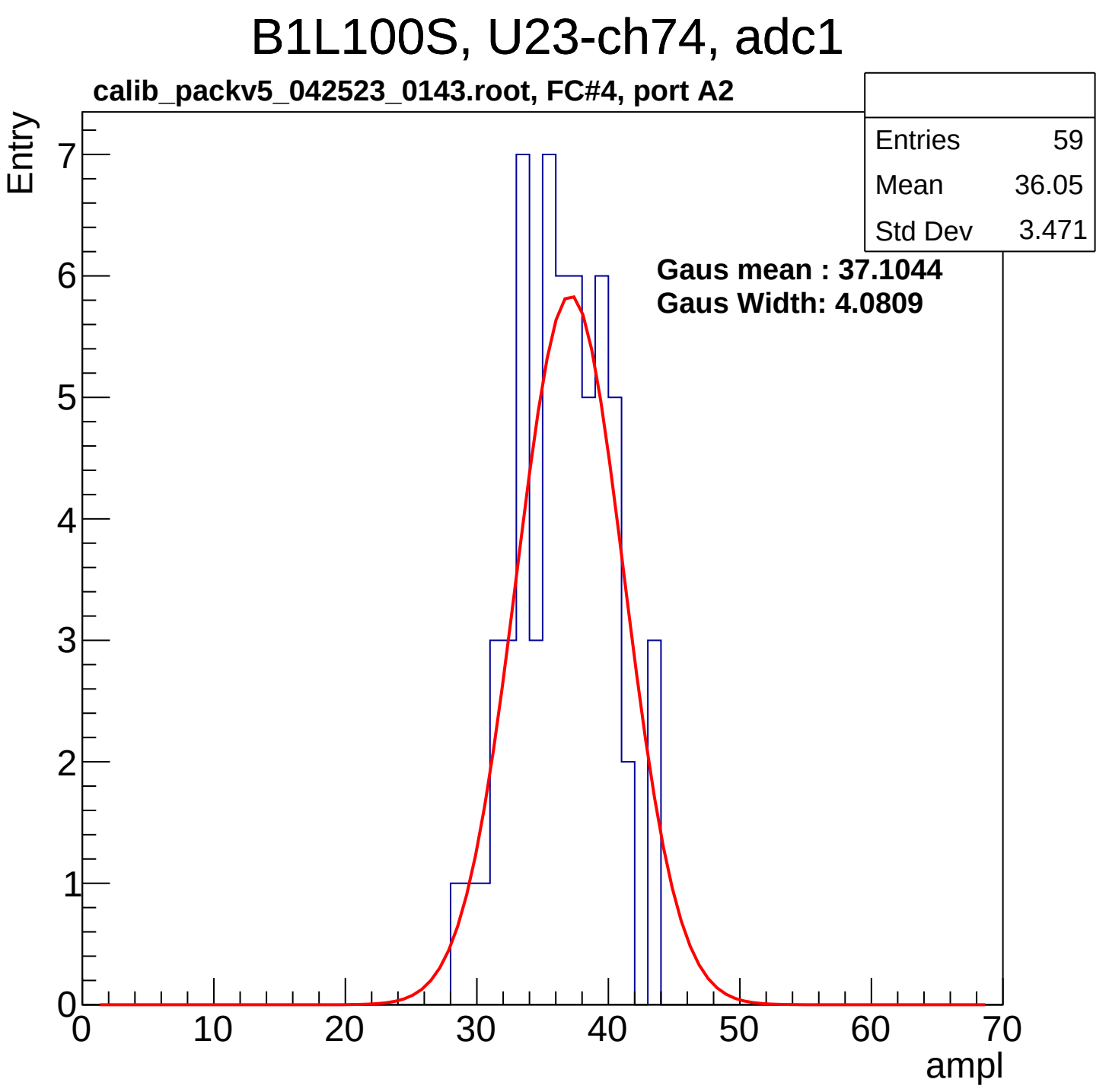
B1L100S, U23-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.05
Std Dev 3.471
Gaus mean : 37.1044
Gaus Width: 4.0809
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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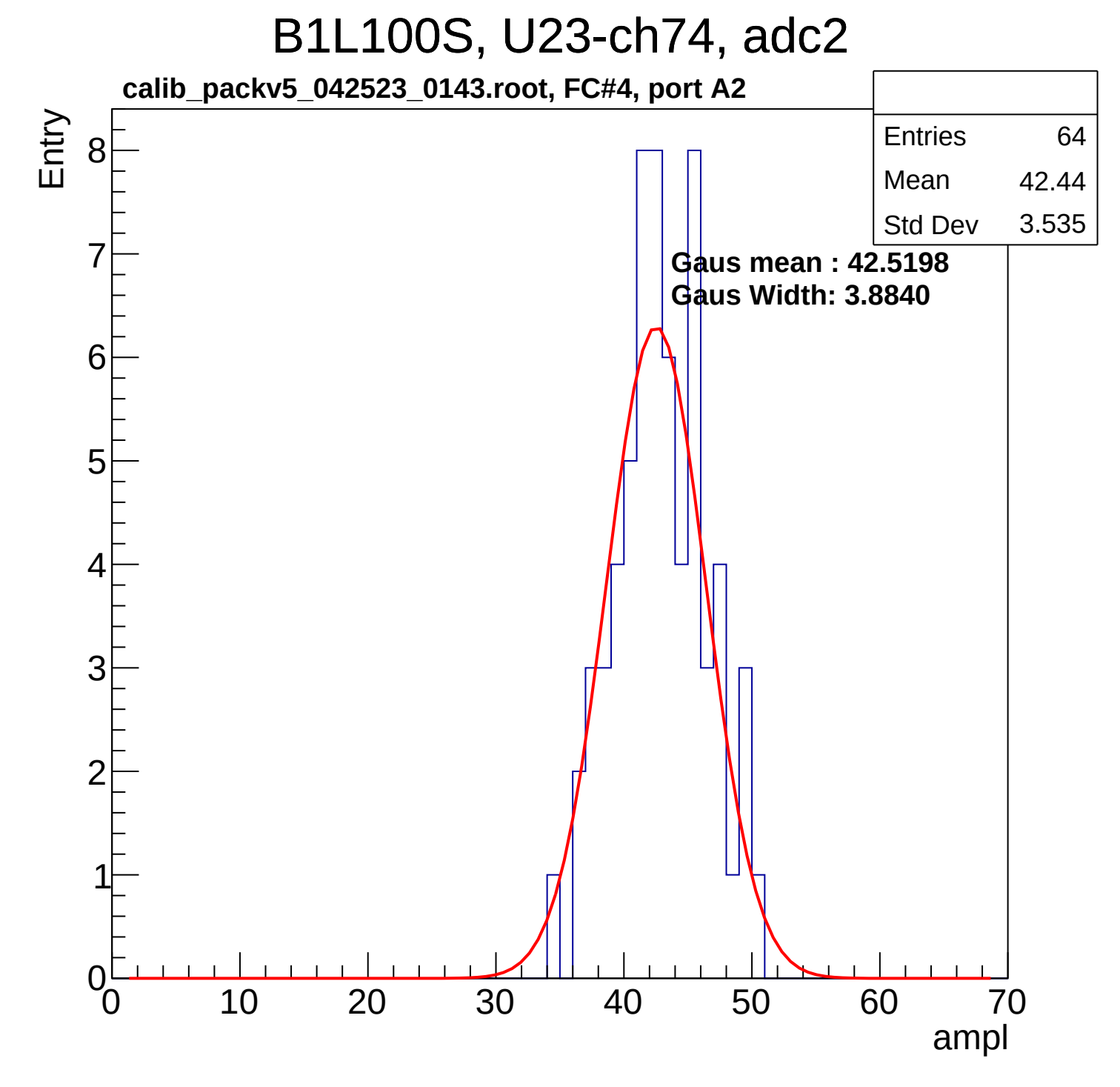
B1L100S, U23-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.44
Std Dev 3.535
Gaus mean : 42.5198
Gaus Width: 3.8840
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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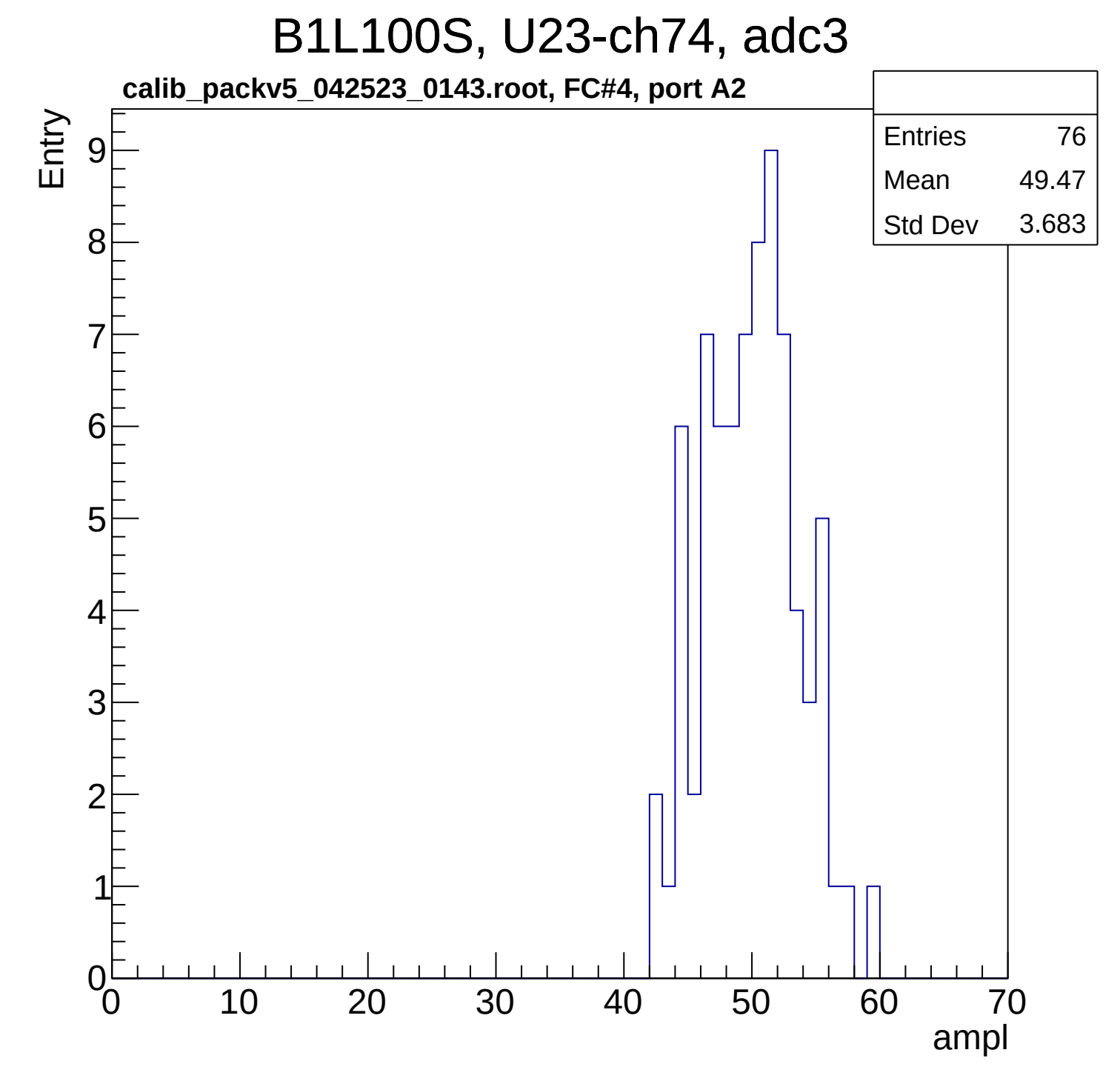
B1L100S, U23-ch74, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.47
Std Dev 3.683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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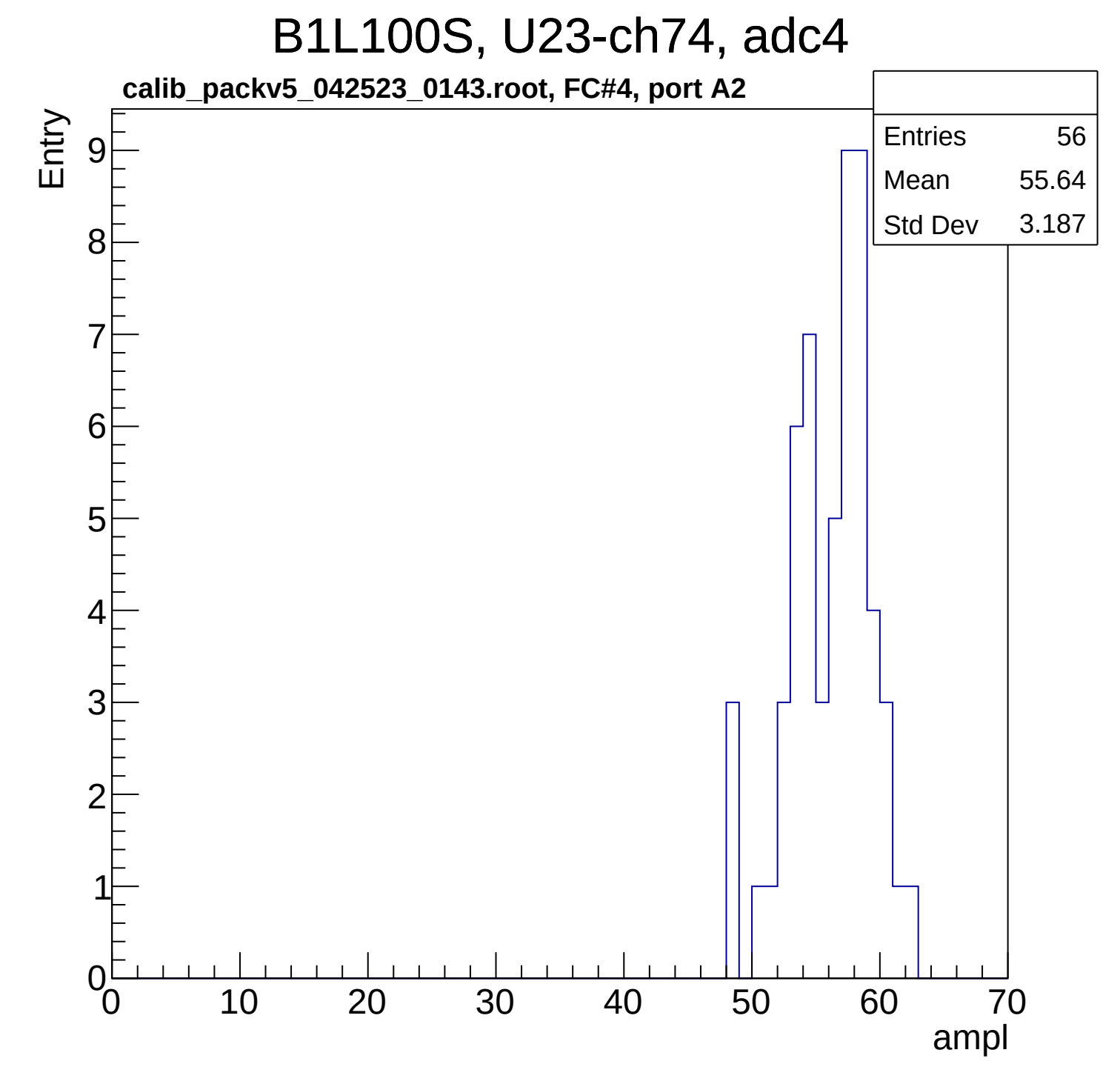
B1L100S, U23-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.64
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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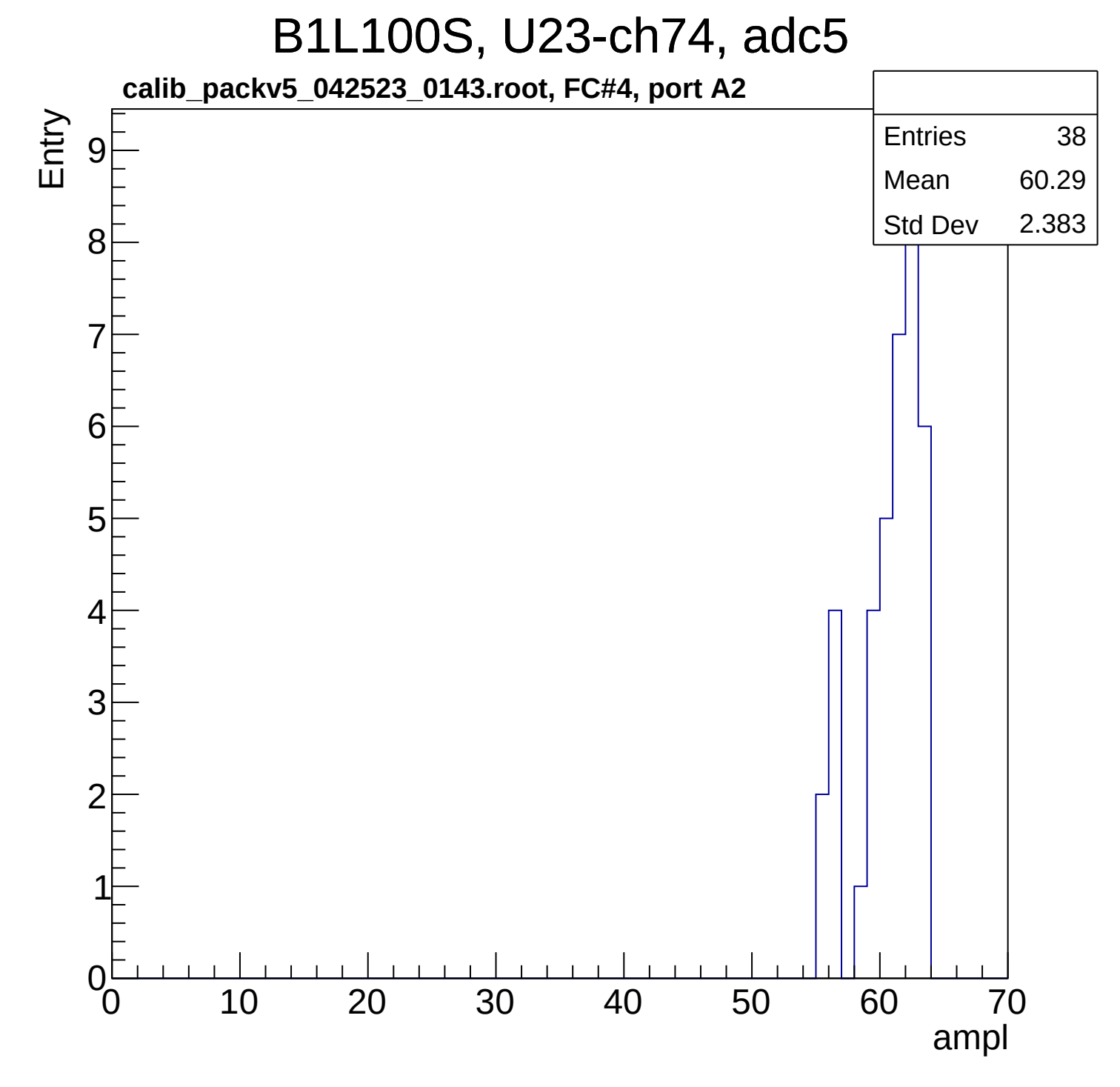
B1L100S, U23-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.29
Std Dev 2.383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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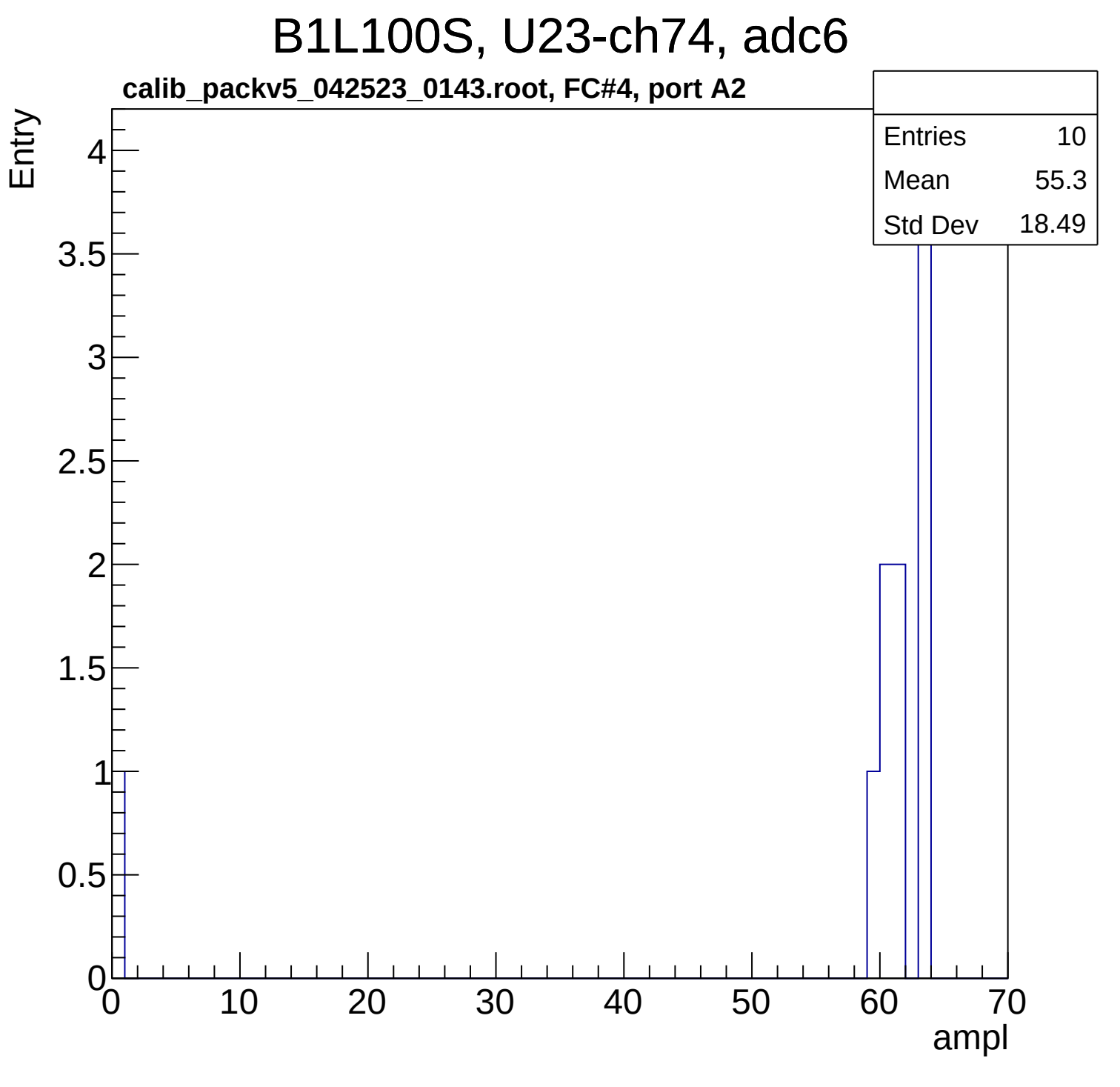
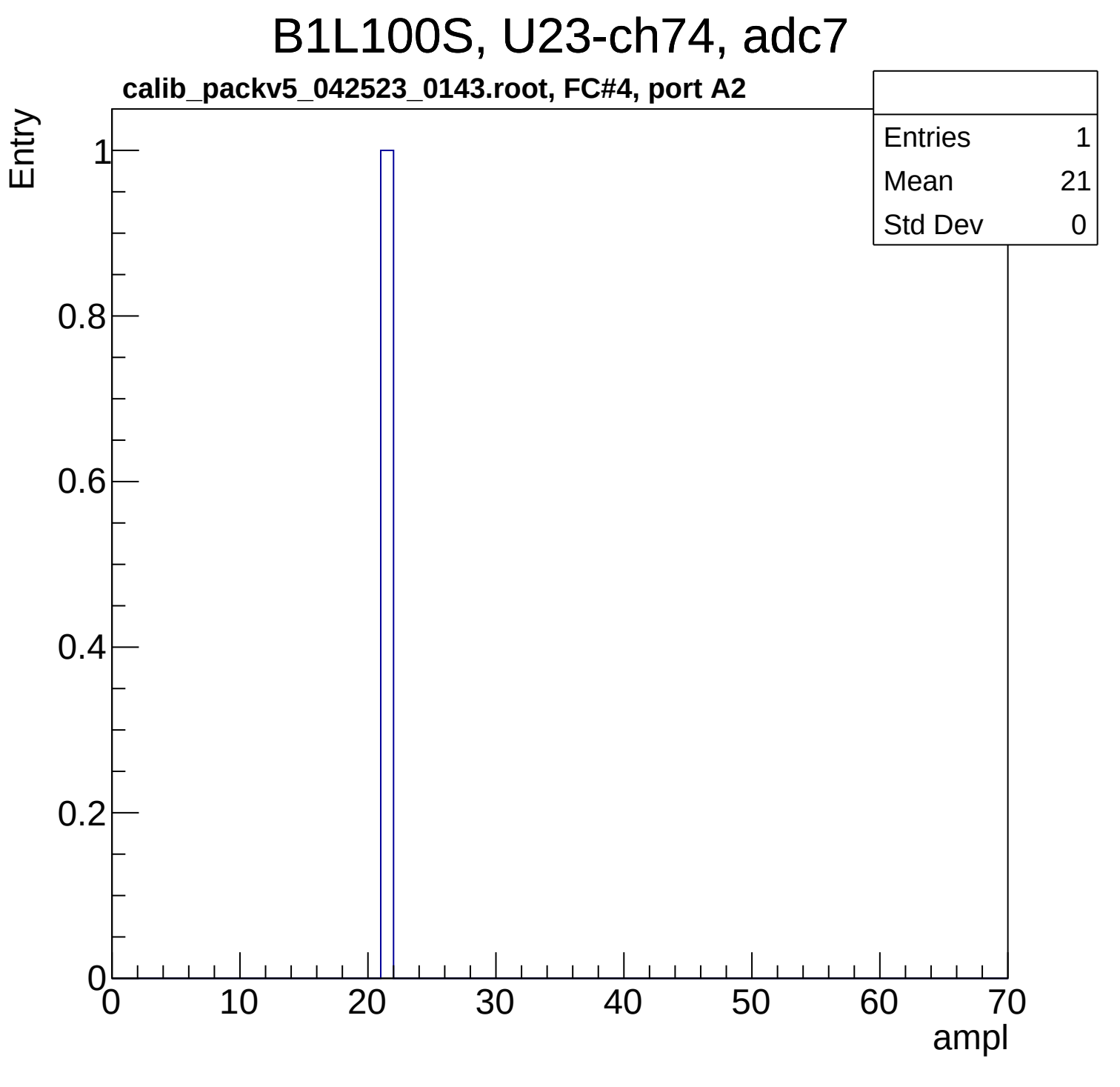
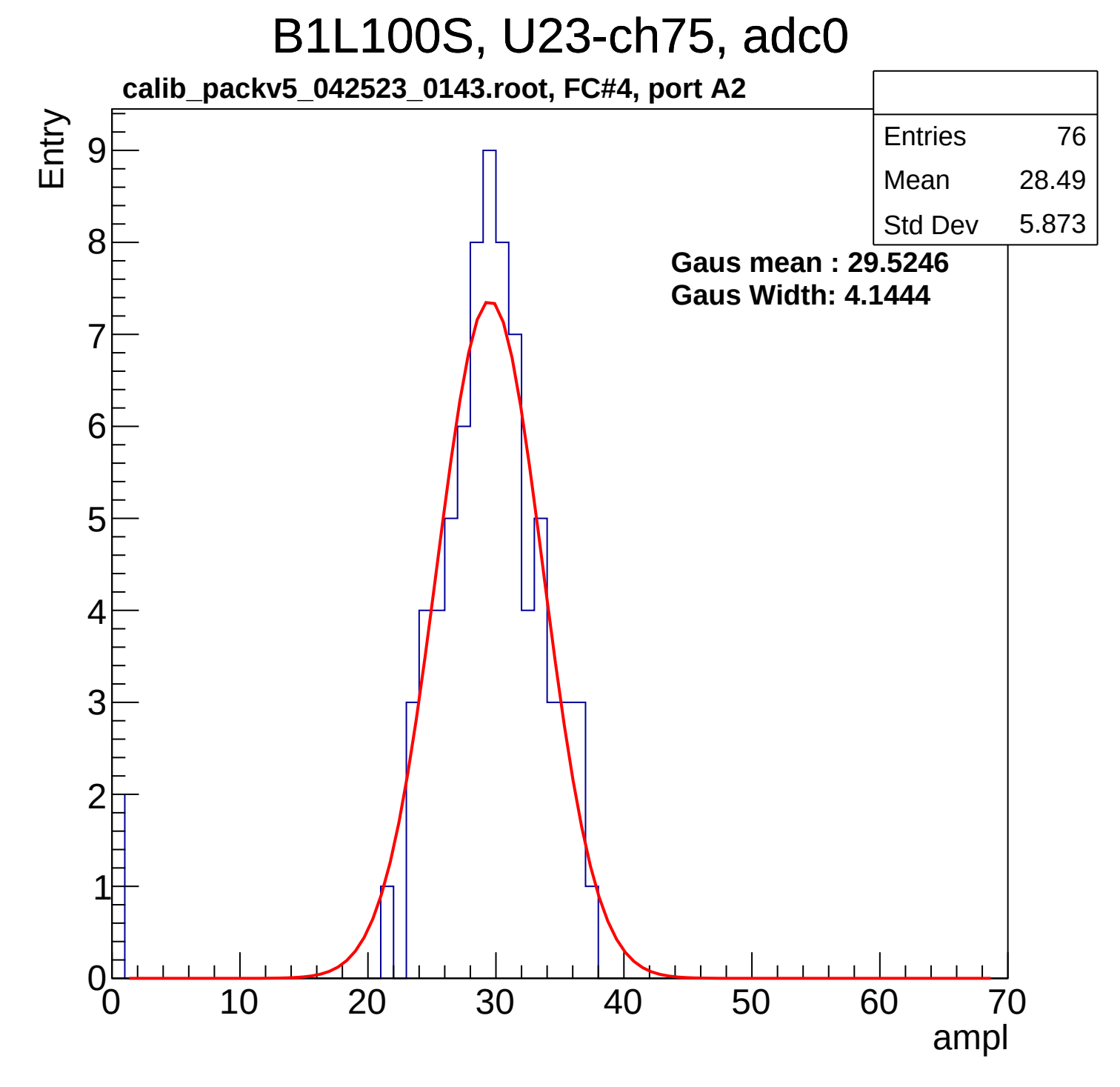

B1L100S, U23-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 55.3
Std Dev 18.49
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U23-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.49
Std Dev 5.873
Gaus mean : 29.5246
Gaus Width: 4.1444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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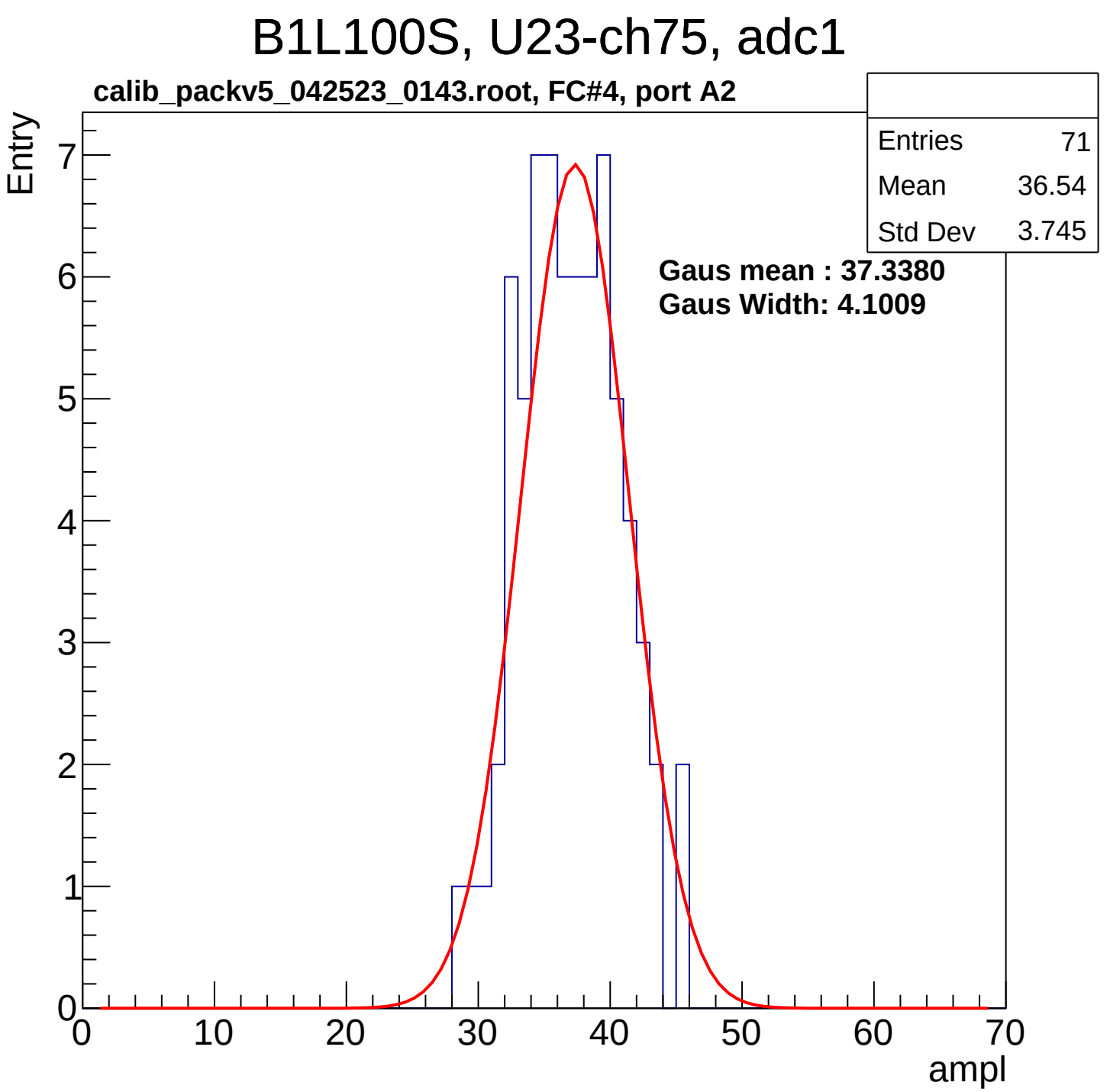
40
50
60
70


B1L100S, U23-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.54
Std Dev 3.745
Gaus mean : 37.3380
Gaus Width: 4.1009
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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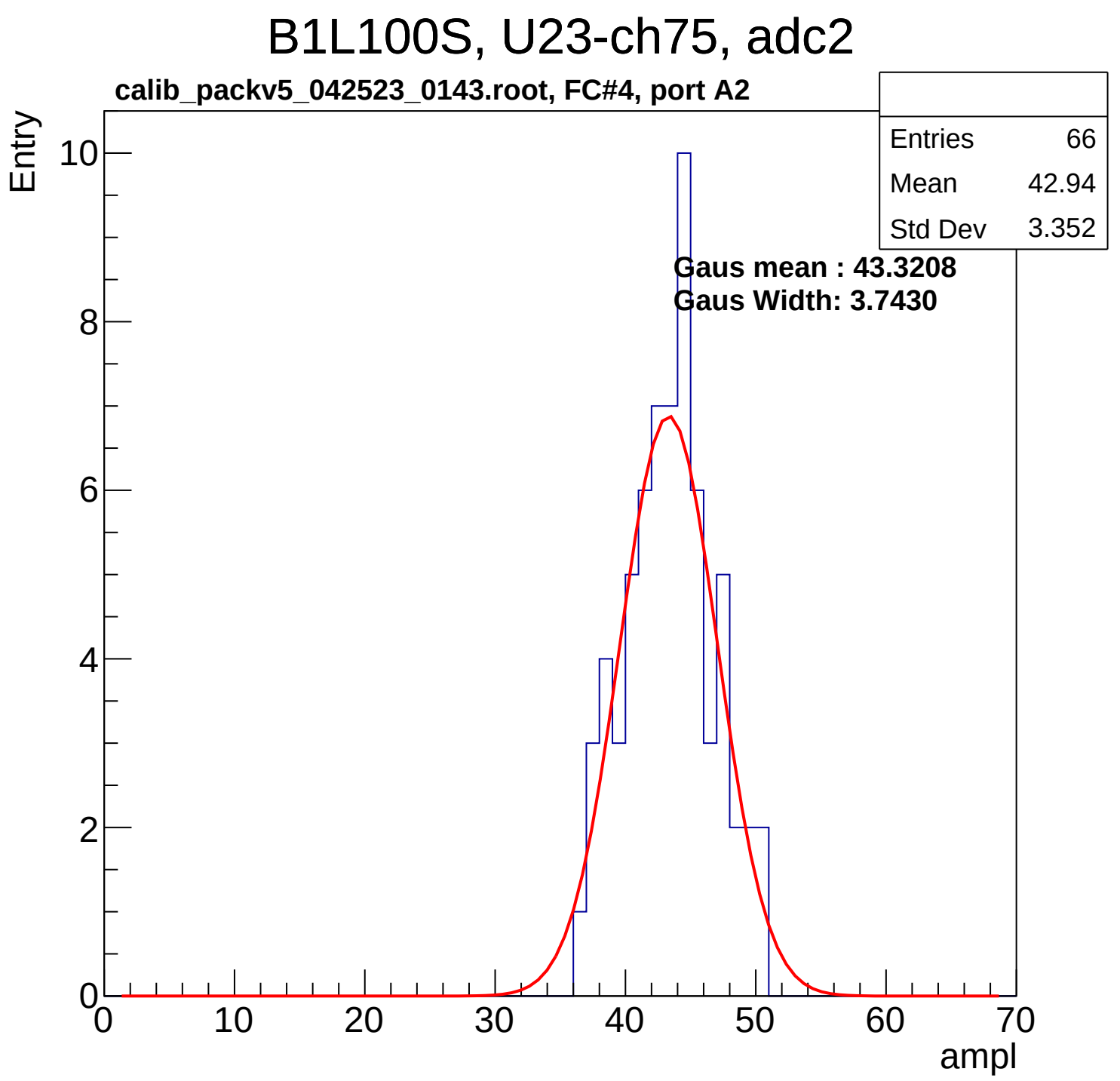
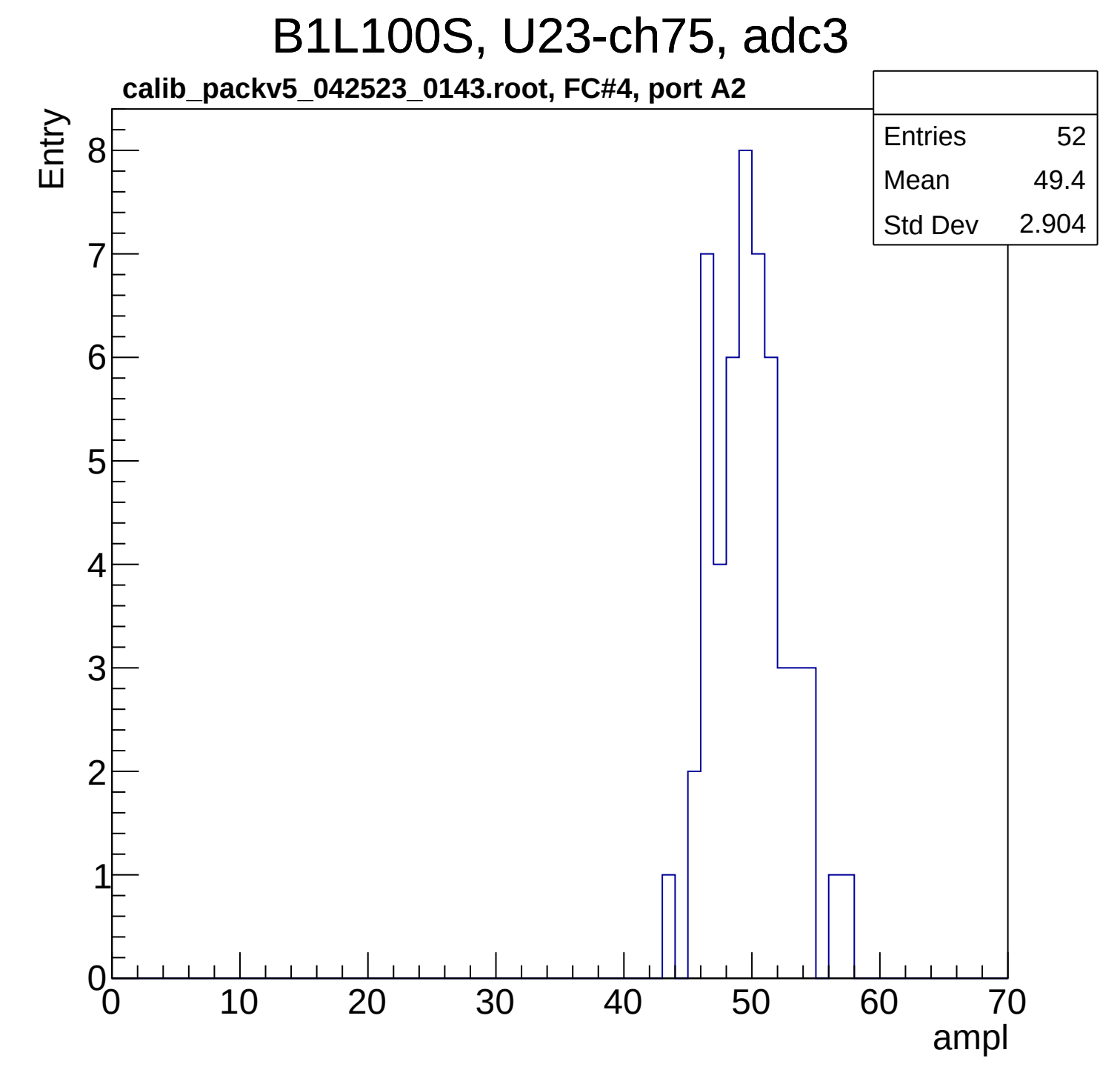

B1L100S, U23-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.94
Std Dev 3.352
Gaus mean : 43.3208
Gaus Width: 3.7430
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch75, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 49.4
Std Dev 2.904
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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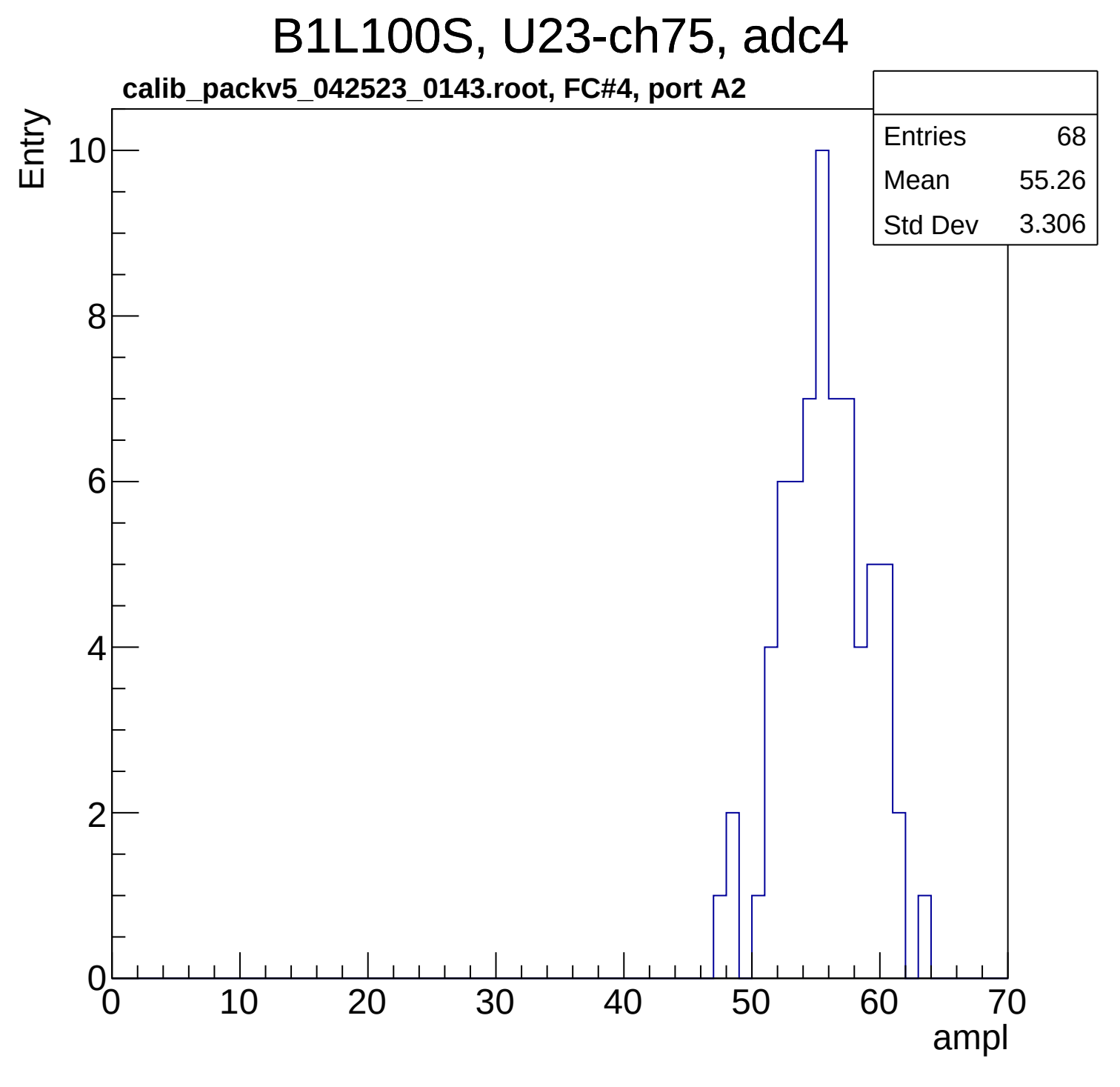
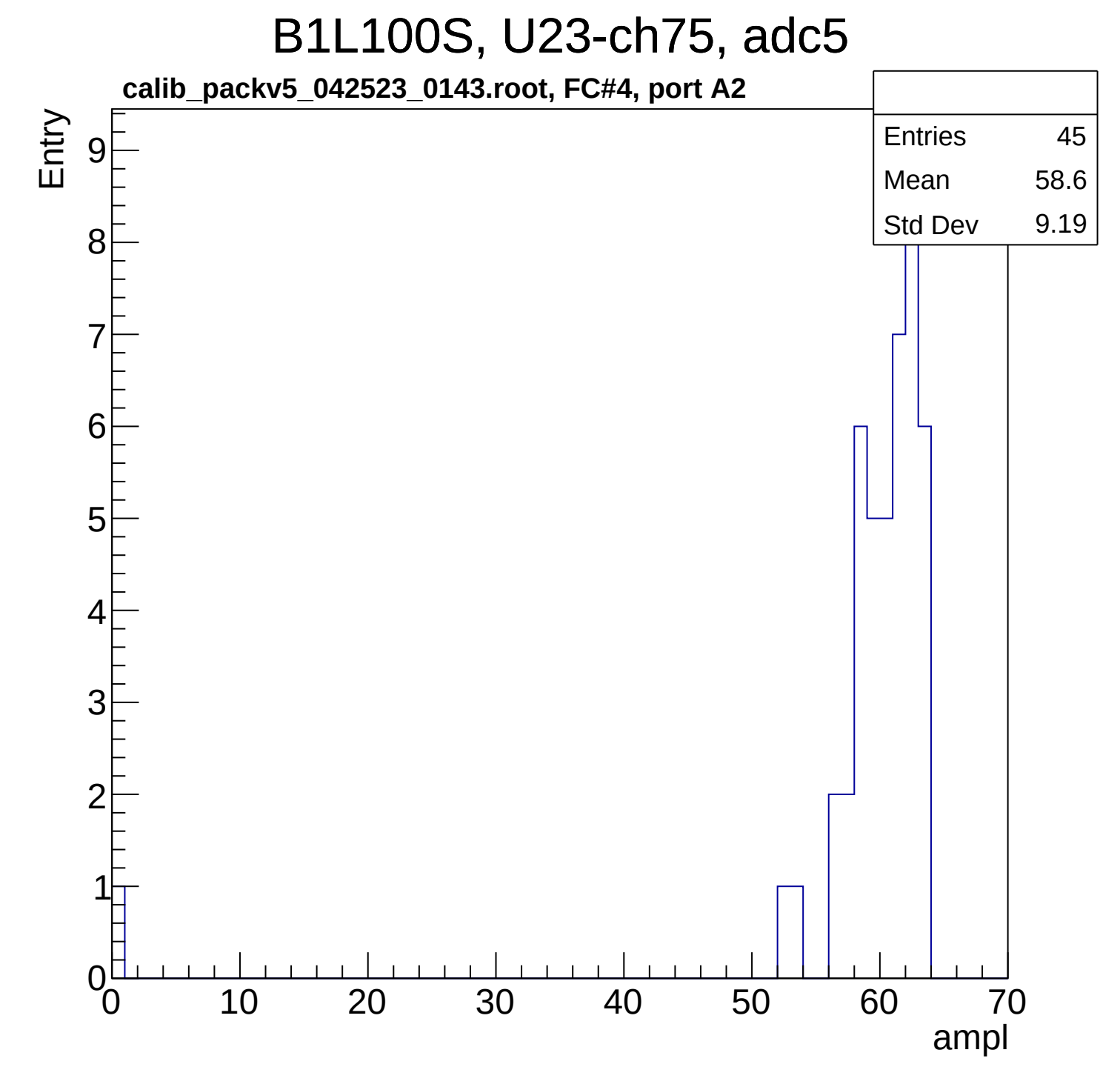
40
50
60
70


B1L100S, U23-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 55.26
Std Dev 3.306
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.6
Std Dev 9.19
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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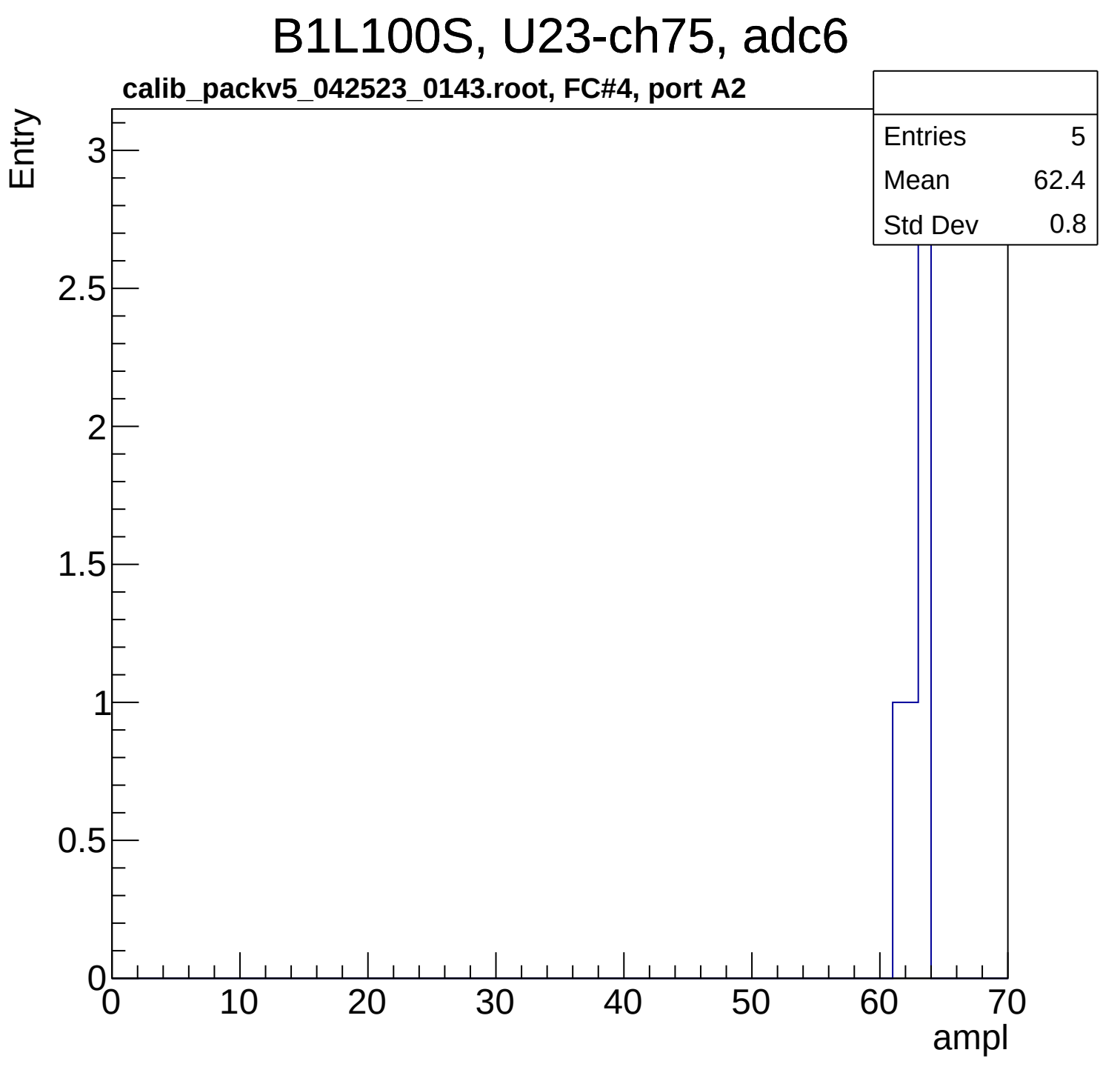
B1L100S, U23-ch75, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
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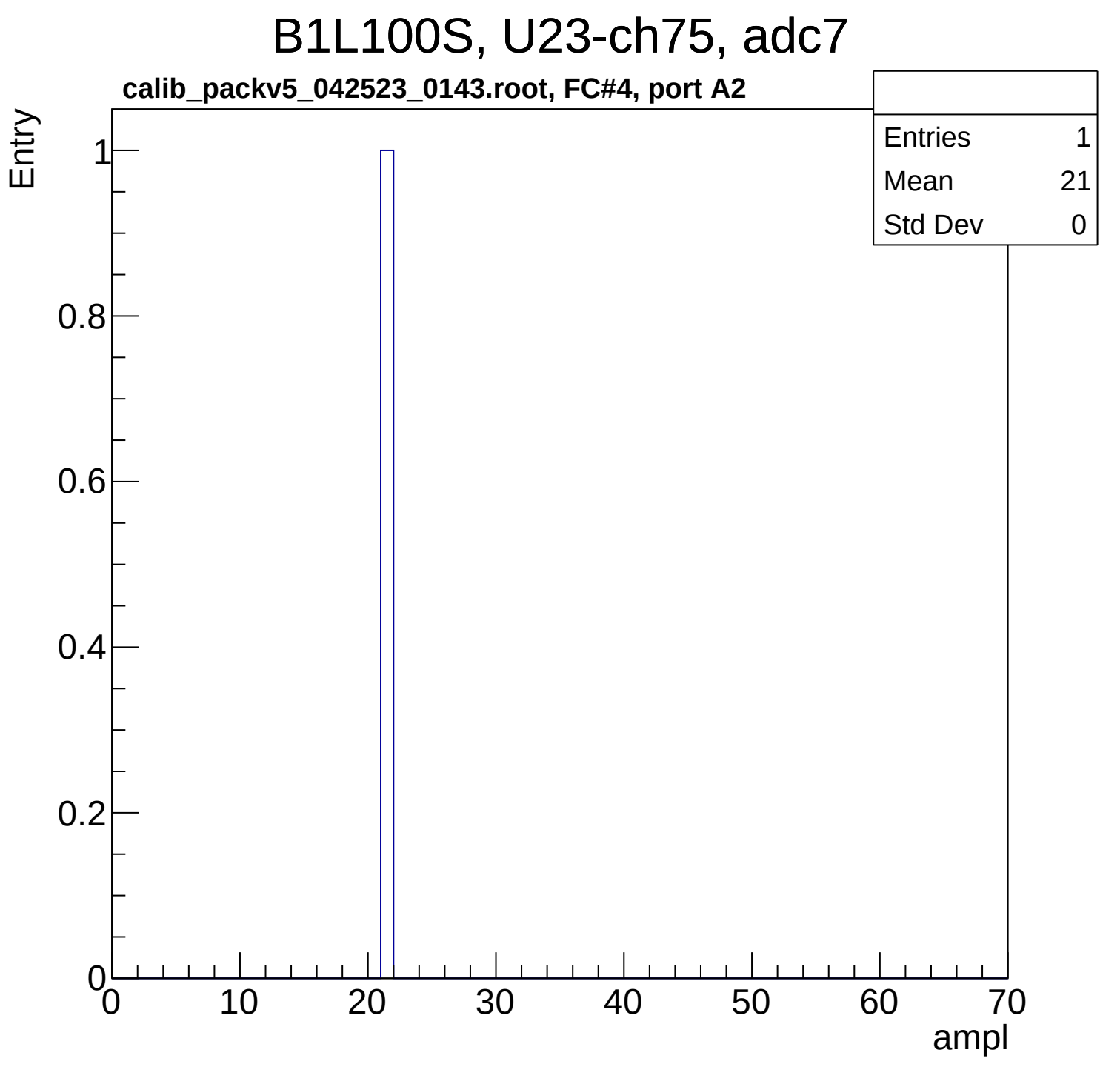
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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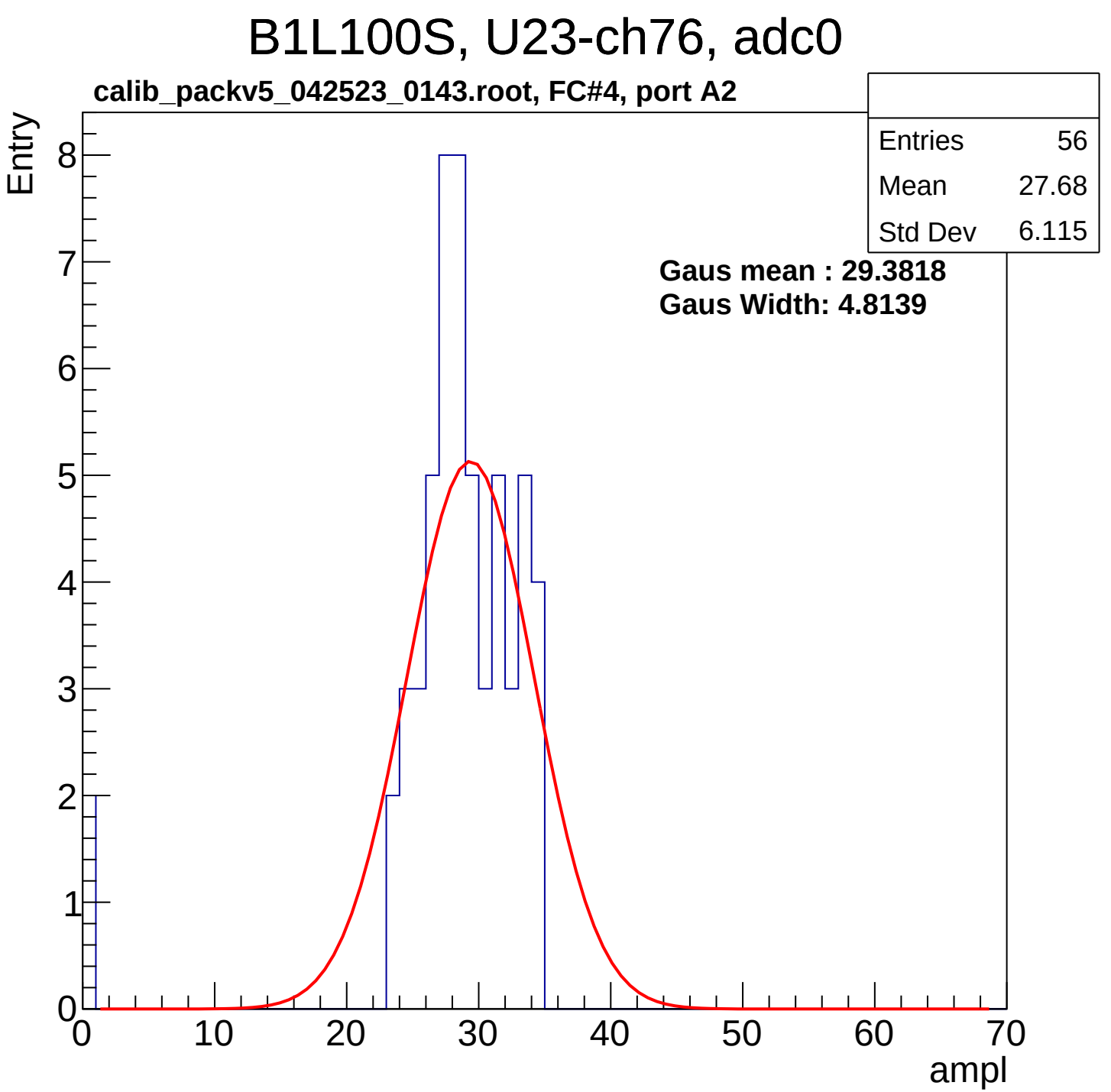
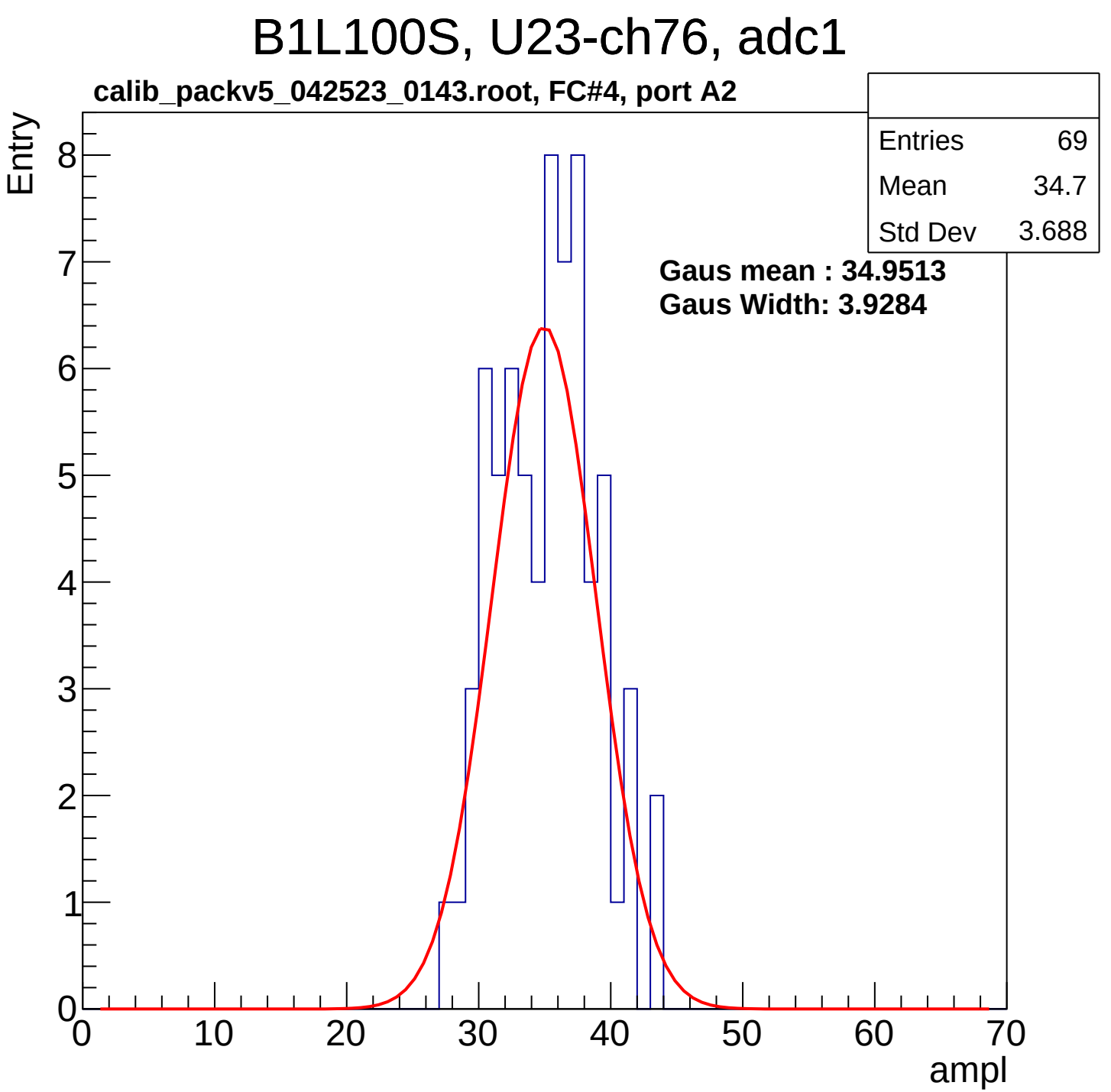
50
60
70
ampl


B1L100S, U23-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 27.68
Std Dev 6.115
Gaus mean : 29.3818
Gaus Width: 4.8139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 34.7
Std Dev 3.688
Gaus mean : 34.9513
Gaus Width: 3.9284
Entry
ampl
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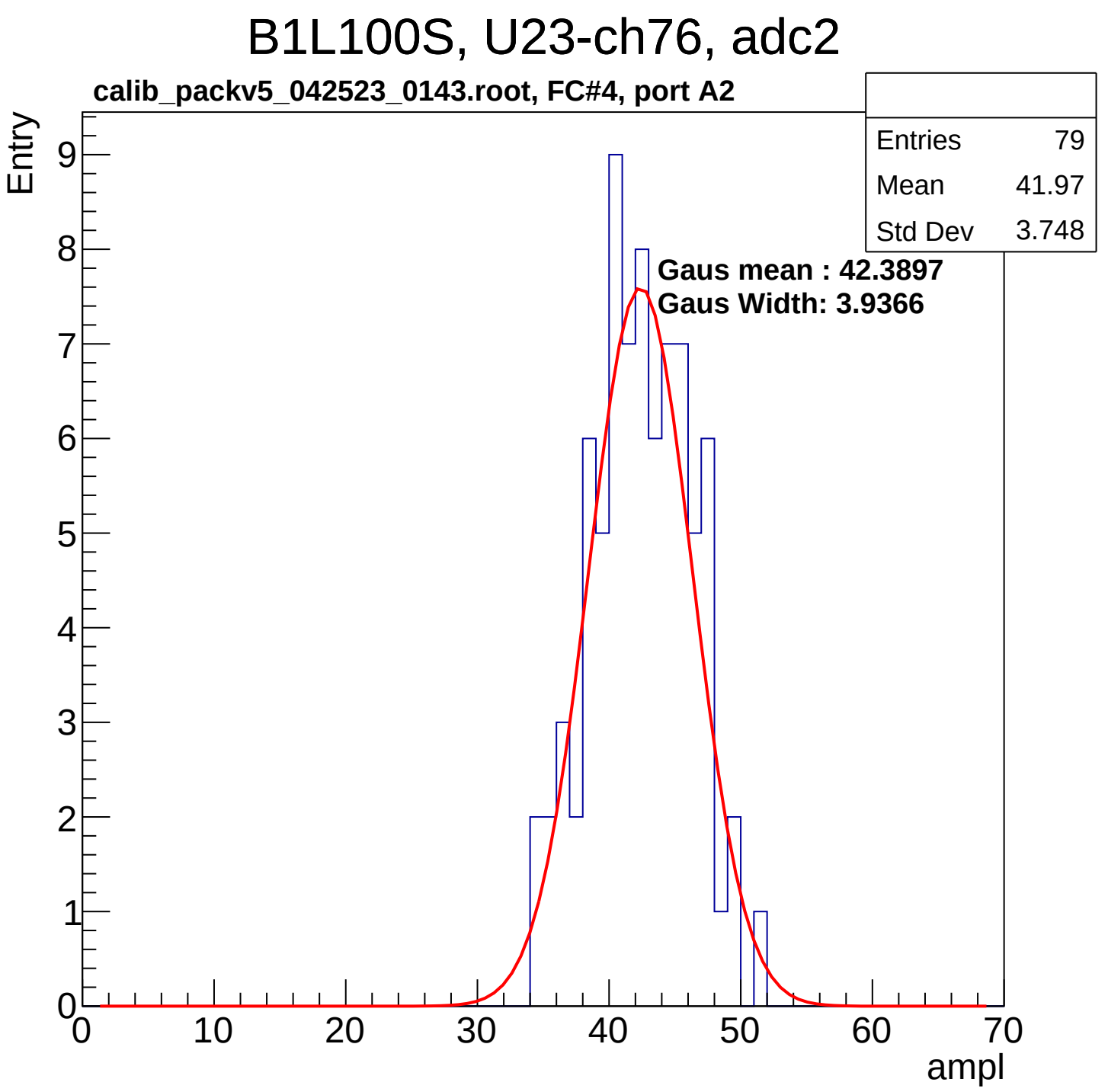
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 41.97
Std Dev 3.748
Gaus mean : 42.3897
Gaus Width: 3.9366
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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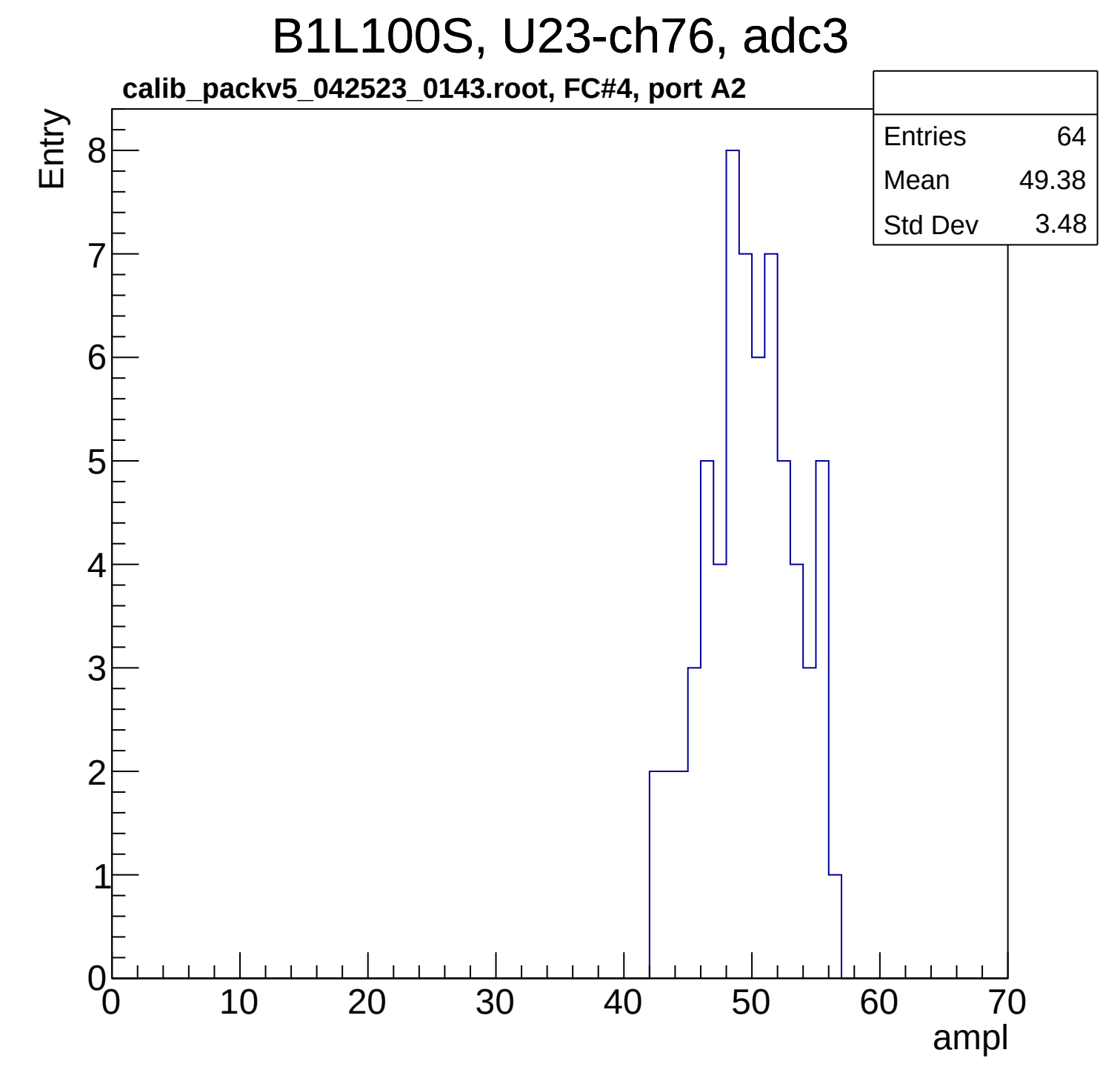
B1L100S, U23-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.38
Std Dev 3.48
Entry
ampl
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0
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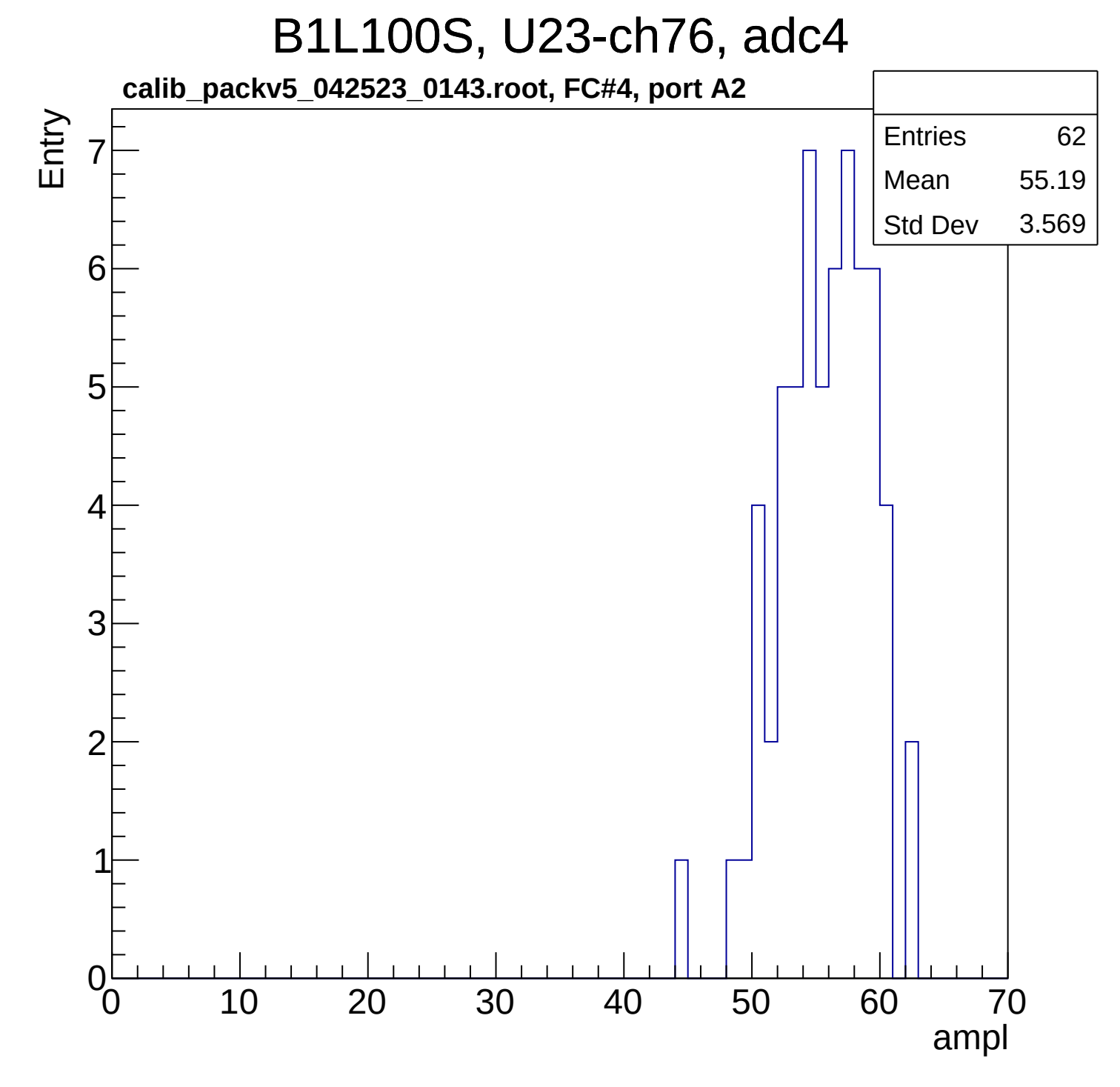
B1L100S, U23-ch76, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.19
Std Dev 3.569
Entry
ampl
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1
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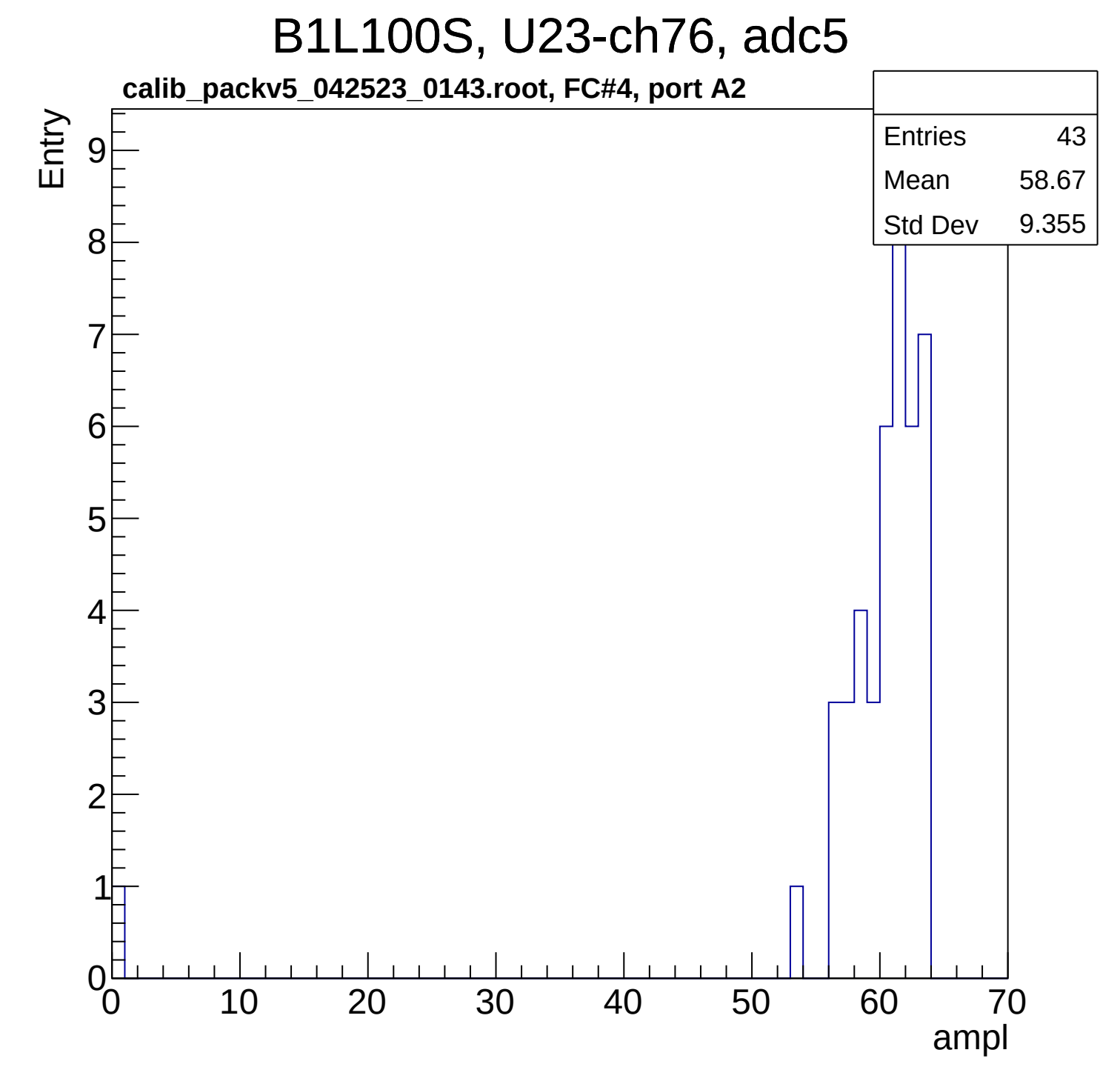
B1L100S, U23-ch76, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.67
Std Dev 9.355
Entry
ampl
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3
4
5
6
7
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0
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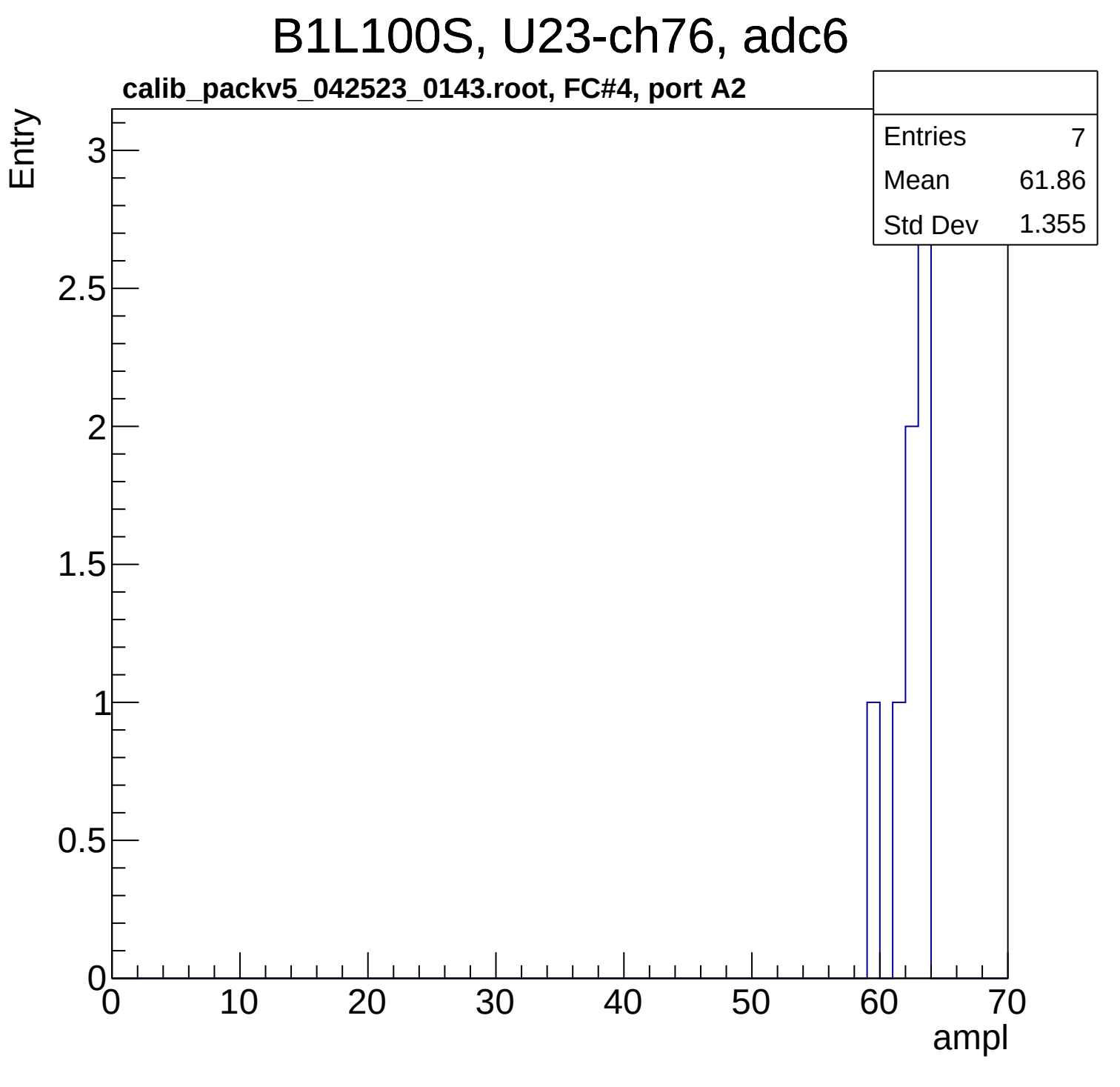
B1L100S, U23-ch76, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 1.355
Entry
ampl
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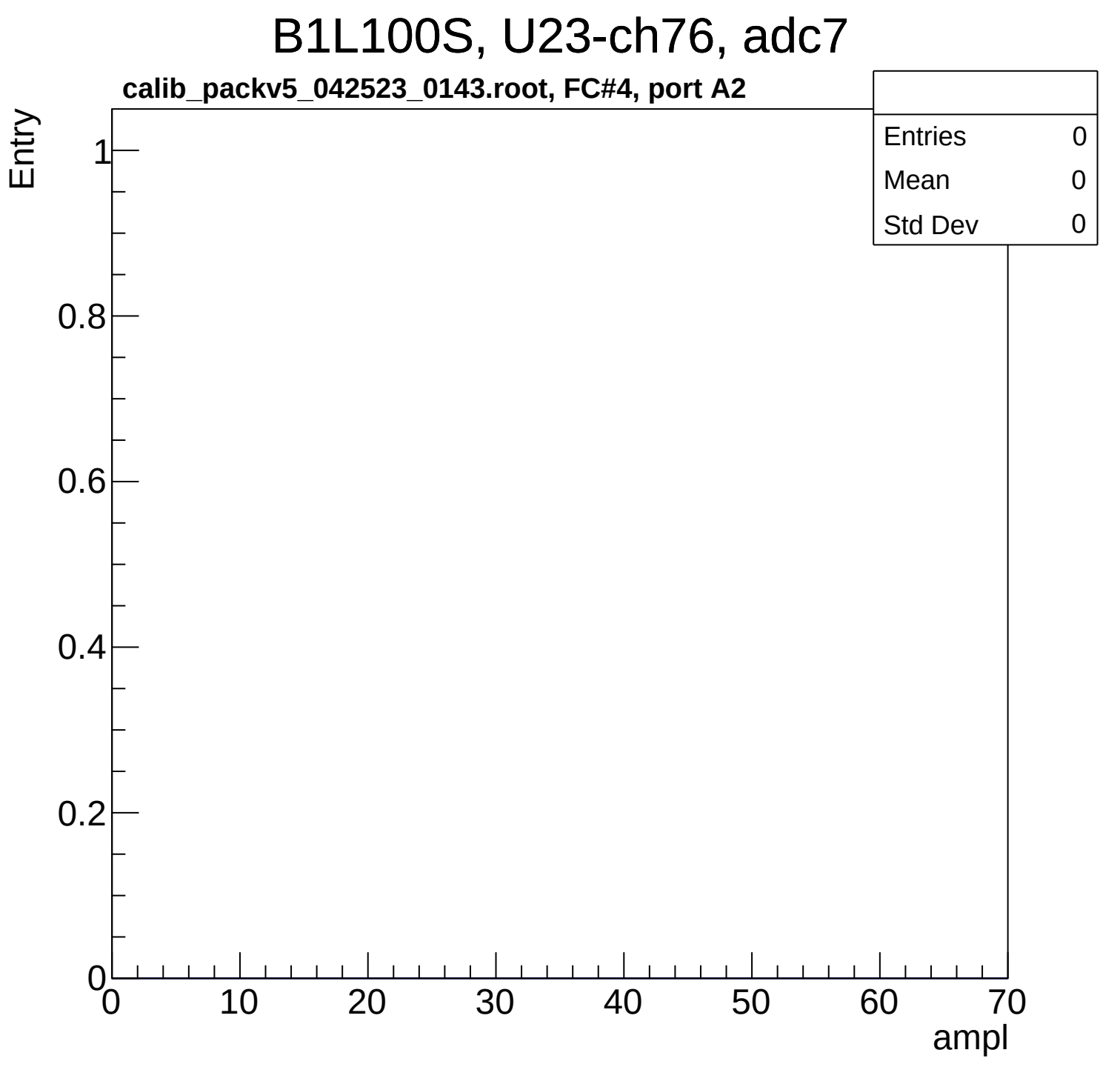
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch76, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
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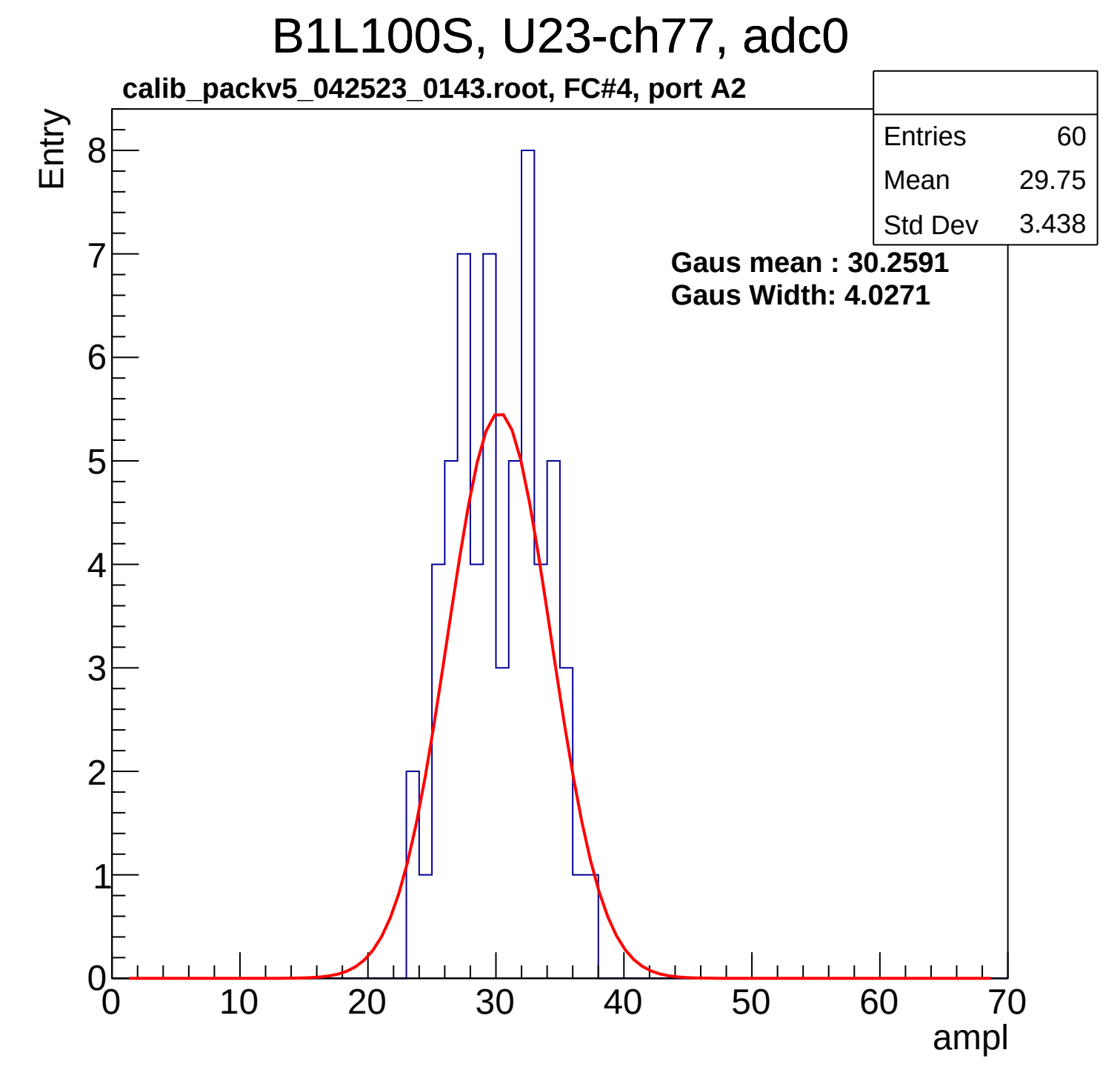
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.75
Std Dev 3.438
Gaus mean : 30.2591
Gaus Width: 4.0271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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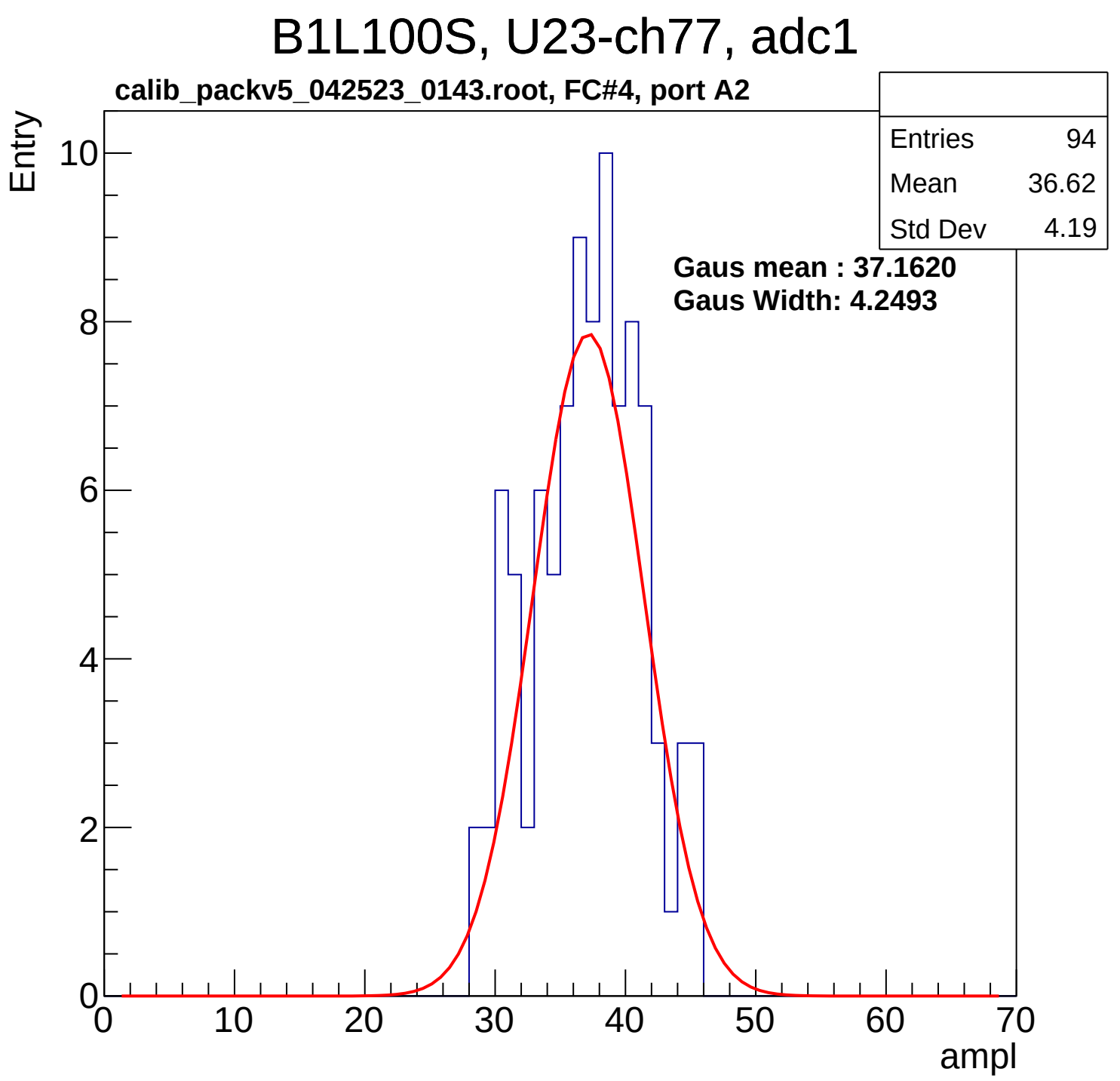
B1L100S, U23-ch77, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 36.62
Std Dev 4.19
Gaus mean : 37.1620
Gaus Width: 4.2493
Entry
ampl
0
2
4
6
8
10
0
10
20
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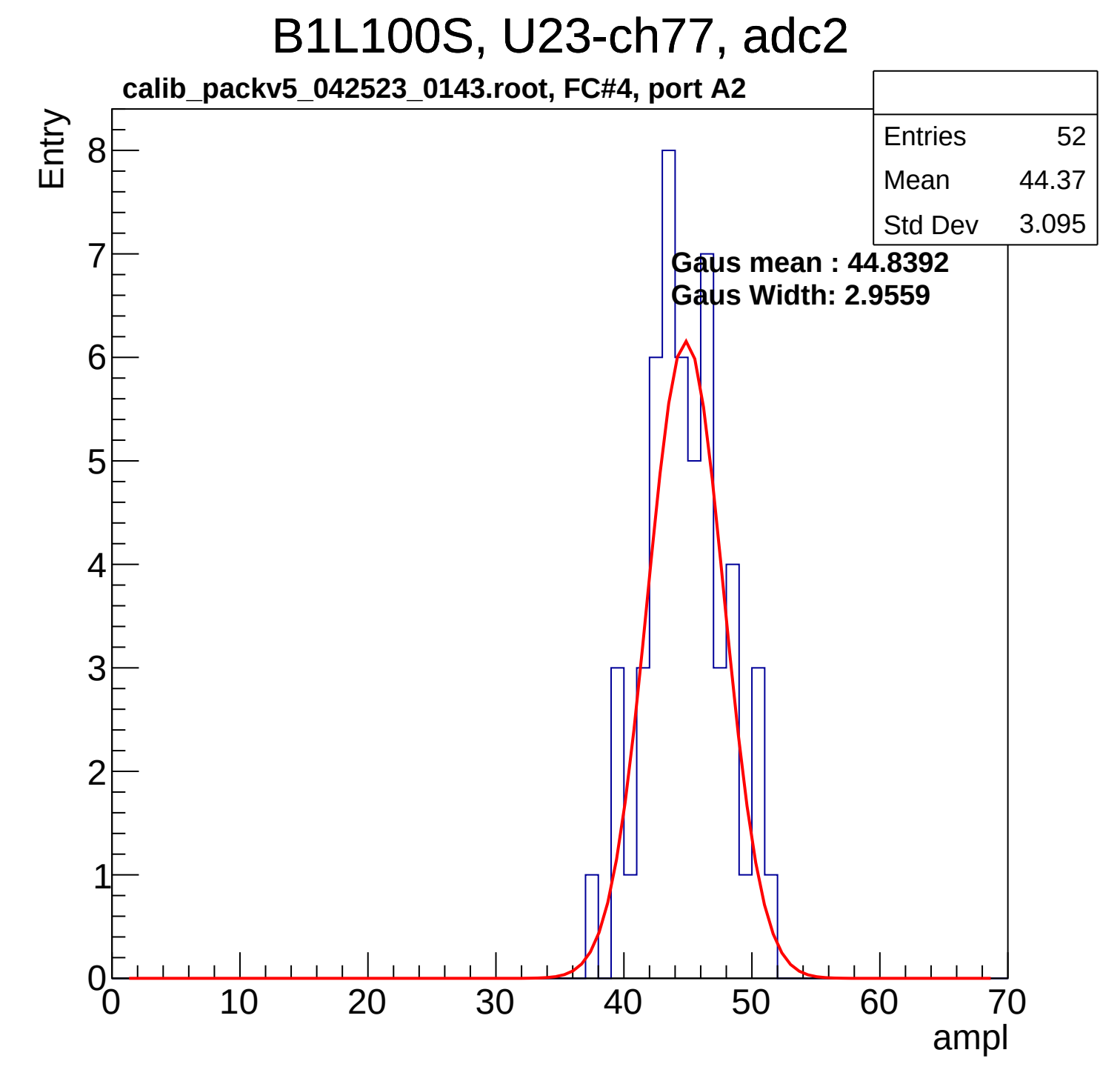
40
50
60
70


B1L100S, U23-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 44.37
Std Dev 3.095
Gaus mean : 44.8392
Gaus Width: 2.9559
Entry
ampl
0
1
2
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5
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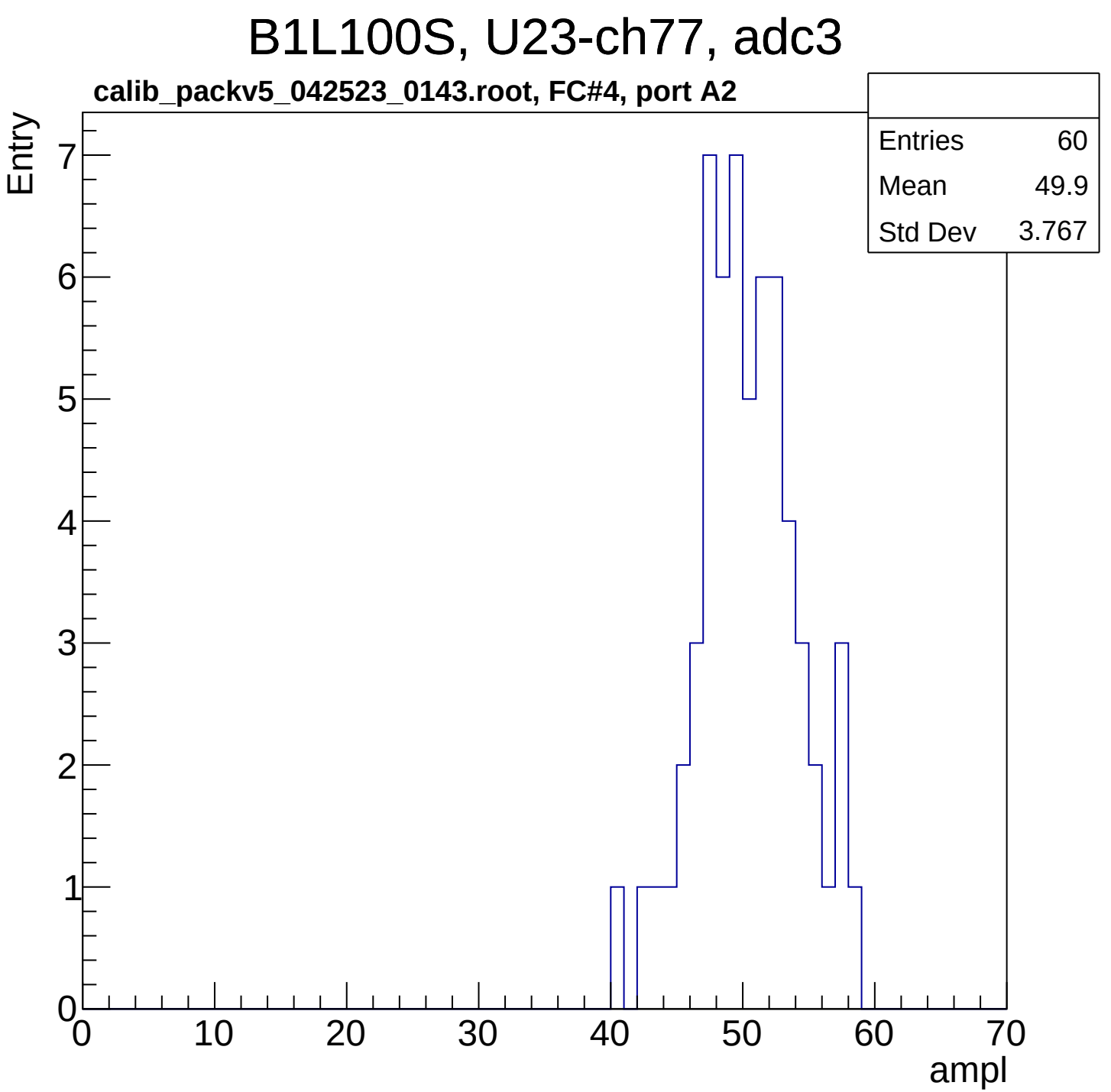
B1L100S, U23-ch77, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.9
Std Dev 3.767
Entry
ampl
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1
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4
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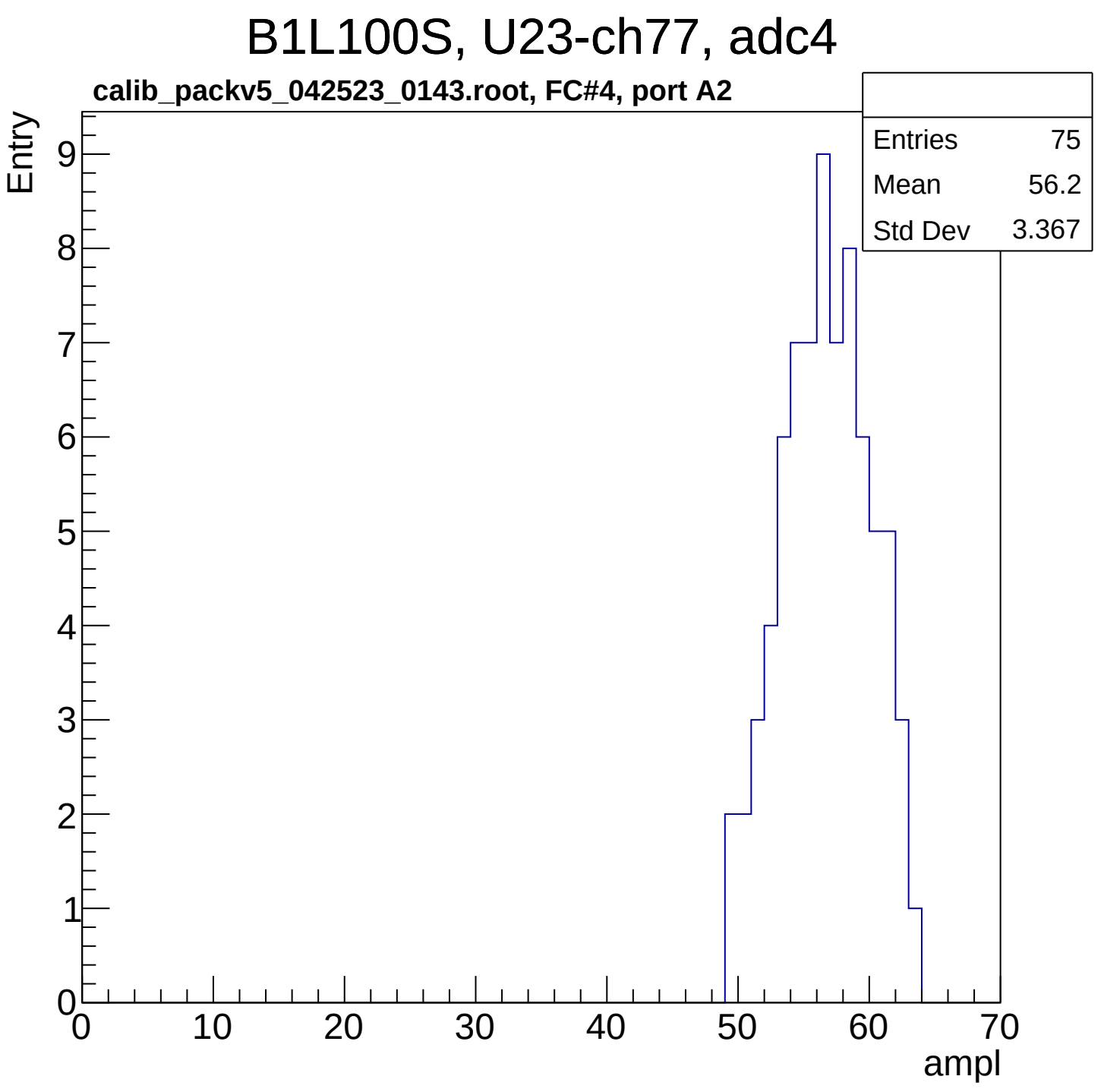
B1L100S, U23-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 56.2
Std Dev 3.367
Entry
ampl
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3
4
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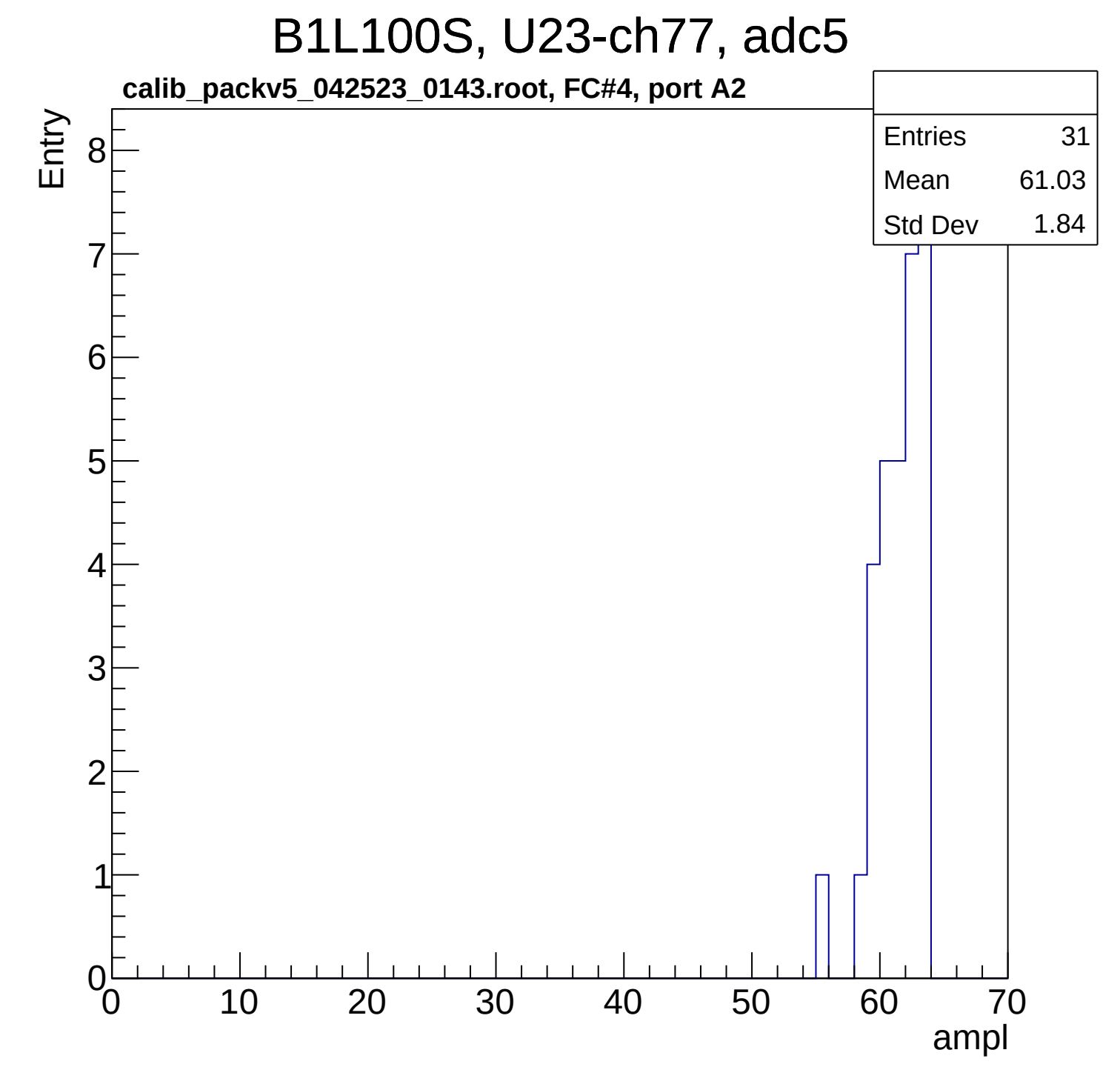
B1L100S, U23-ch77, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 61.03
Std Dev 1.84
Entry
0
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2
3
4
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6
7
8
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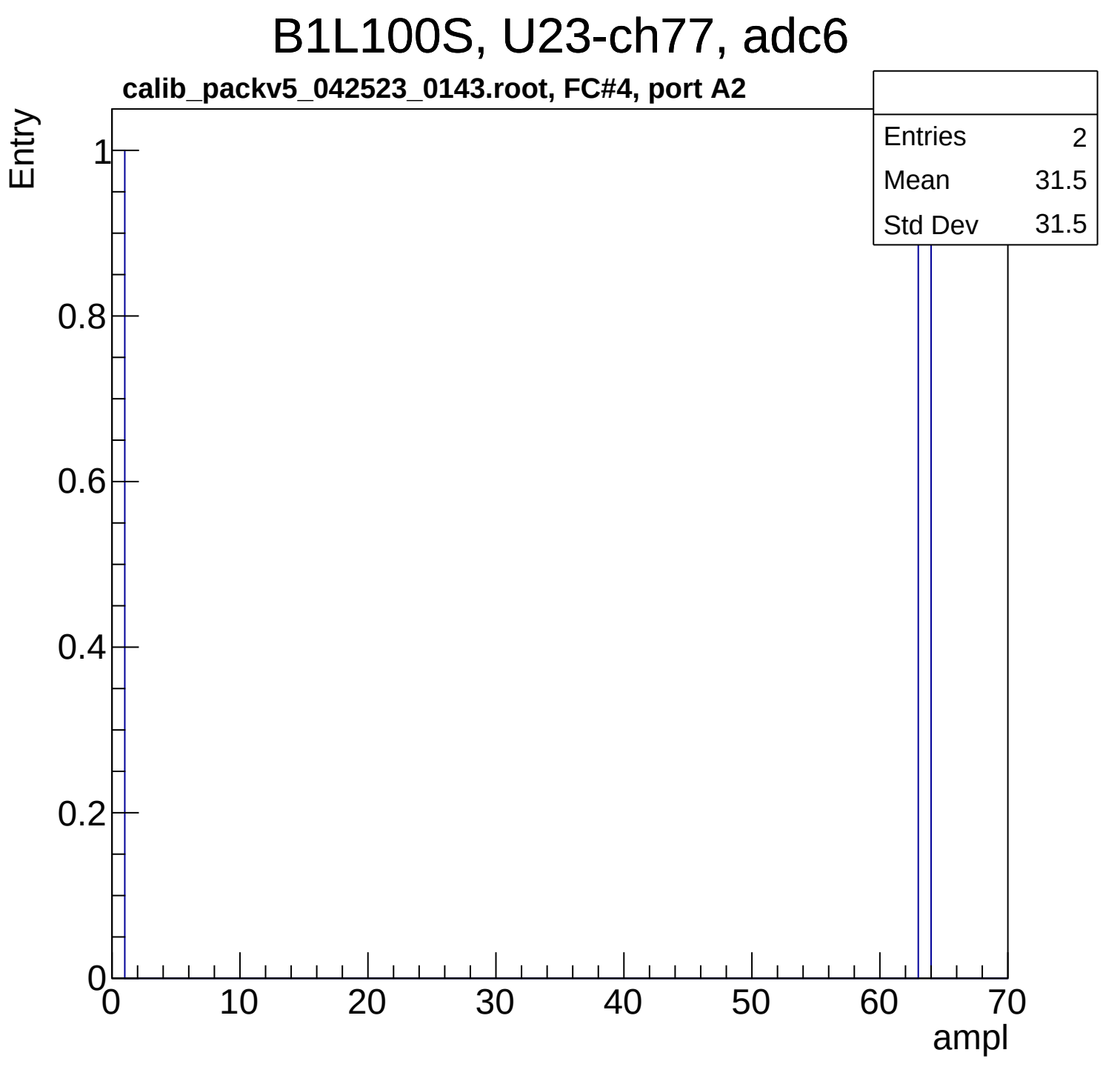
70
ampl


B1L100S, U23-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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30
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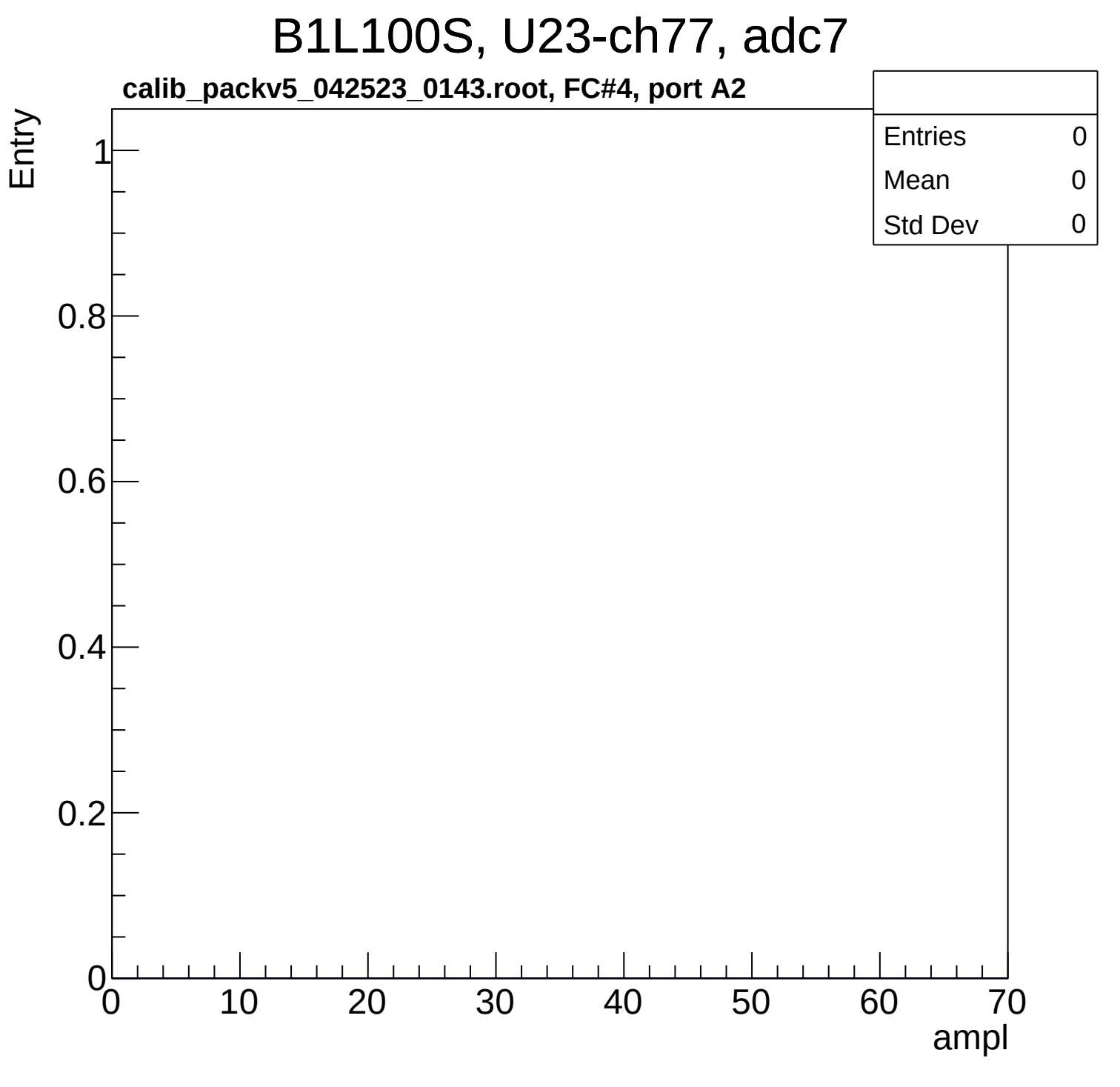
50
60
70
ampl


B1L100S, U23-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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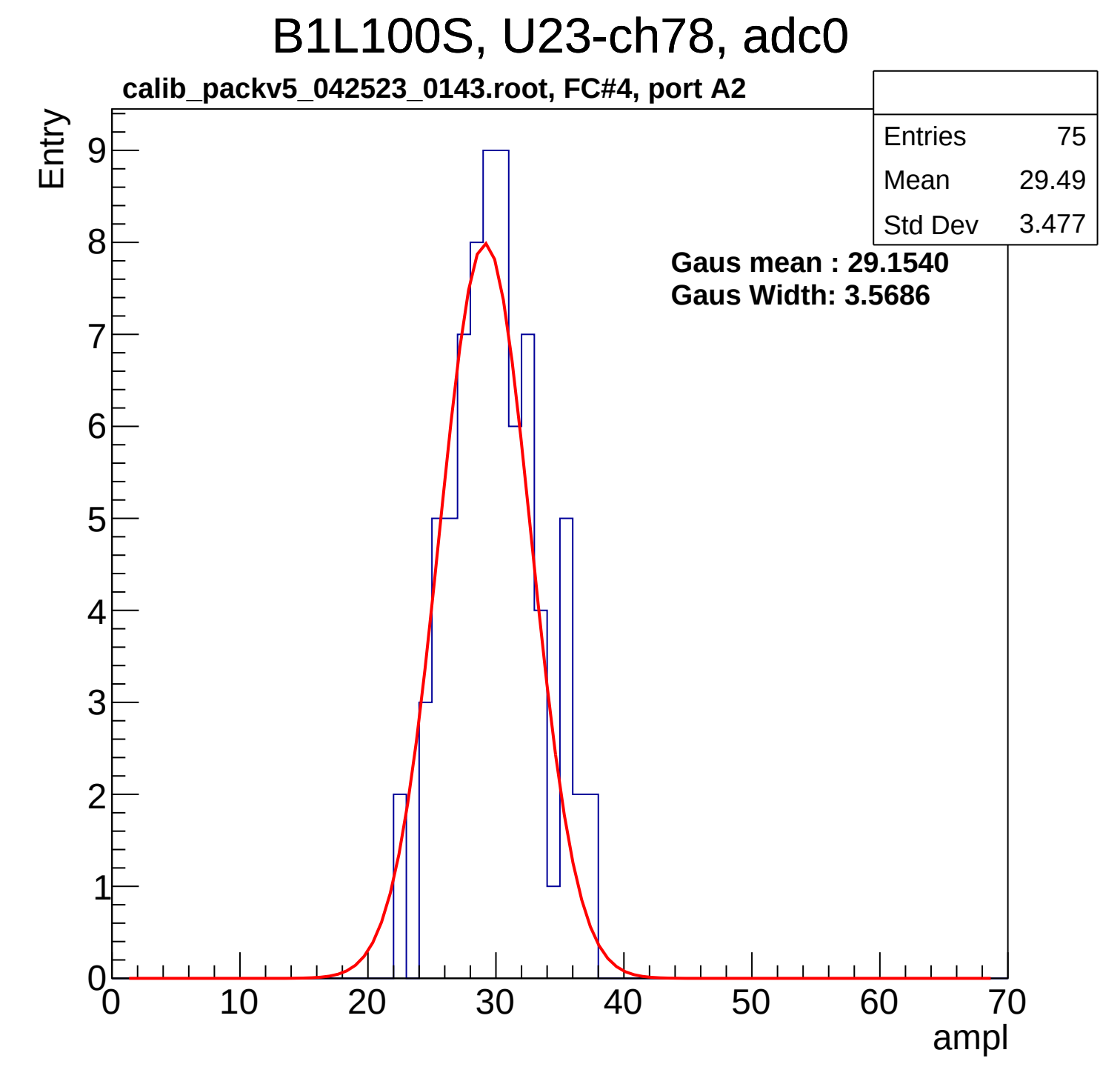
50
60
70
ampl


B1L100S, U23-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.49
Std Dev 3.477
Gaus mean : 29.1540
Gaus Width: 3.5686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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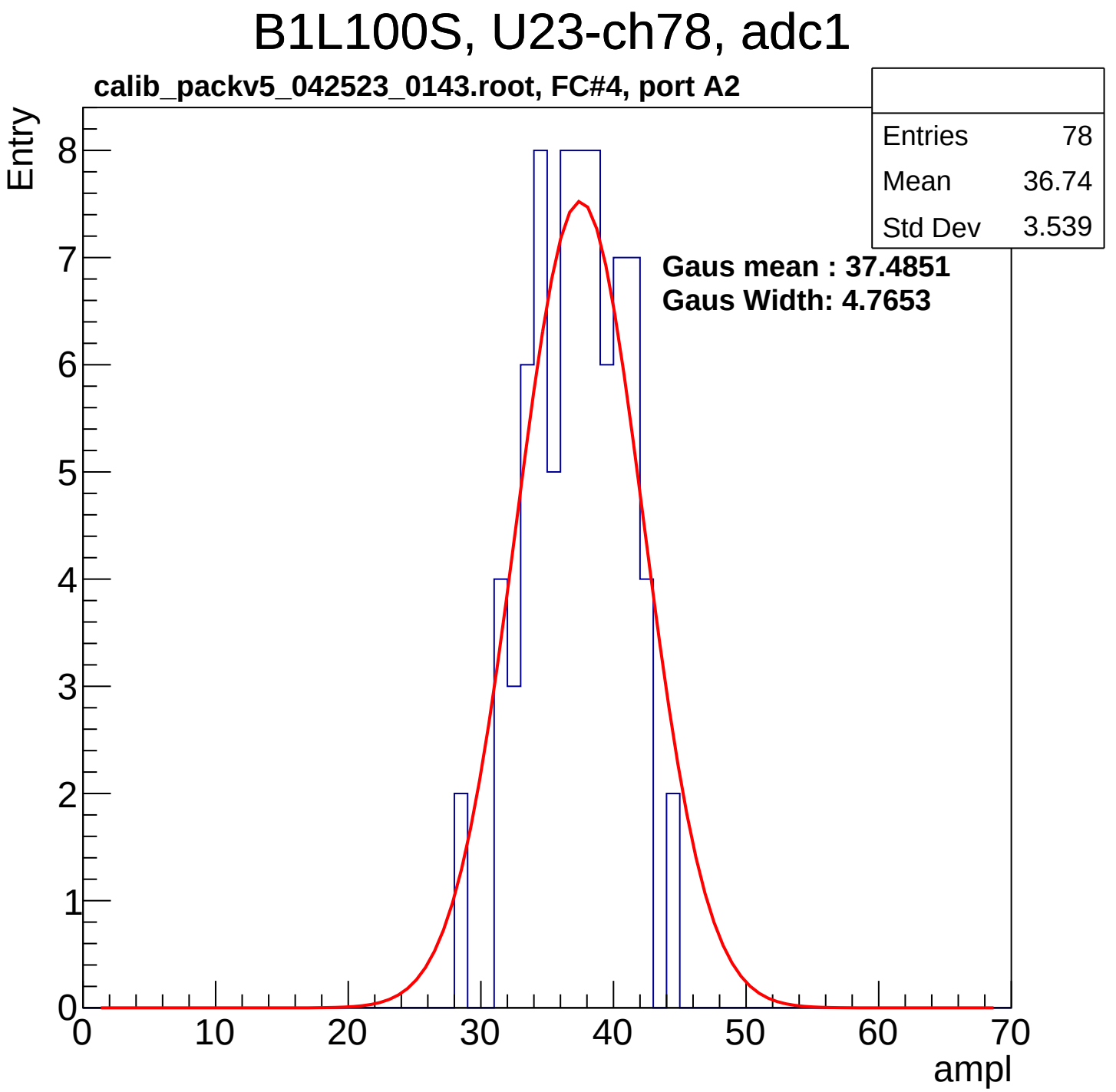
B1L100S, U23-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.74
Std Dev 3.539
Gaus mean : 37.4851
Gaus Width: 4.7653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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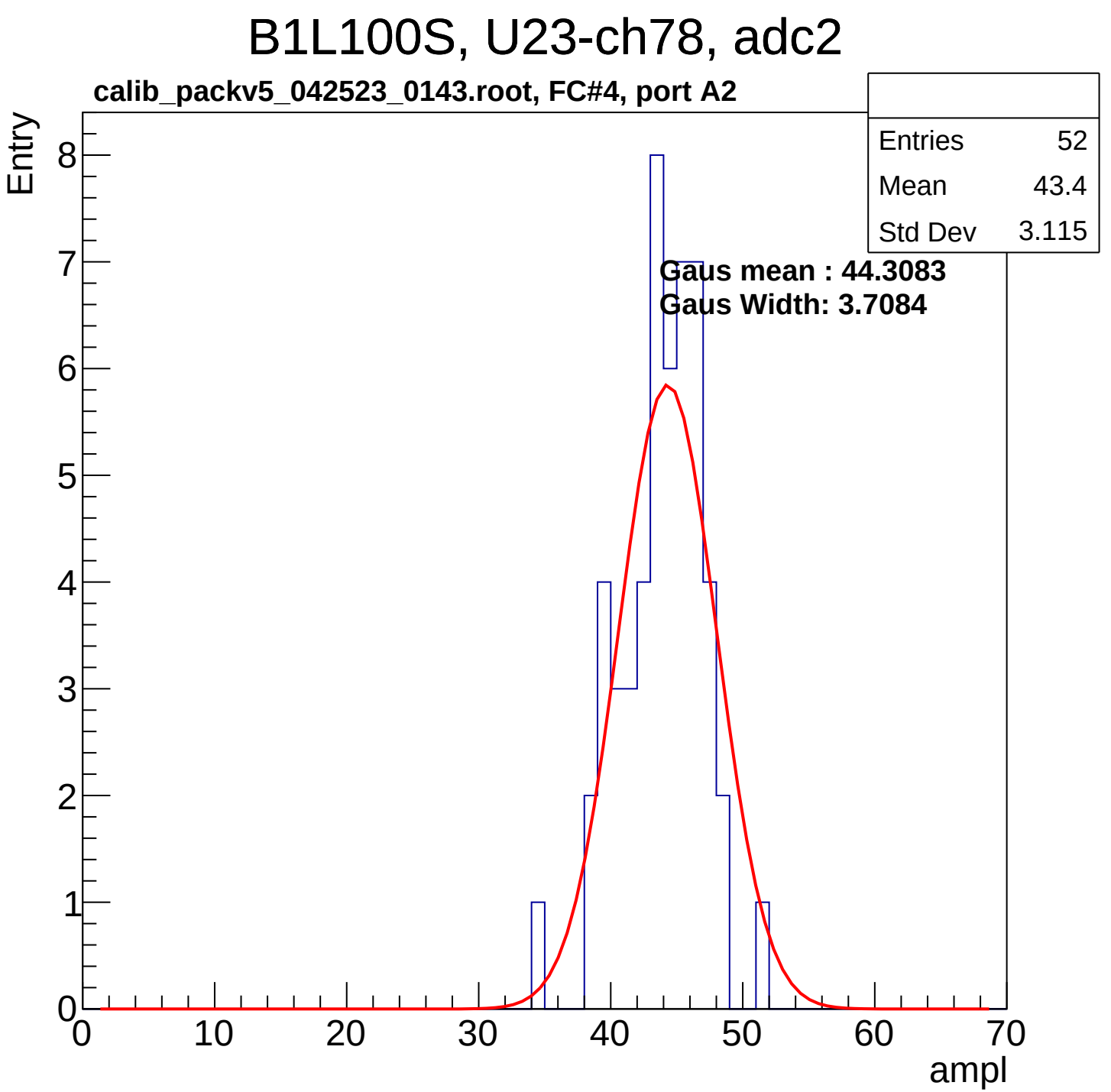
B1L100S, U23-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.4
Std Dev 3.115
Gaus mean : 44.3083
Gaus Width: 3.7084
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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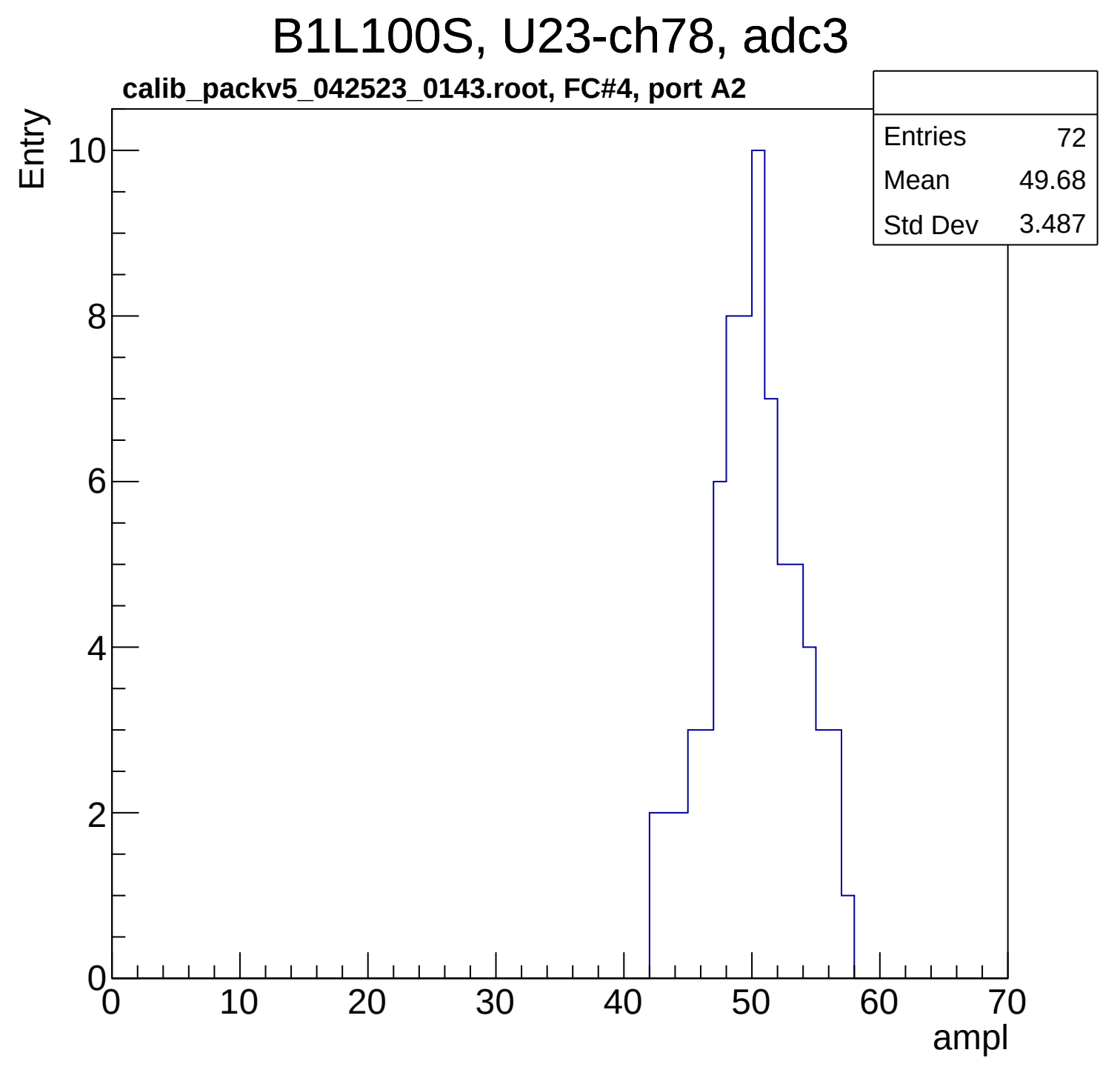
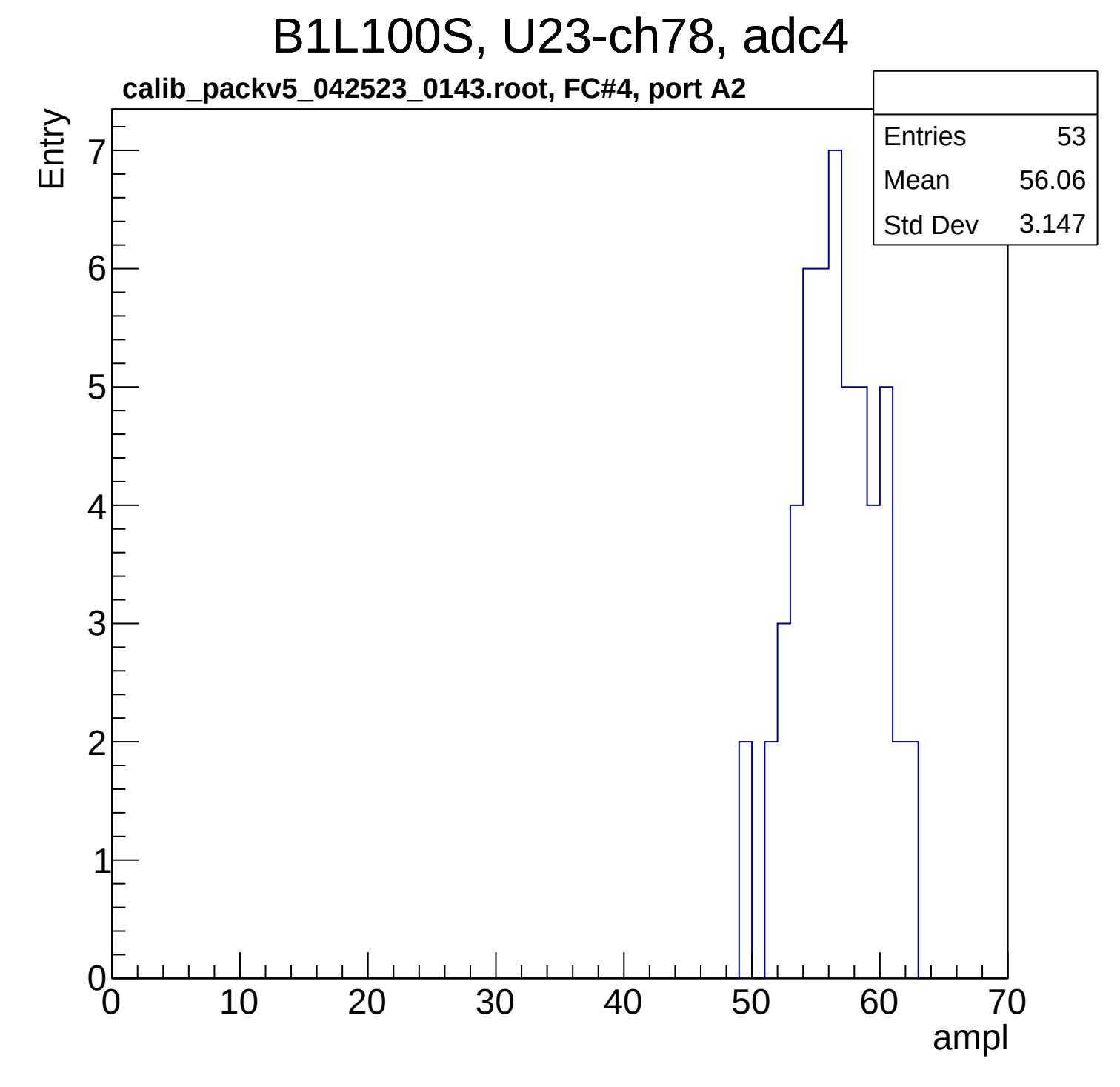

B1L100S, U23-ch78, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 49.68
Std Dev 3.487
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.06
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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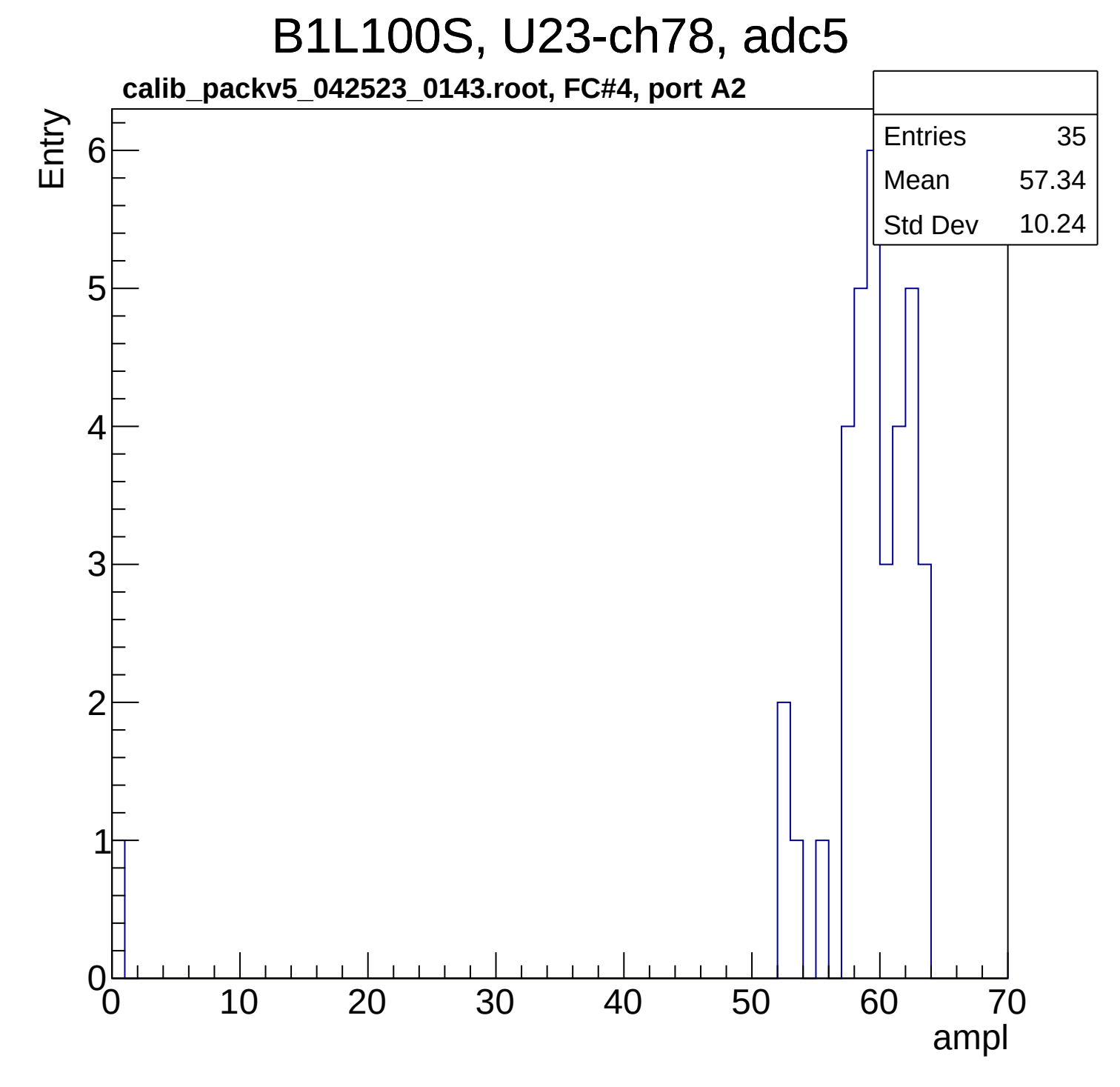
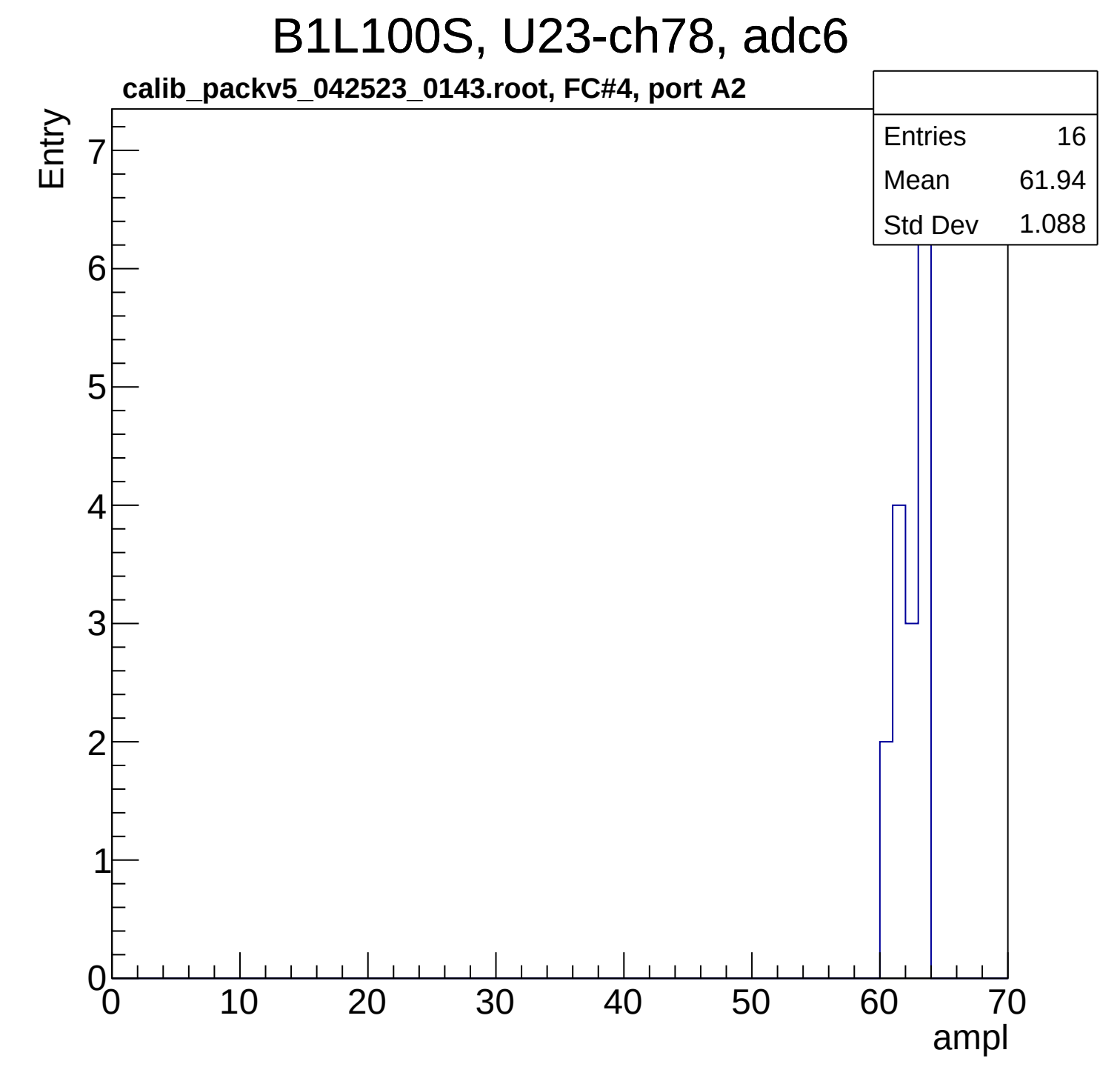

B1L100S, U23-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 57.34
Std Dev 10.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 61.94
Std Dev 1.088
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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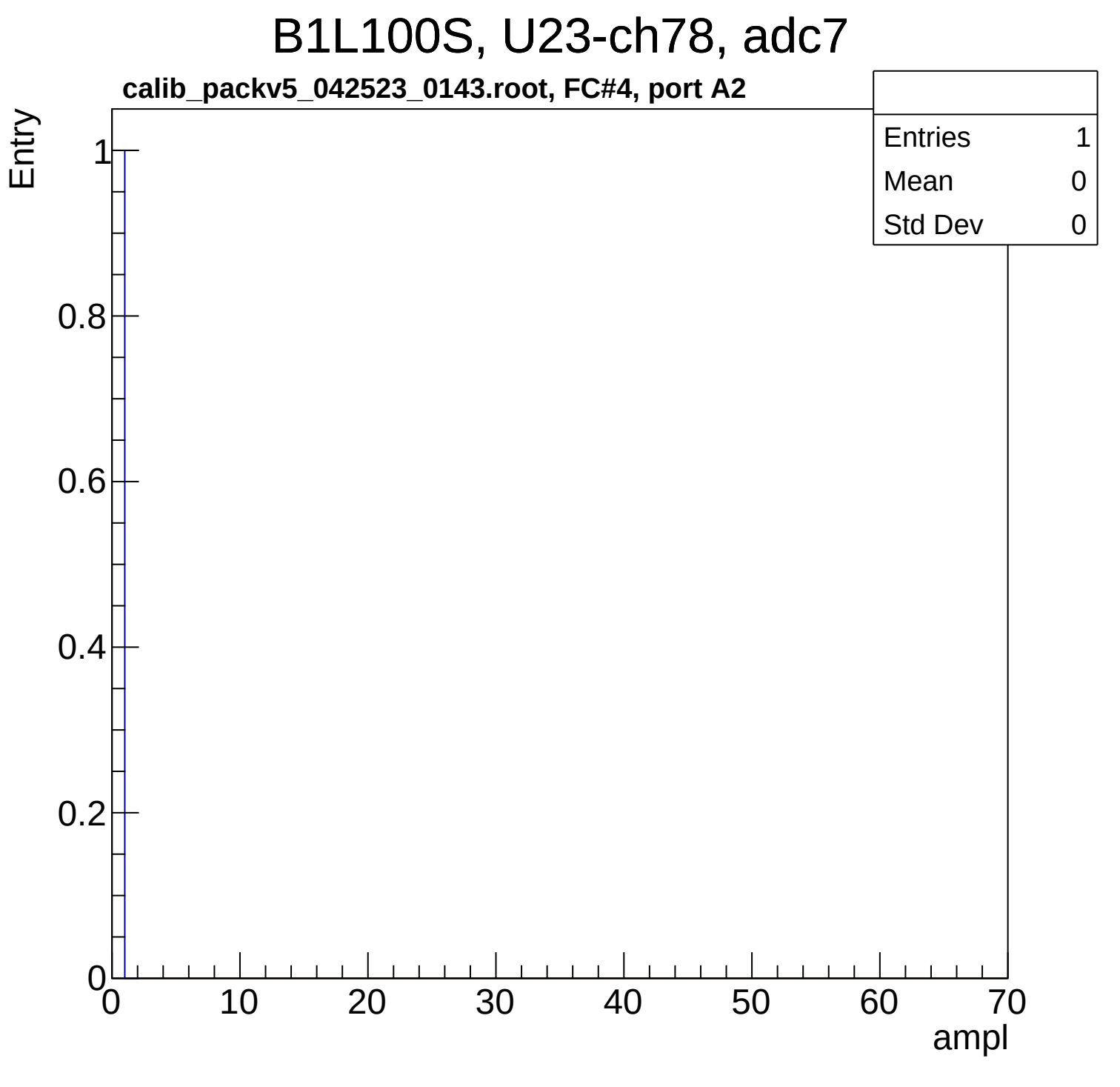
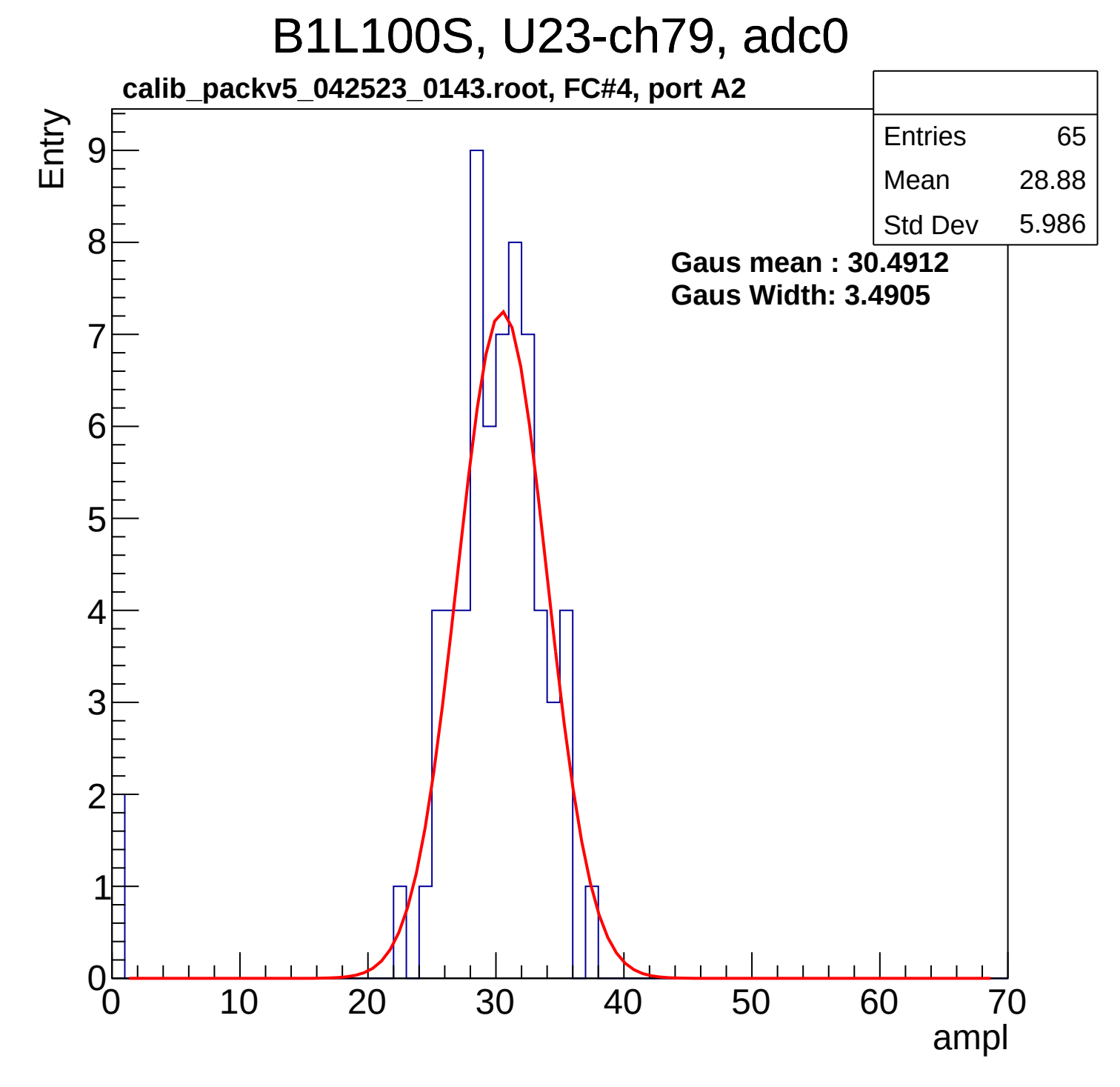
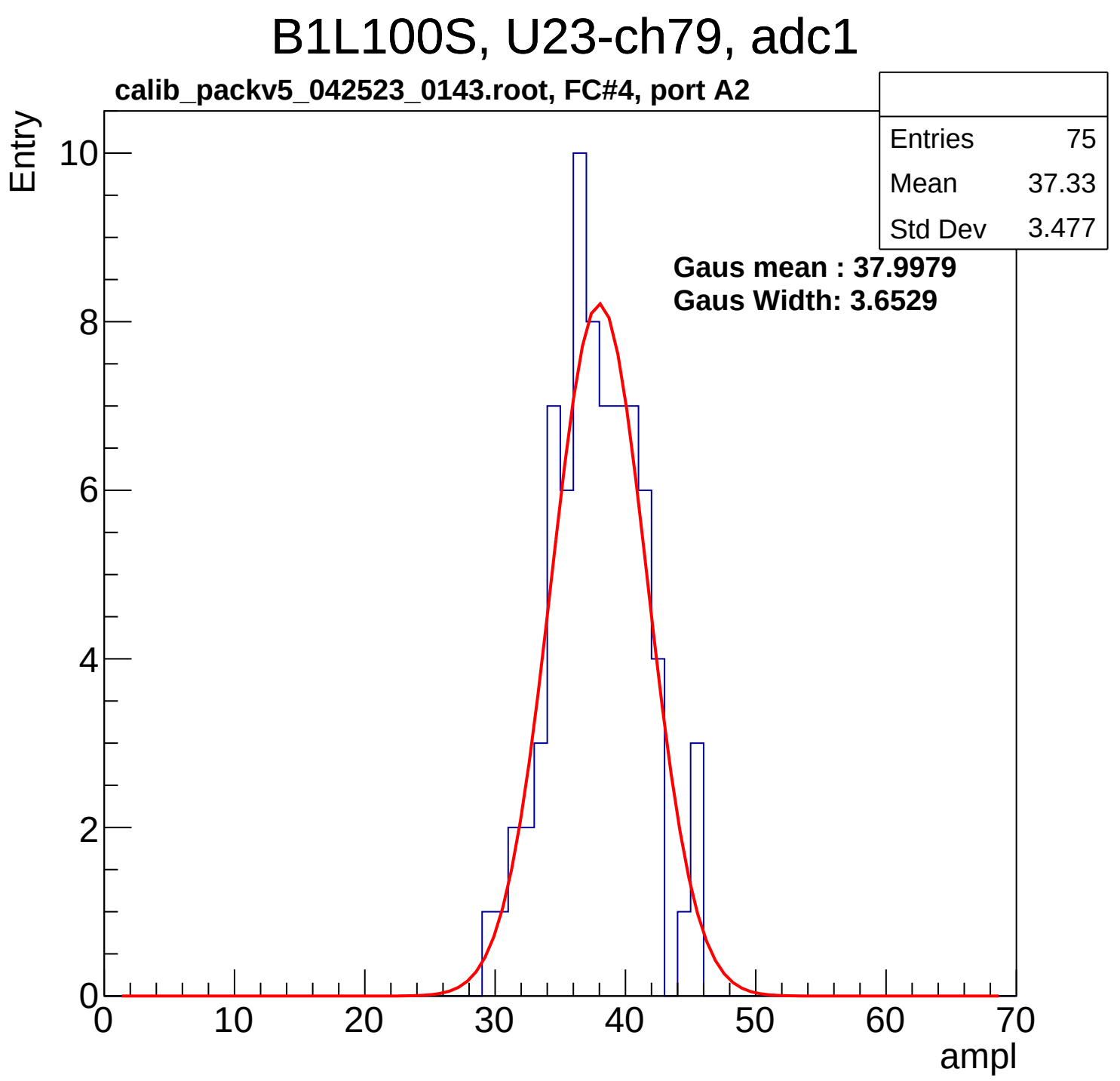
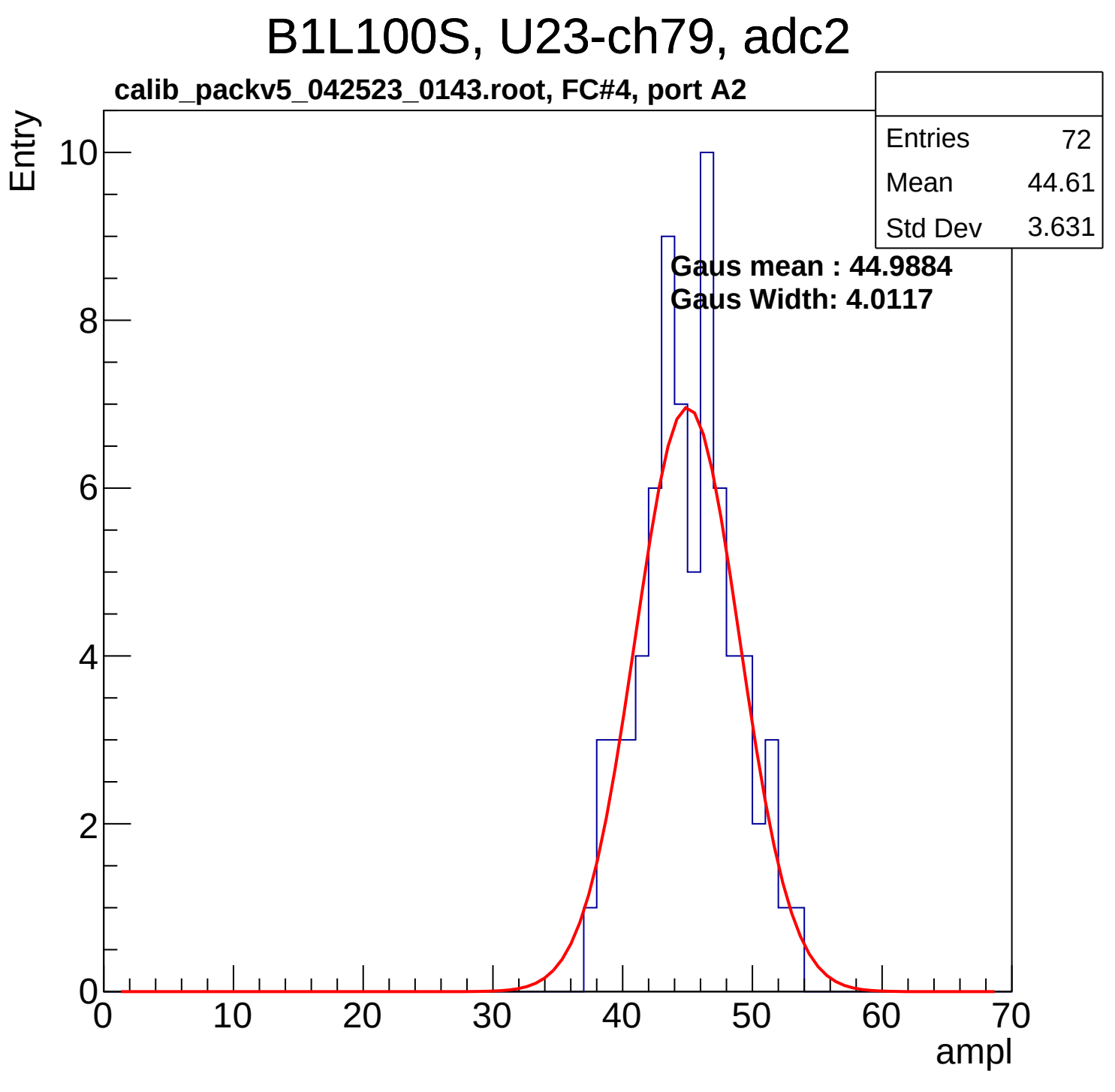
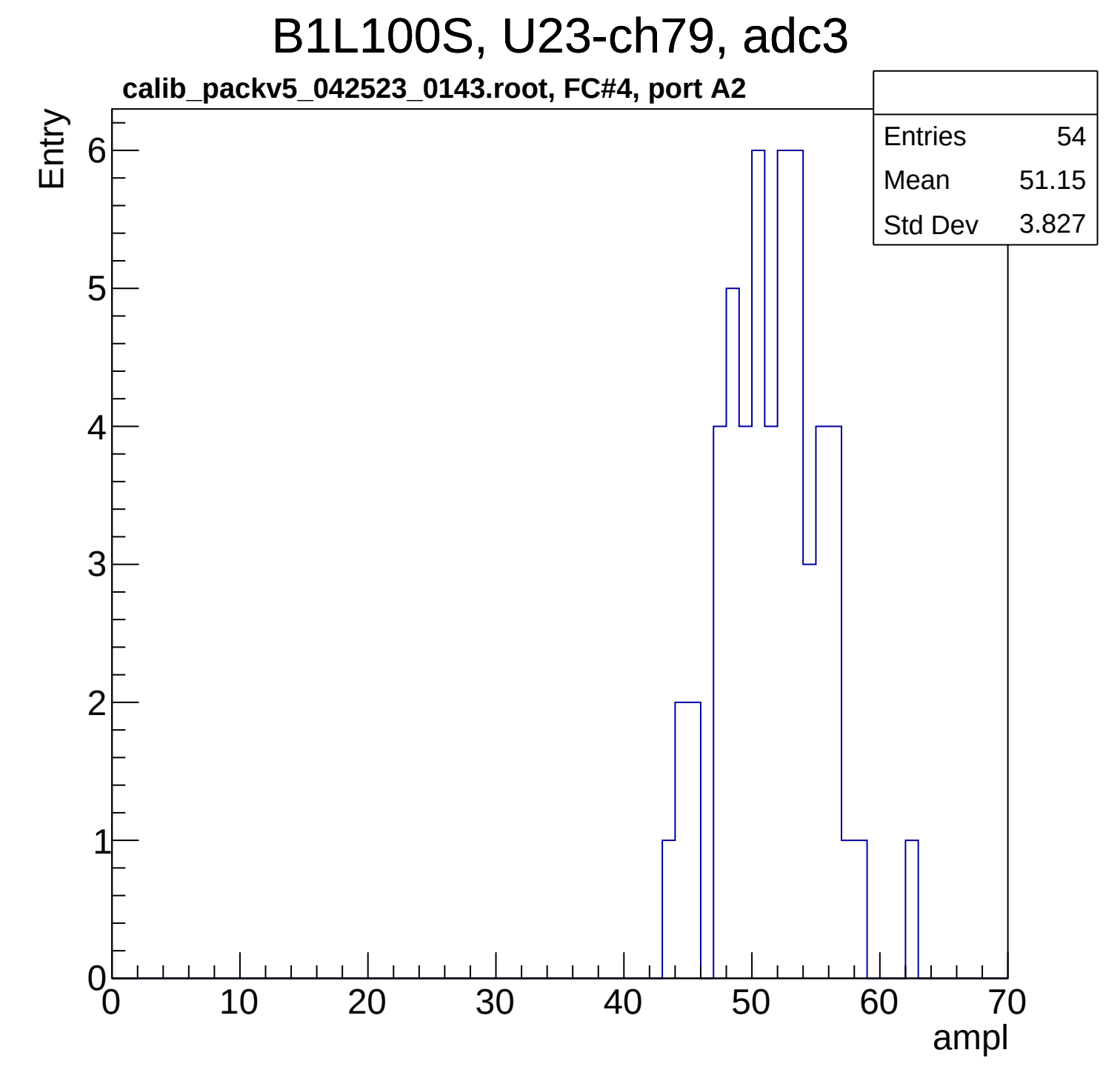
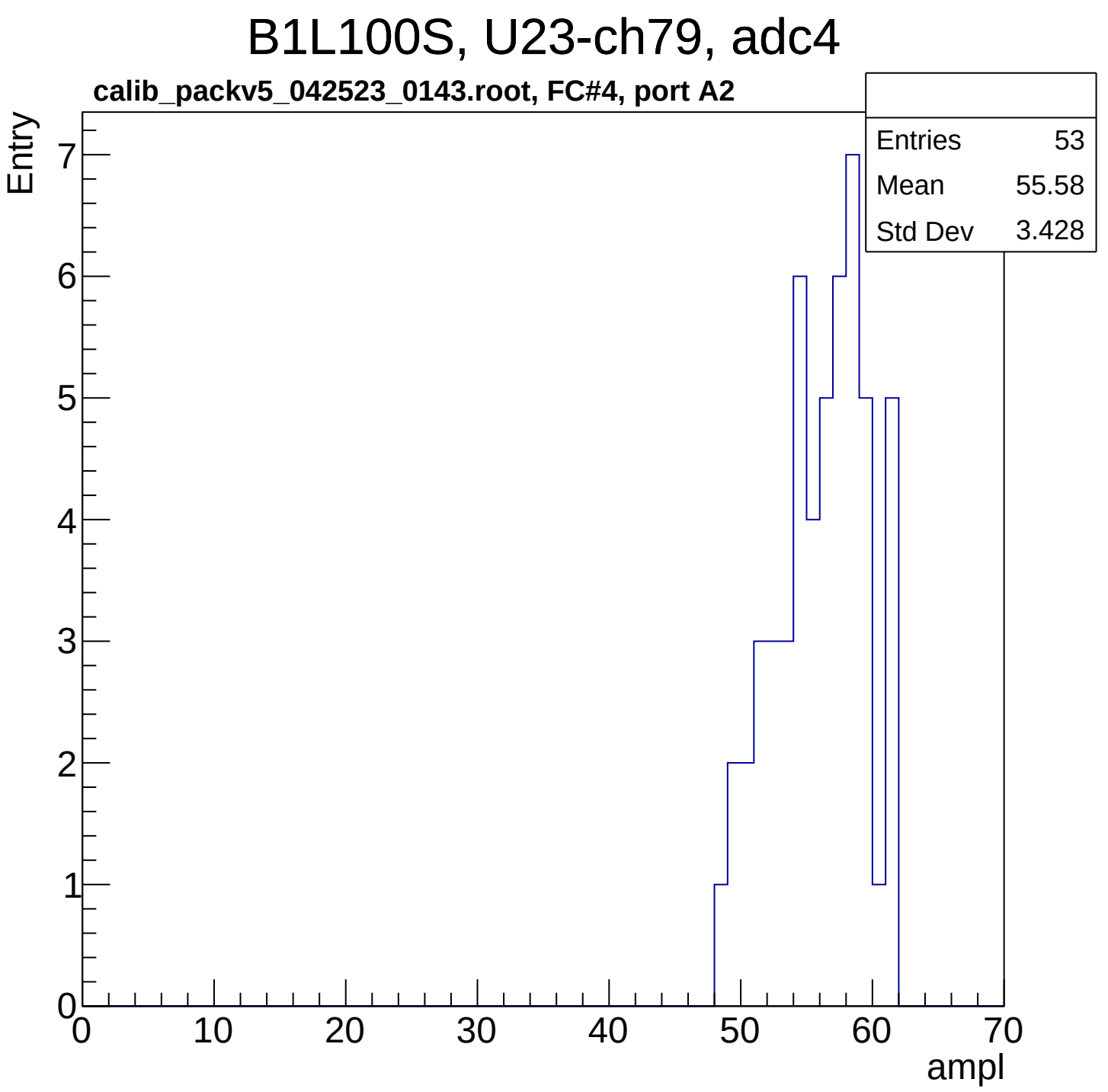

B1L100S, U23-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.88
Std Dev 5.986
Gaus mean : 30.4912
Gaus Width: 3.4905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.33
Std Dev 3.477
Gaus mean : 37.9979
Gaus Width: 3.6529
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.61
Std Dev 3.631
Gaus mean : 44.9884
Gaus Width: 4.0117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 51.15
Std Dev 3.827
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch79, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.58
Std Dev 3.428
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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40
50
60
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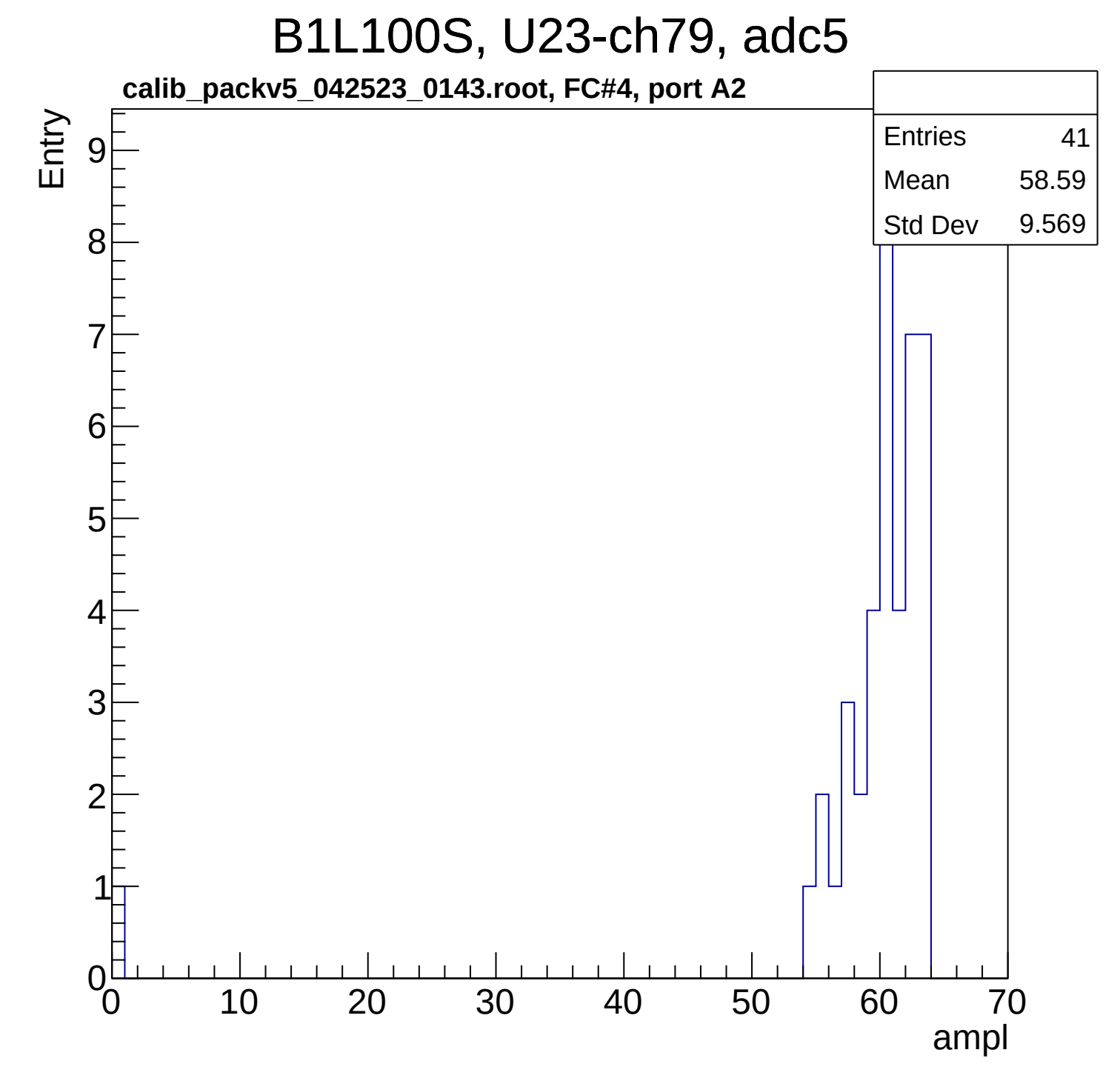
B1L100S, U23-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.59
Std Dev 9.569
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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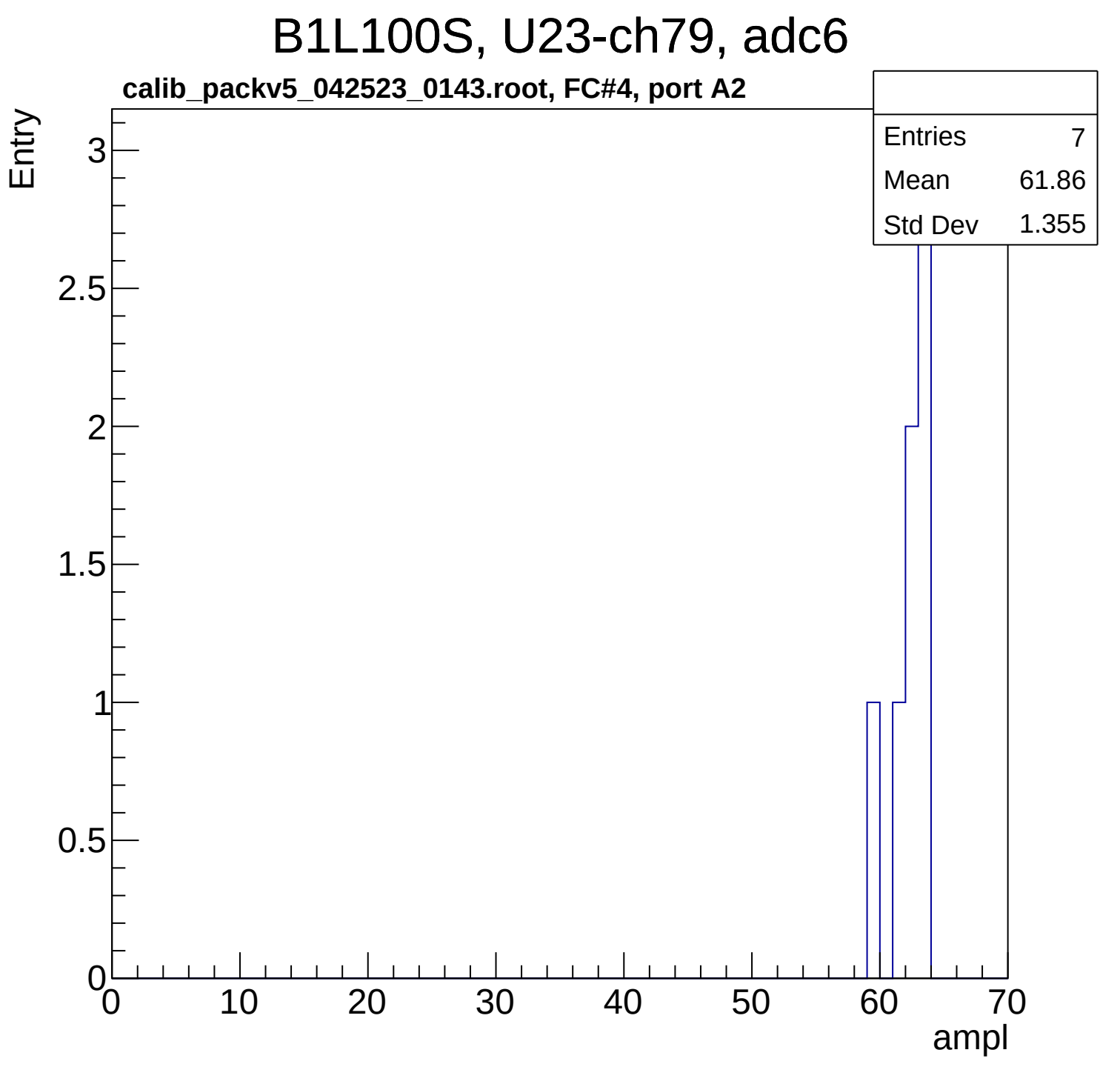
B1L100S, U23-ch79, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 1.355
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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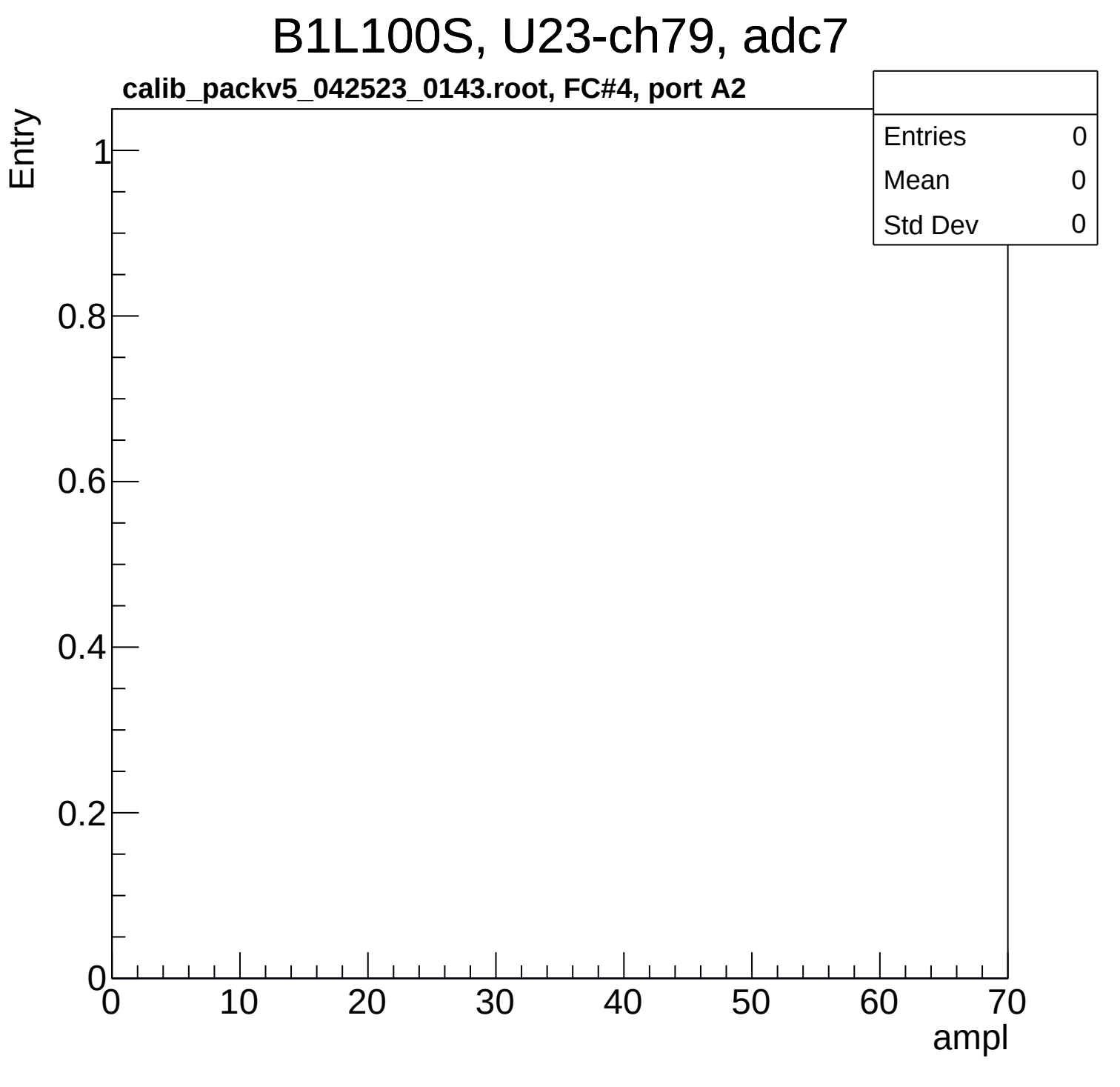
B1L100S, U23-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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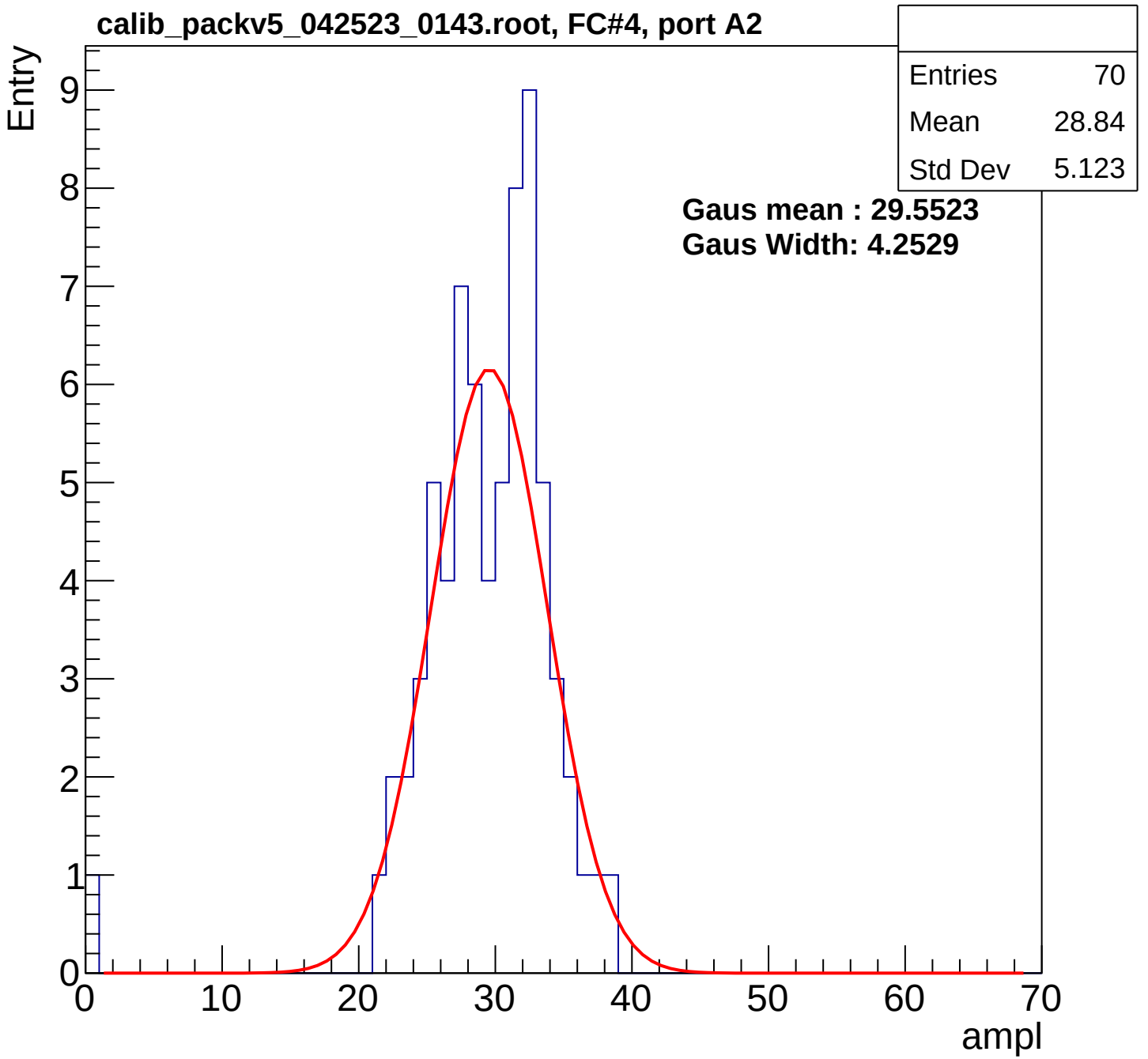
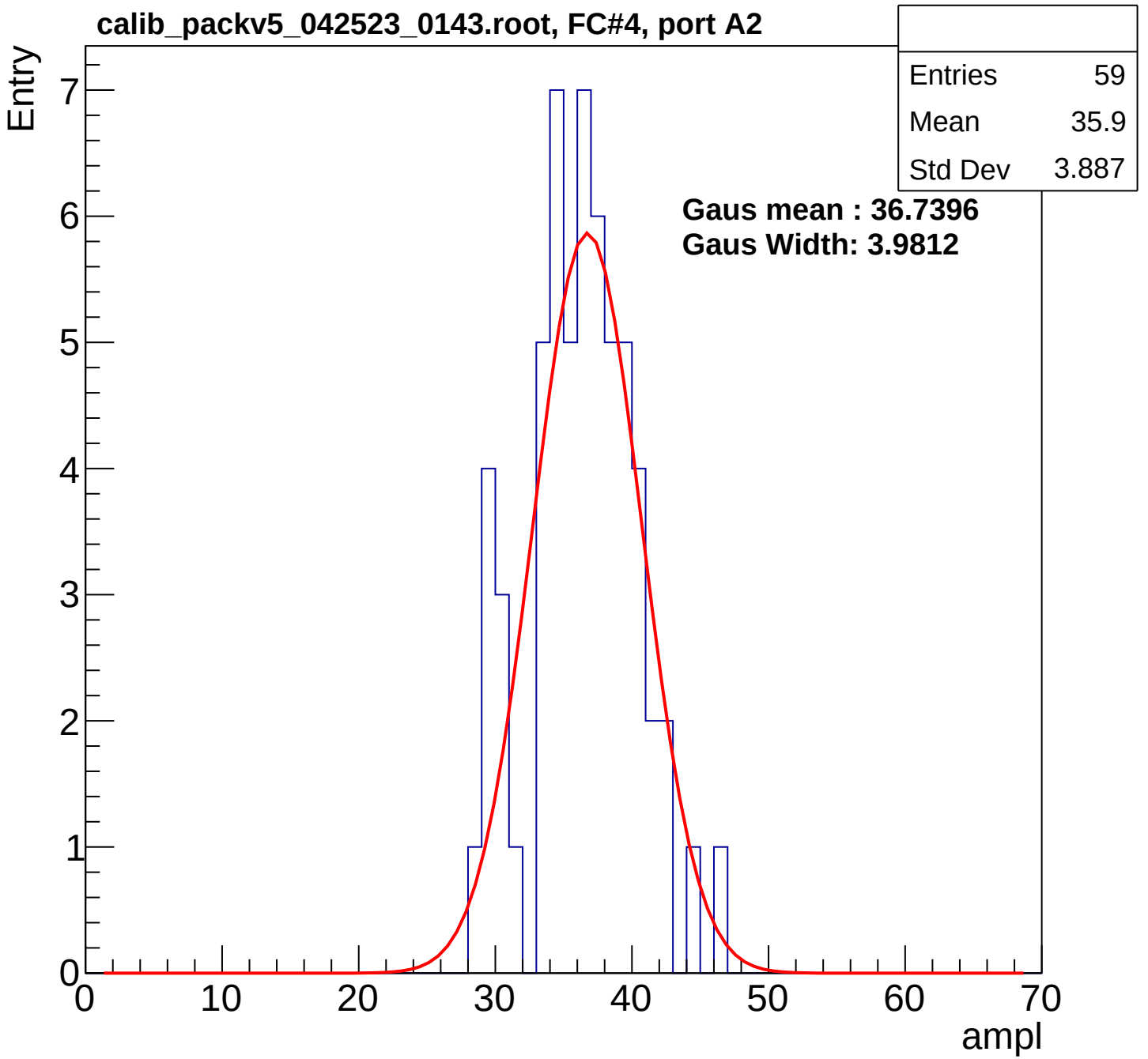
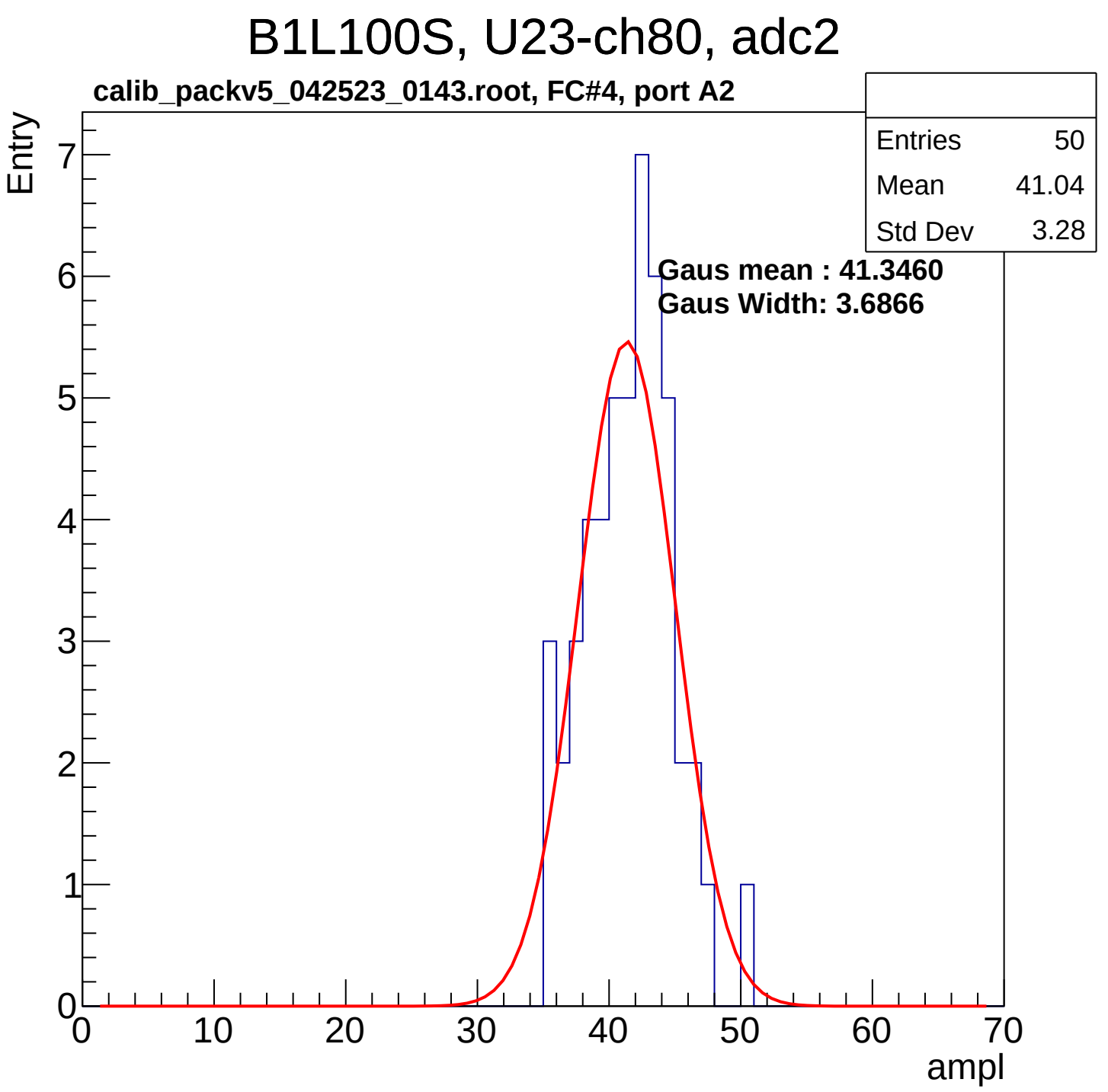
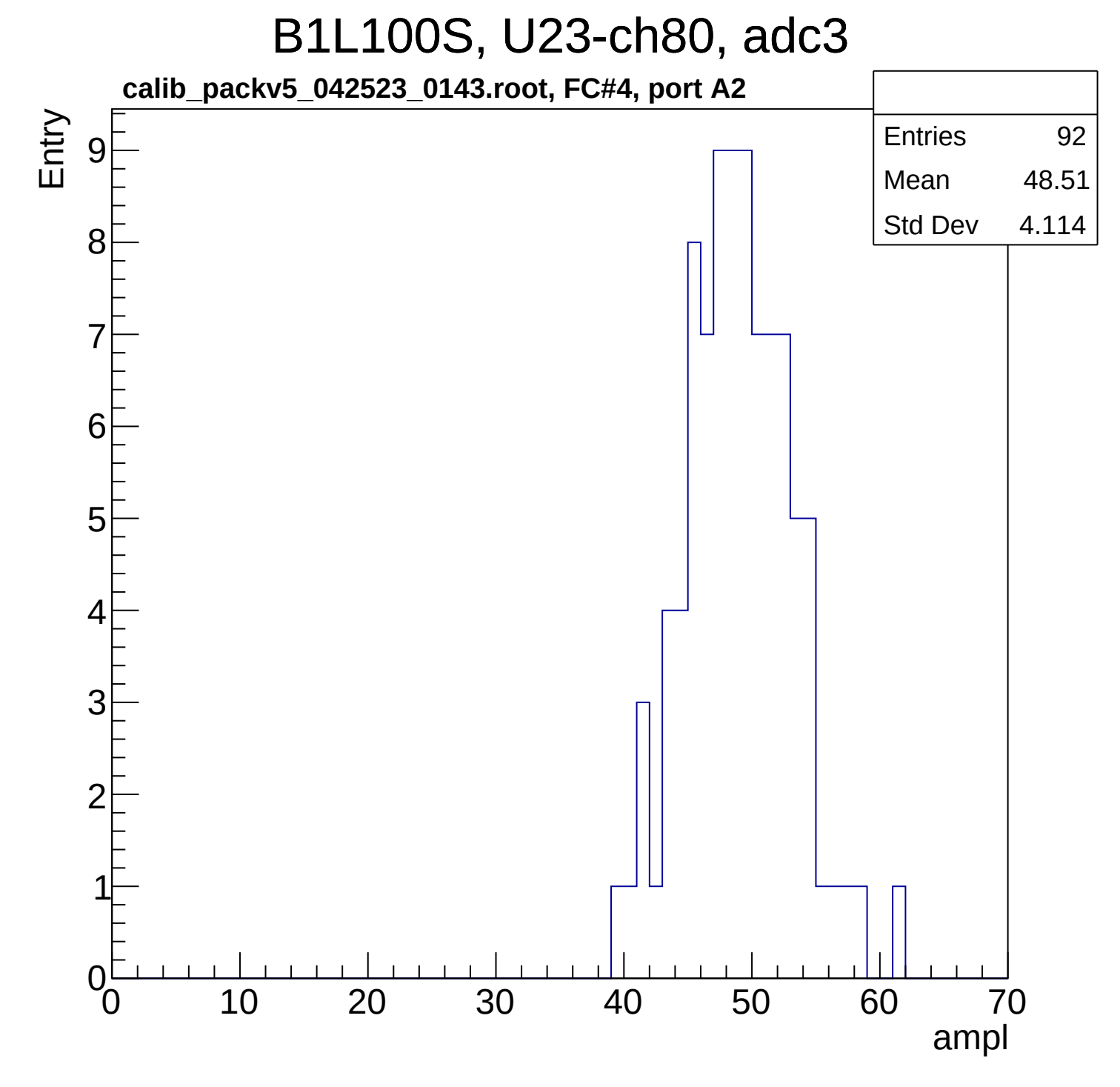
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.84
Std Dev 5.123
Gaus mean : 29.5523
Gaus Width: 4.2529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.9
Std Dev 3.887
Gaus mean : 36.7396
Gaus Width: 3.9812
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 41.04
Std Dev 3.28
Gaus mean : 41.3460
Gaus Width: 3.6866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 92
Mean 48.51
Std Dev 4.114
Entry
ampl
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3
4
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6
7
8
9
0
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20
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40
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60
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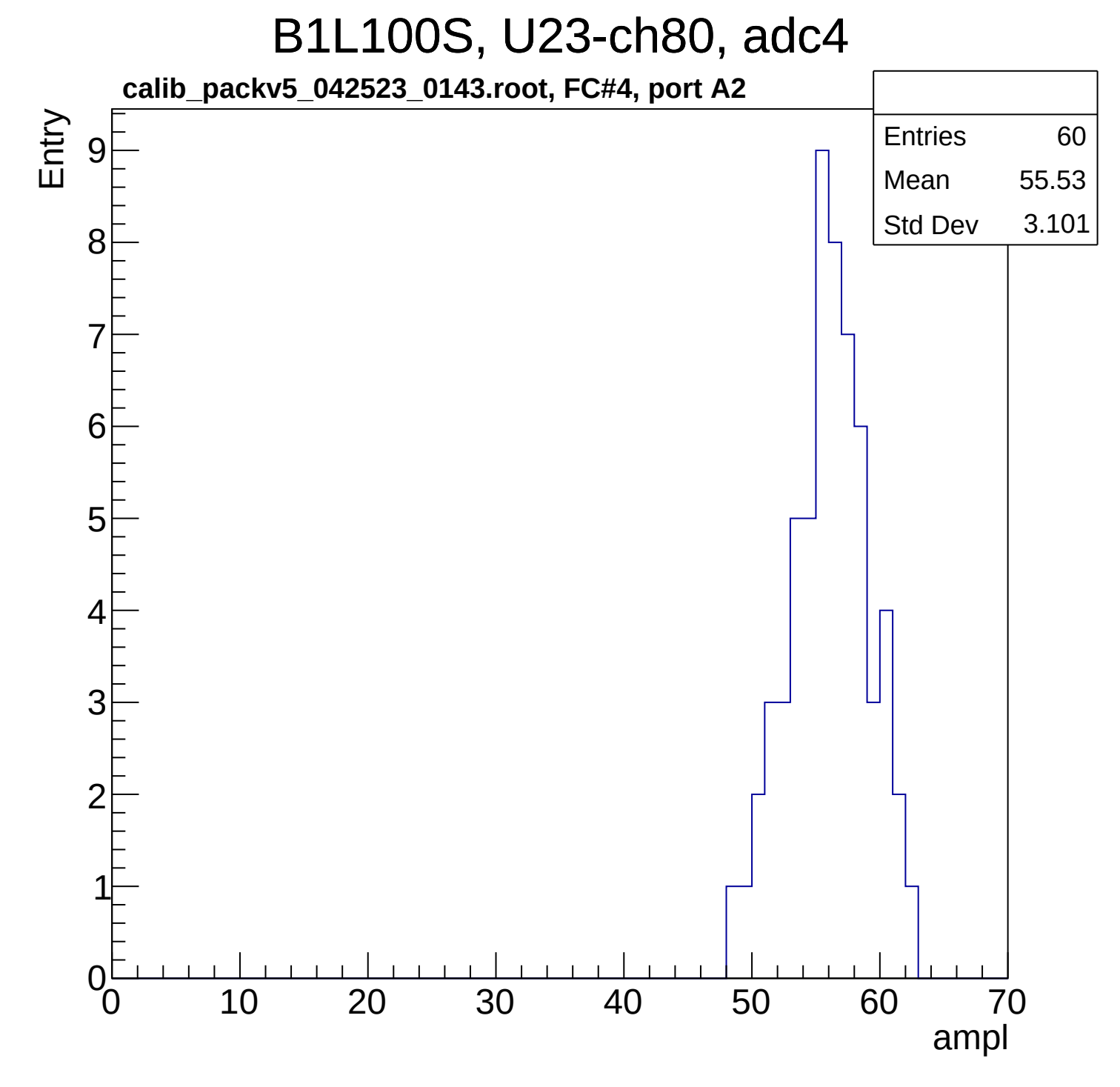
B1L100S, U23-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.53
Std Dev 3.101
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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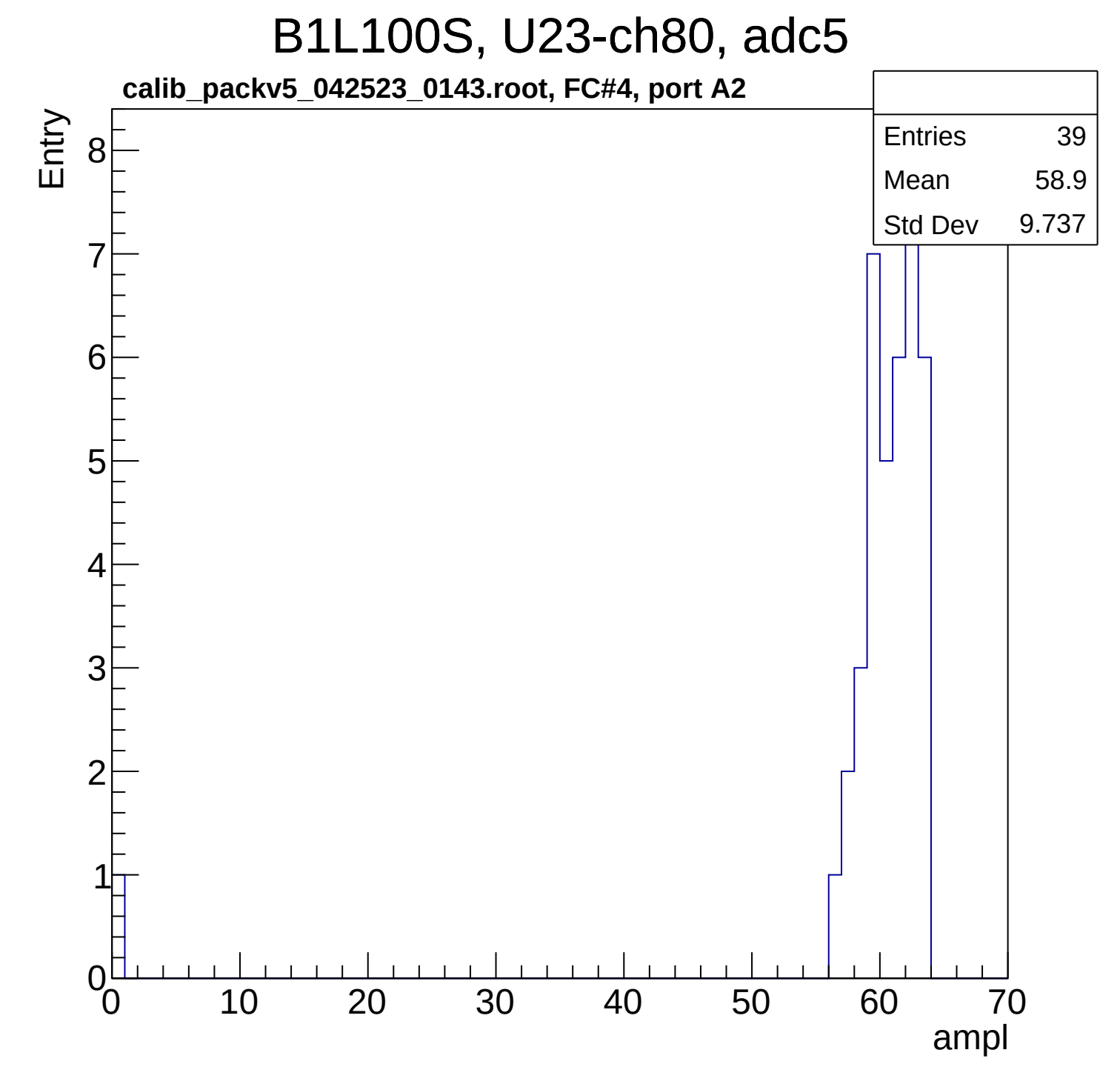
B1L100S, U23-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.9
Std Dev 9.737
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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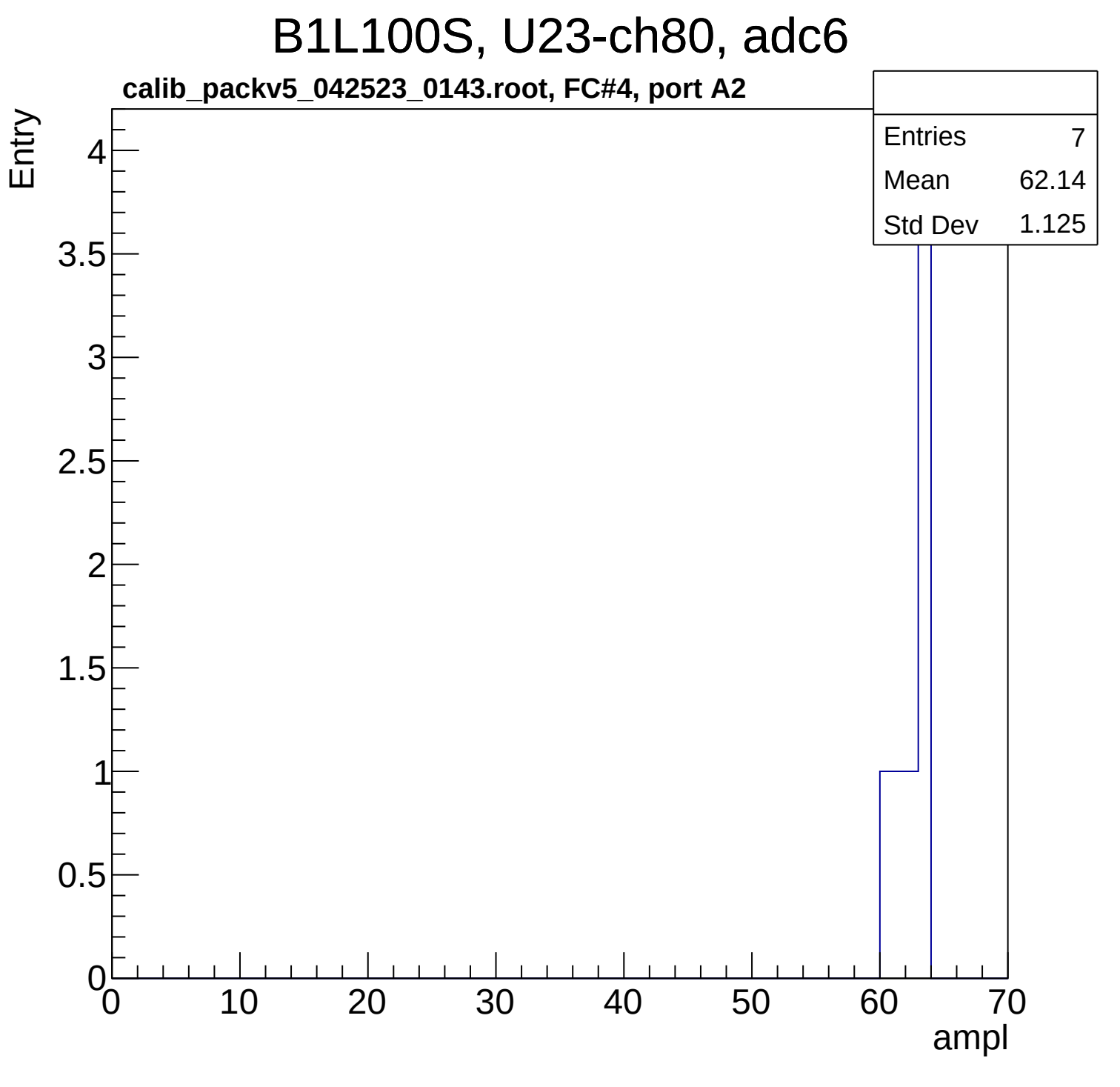
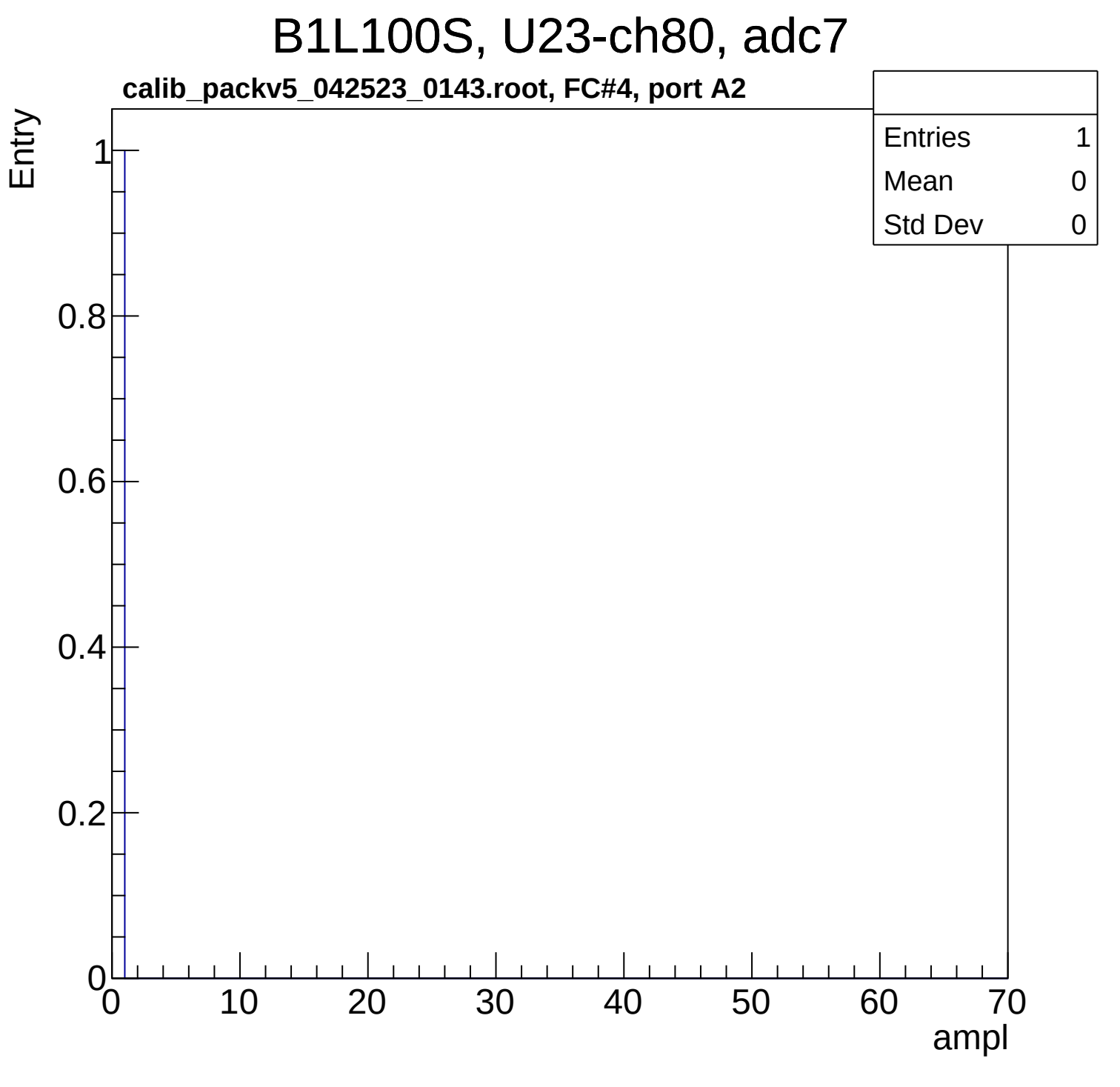
40
50
60
70


B1L100S, U23-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U23-ch80, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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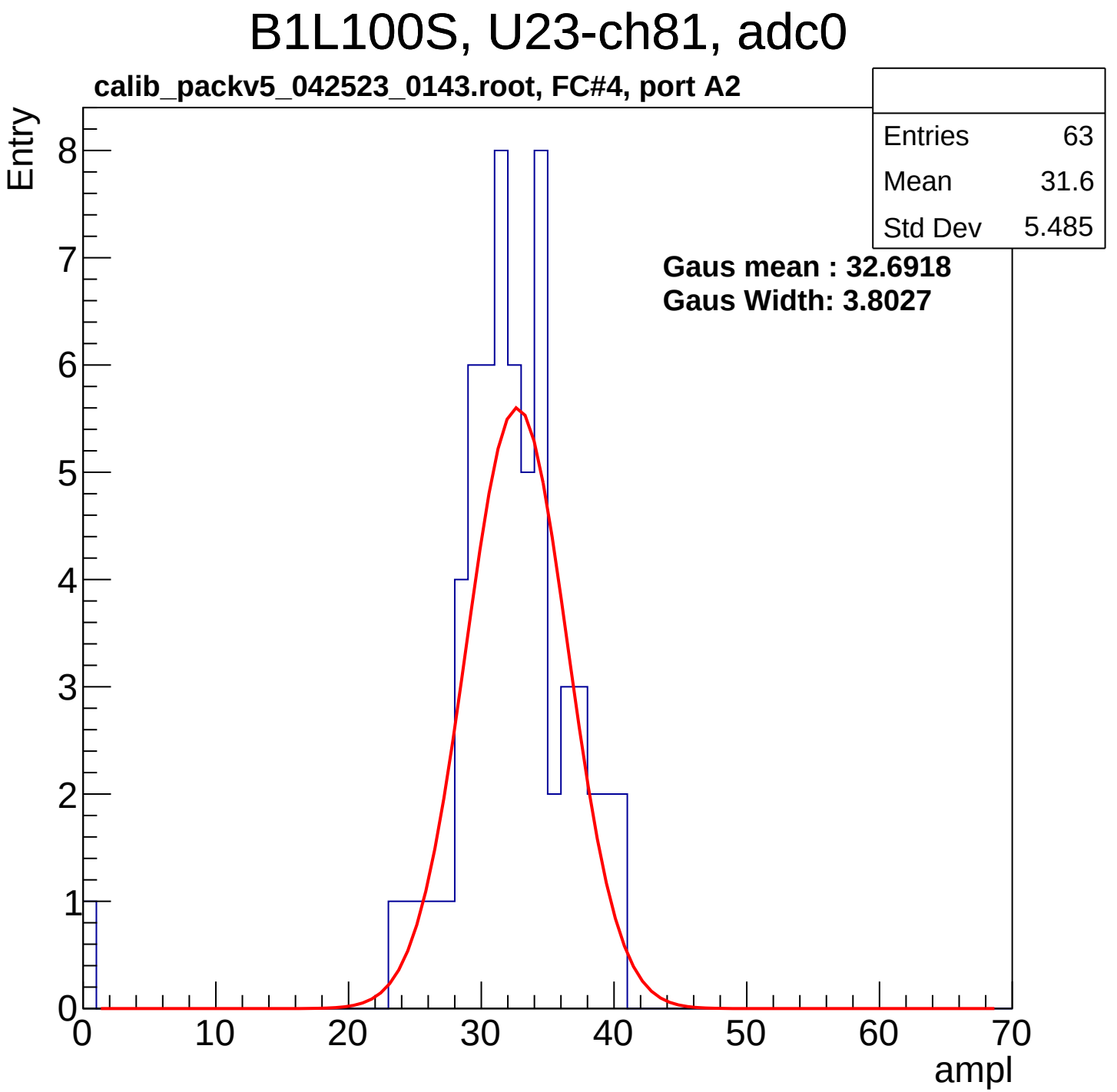
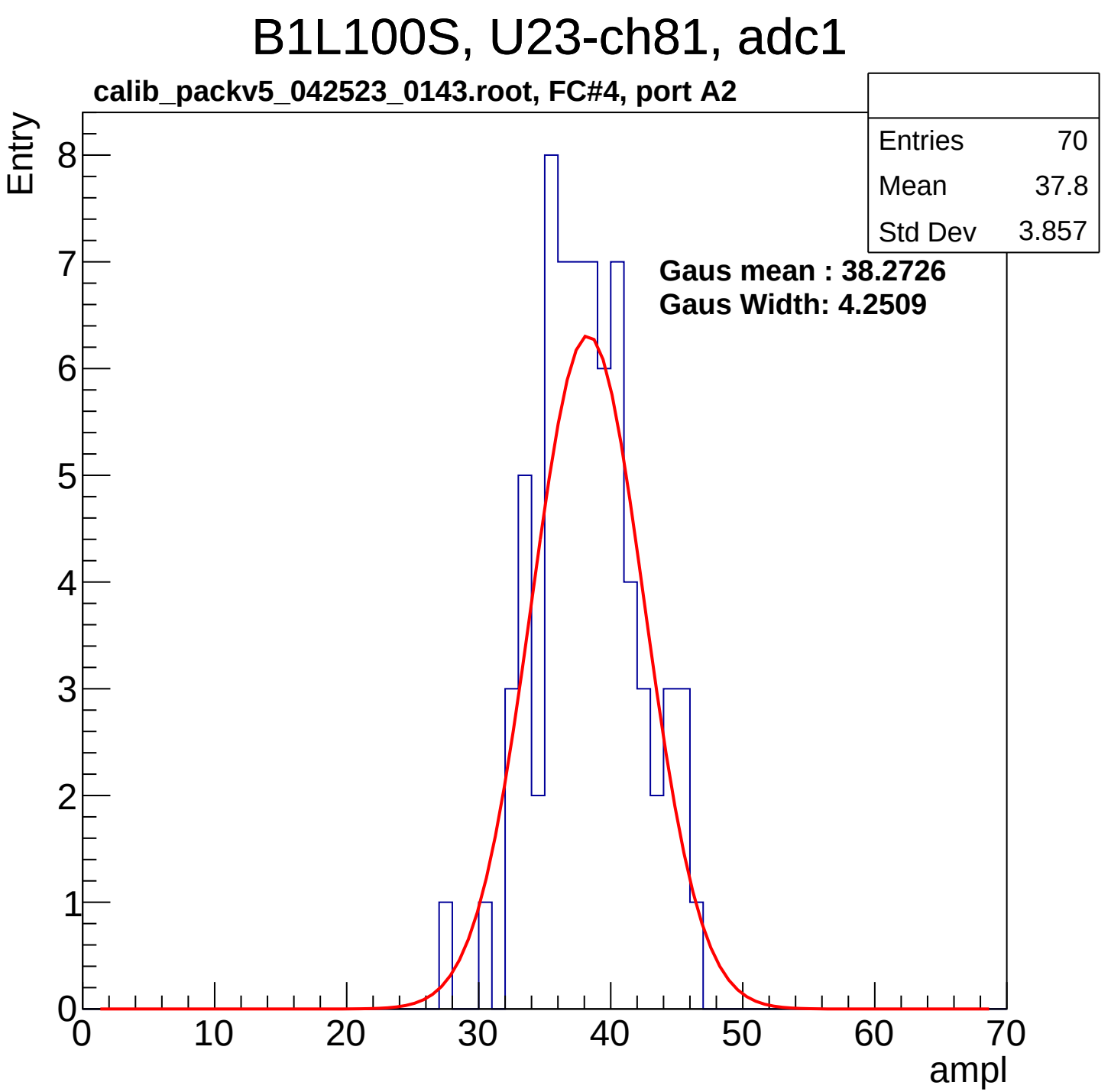
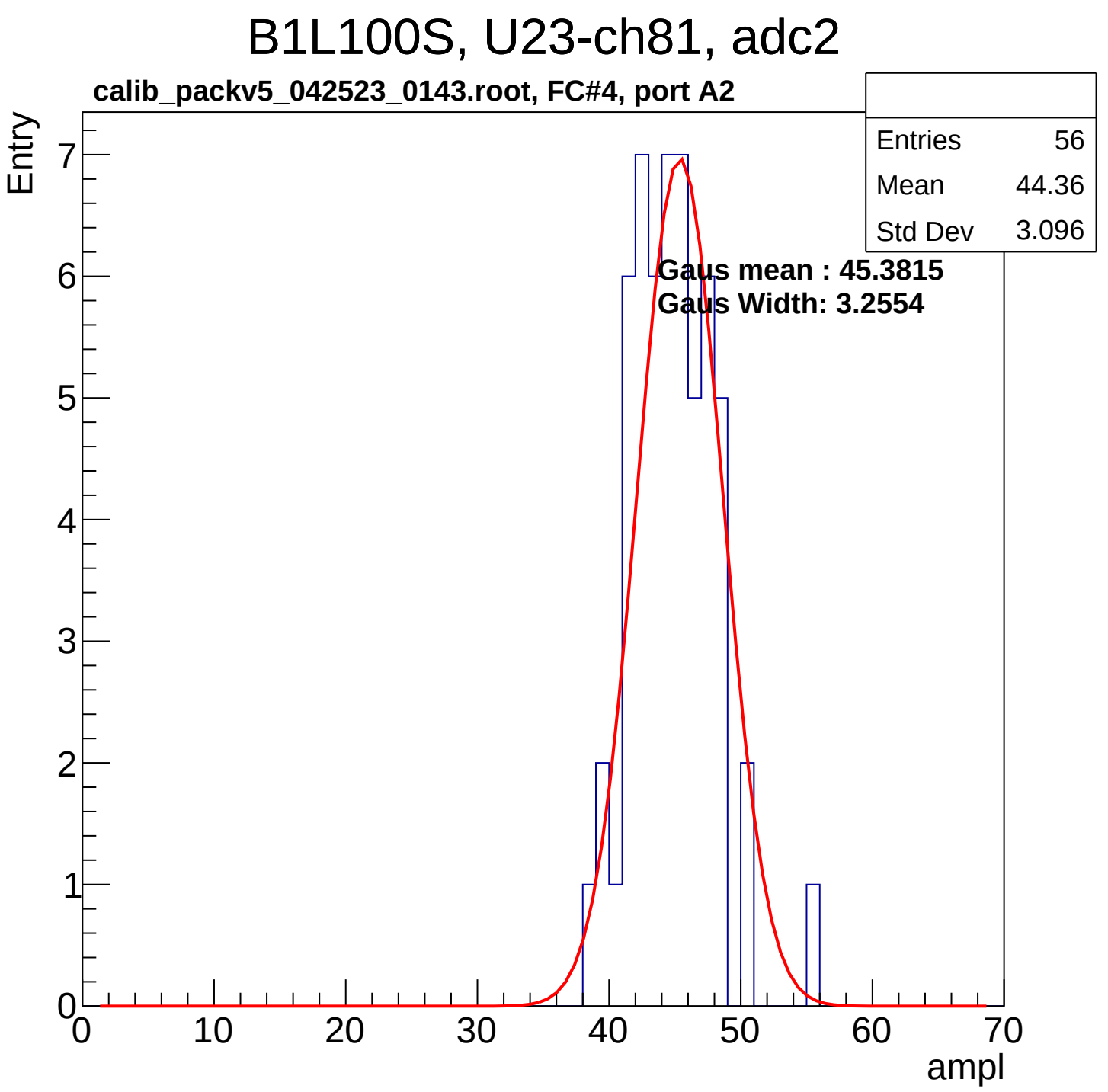
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.6
Std Dev 5.485
Gaus mean : 32.6918
Gaus Width: 3.8027
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.8
Std Dev 3.857
Gaus mean : 38.2726
Gaus Width: 4.2509
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.36
Std Dev 3.096
Gaus mean : 45.3815
Gaus Width: 3.2554
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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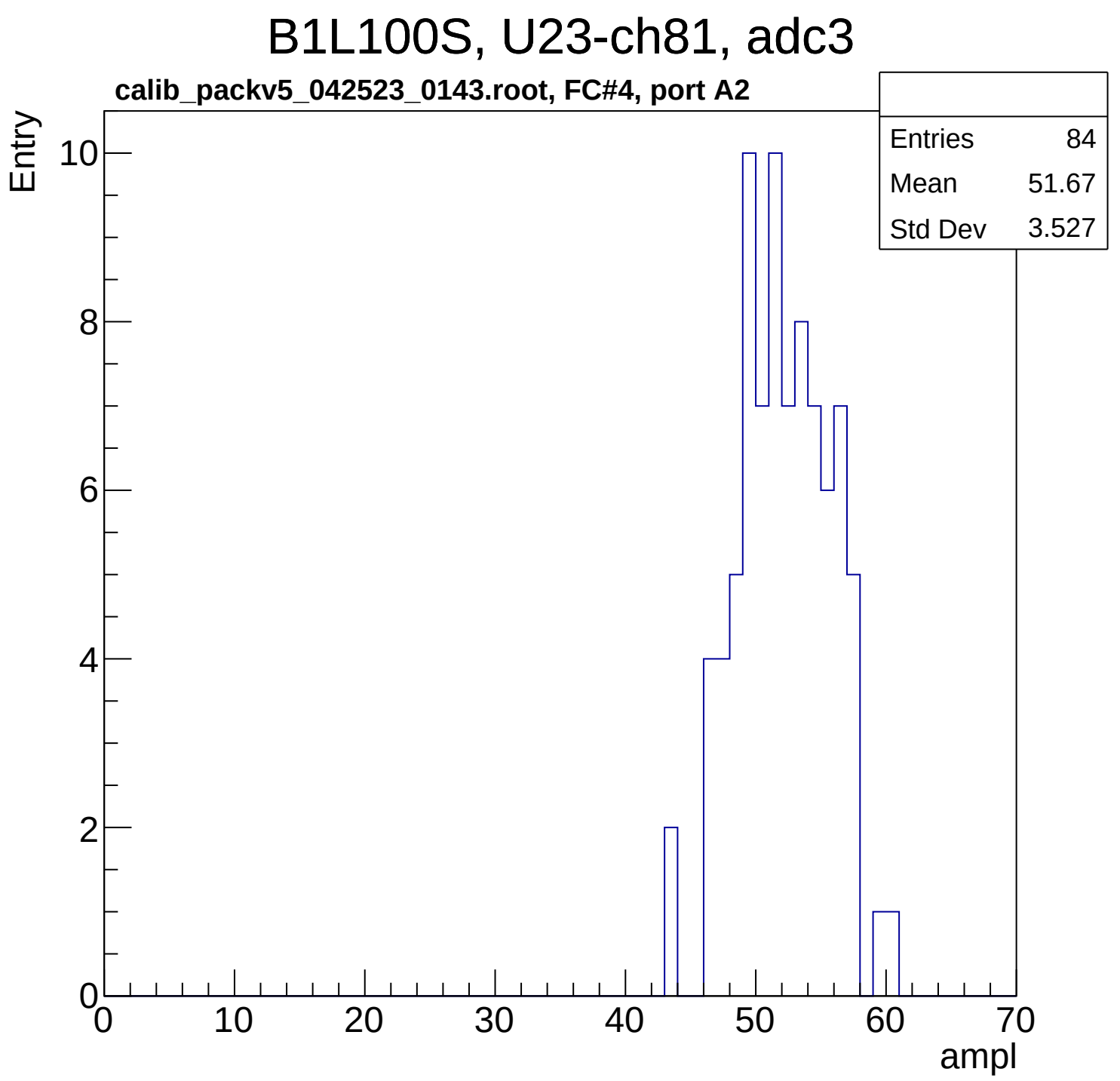
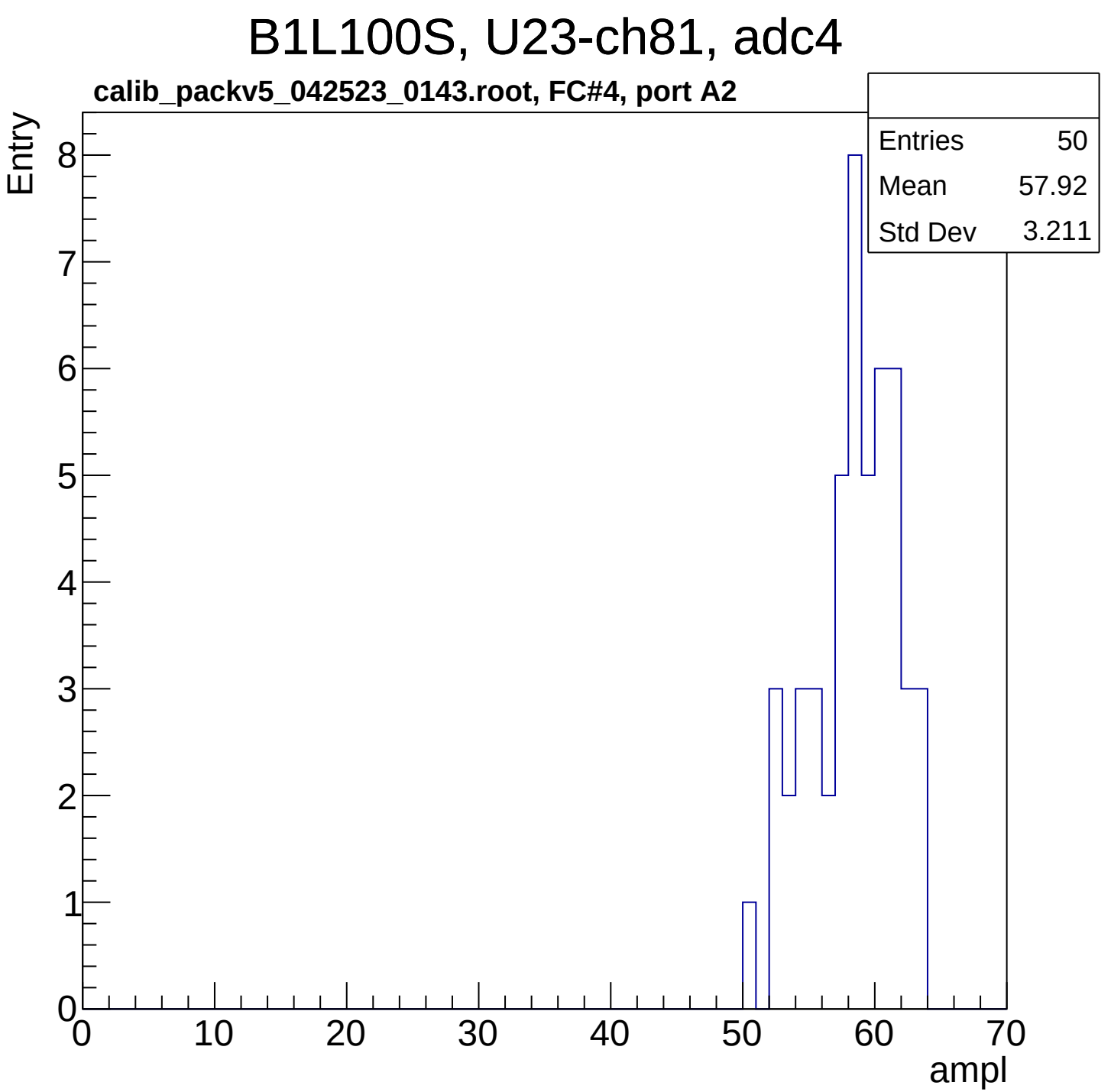

B1L100S, U23-ch81, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 51.67
Std Dev 3.527
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch81, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.92
Std Dev 3.211
Entry
ampl
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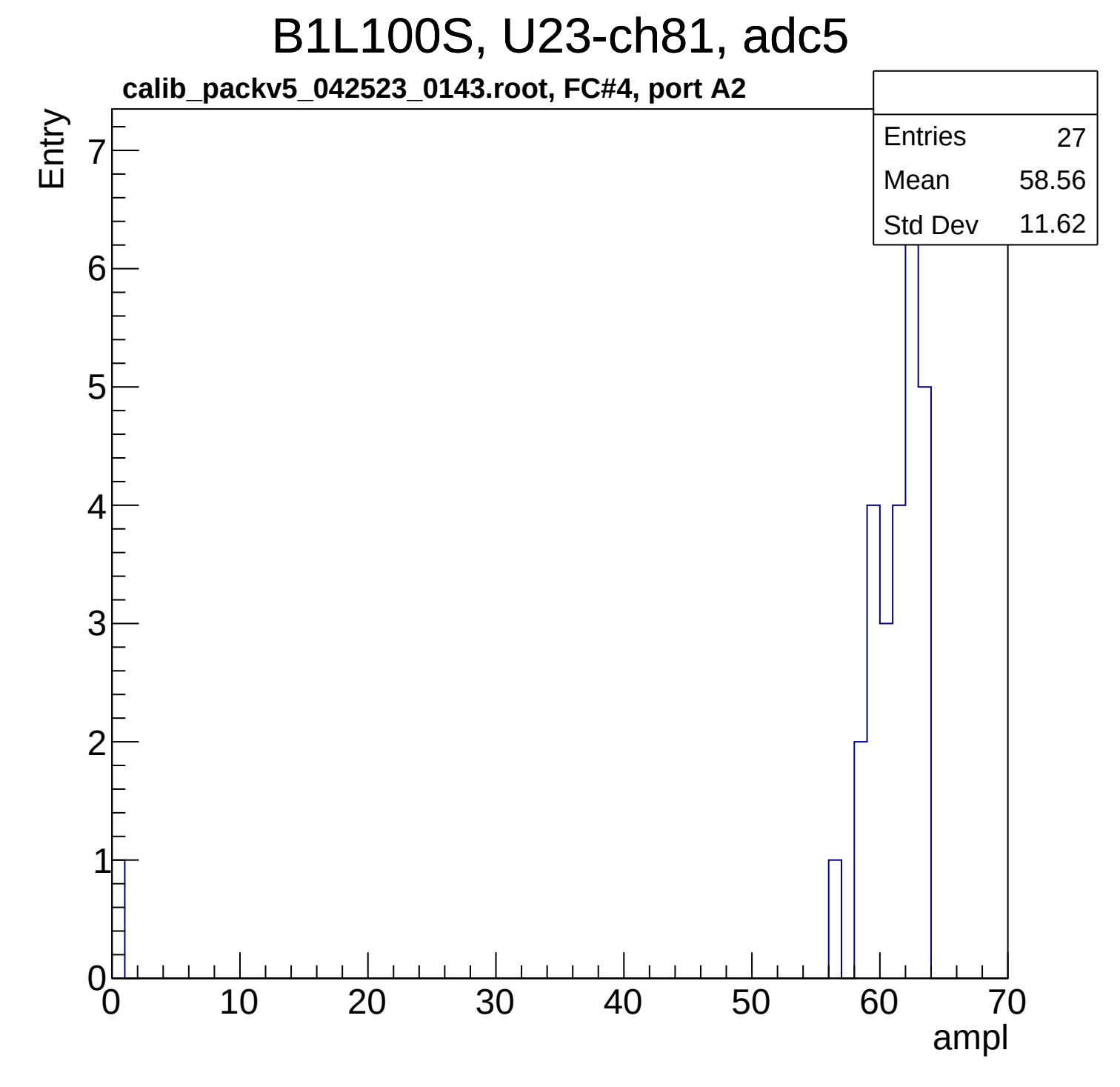
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.56
Std Dev 11.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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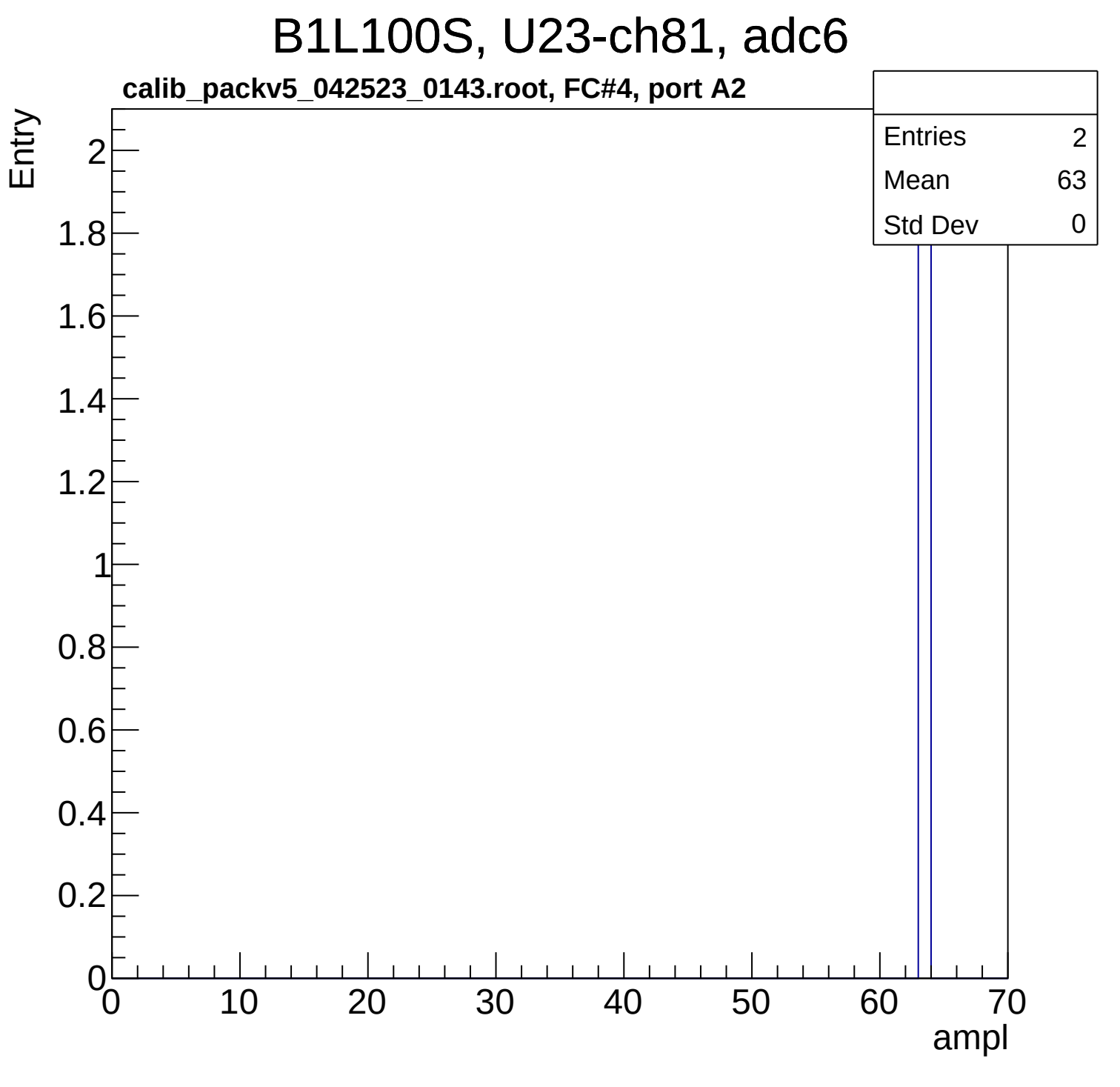
B1L100S, U23-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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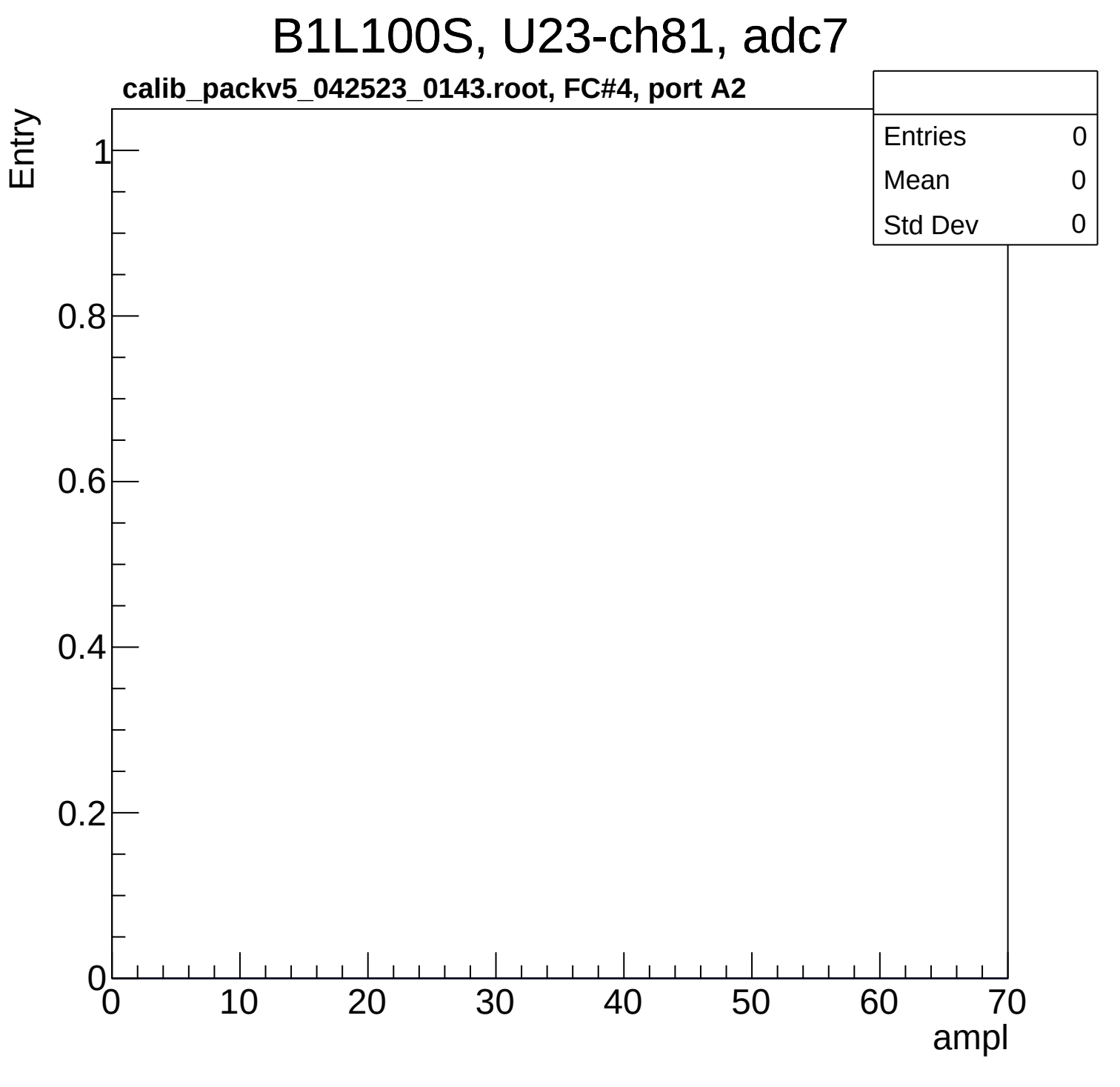
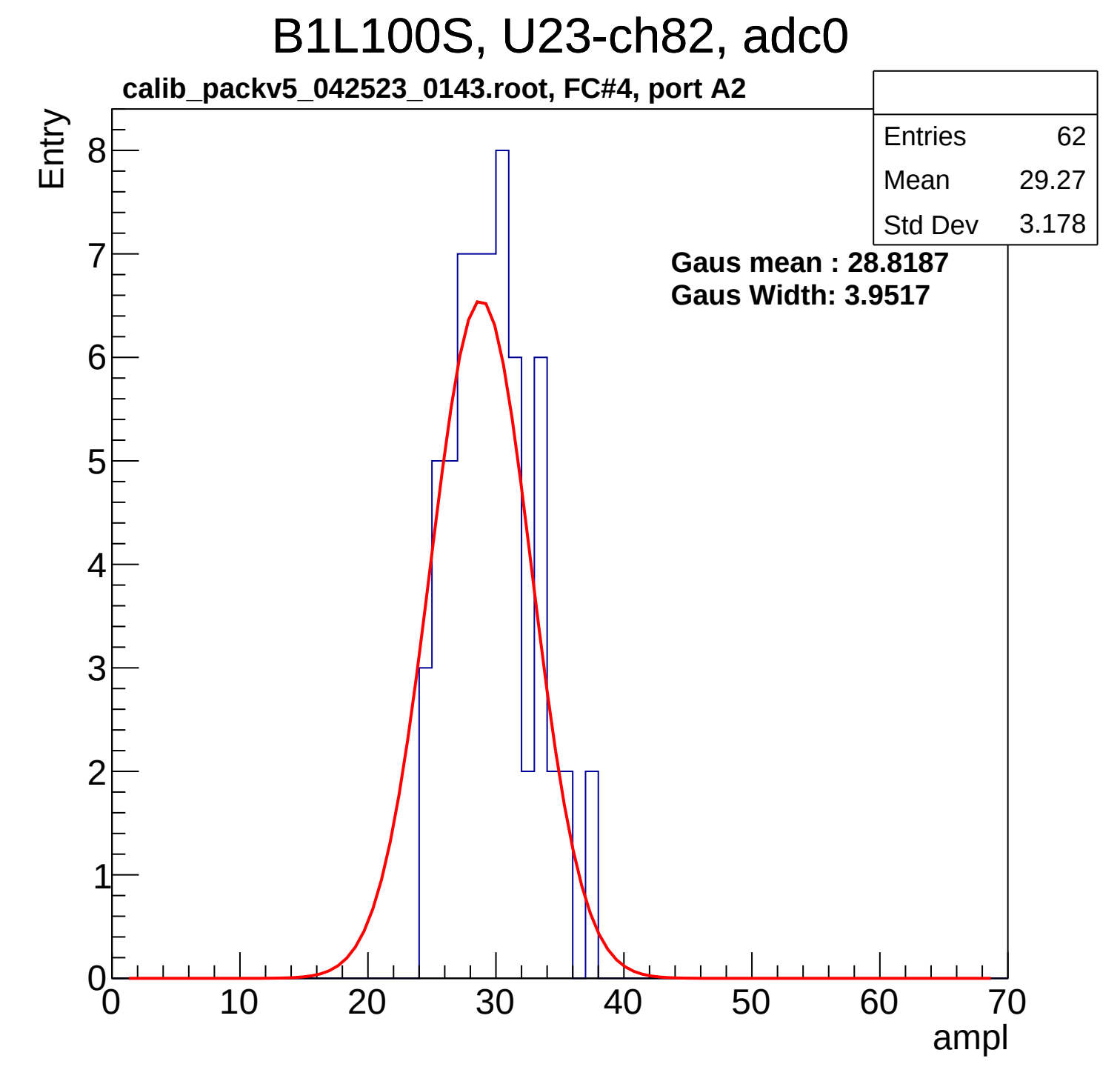
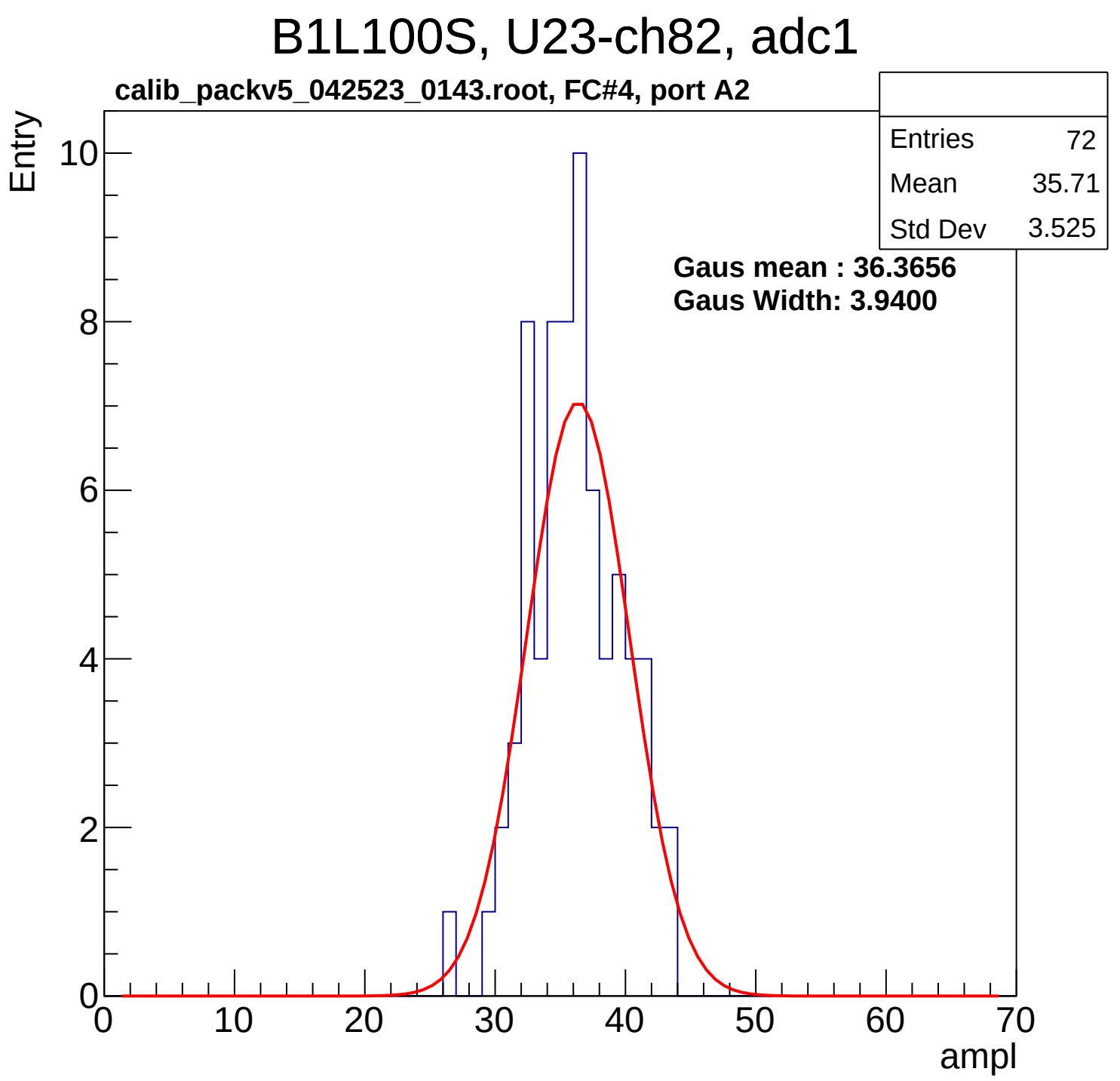
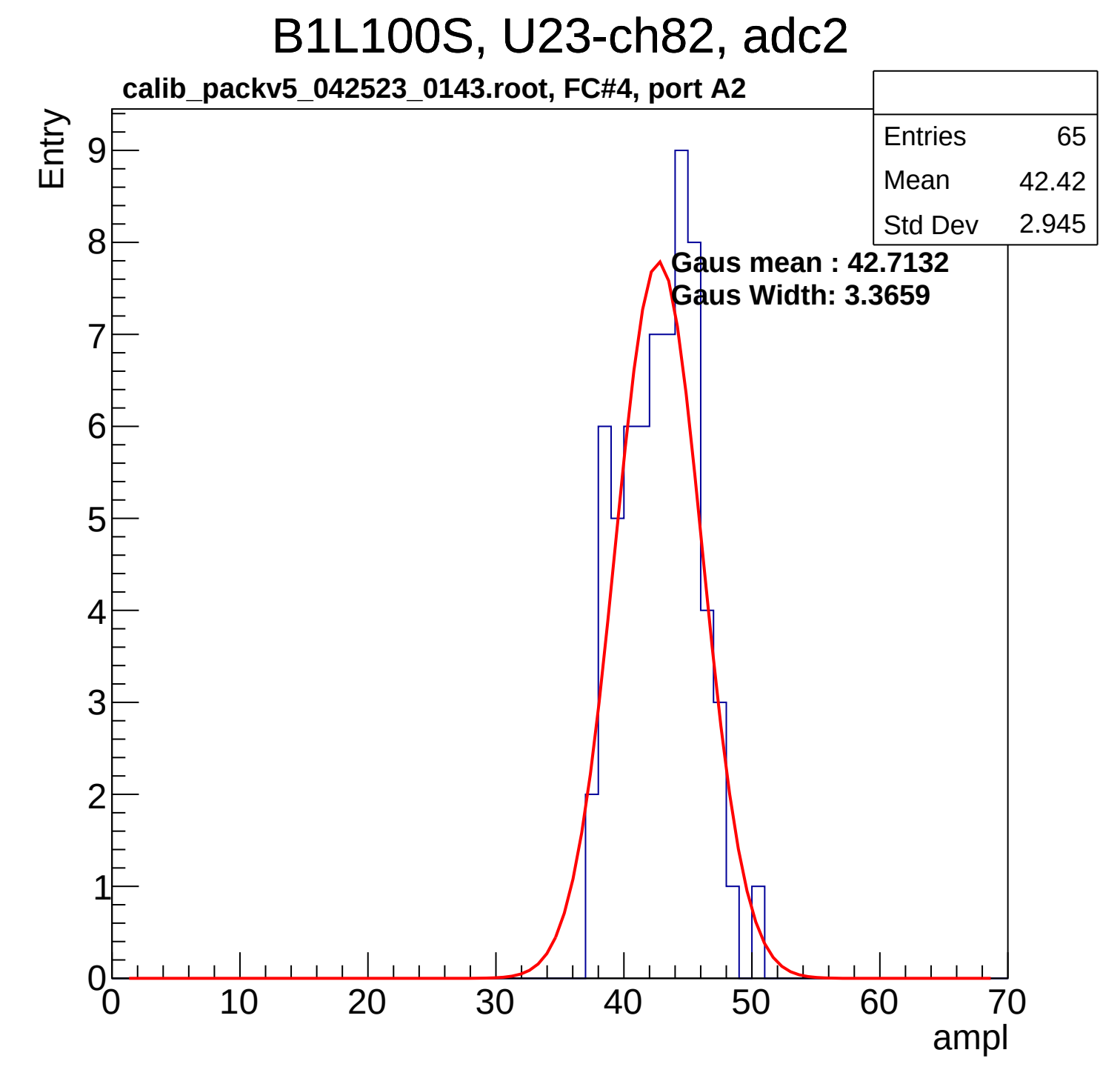
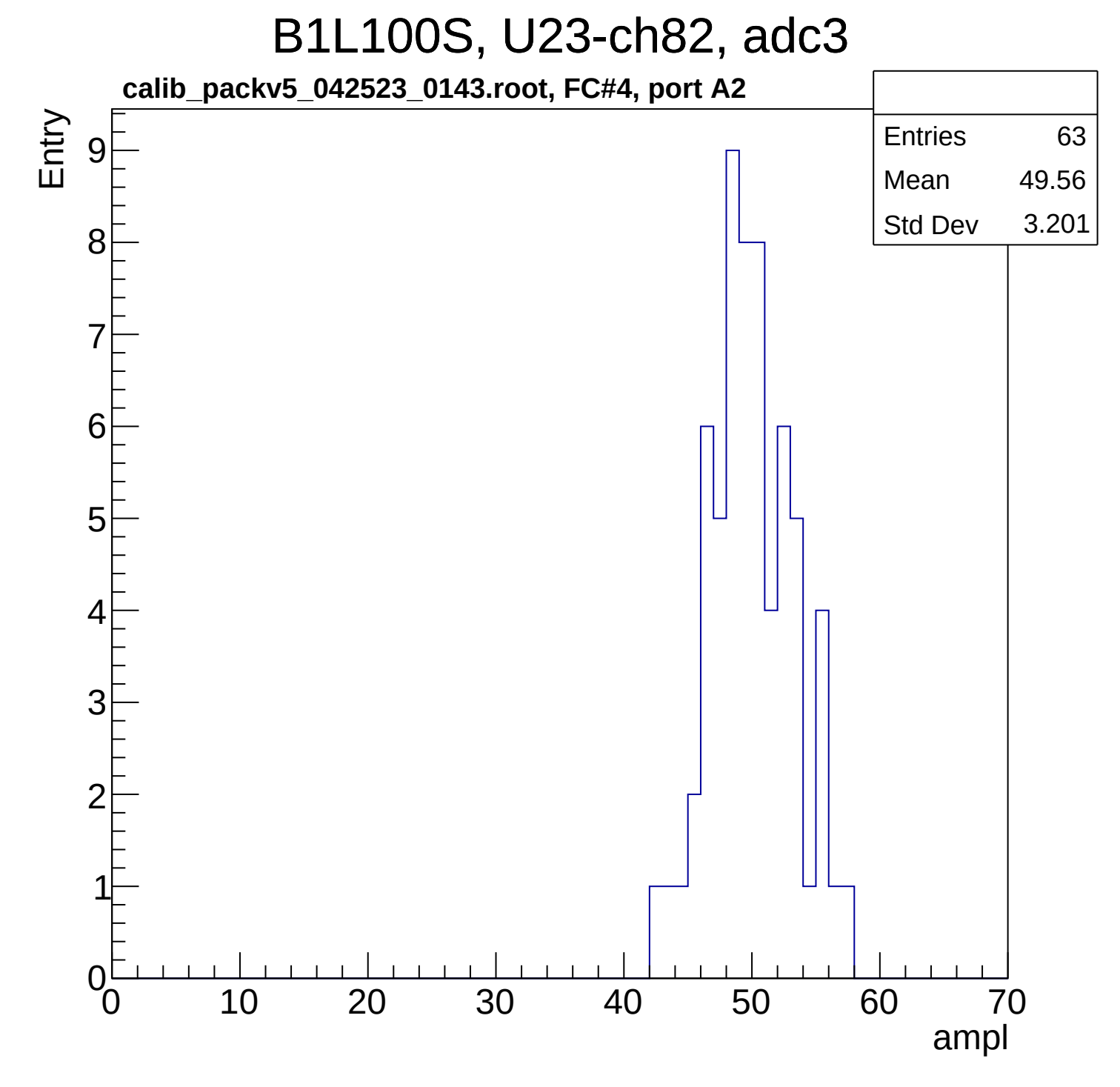

B1L100S, U23-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.27
Std Dev 3.178
Gaus mean : 28.8187
Gaus Width: 3.9517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 35.71
Std Dev 3.525
Gaus mean : 36.3656
Gaus Width: 3.9400
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.42
Std Dev 2.945
Gaus mean : 42.7132
Gaus Width: 3.3659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.56
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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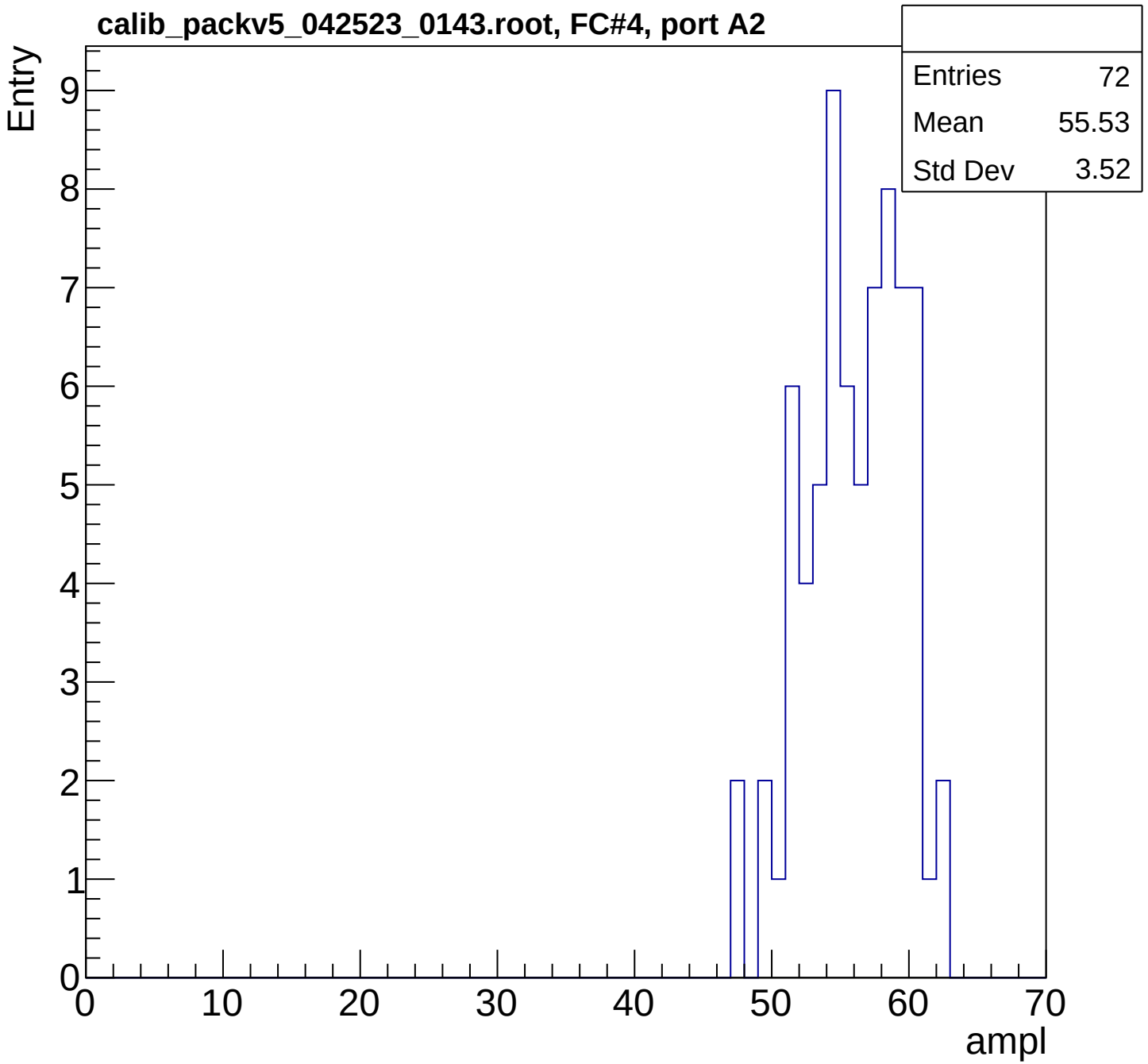
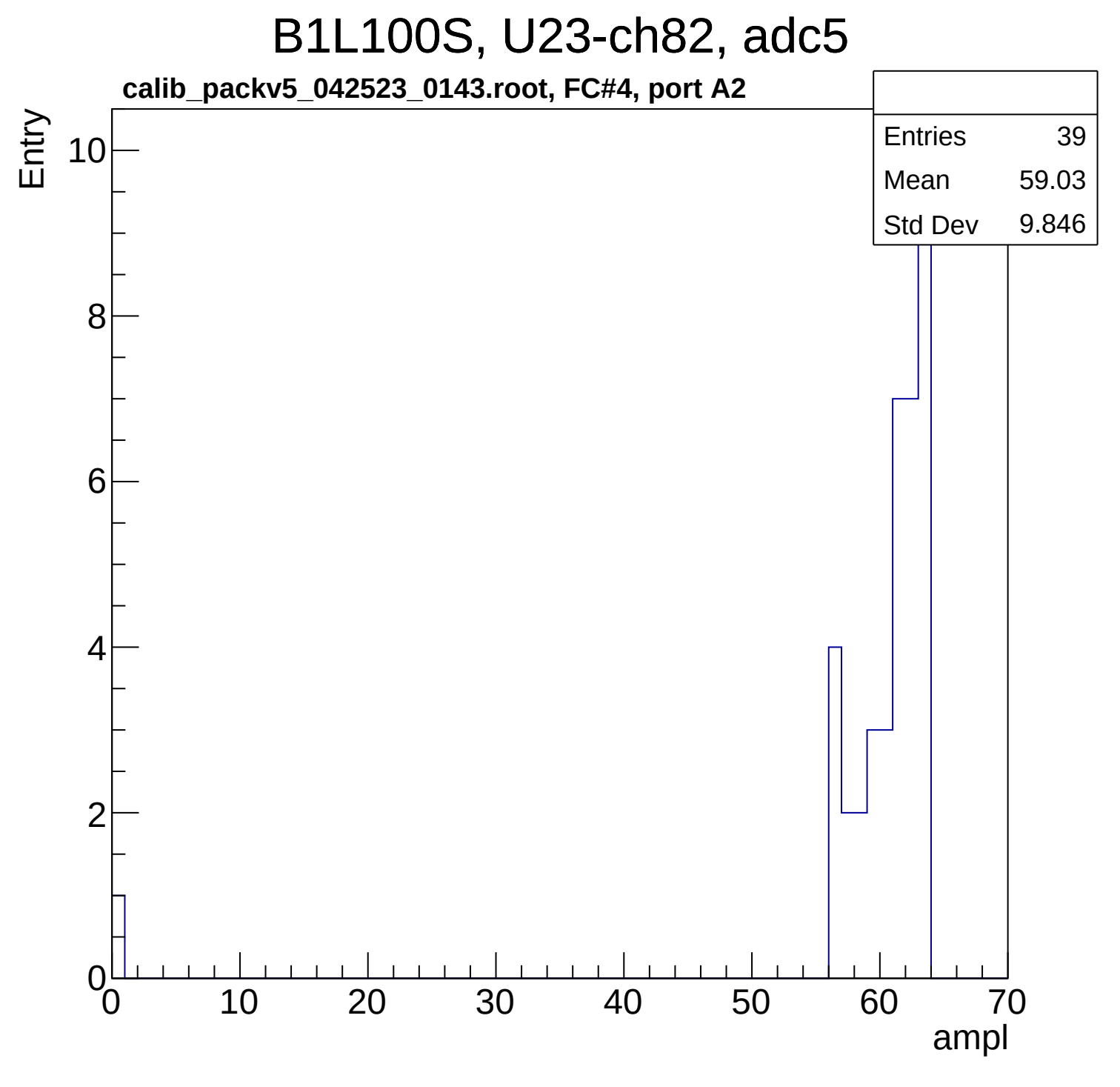

B1L100S, U23-ch82, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 55.53
Std Dev 3.52
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.03
Std Dev 9.846
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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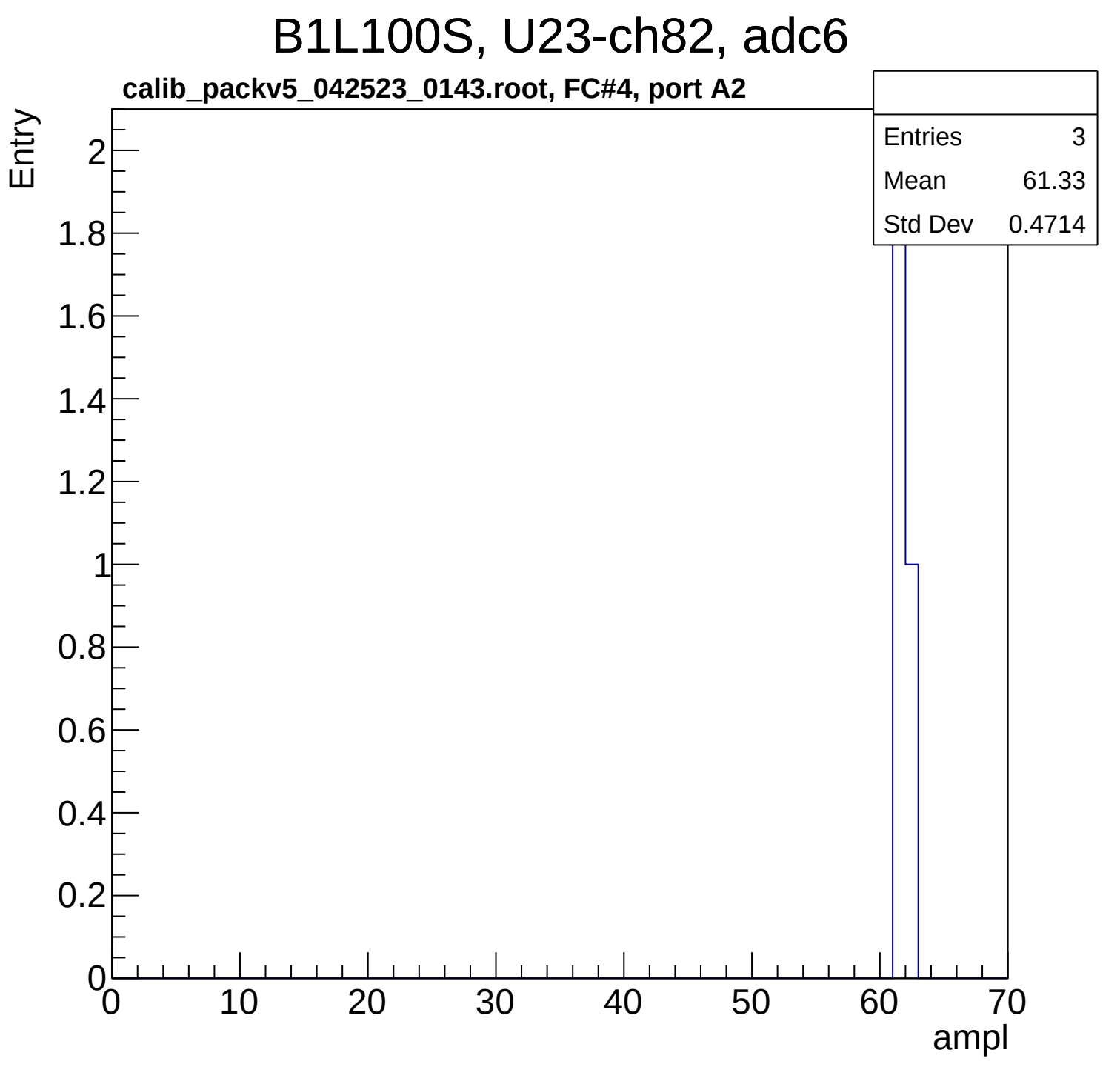
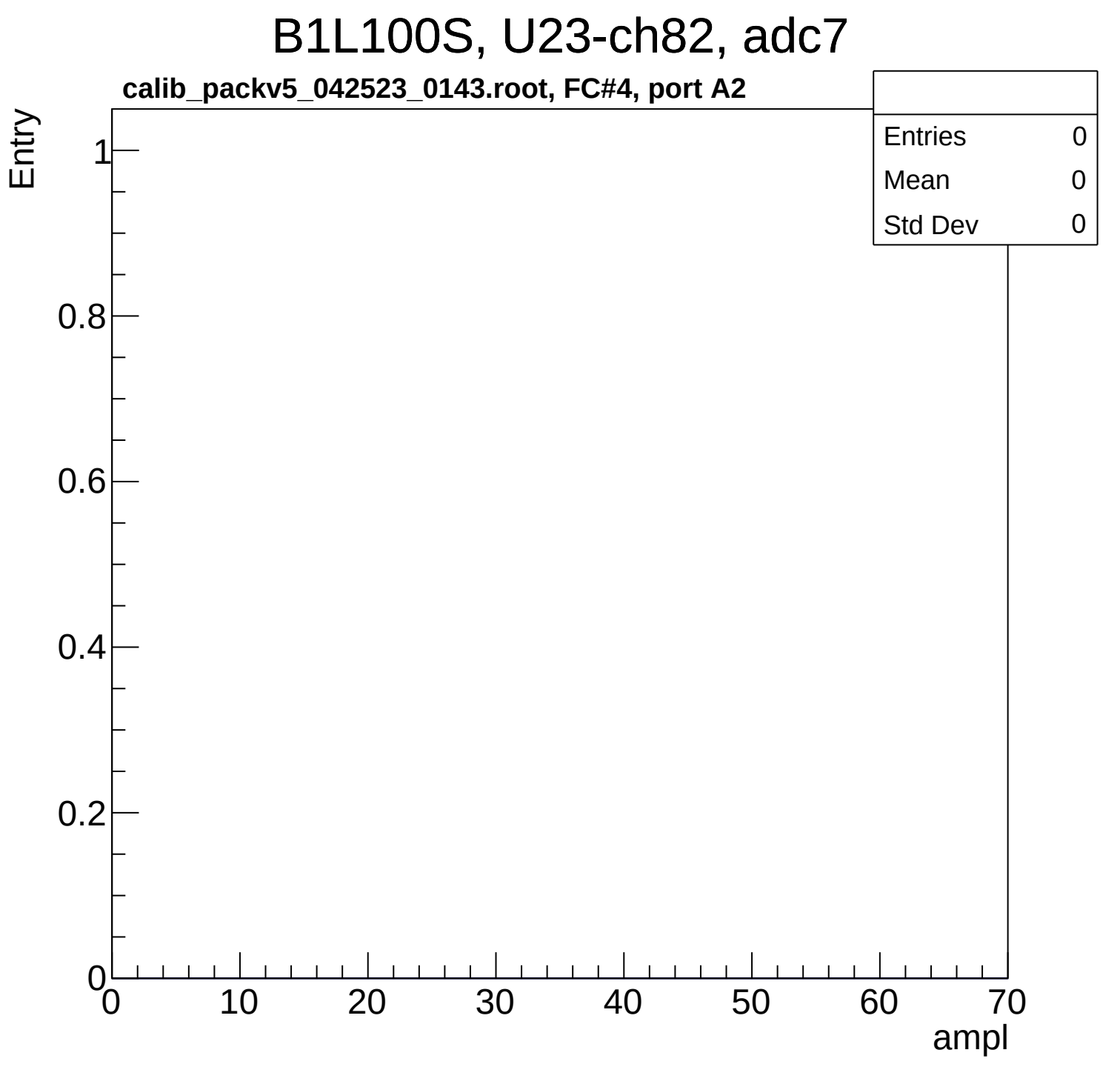
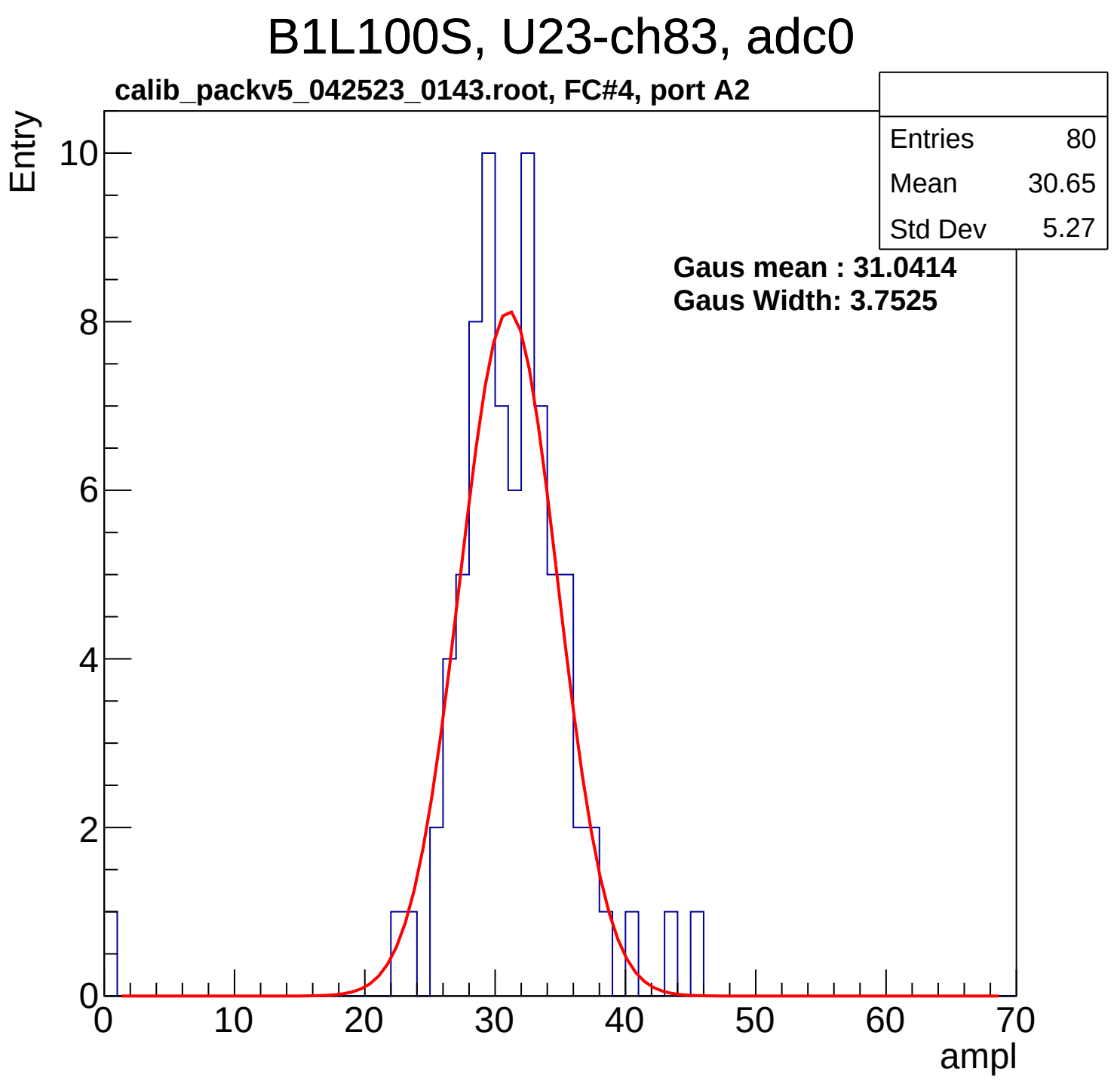
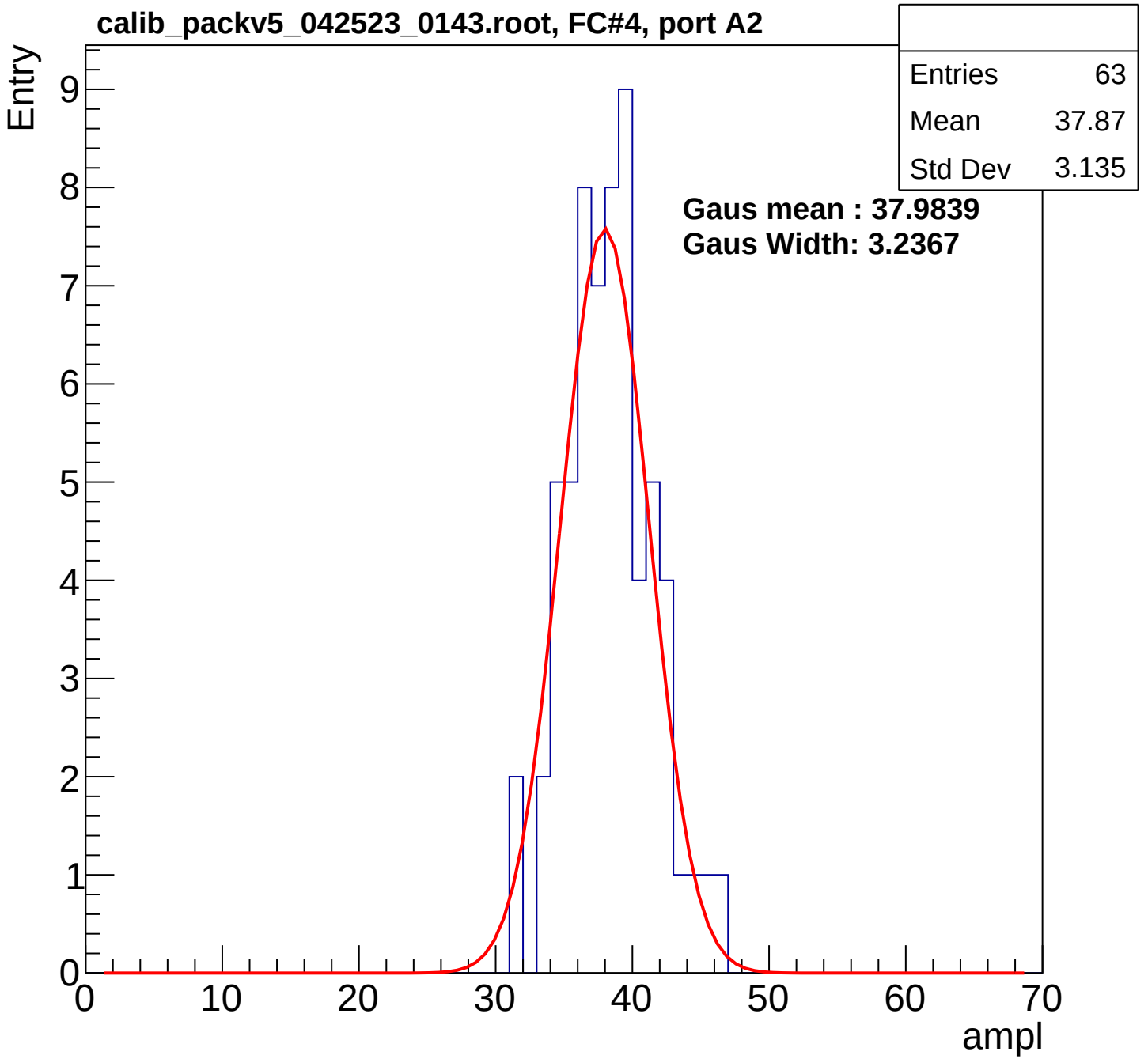
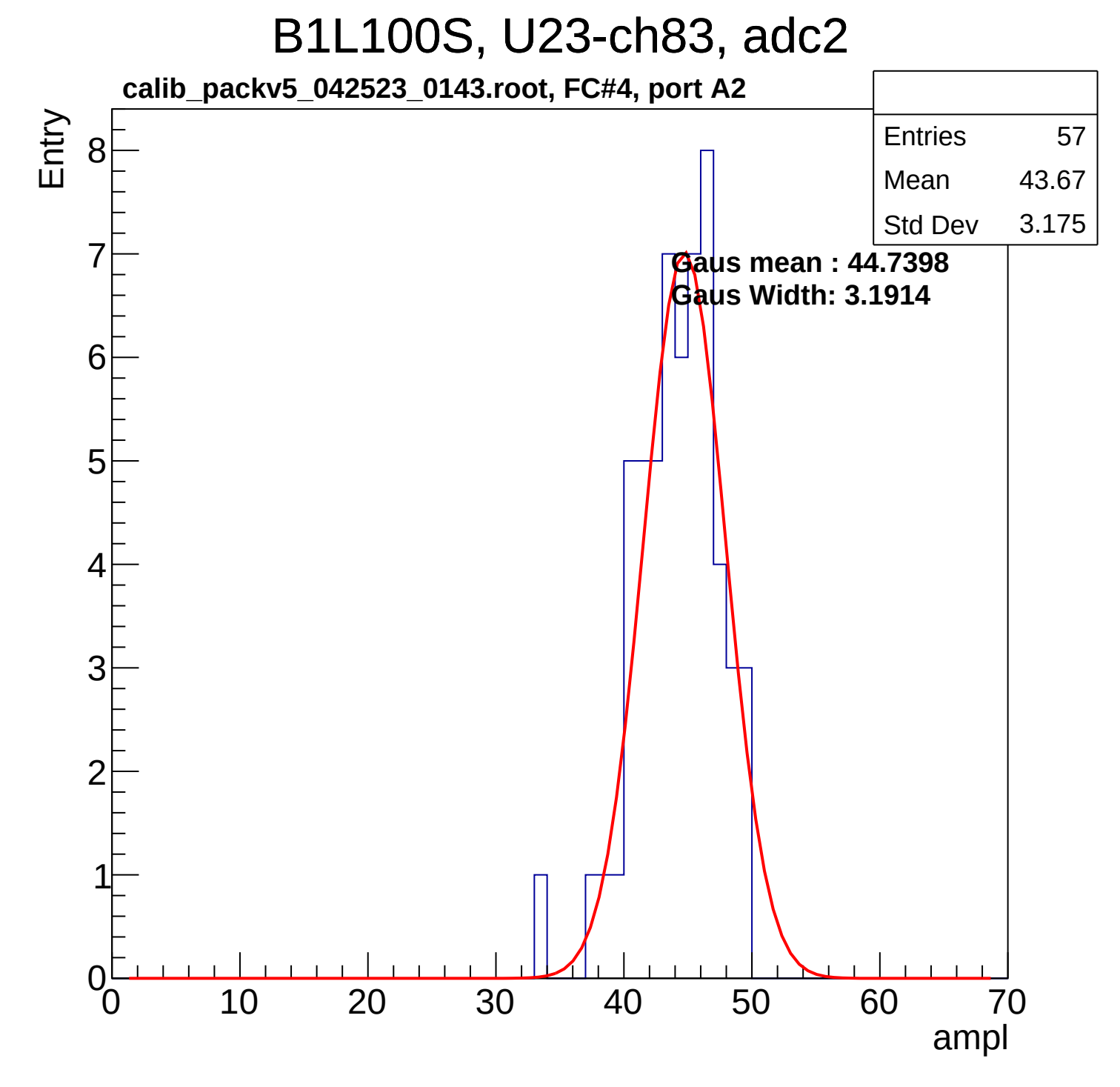

B1L100S, U23-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch82, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 30.65
Std Dev 5.27
Gaus mean : 31.0414
Gaus Width: 3.7525
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch83, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 37.87
Std Dev 3.135
Gaus mean : 37.9839
Gaus Width: 3.2367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43.67
Std Dev 3.175
Gaus mean : 44.7398
Gaus Width: 3.1914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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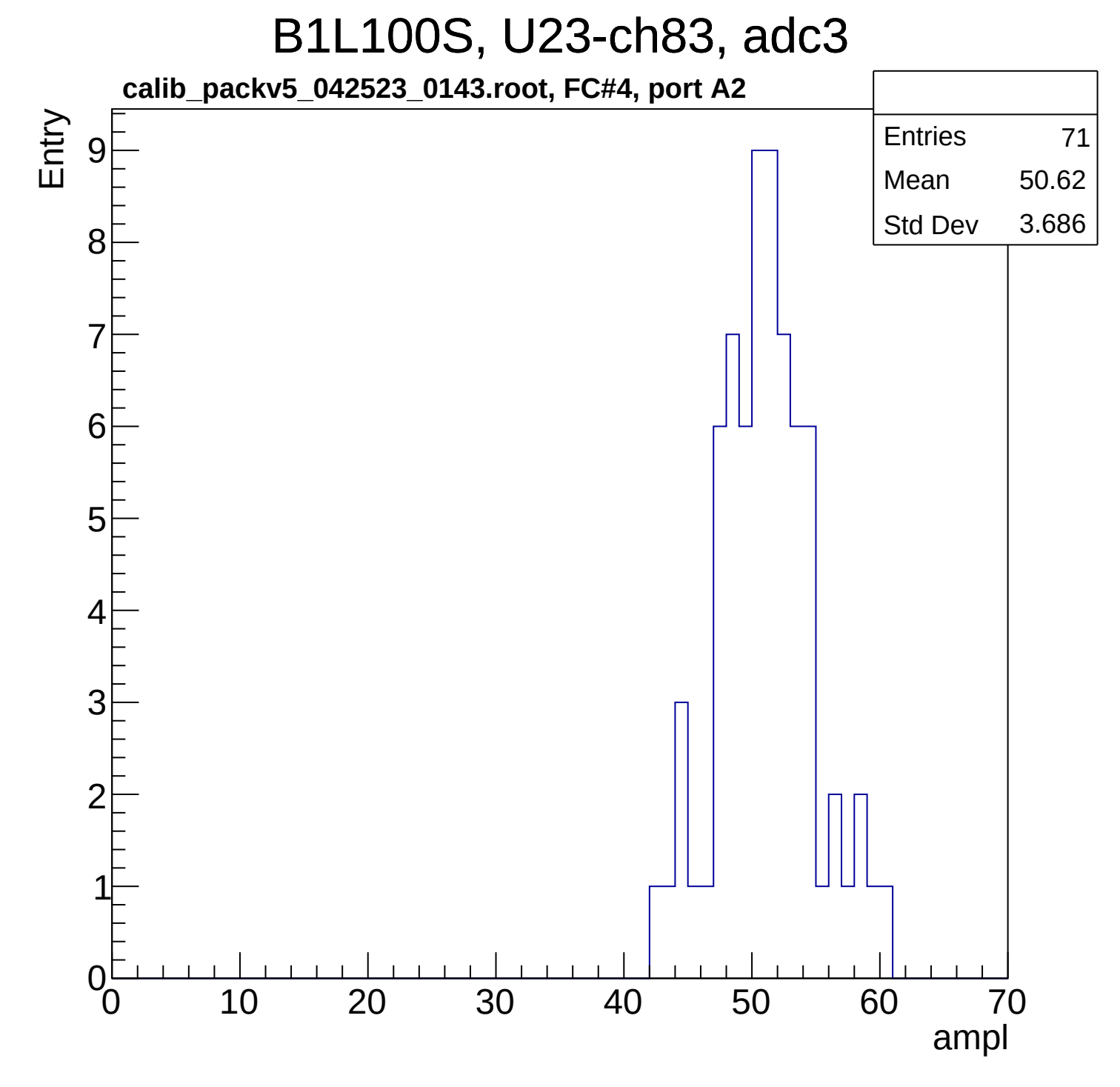
40
50
60
70


B1L100S, U23-ch83, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.62
Std Dev 3.686
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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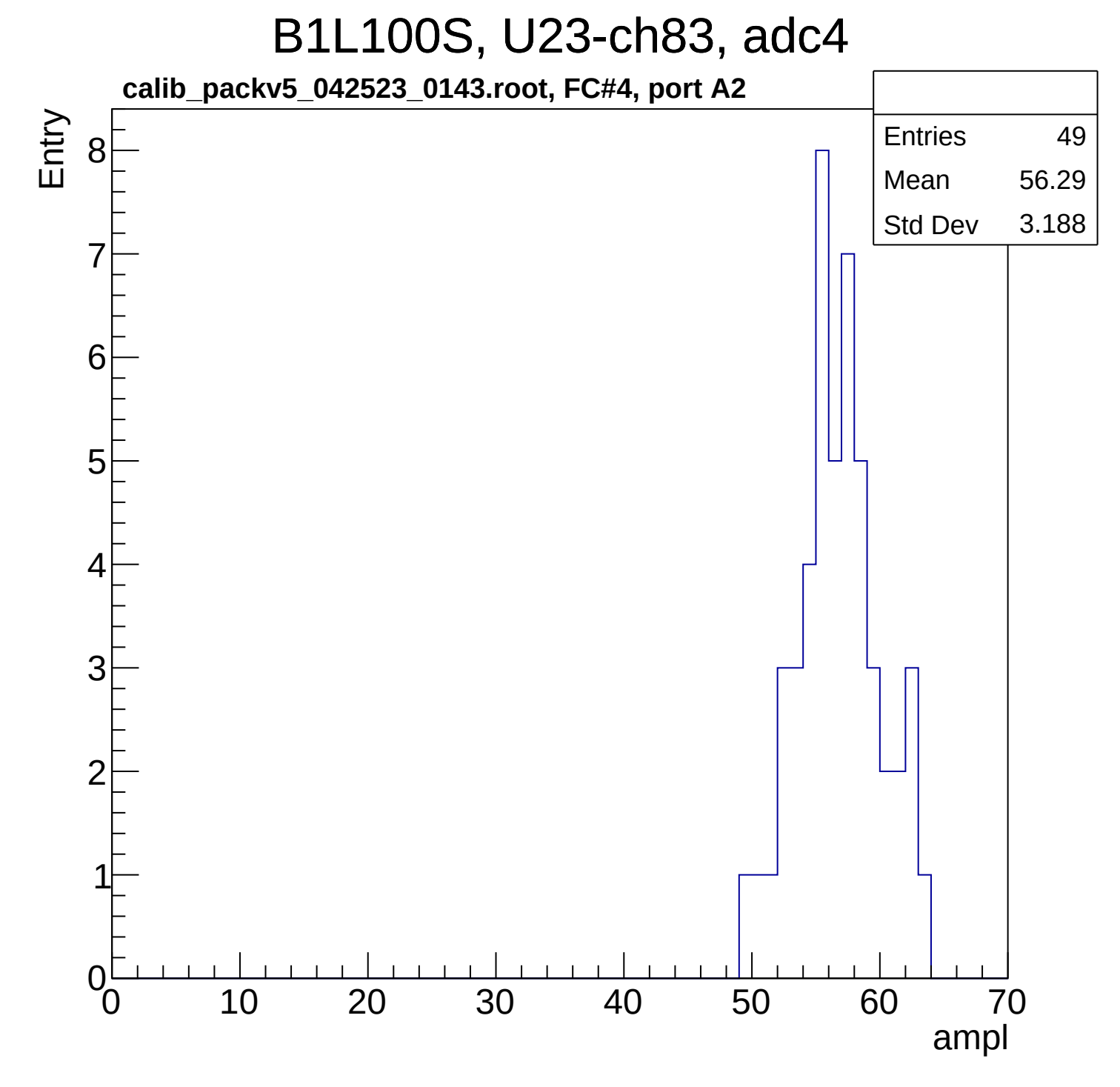
B1L100S, U23-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.29
Std Dev 3.188
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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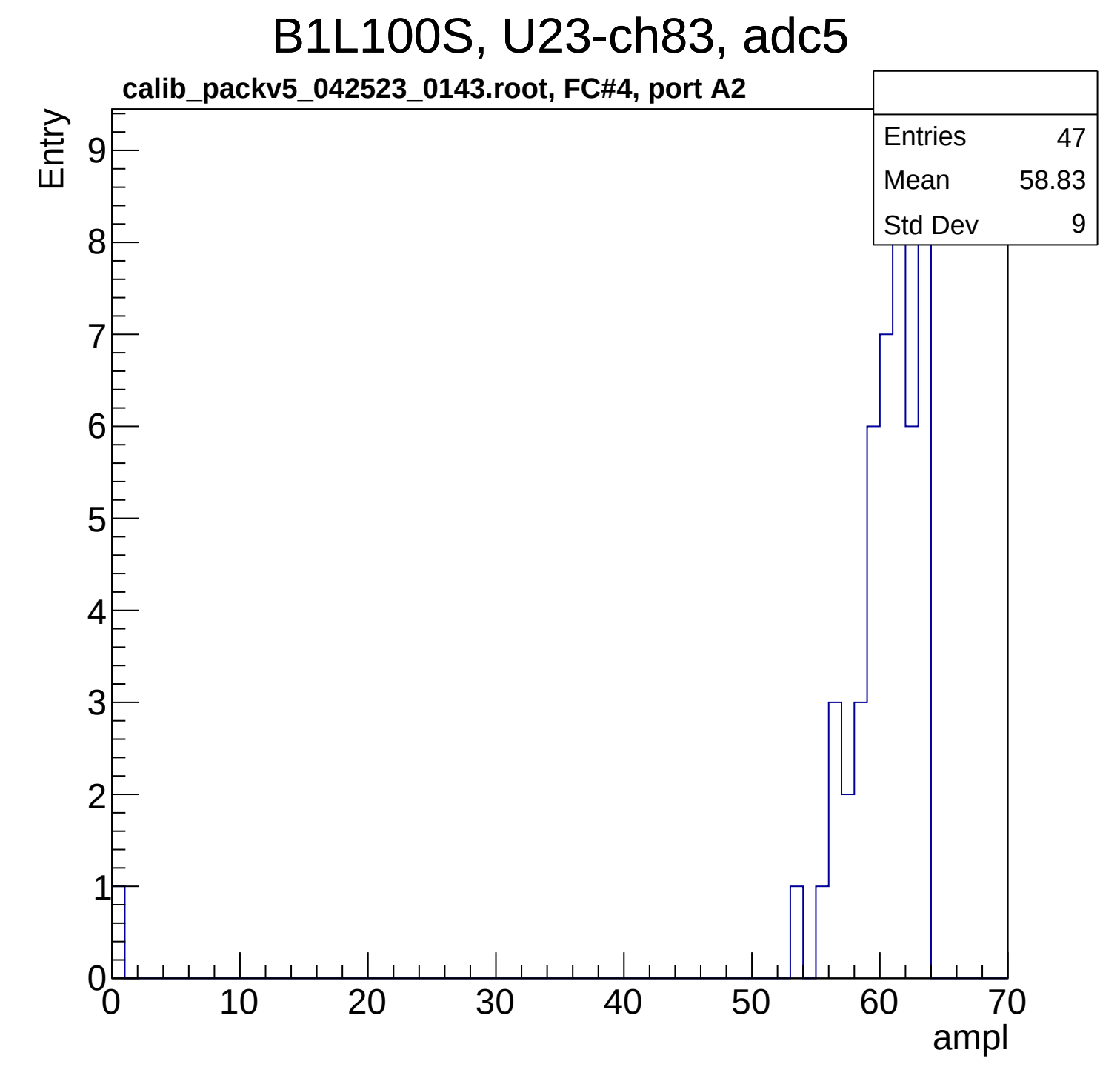
B1L100S, U23-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.83
Std Dev 9
Entry
ampl
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3
4
5
6
7
8
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0
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60
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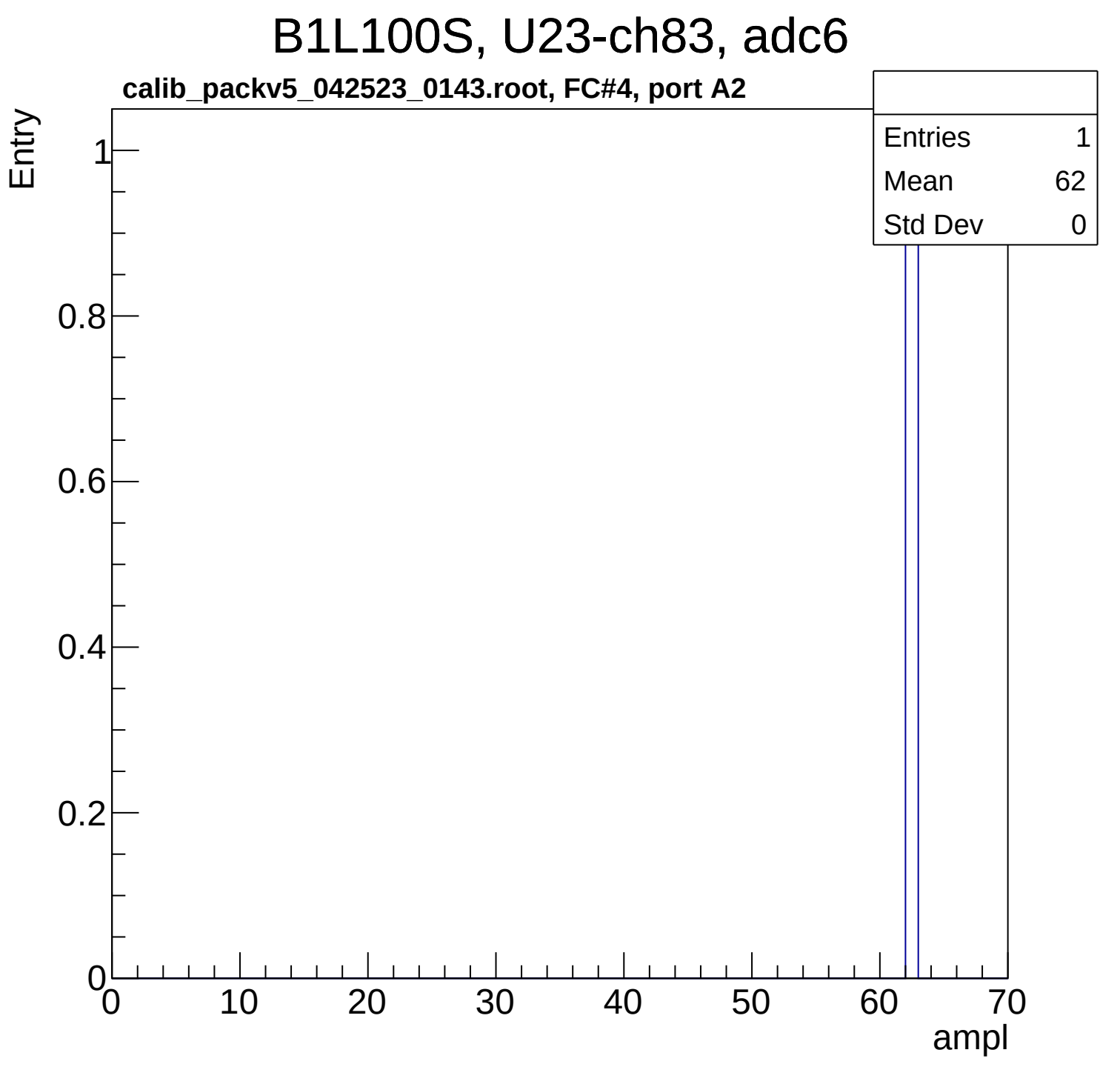
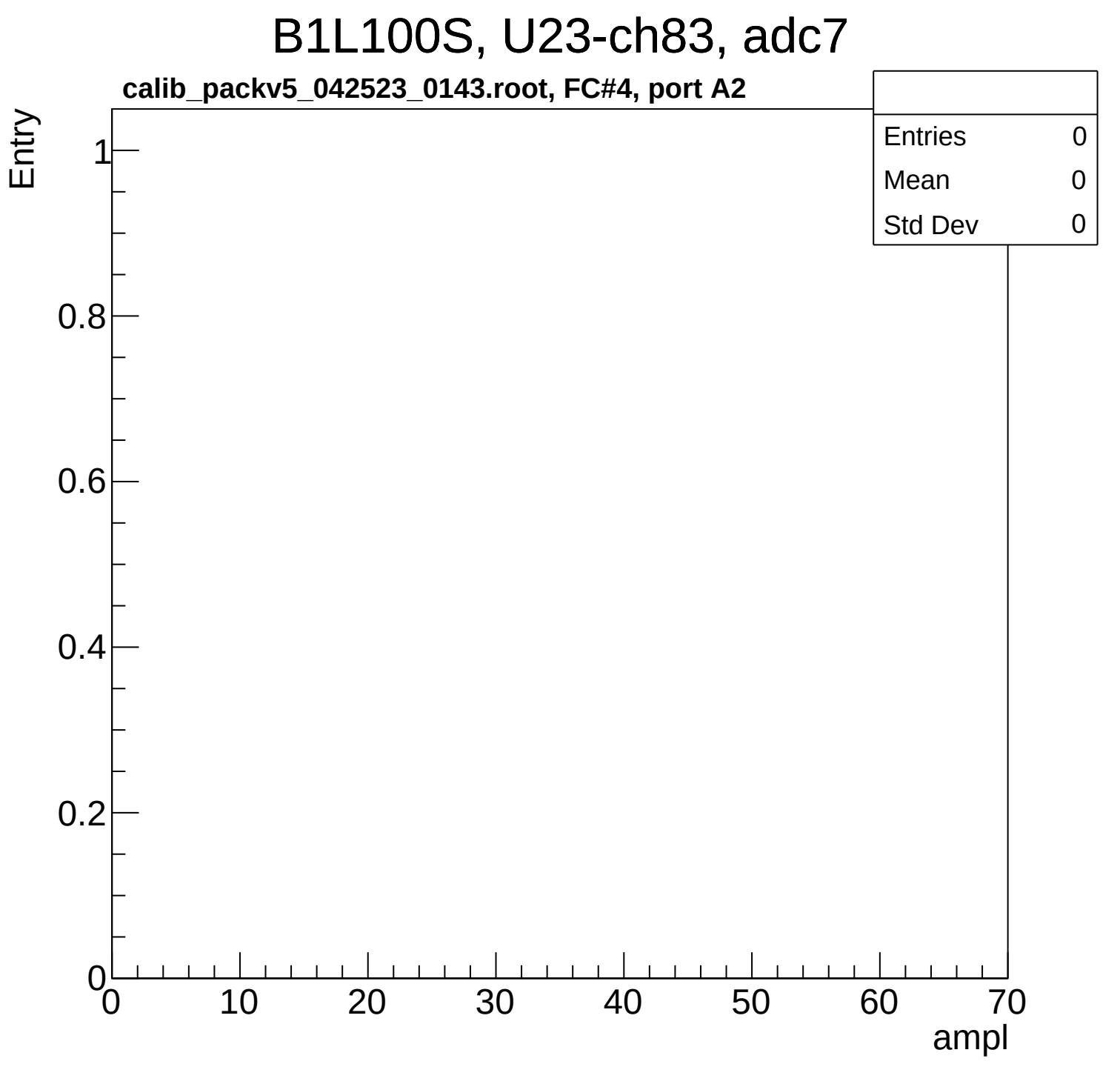

B1L100S, U23-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch83, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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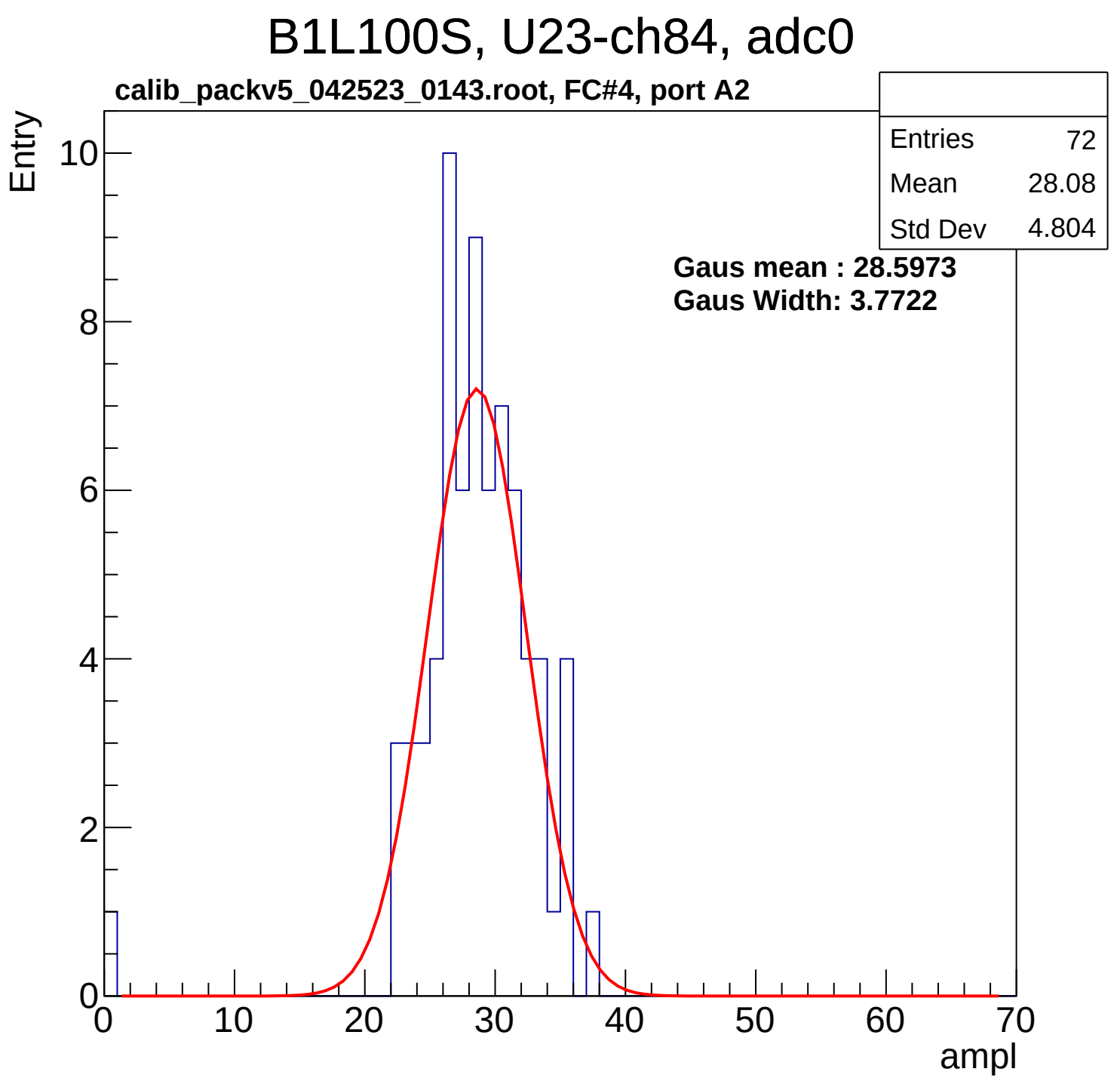
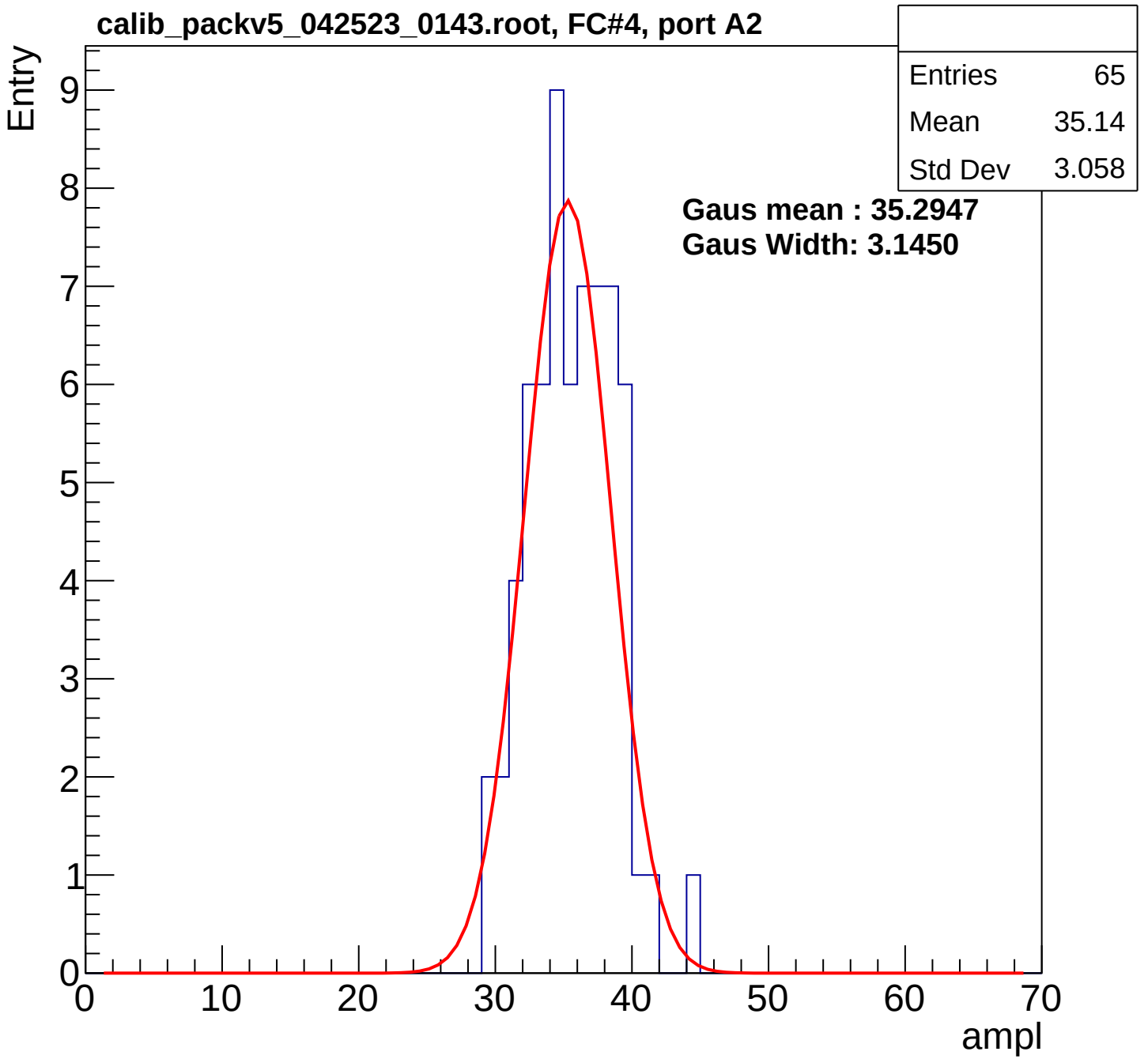
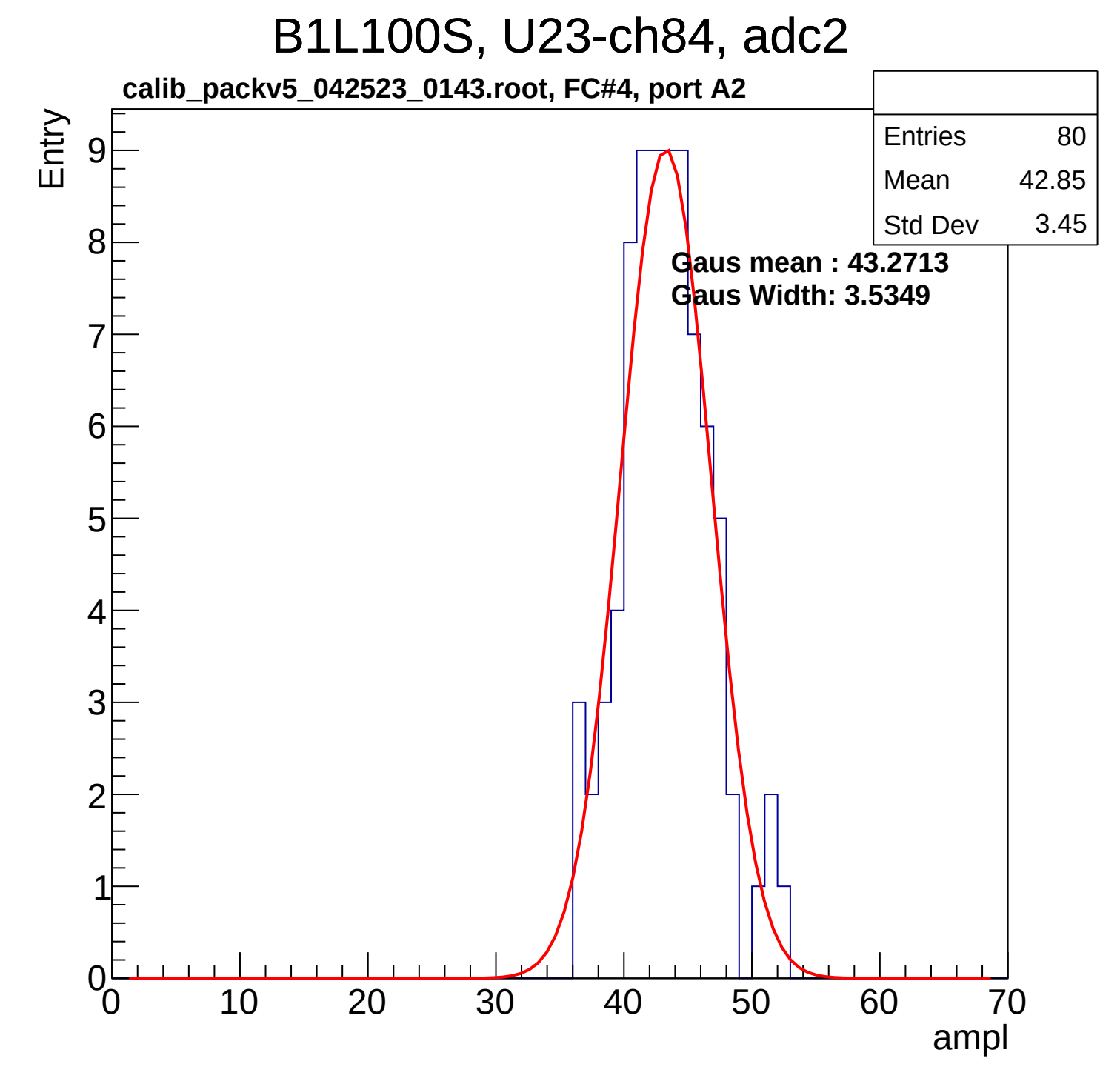
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.08
Std Dev 4.804
Gaus mean : 28.5973
Gaus Width: 3.7722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 35.14
Std Dev 3.058
Gaus mean : 35.2947
Gaus Width: 3.1450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch84, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 42.85
Std Dev 3.45
Gaus mean : 43.2713
Gaus Width: 3.5349
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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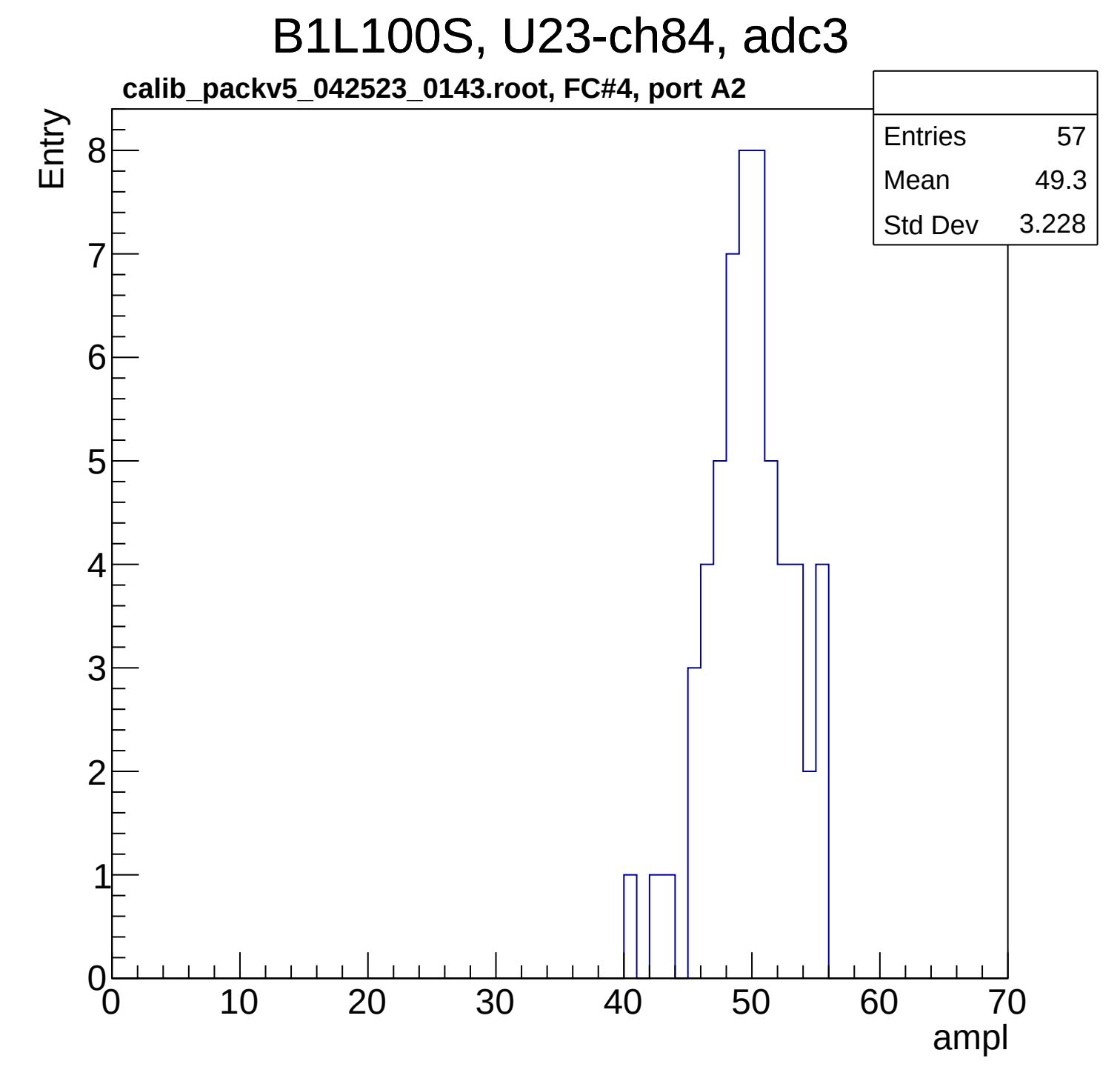
B1L100S, U23-ch84, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.3
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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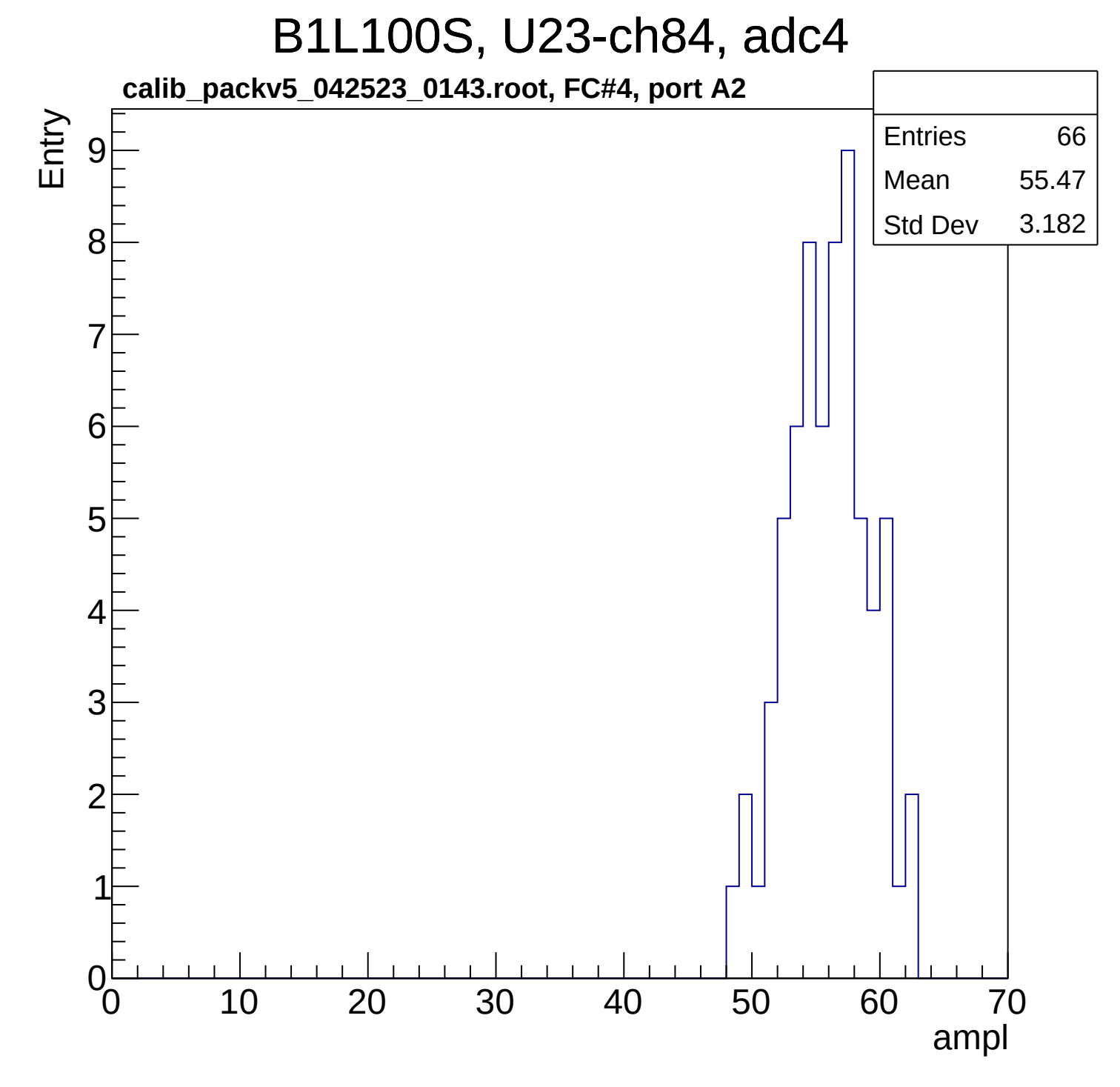
B1L100S, U23-ch84, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.47
Std Dev 3.182
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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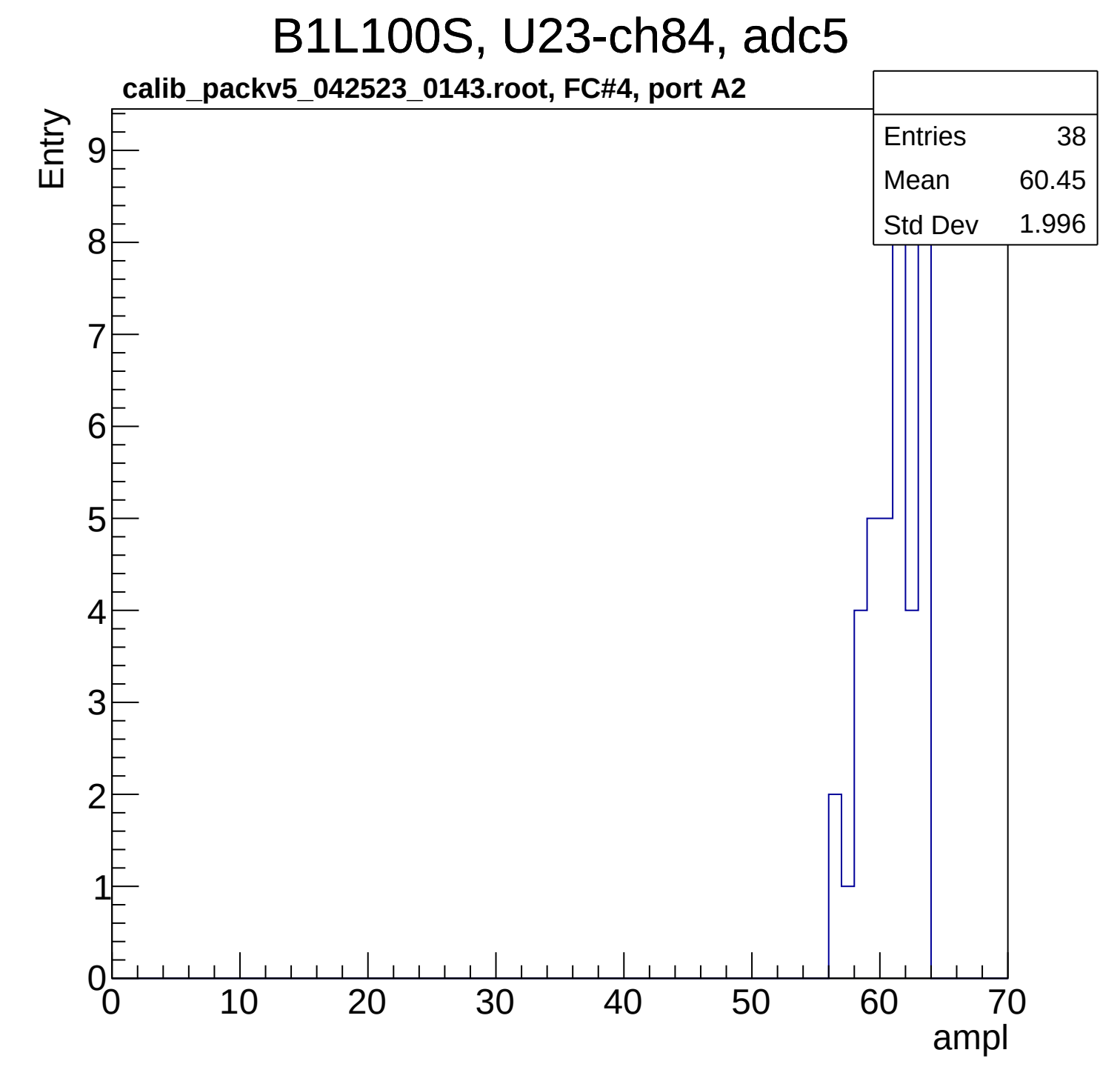
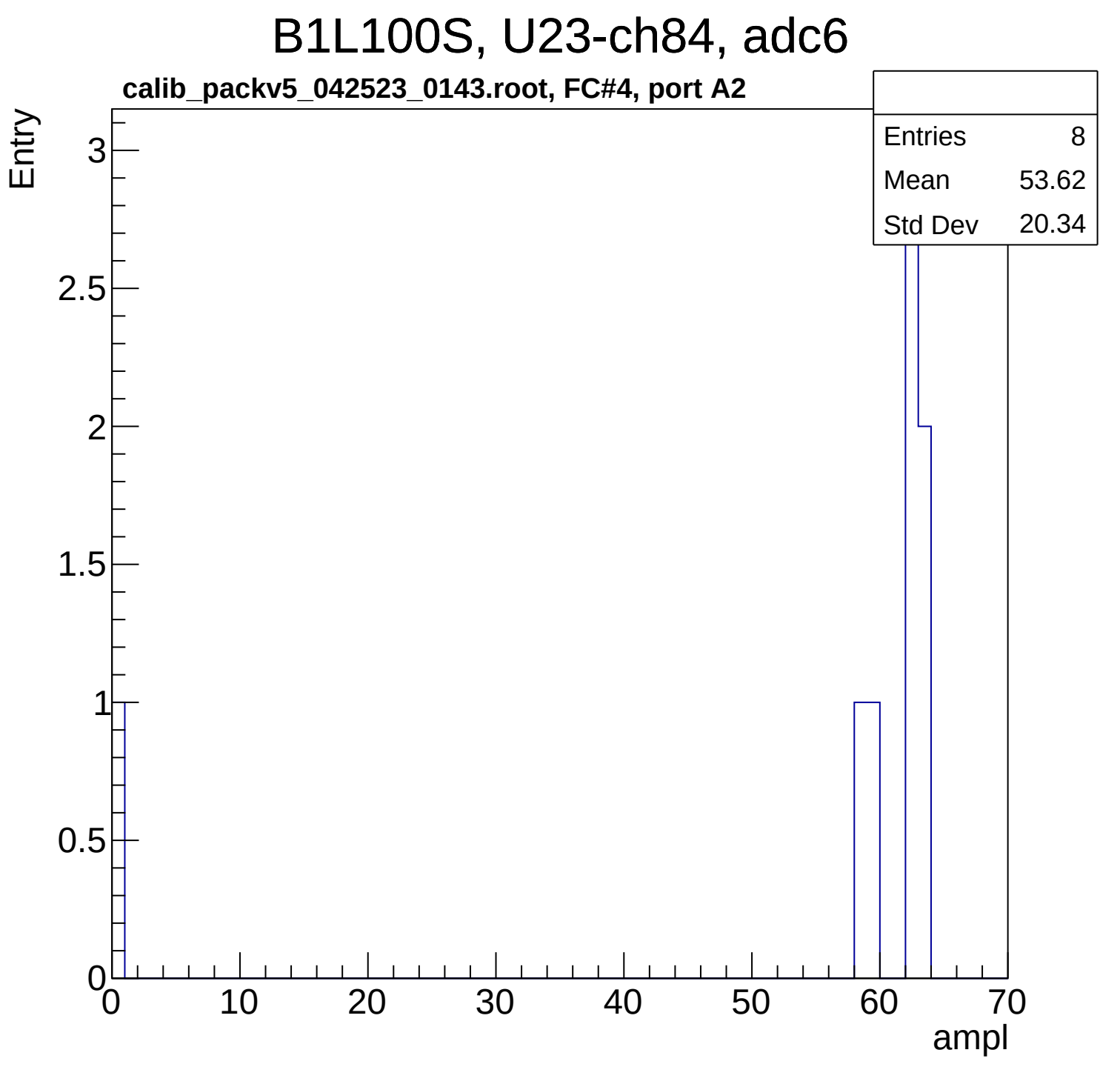

B1L100S, U23-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.45
Std Dev 1.996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 53.62
Std Dev 20.34
Entry
ampl
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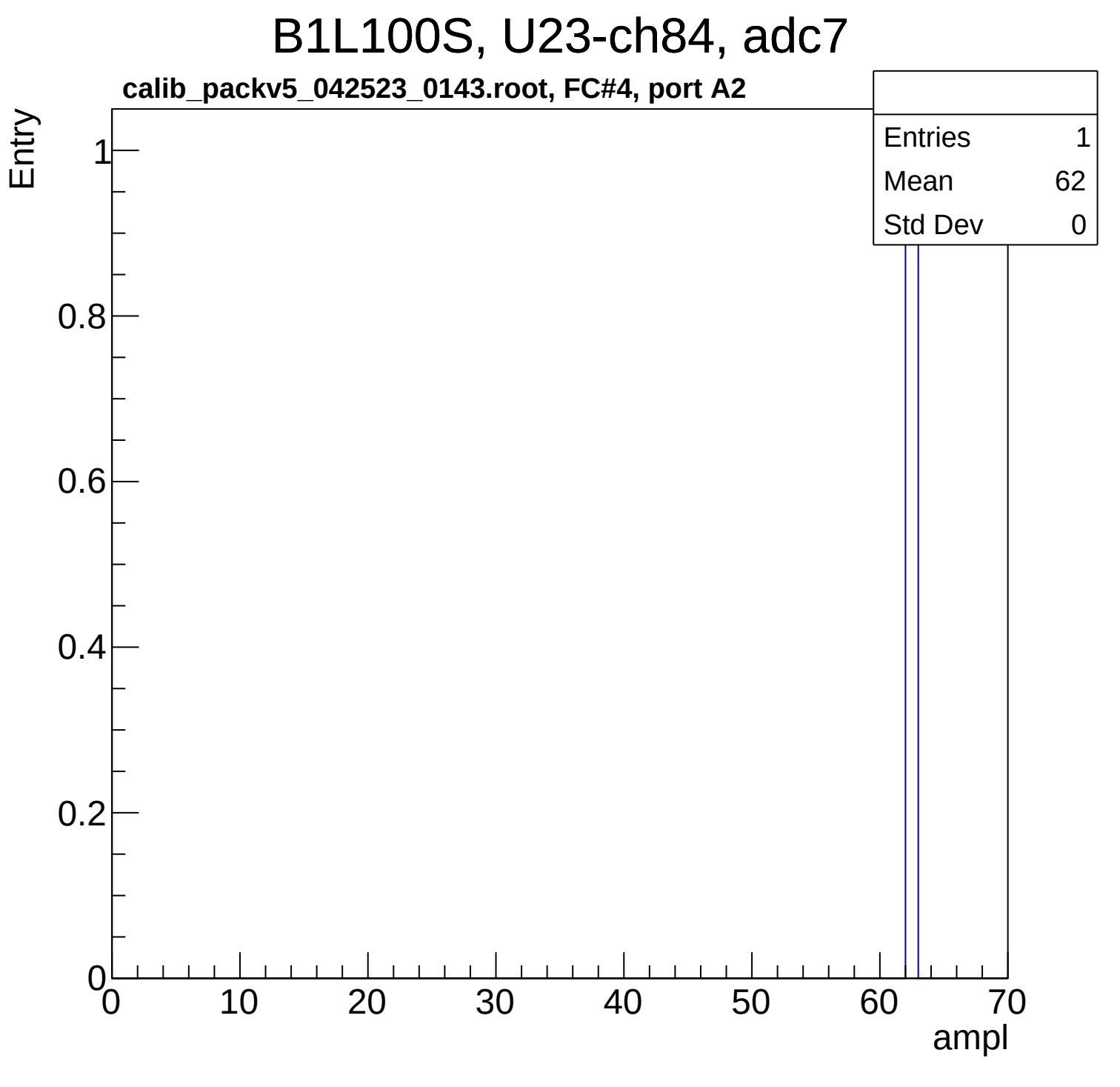
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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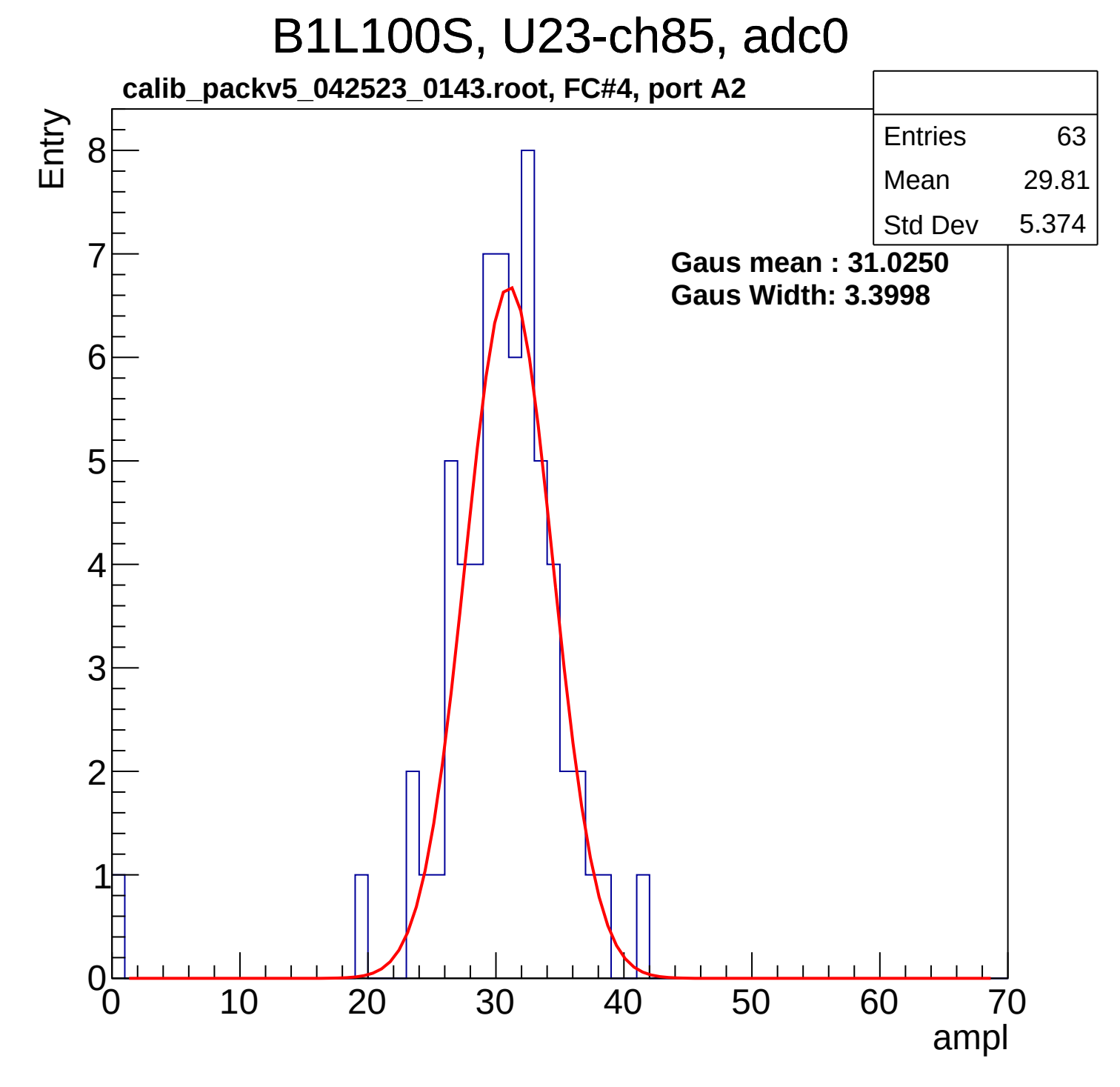
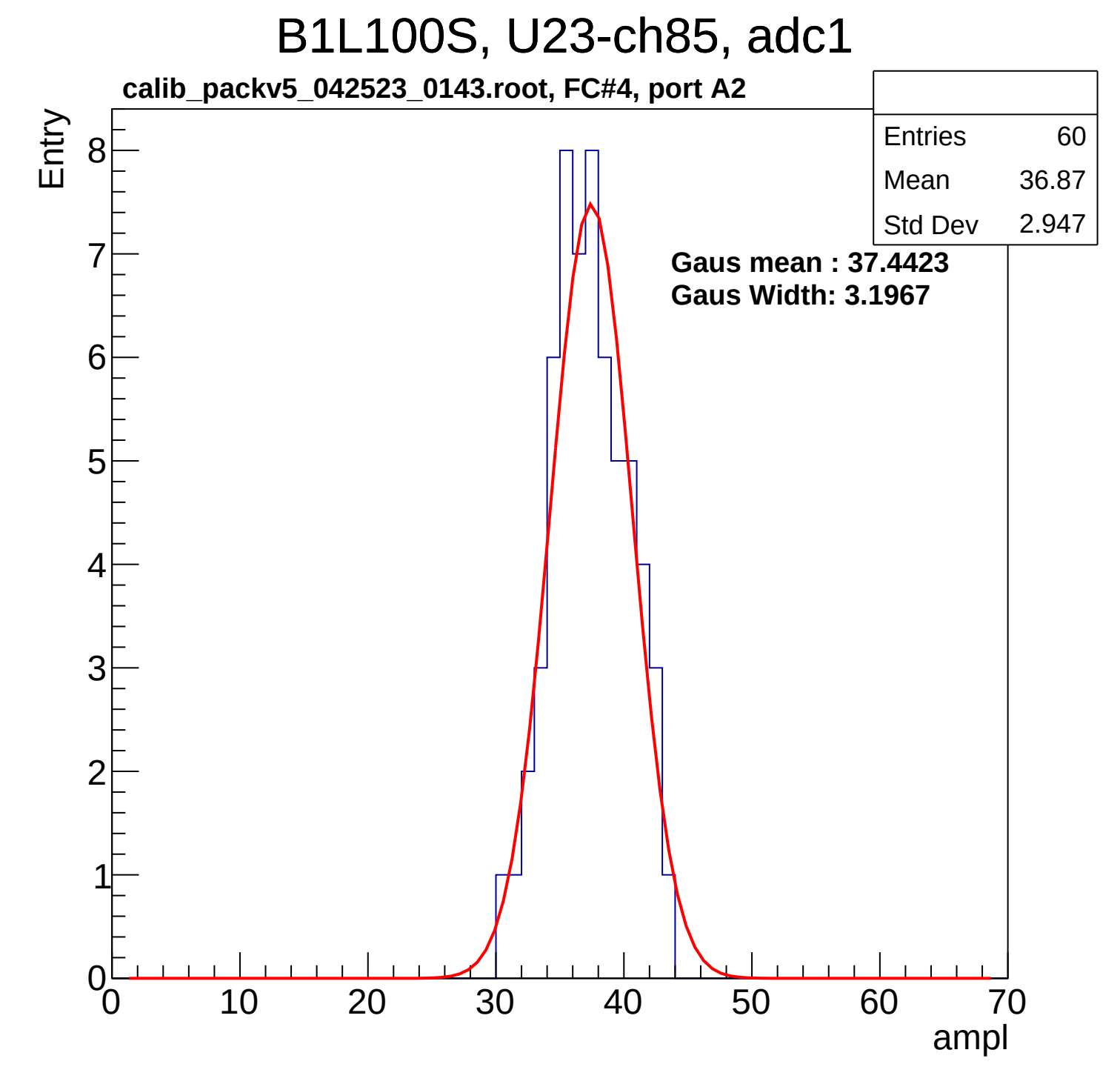
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.81
Std Dev 5.374
Gaus mean : 31.0250
Gaus Width: 3.3998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.87
Std Dev 2.947
Gaus mean : 37.4423
Gaus Width: 3.1967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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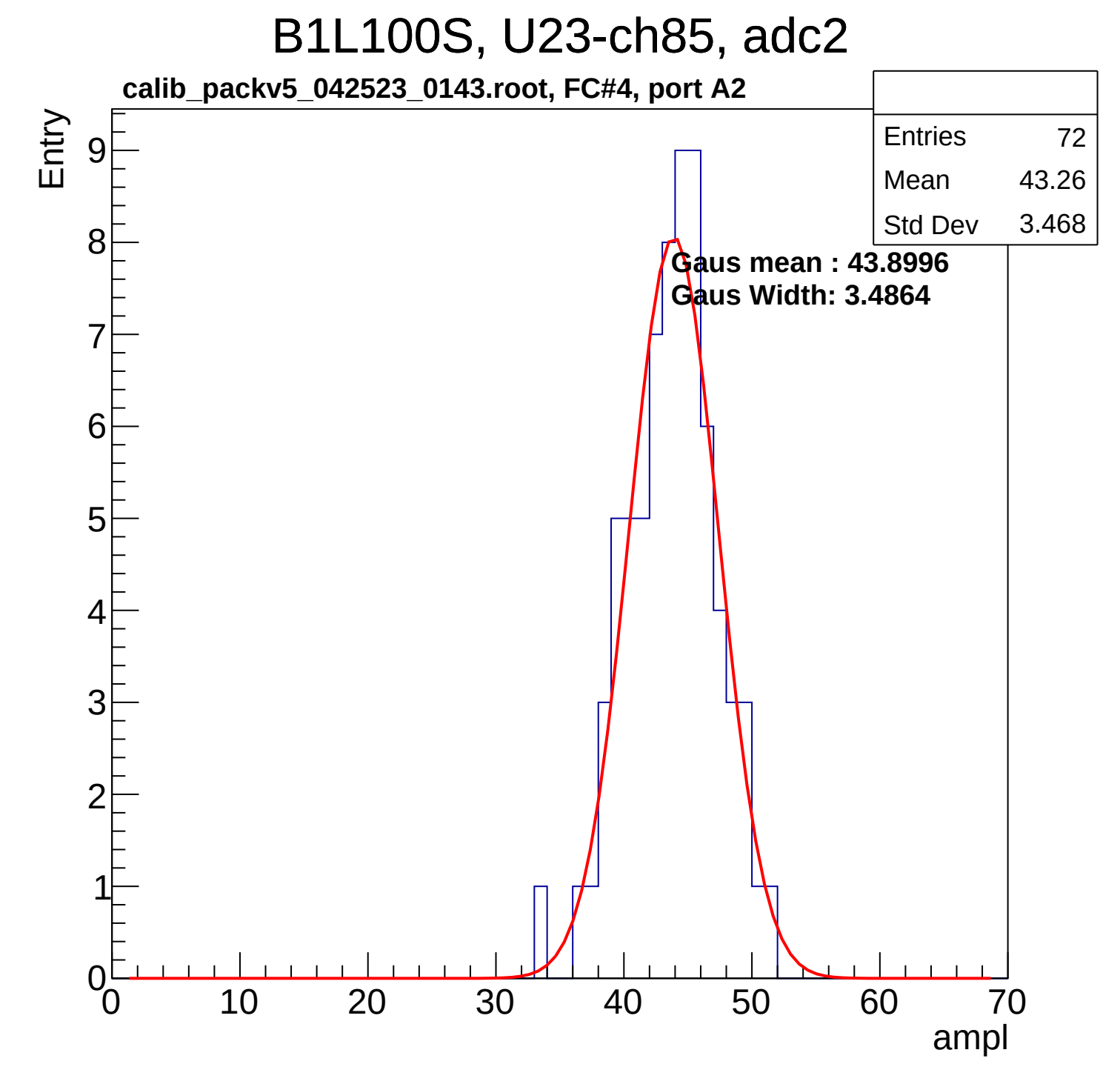
B1L100S, U23-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 43.26
Std Dev 3.468
Gaus mean : 43.8996
Gaus Width: 3.4864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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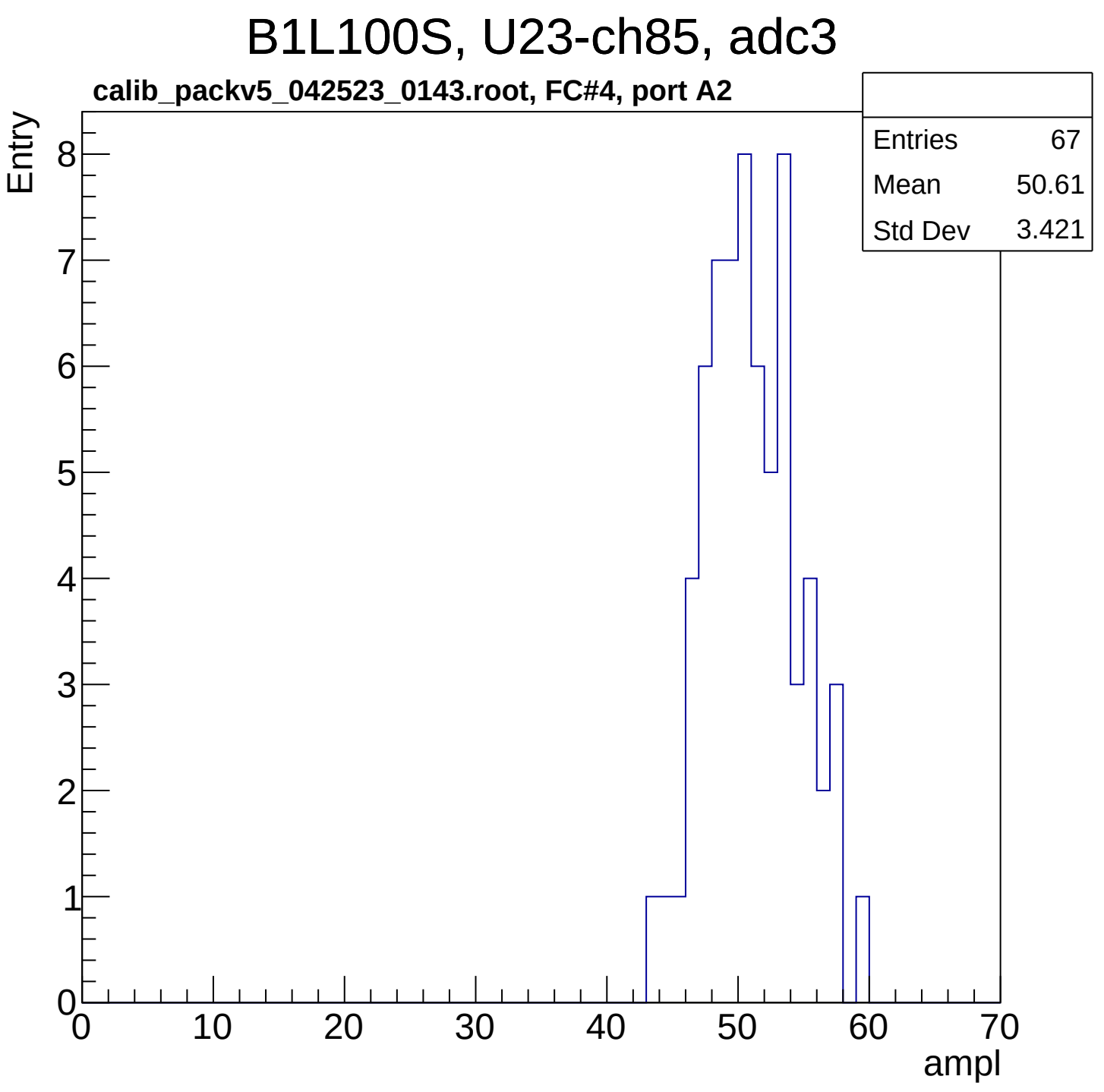
B1L100S, U23-ch85, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.61
Std Dev 3.421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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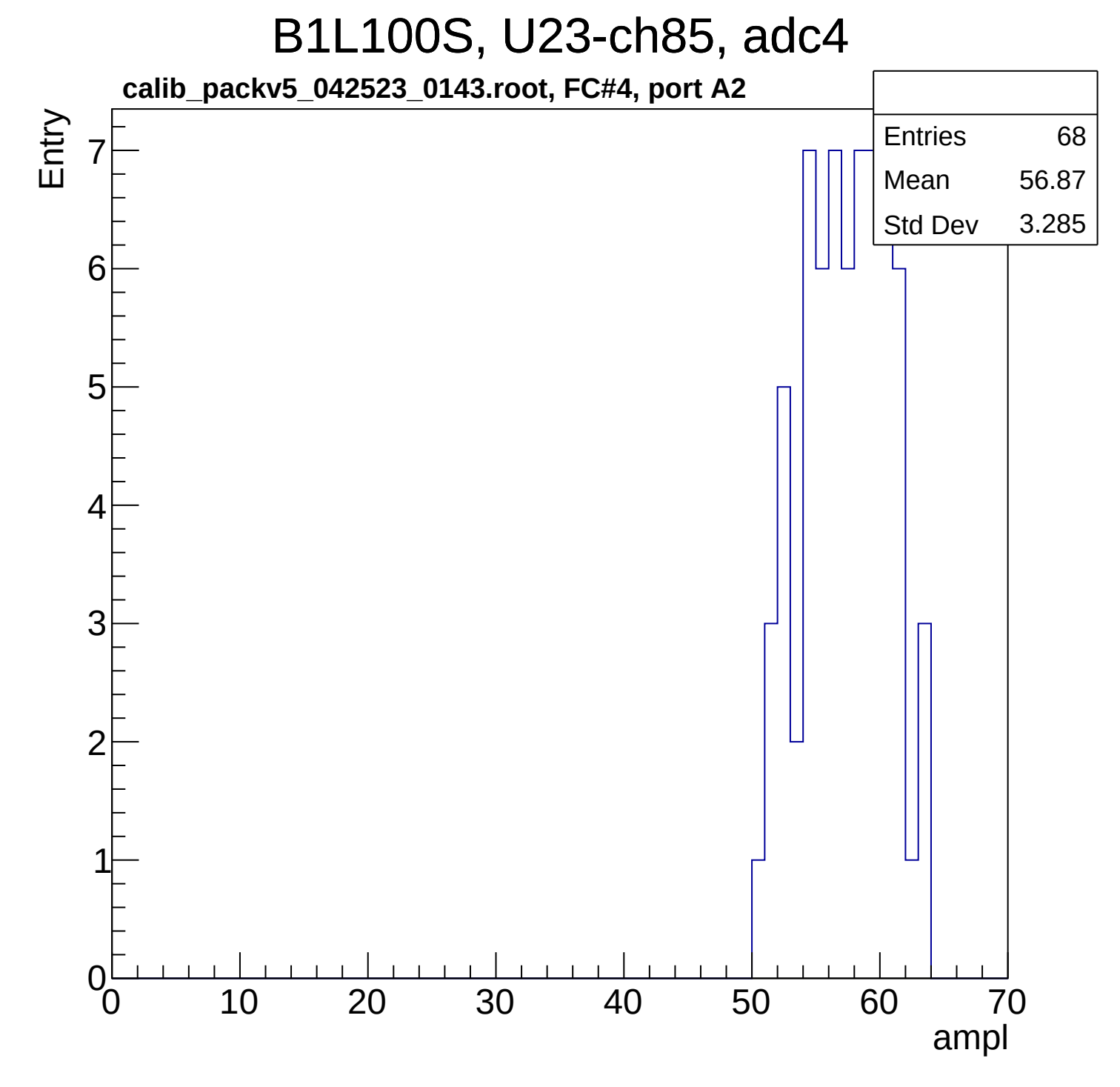
B1L100S, U23-ch85, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.87
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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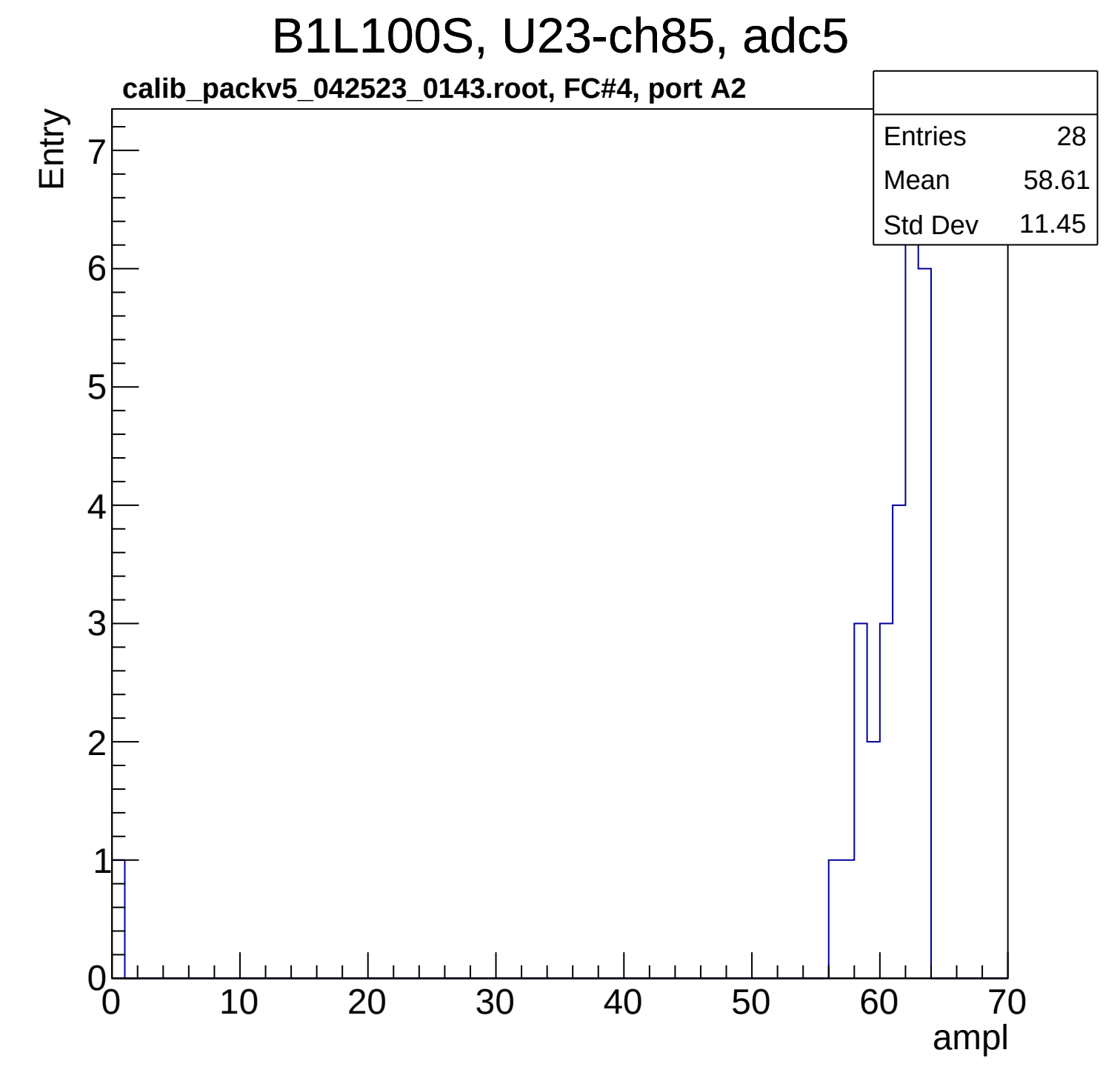
40
50
60
70


B1L100S, U23-ch85, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.61
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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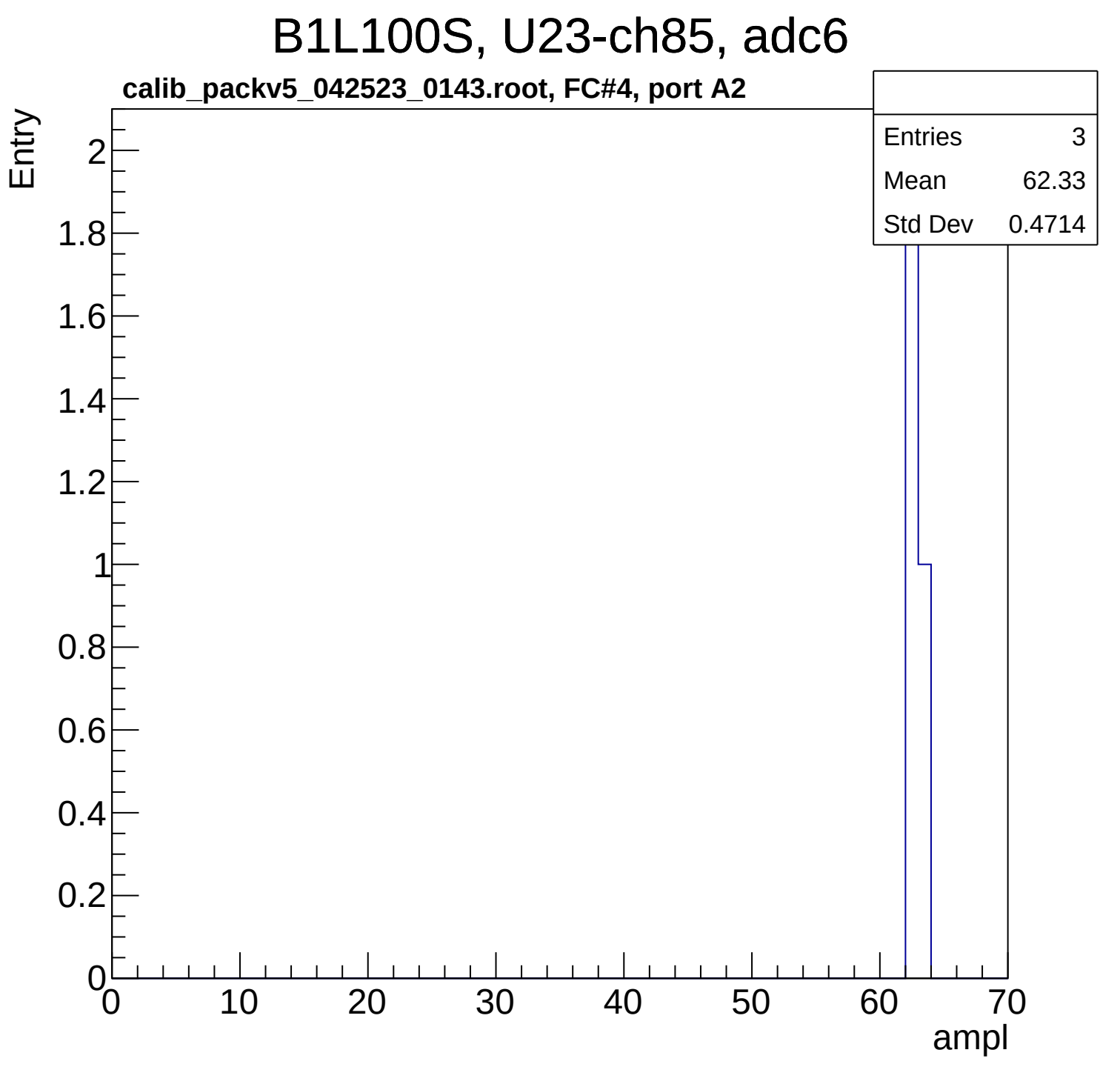
B1L100S, U23-ch85, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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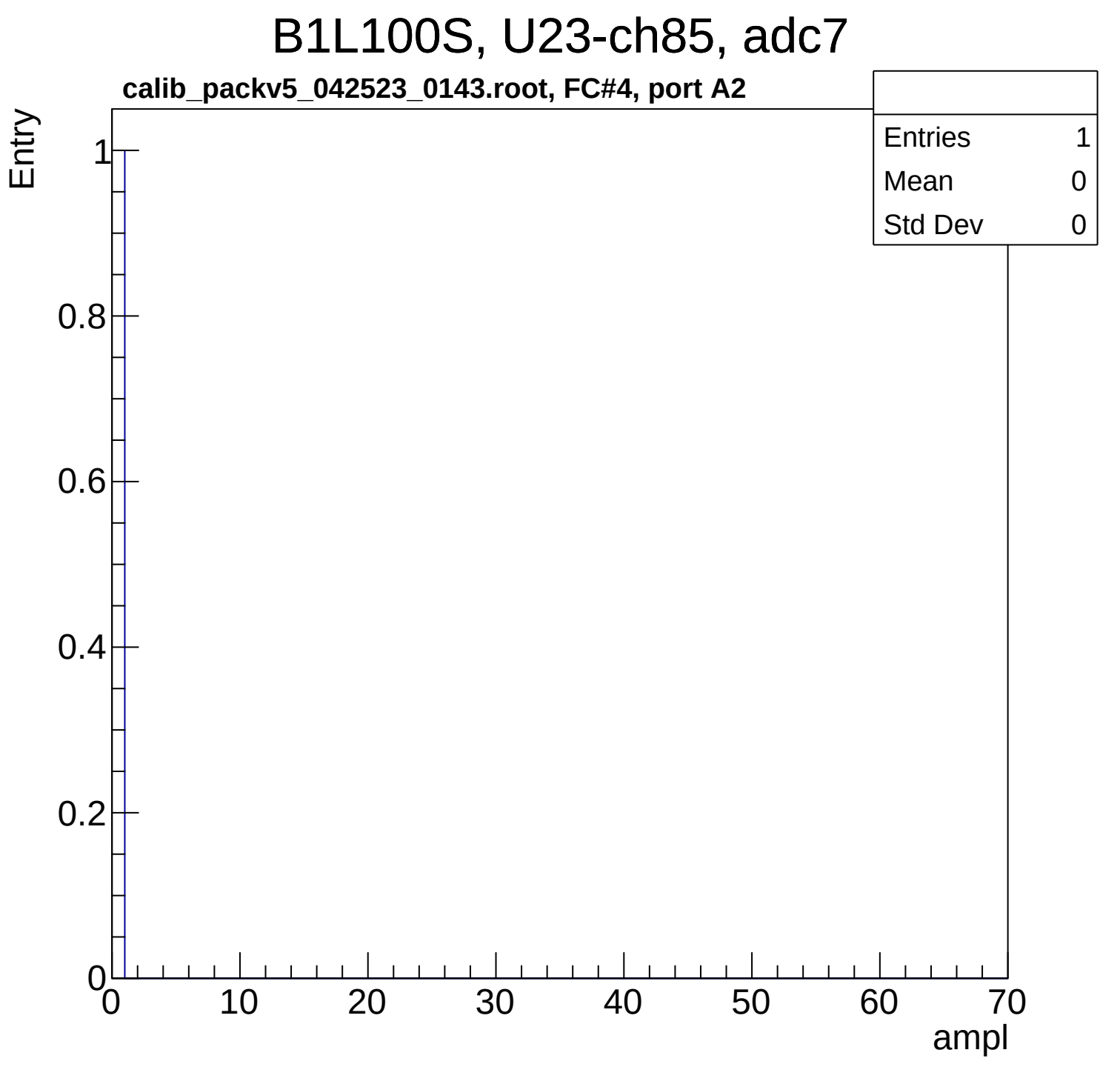
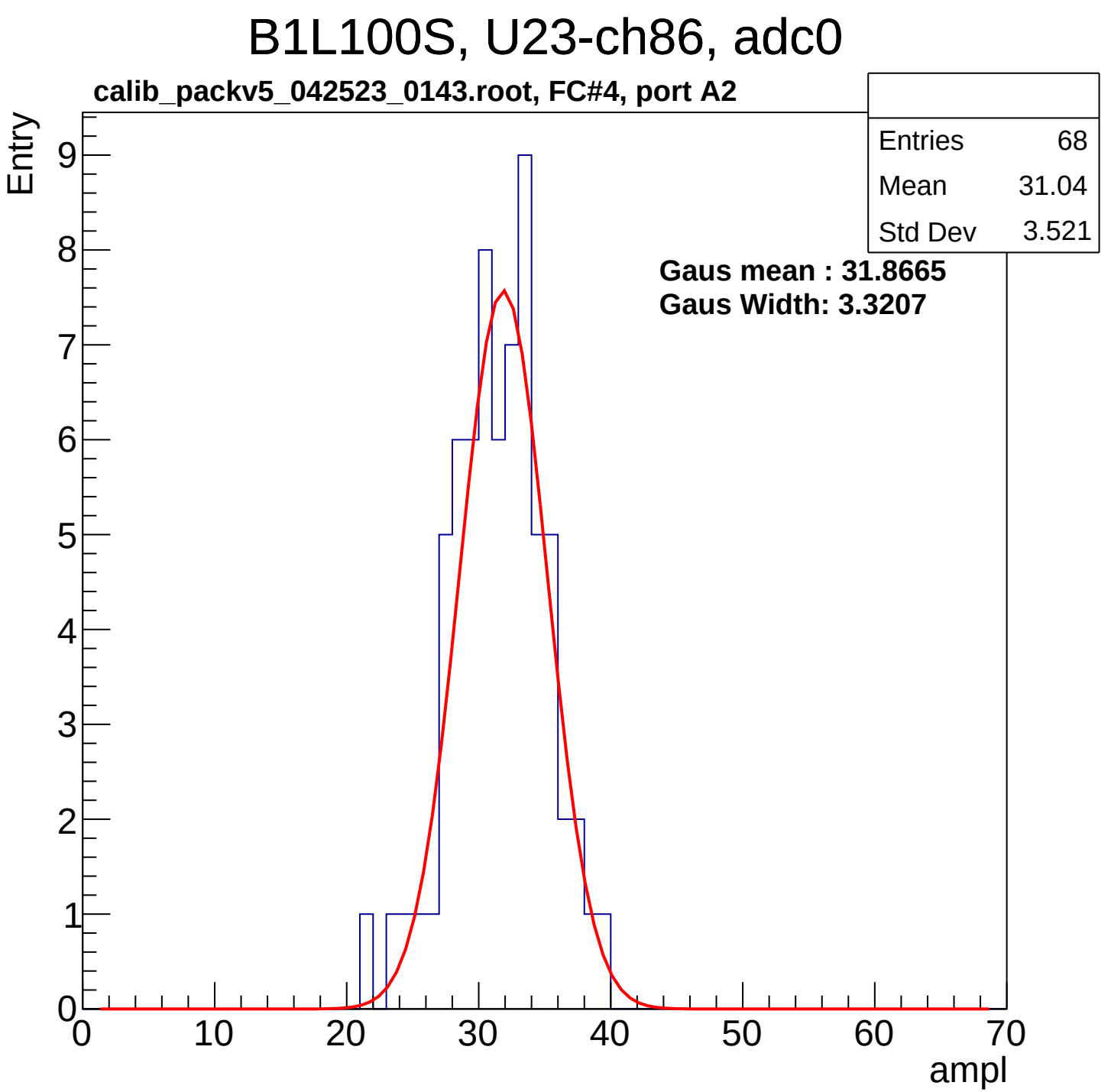

B1L100S, U23-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 31.04
Std Dev 3.521
Gaus mean : 31.8665
Gaus Width: 3.3207
Entry
ampl
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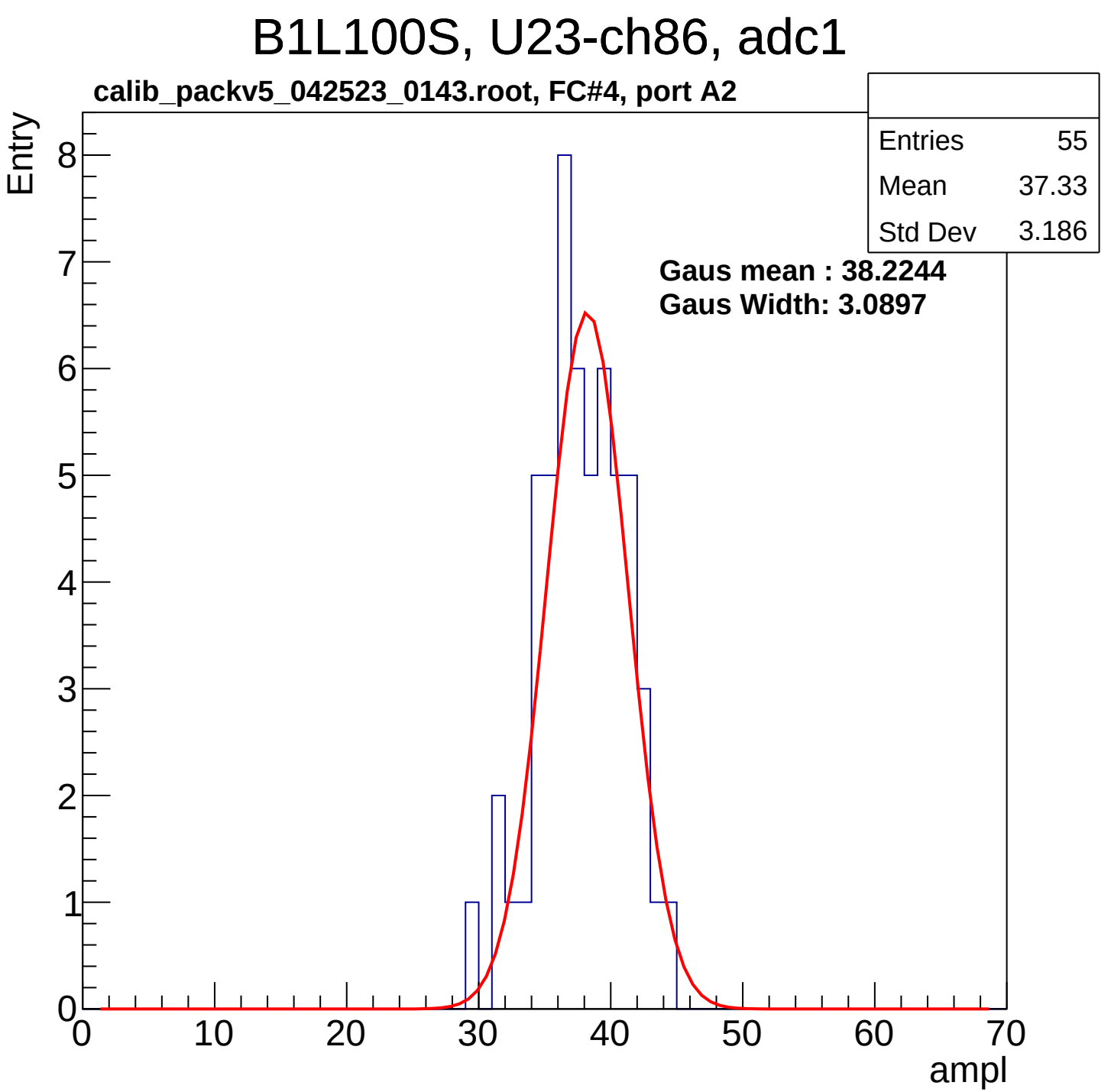
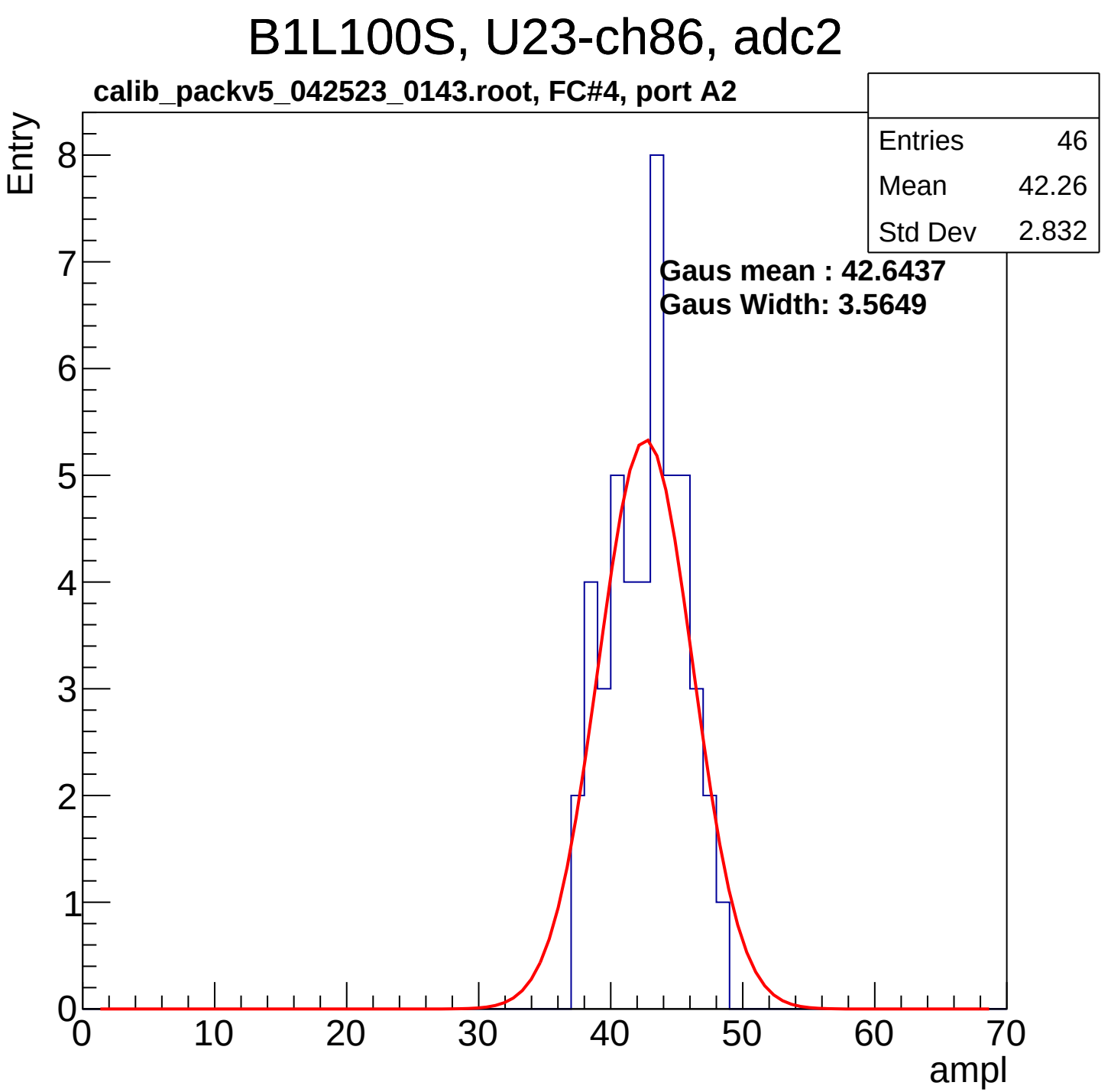
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 37.33
Std Dev 3.186
Gaus mean : 38.2244
Gaus Width: 3.0897
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 42.26
Std Dev 2.832
Gaus mean : 42.6437
Gaus Width: 3.5649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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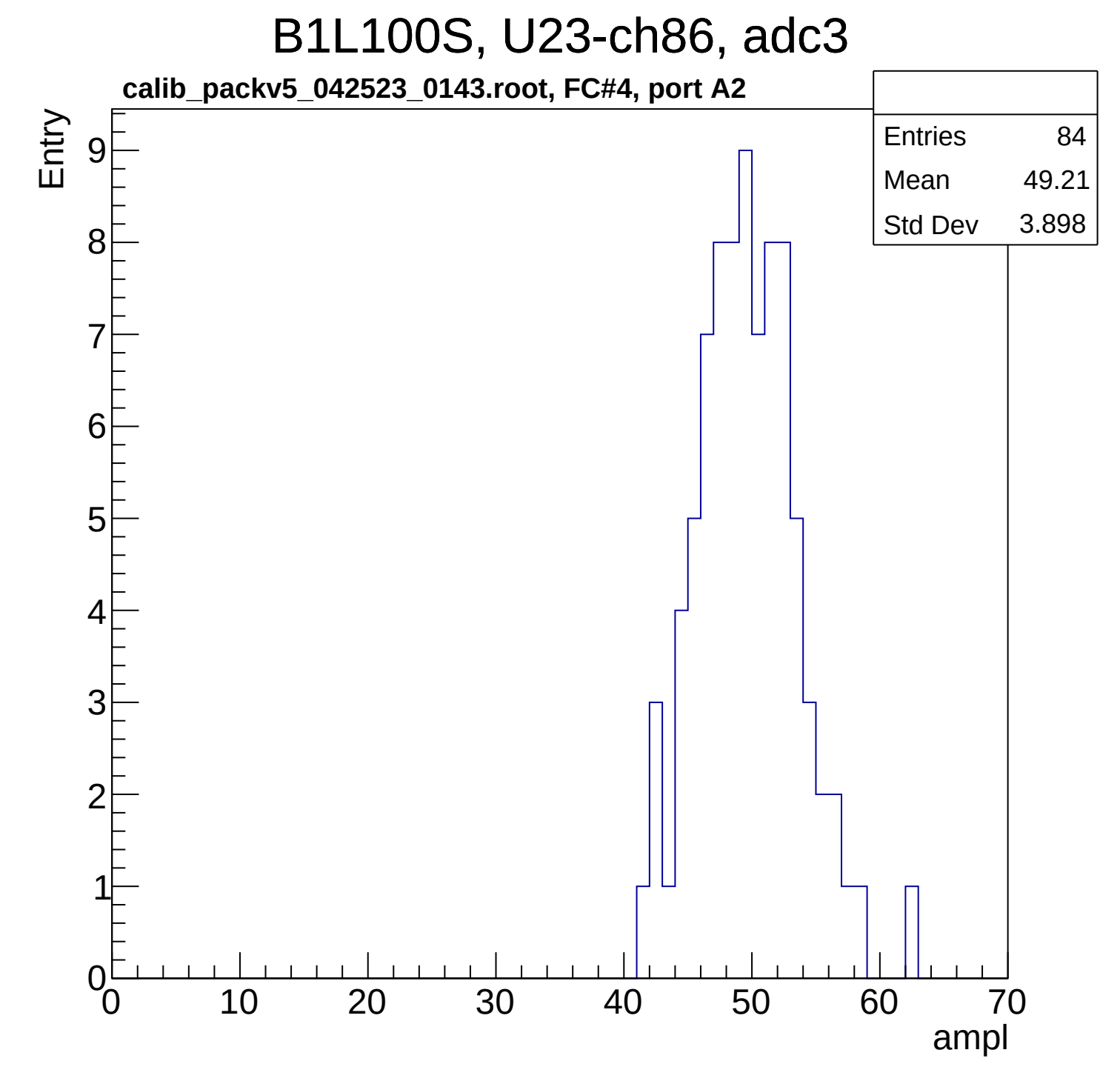
B1L100S, U23-ch86, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 49.21
Std Dev 3.898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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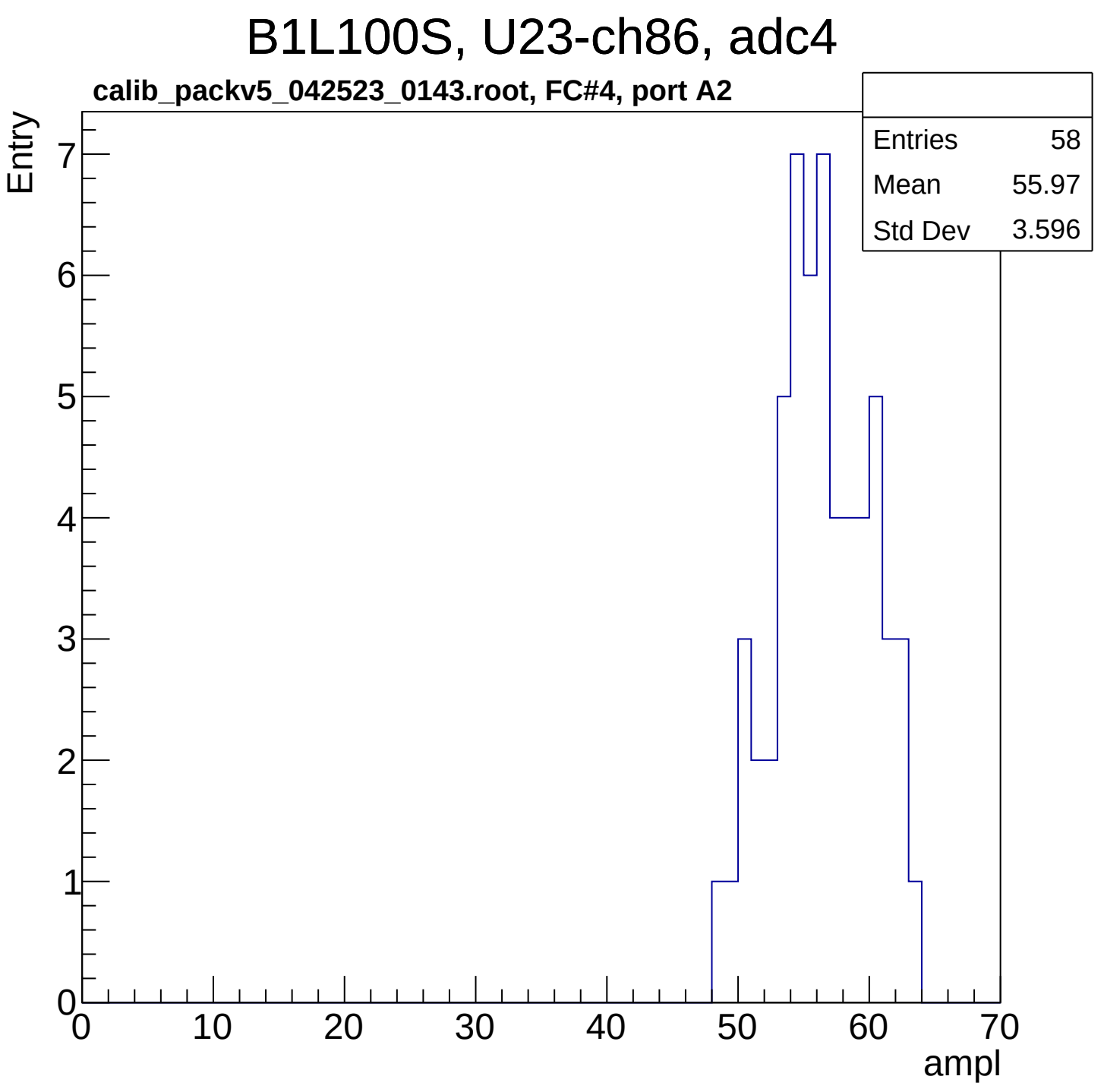
B1L100S, U23-ch86, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.97
Std Dev 3.596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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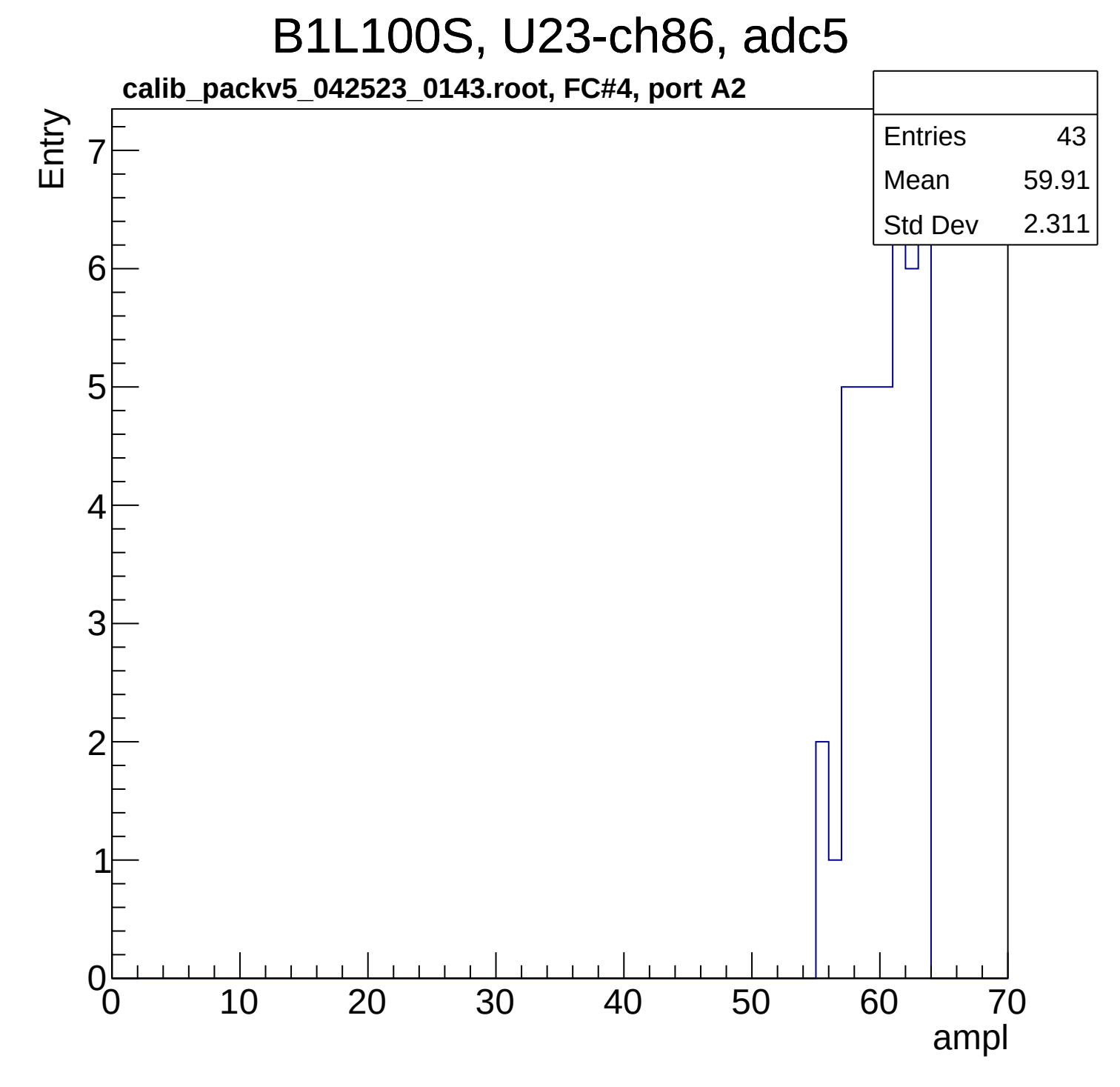
B1L100S, U23-ch86, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59.91
Std Dev 2.311
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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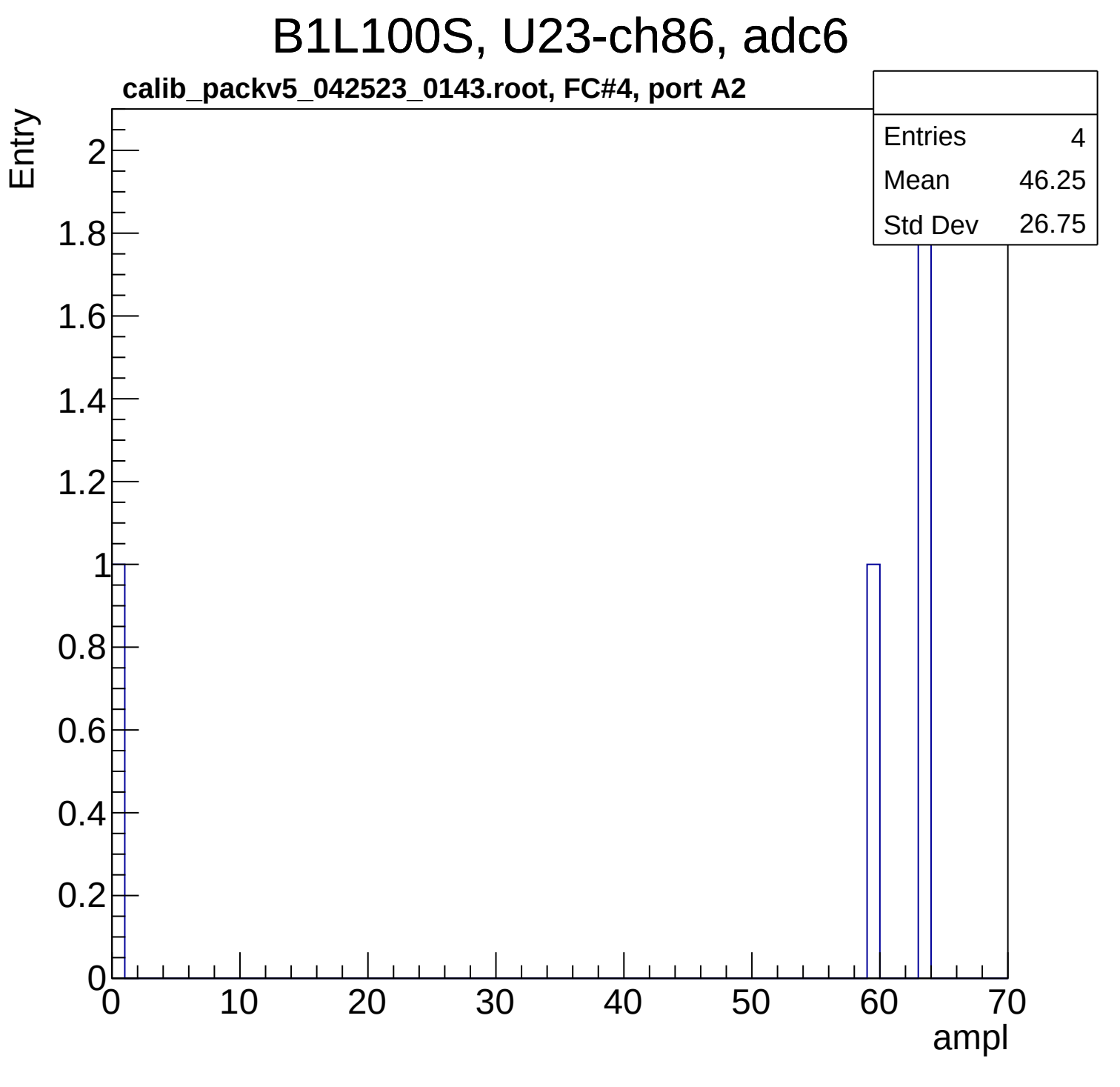
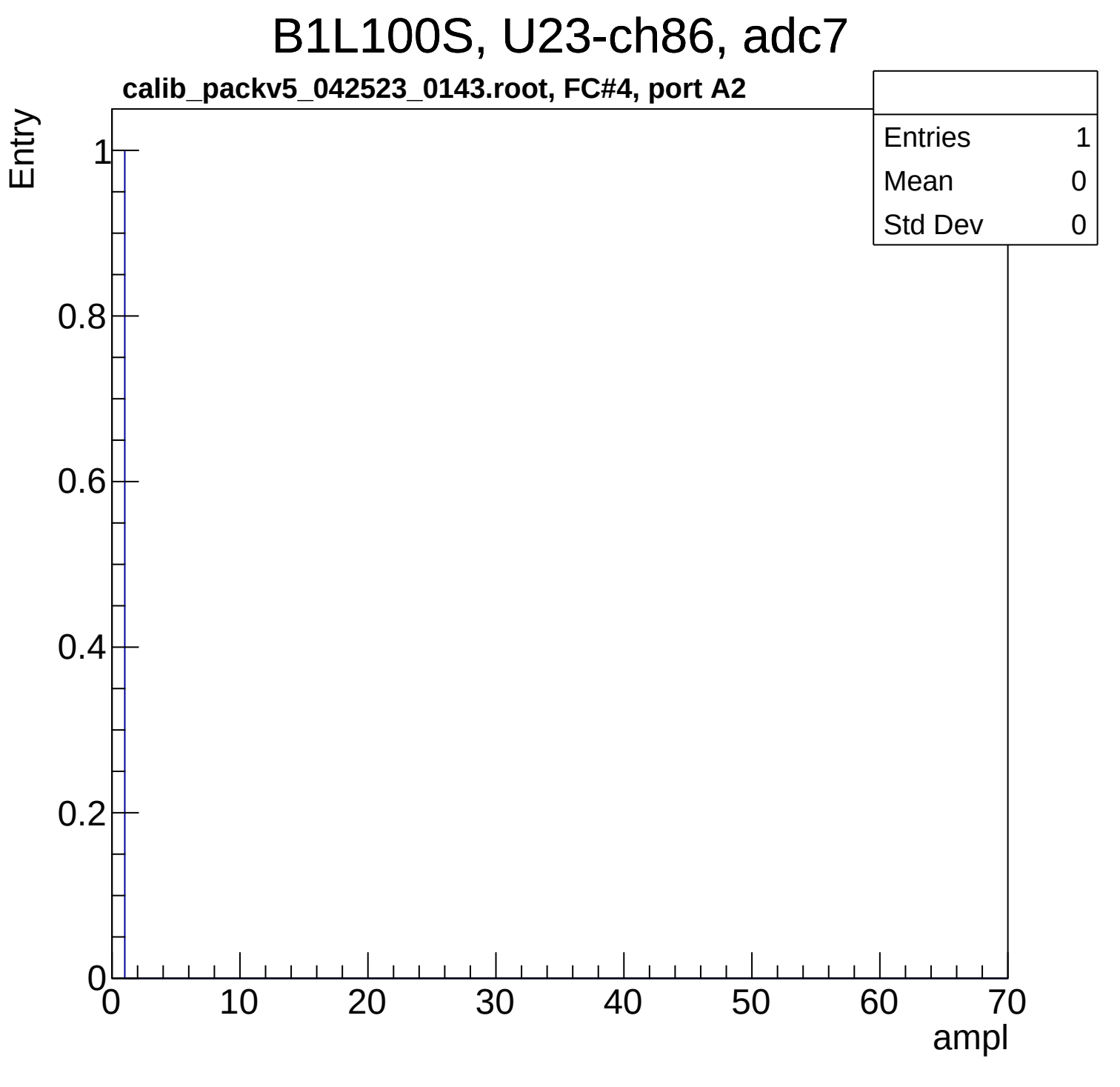
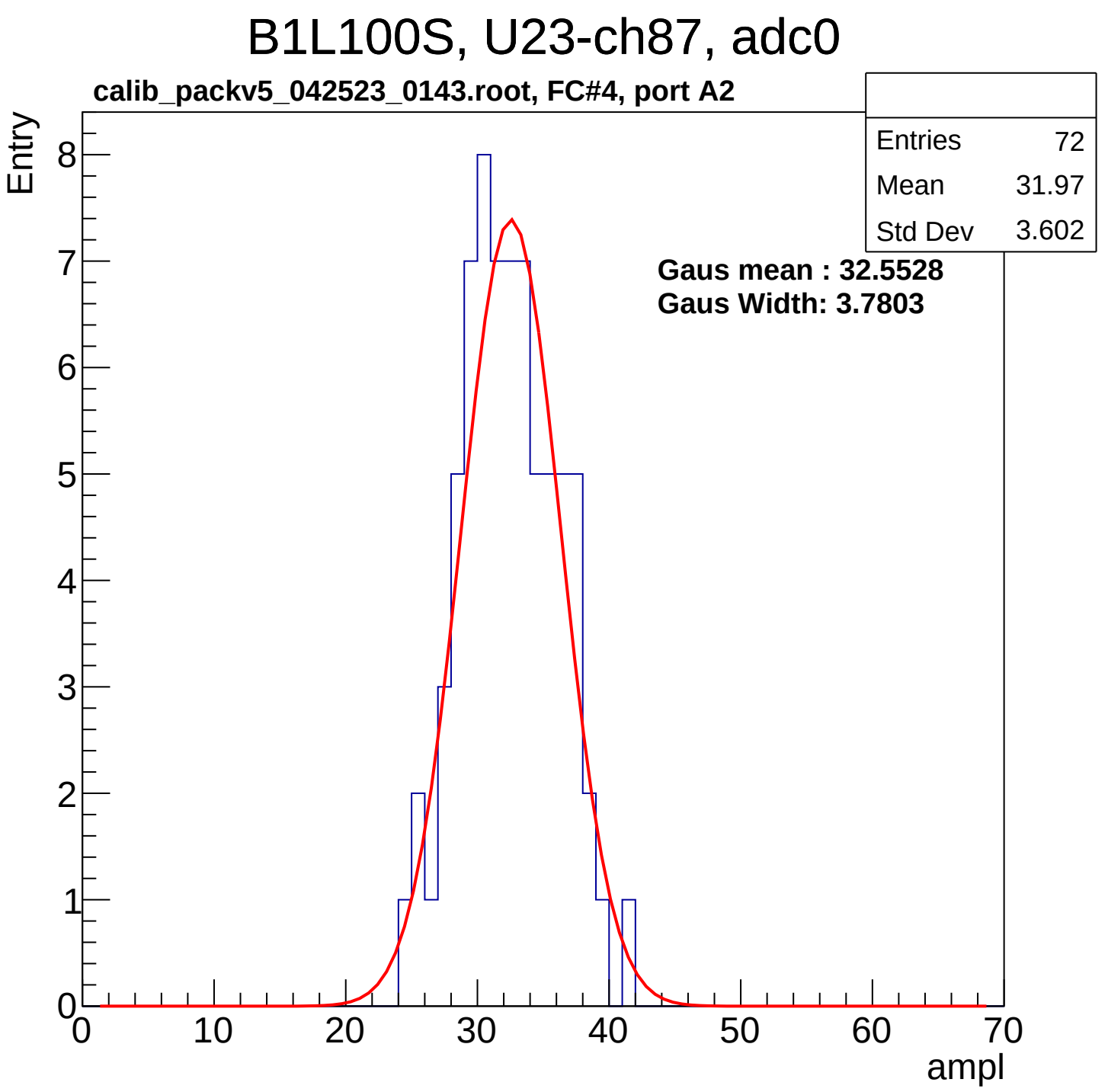
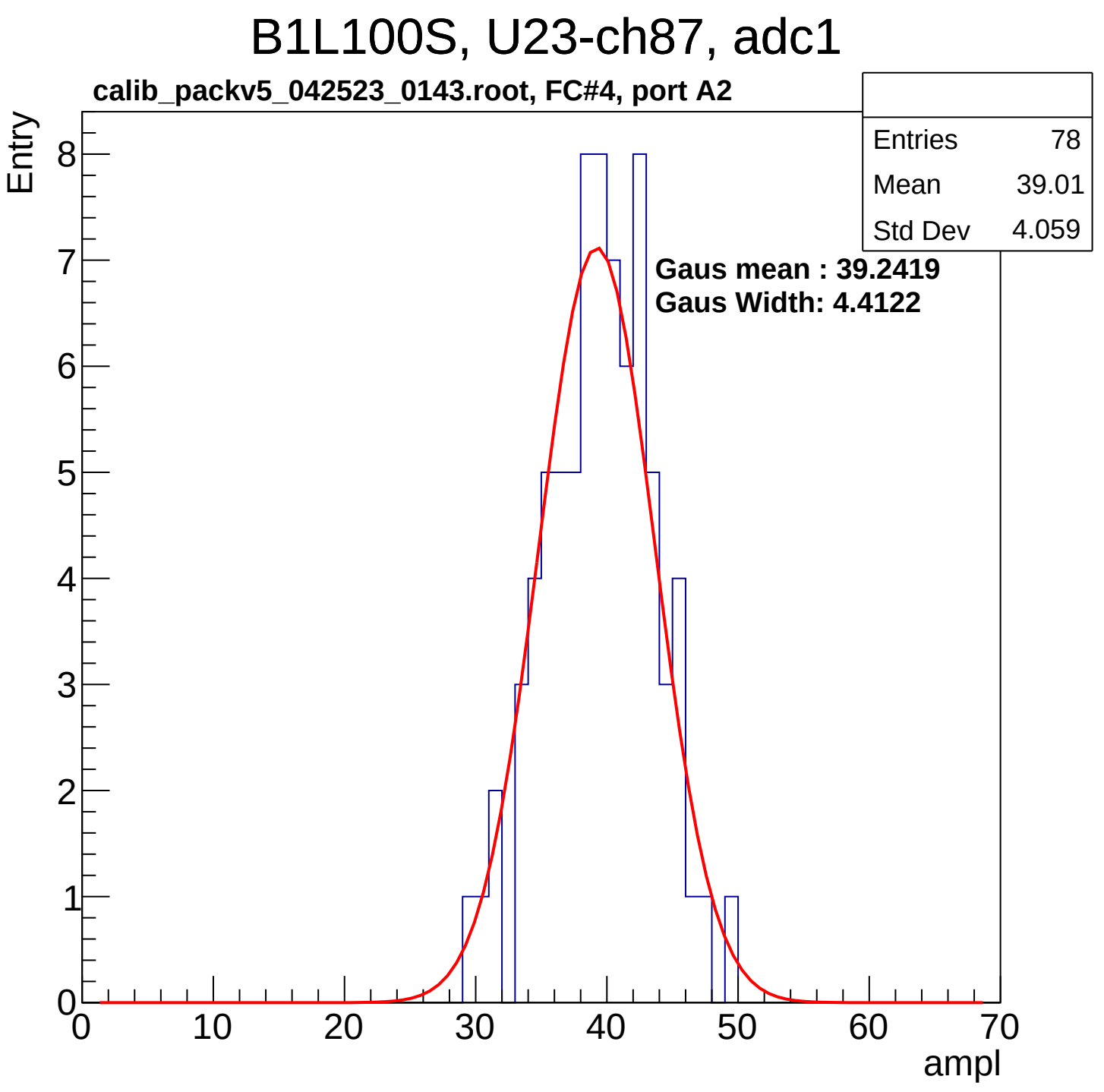
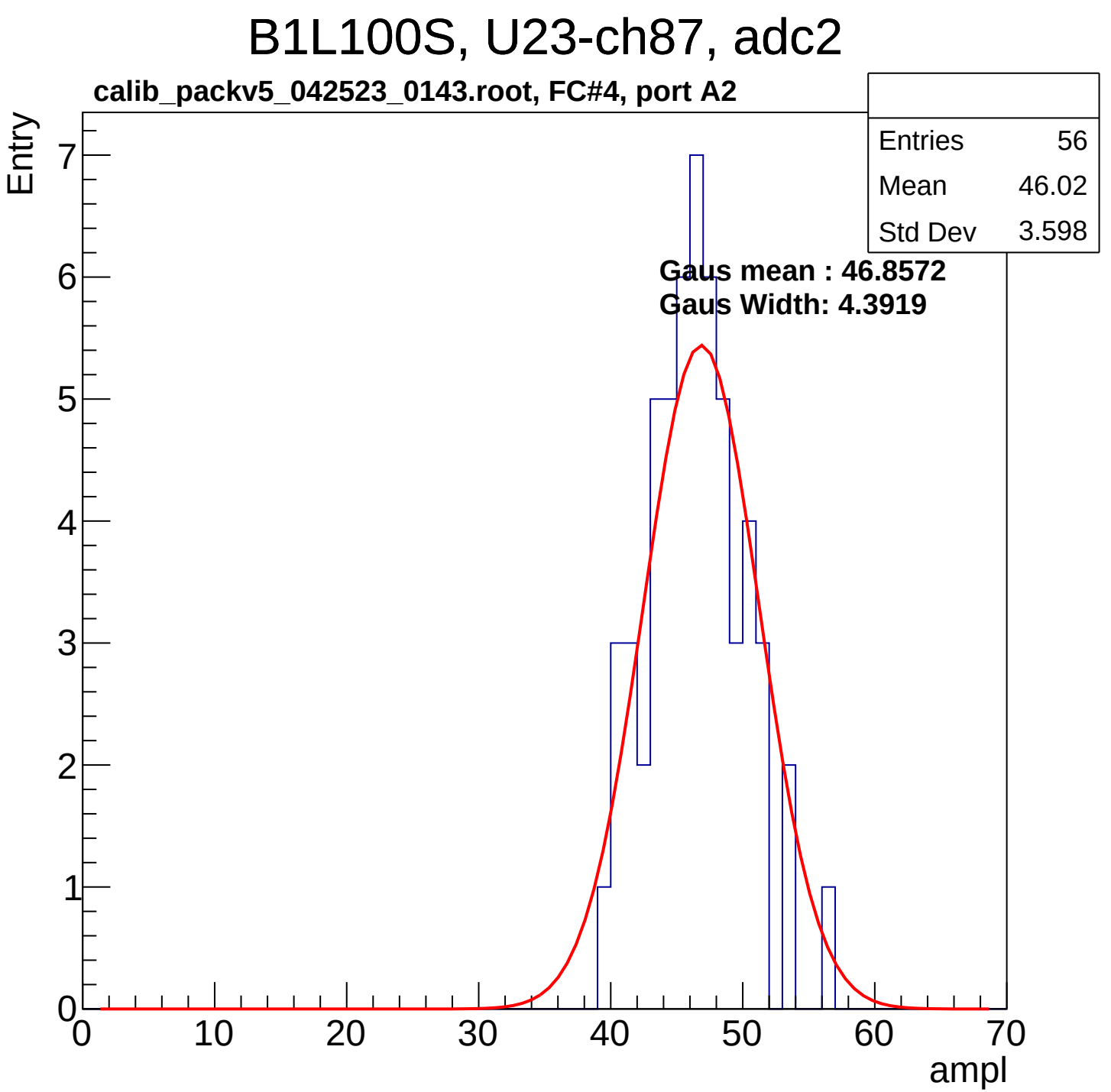
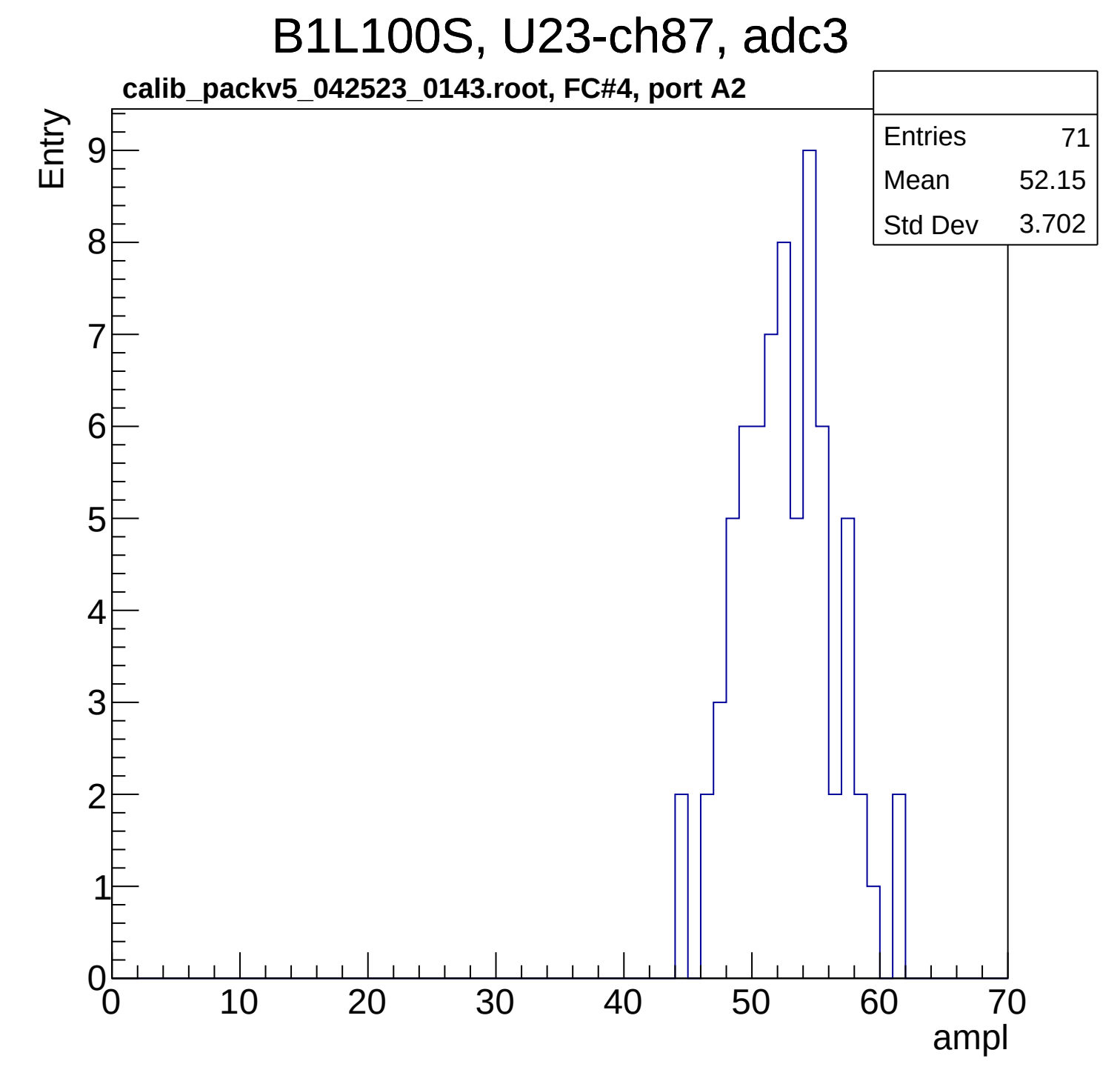

B1L100S, U23-ch86, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.25
Std Dev 26.75
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch86, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 31.97
Std Dev 3.602
Gaus mean : 32.5528
Gaus Width: 3.7803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 39.01
Std Dev 4.059
Gaus mean : 39.2419
Gaus Width: 4.4122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 46.02
Std Dev 3.598
Gaus mean : 46.8572
Gaus Width: 4.3919
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch87, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 52.15
Std Dev 3.702
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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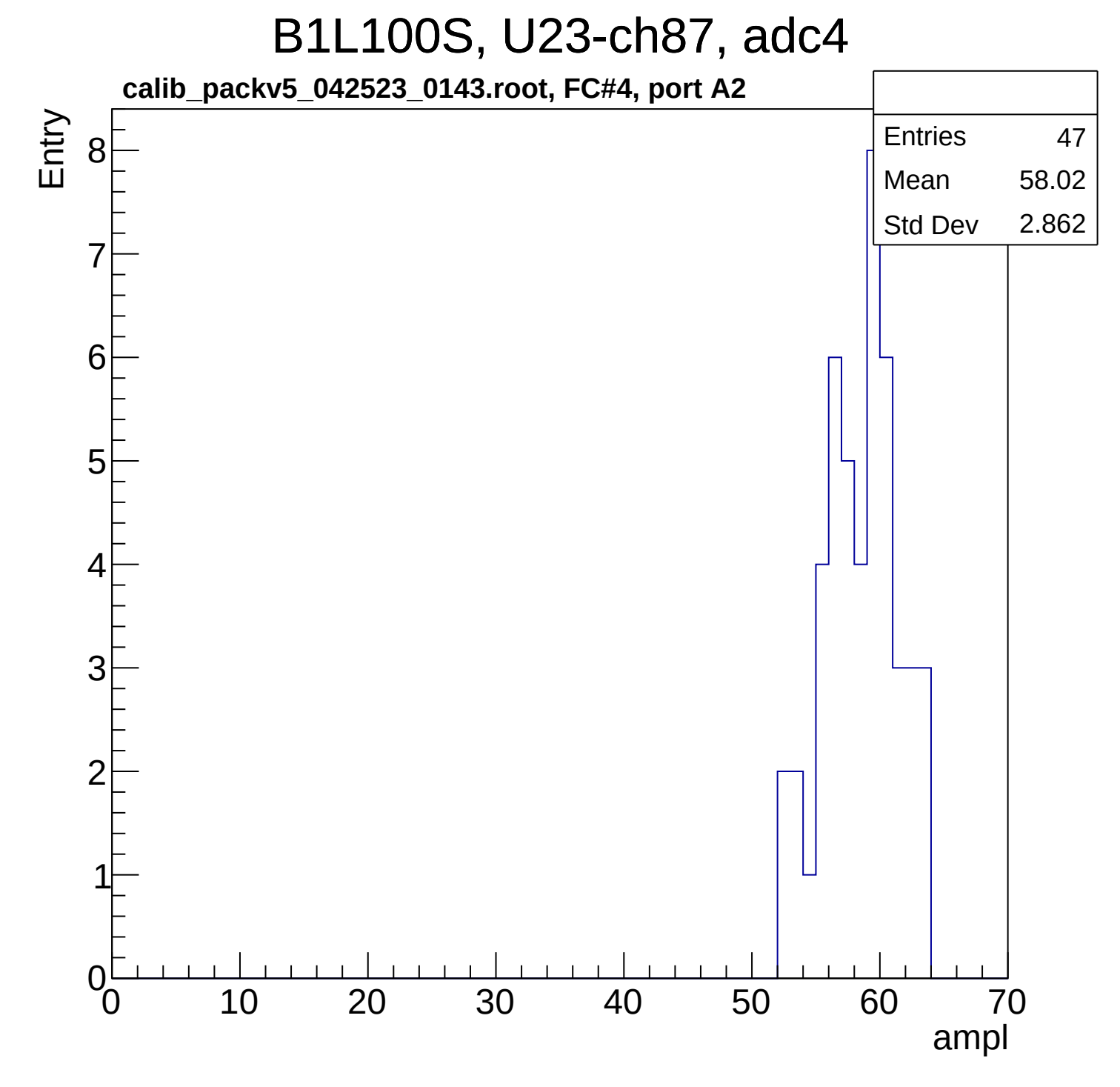
B1L100S, U23-ch87, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.02
Std Dev 2.862
Entry
ampl
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2
3
4
5
6
7
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0
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40
50
60
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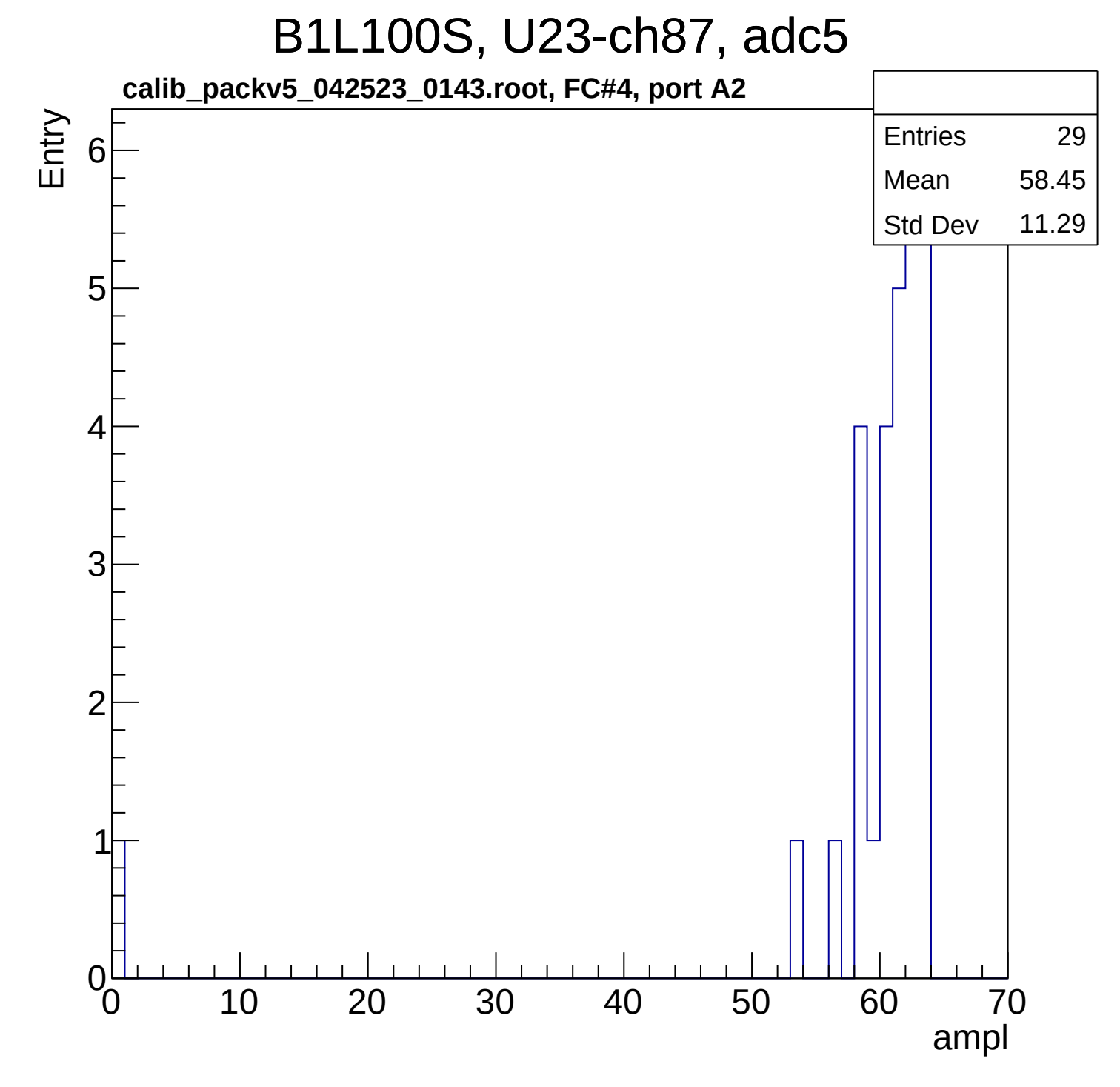
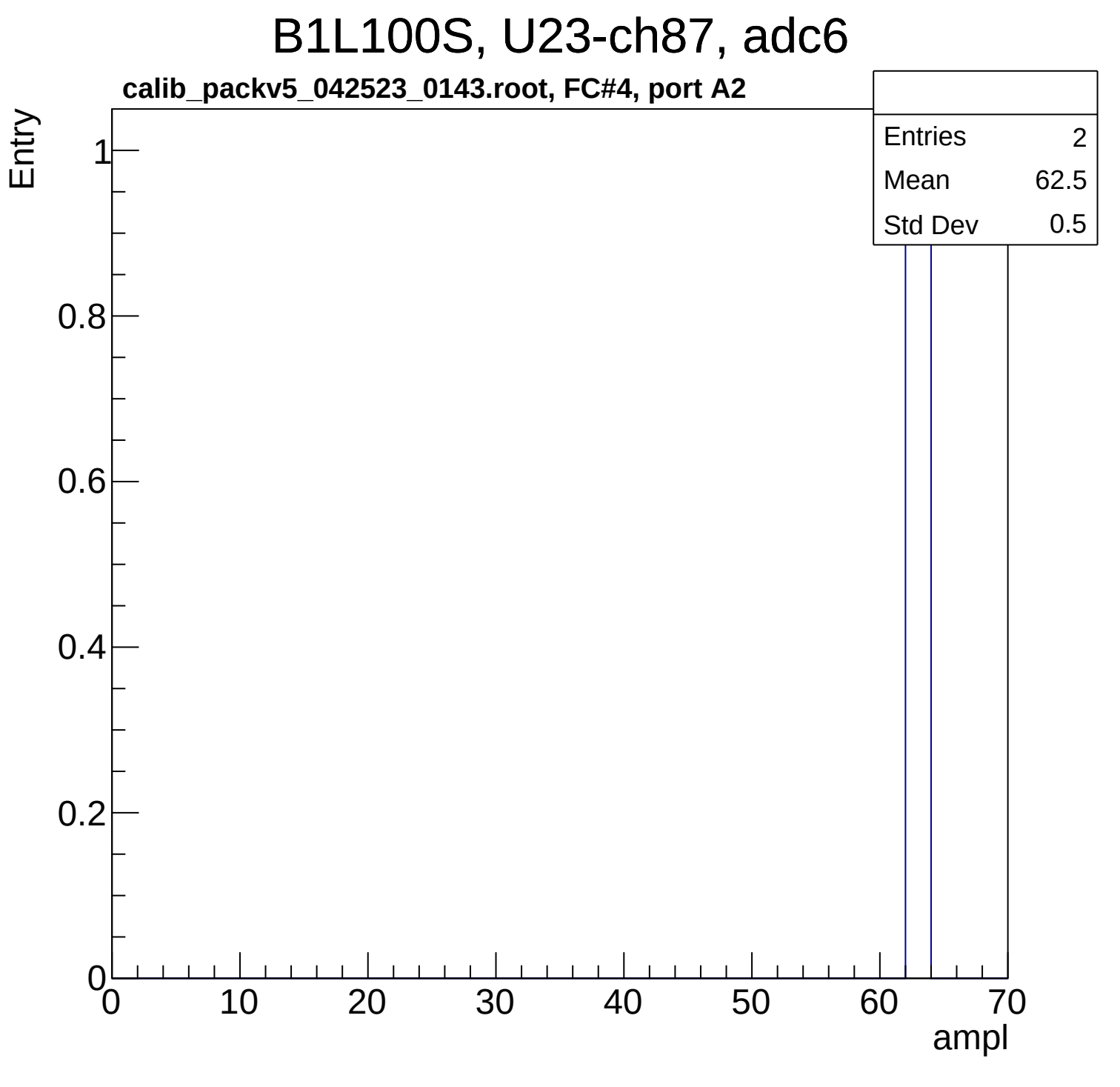
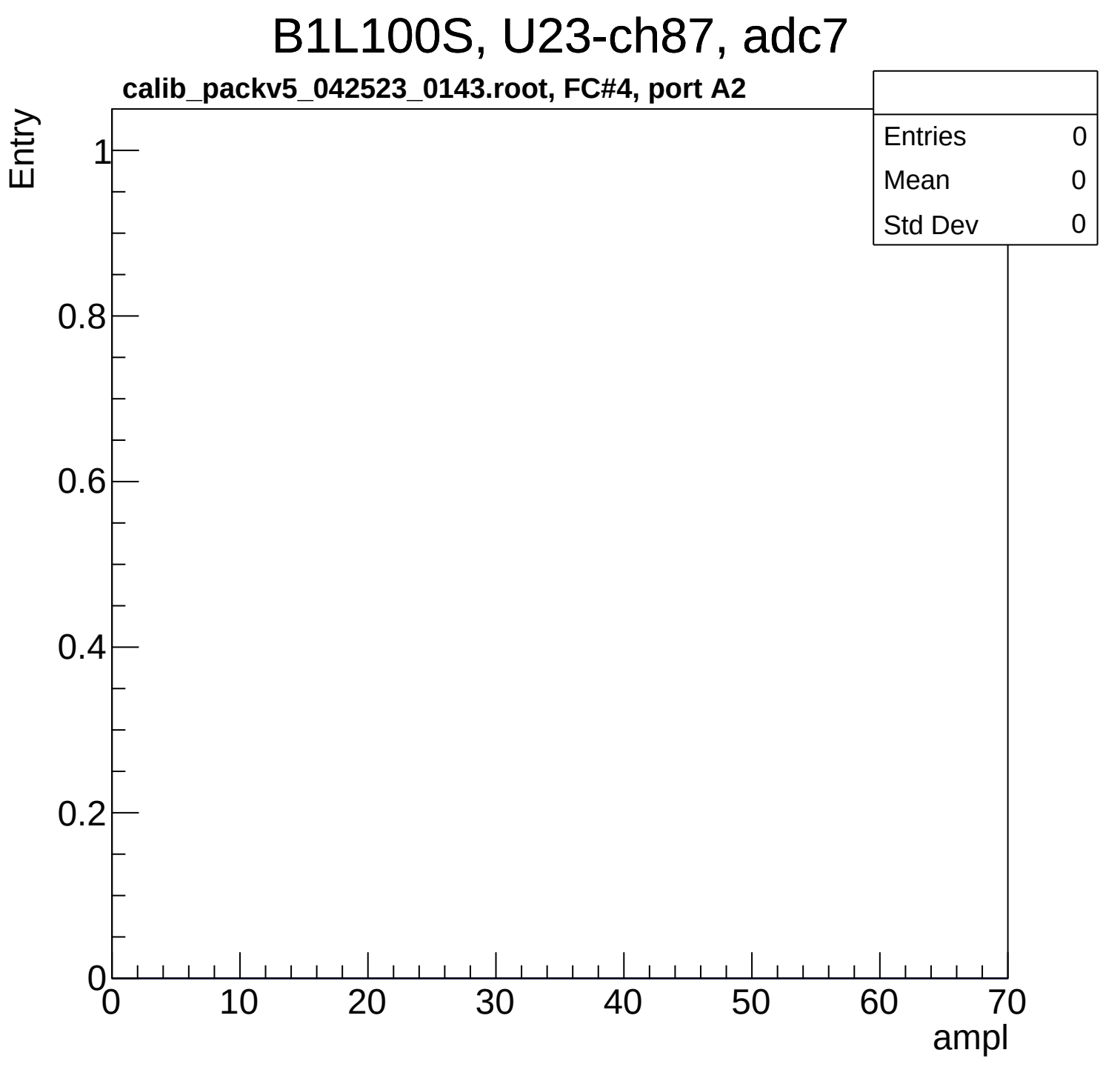

B1L100S, U23-ch87, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.45
Std Dev 11.29
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch87, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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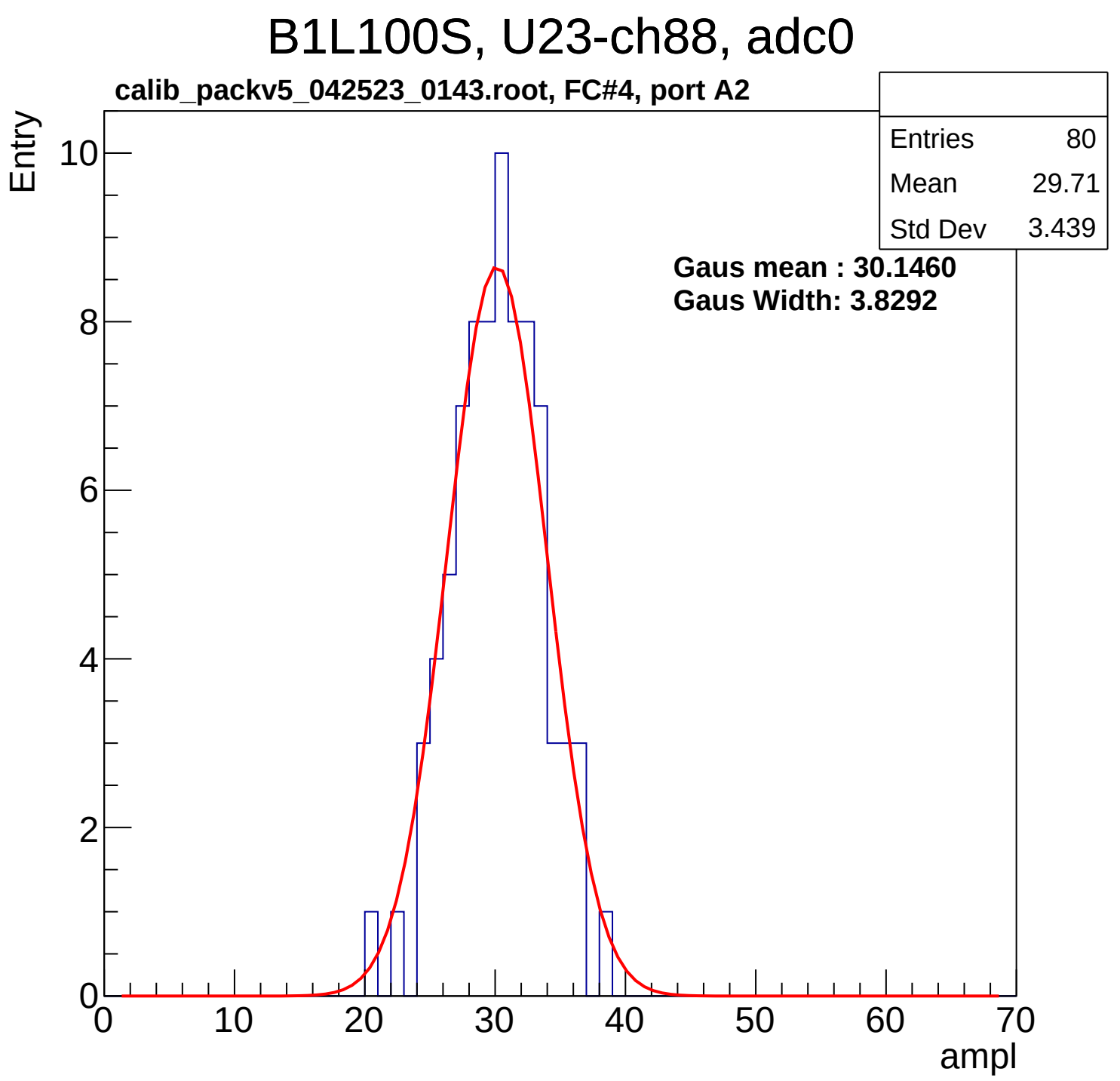
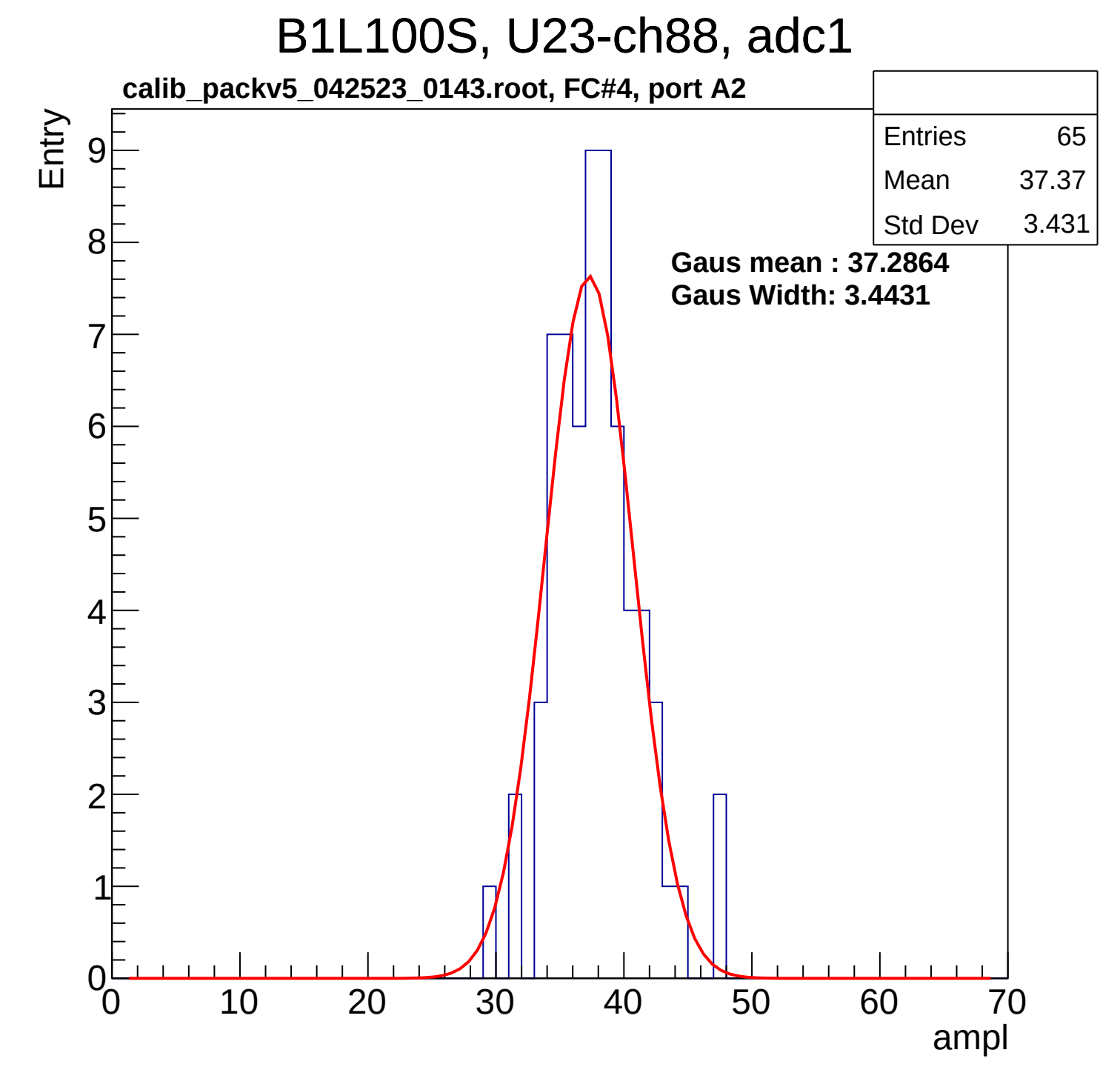
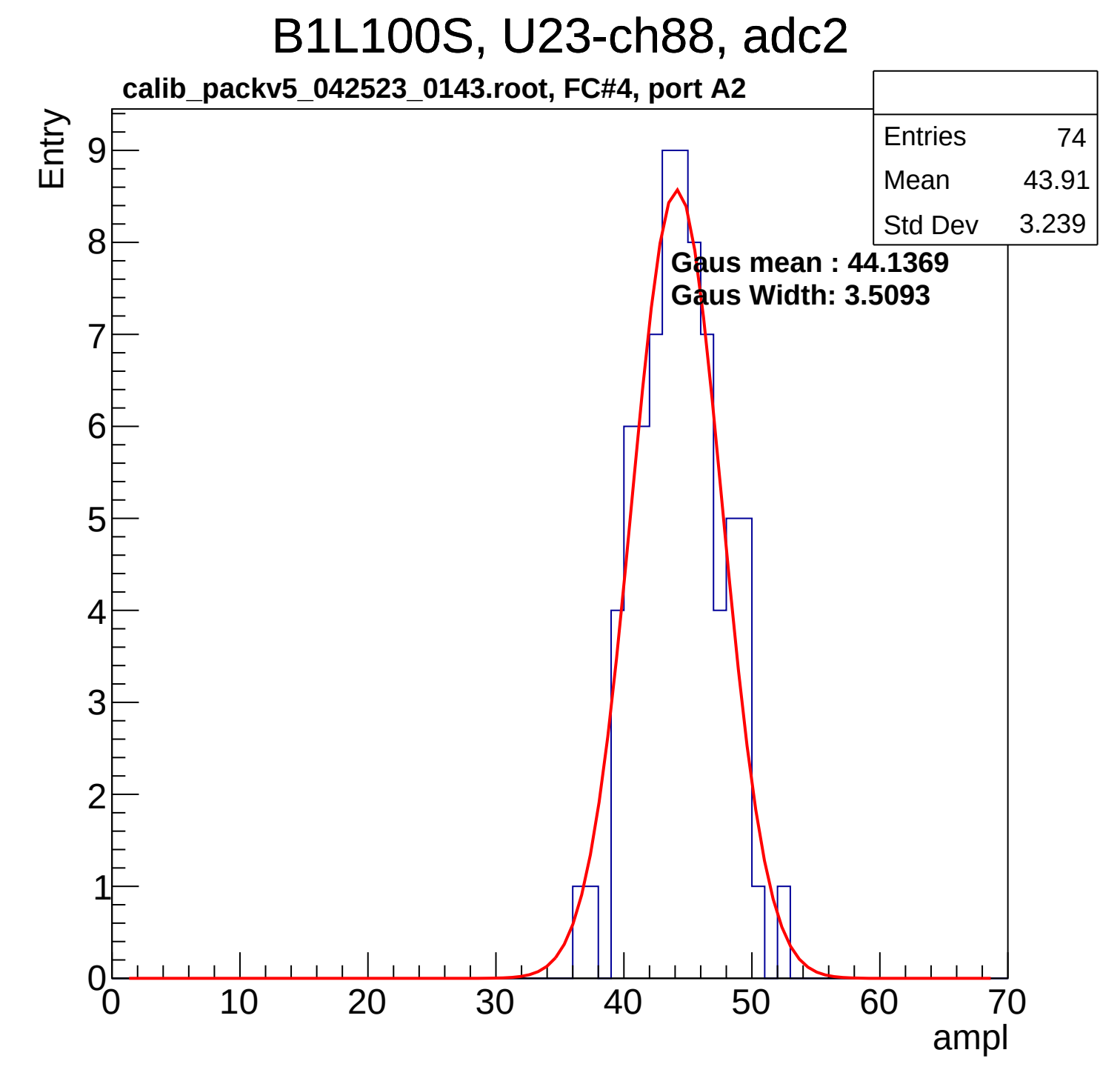
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch88, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.71
Std Dev 3.439
Gaus mean : 30.1460
Gaus Width: 3.8292
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch88, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 37.37
Std Dev 3.431
Gaus mean : 37.2864
Gaus Width: 3.4431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.91
Std Dev 3.239
Gaus mean : 44.1369
Gaus Width: 3.5093
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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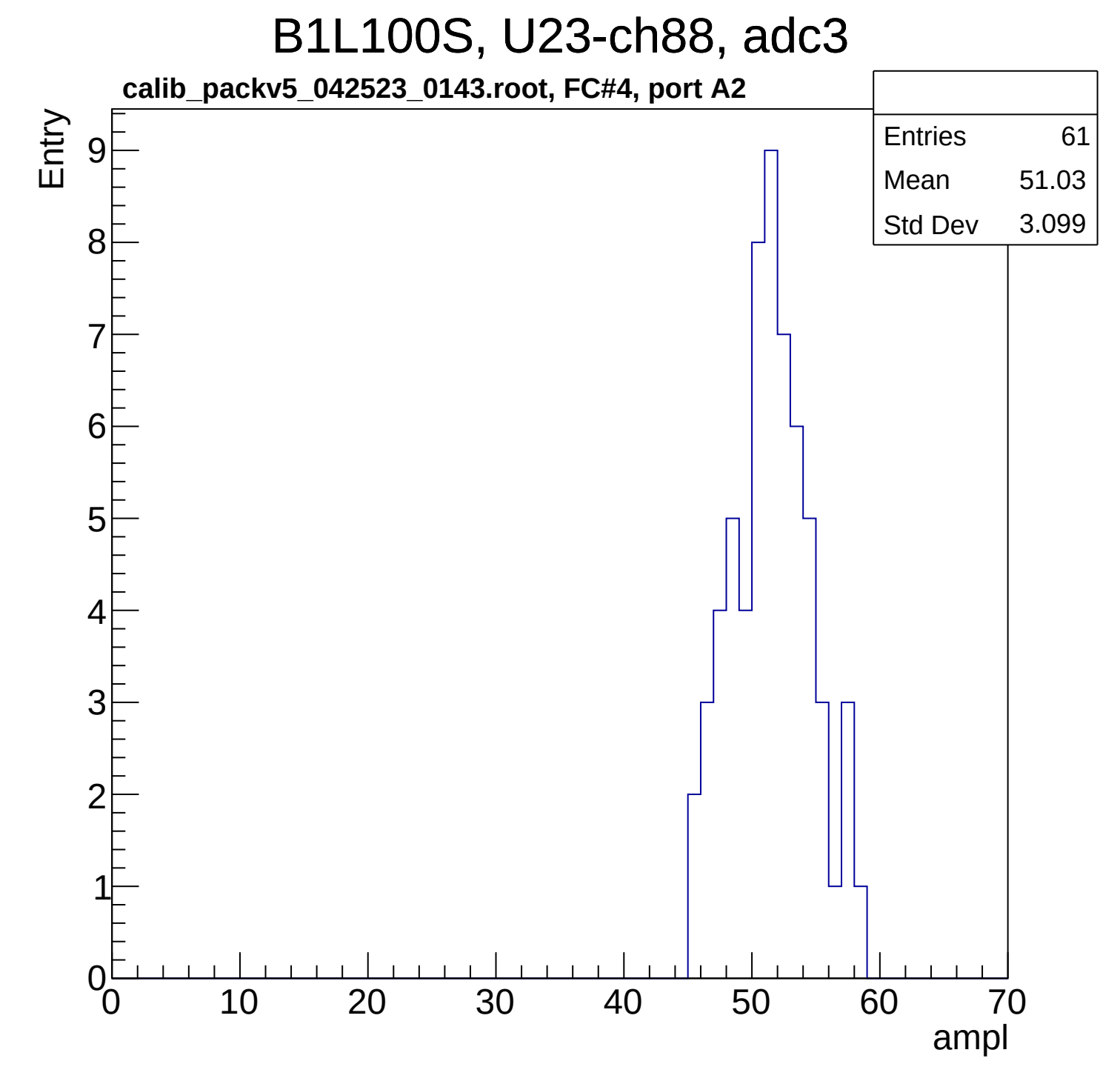
B1L100S, U23-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 51.03
Std Dev 3.099
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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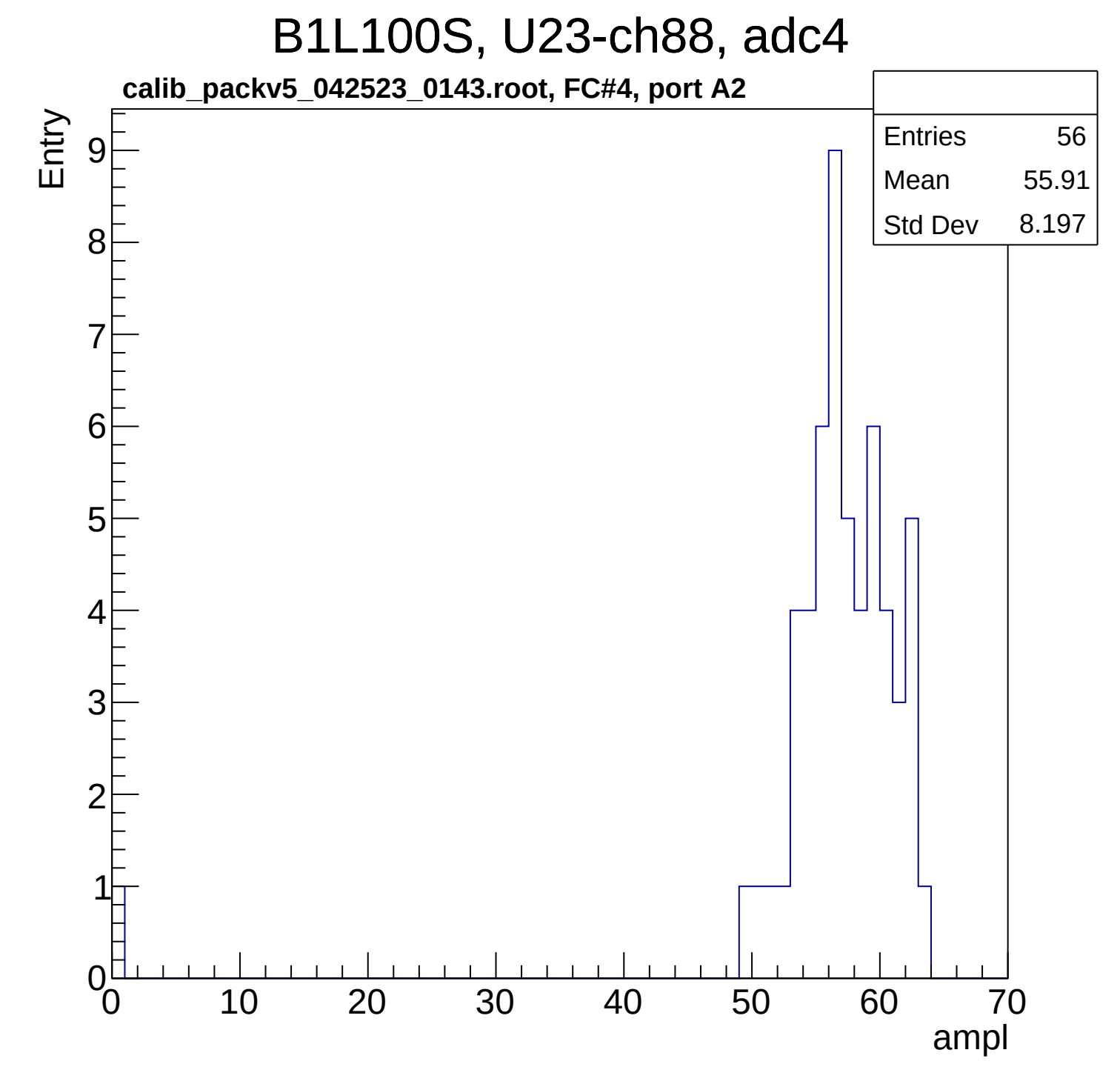
B1L100S, U23-ch88, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.91
Std Dev 8.197
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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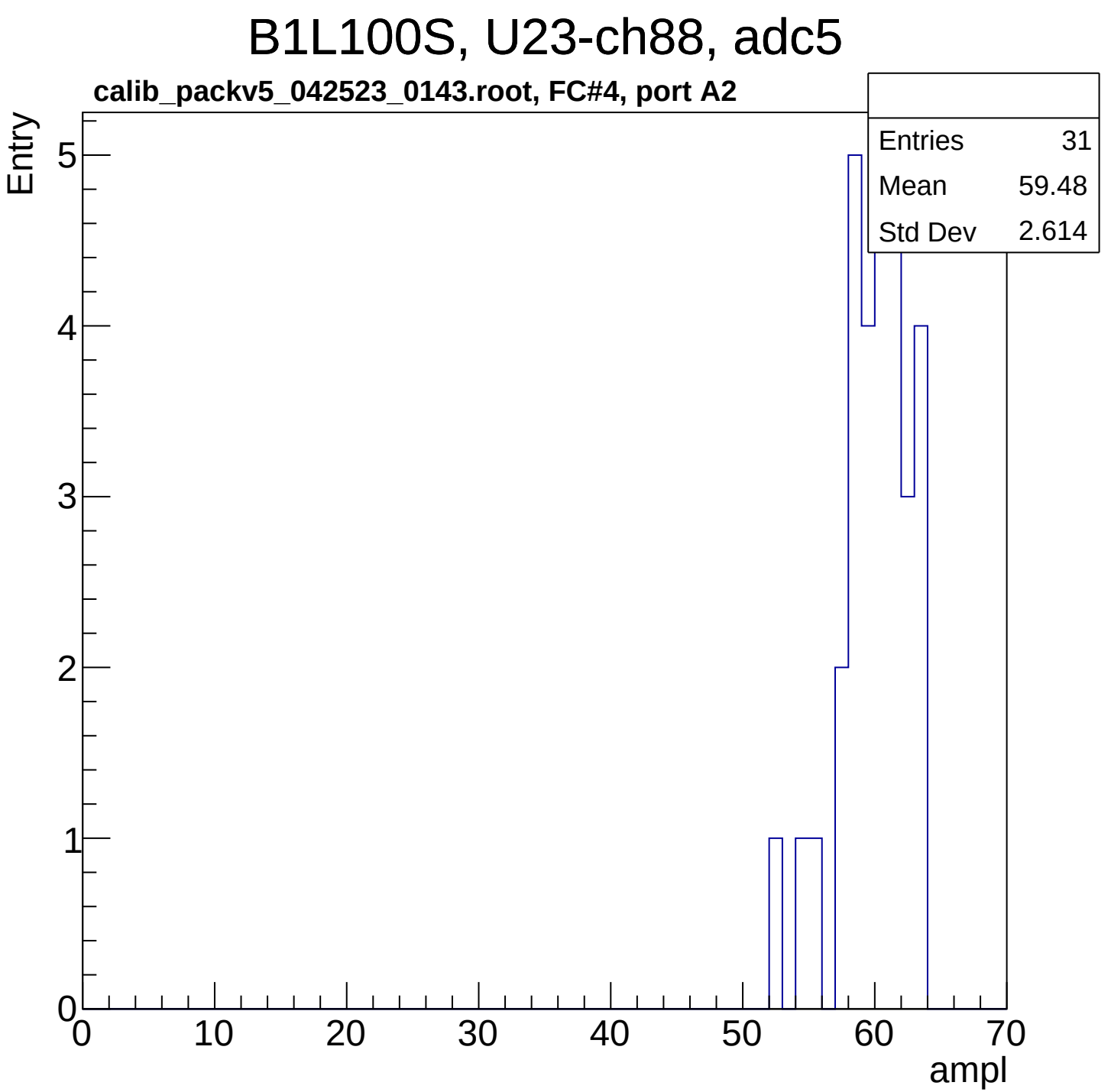
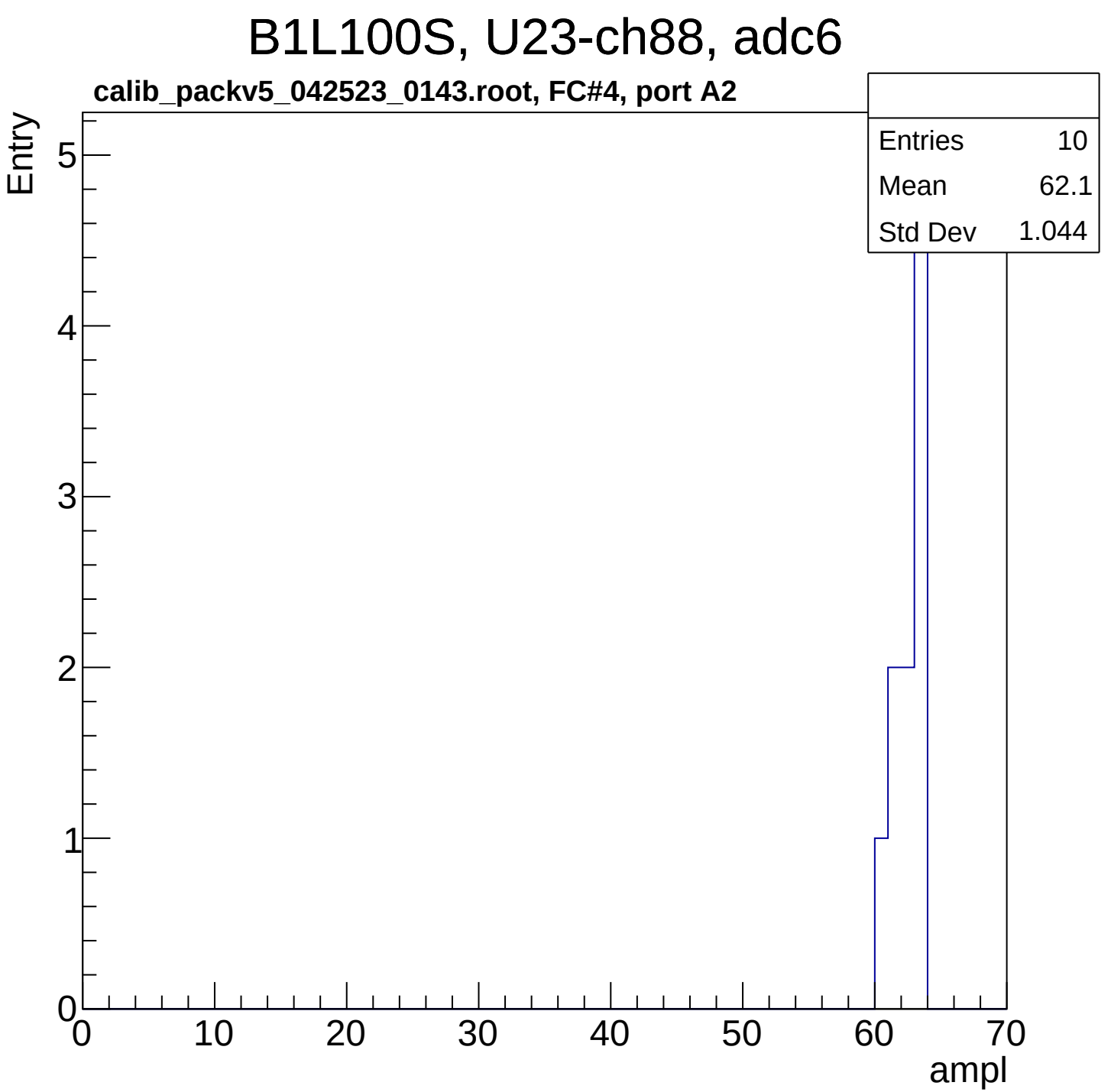

B1L100S, U23-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59.48
Std Dev 2.614
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch88, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 62.1
Std Dev 1.044
Entry
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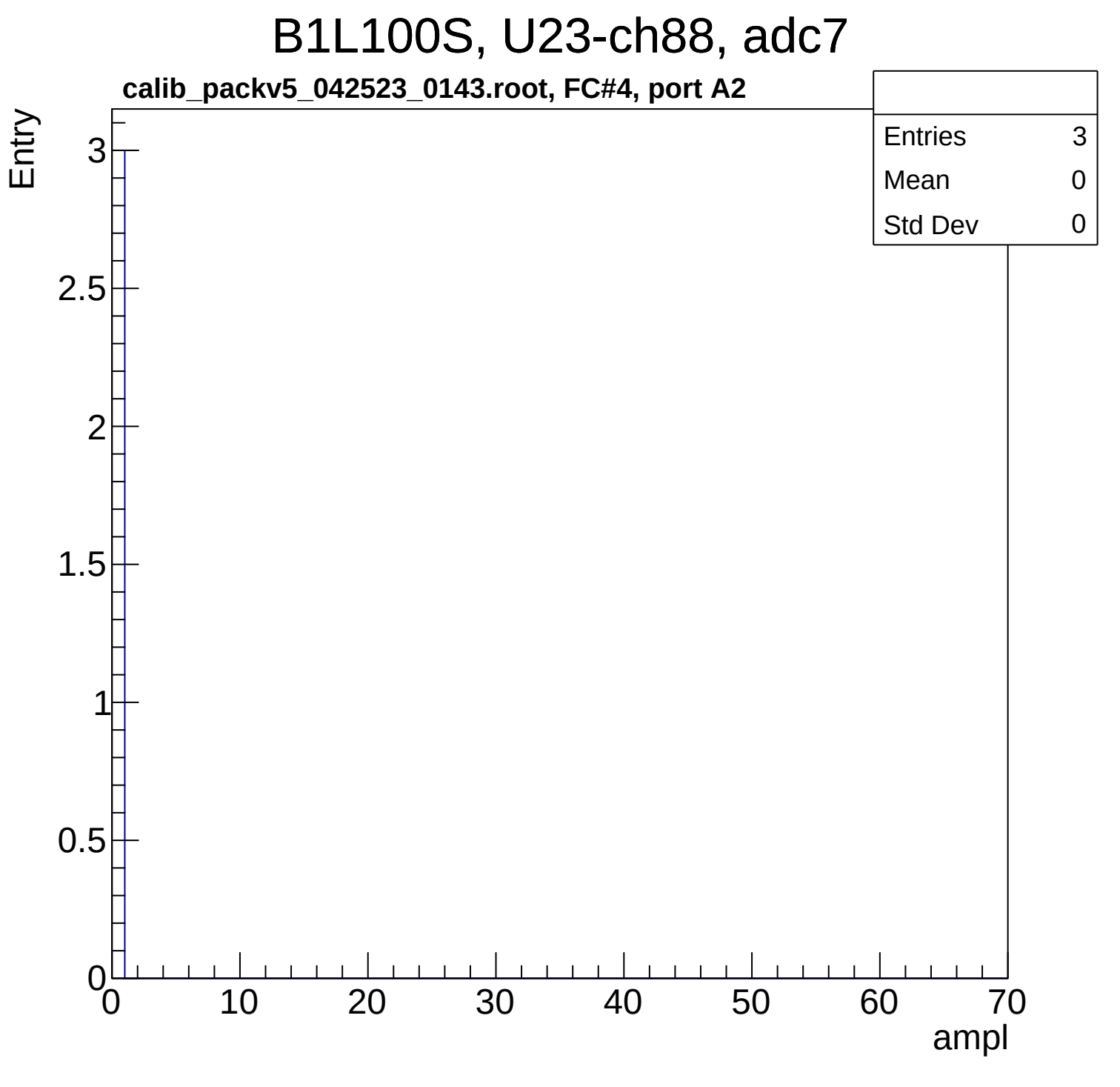
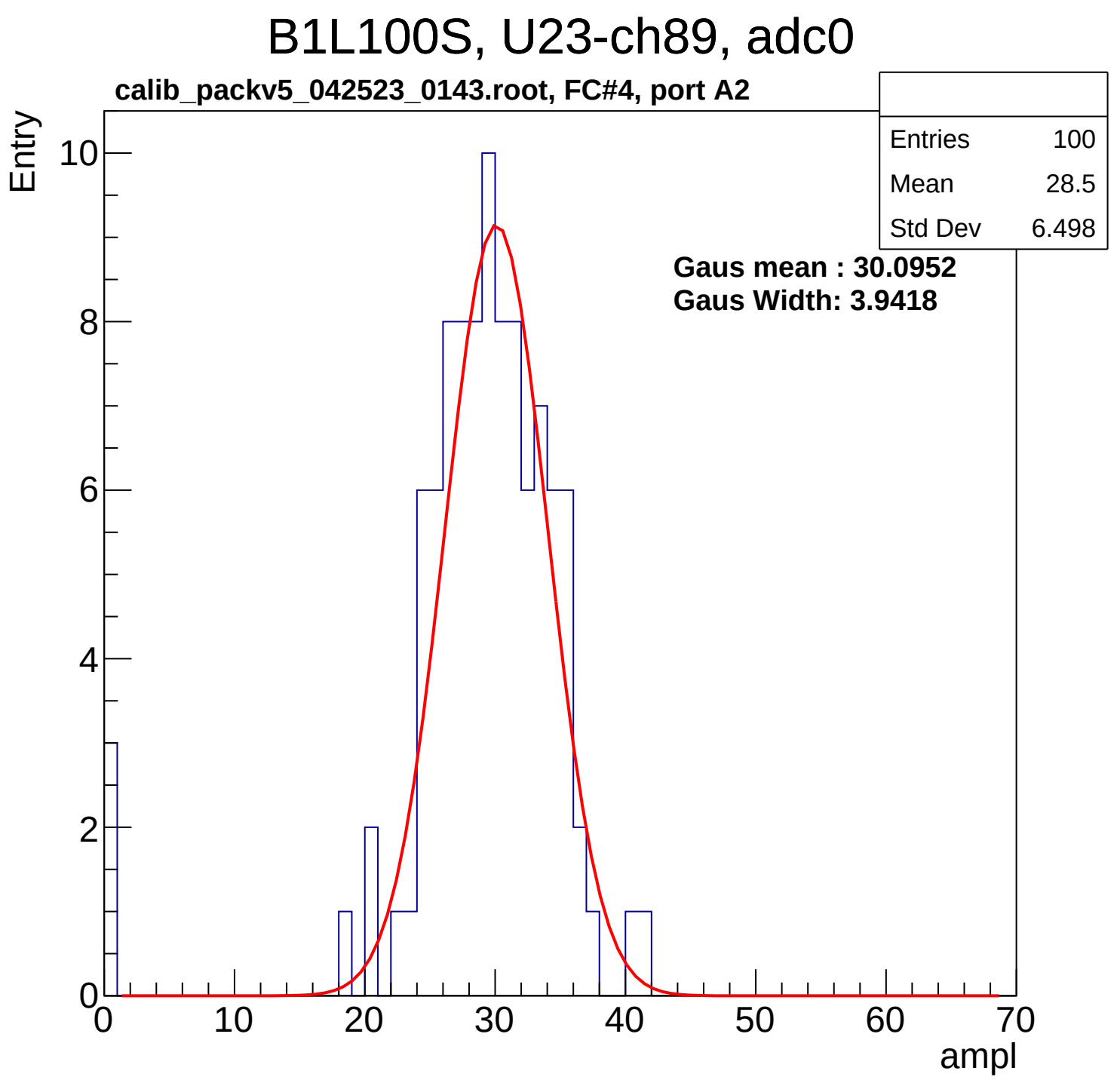
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch88, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 100
Mean 28.5
Std Dev 6.498
Gaus mean : 30.0952
Gaus Width: 3.9418
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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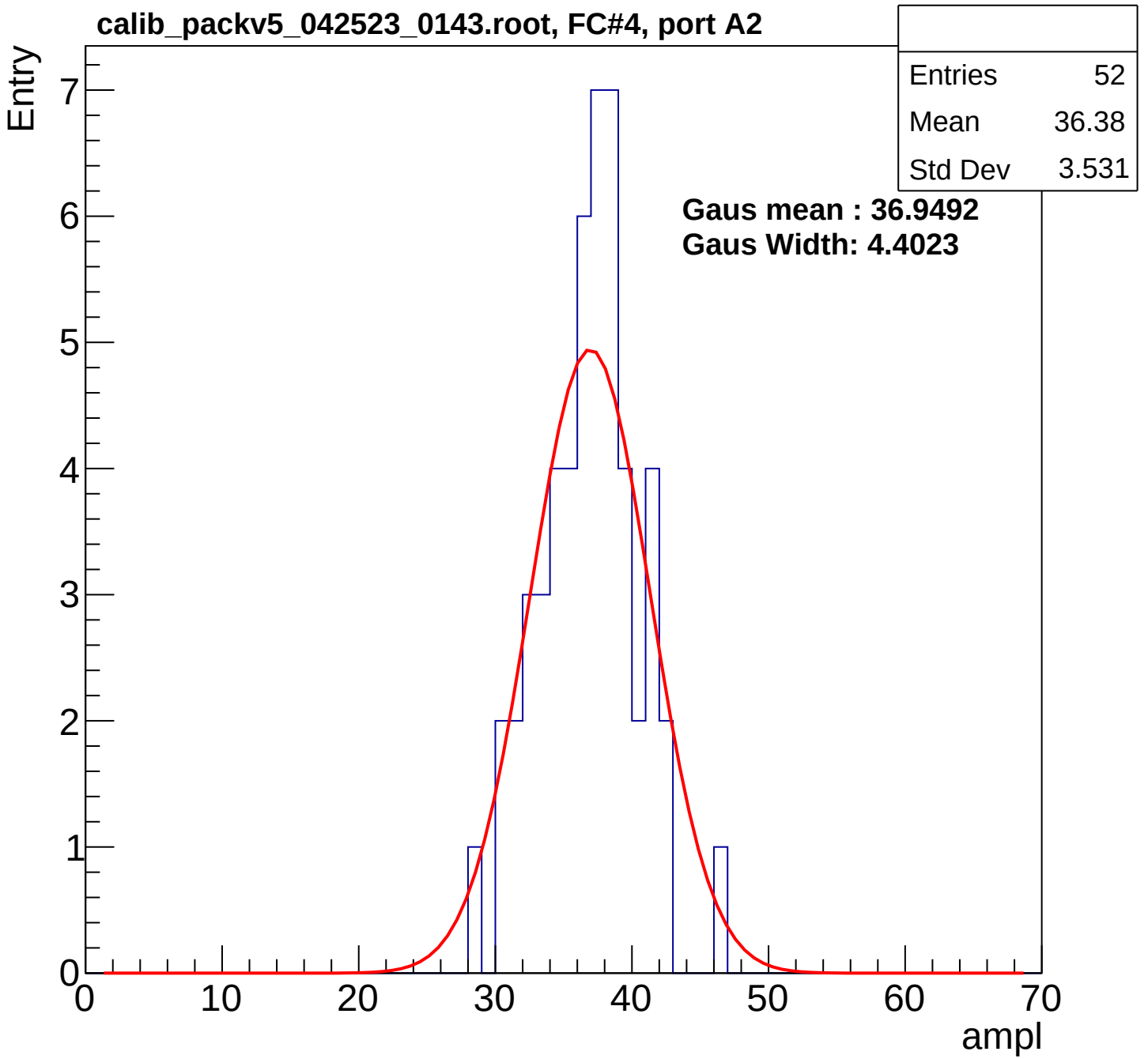
B1L100S, U23-ch89, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 36.38
Std Dev 3.531
Gaus mean : 36.9492
Gaus Width: 4.4023
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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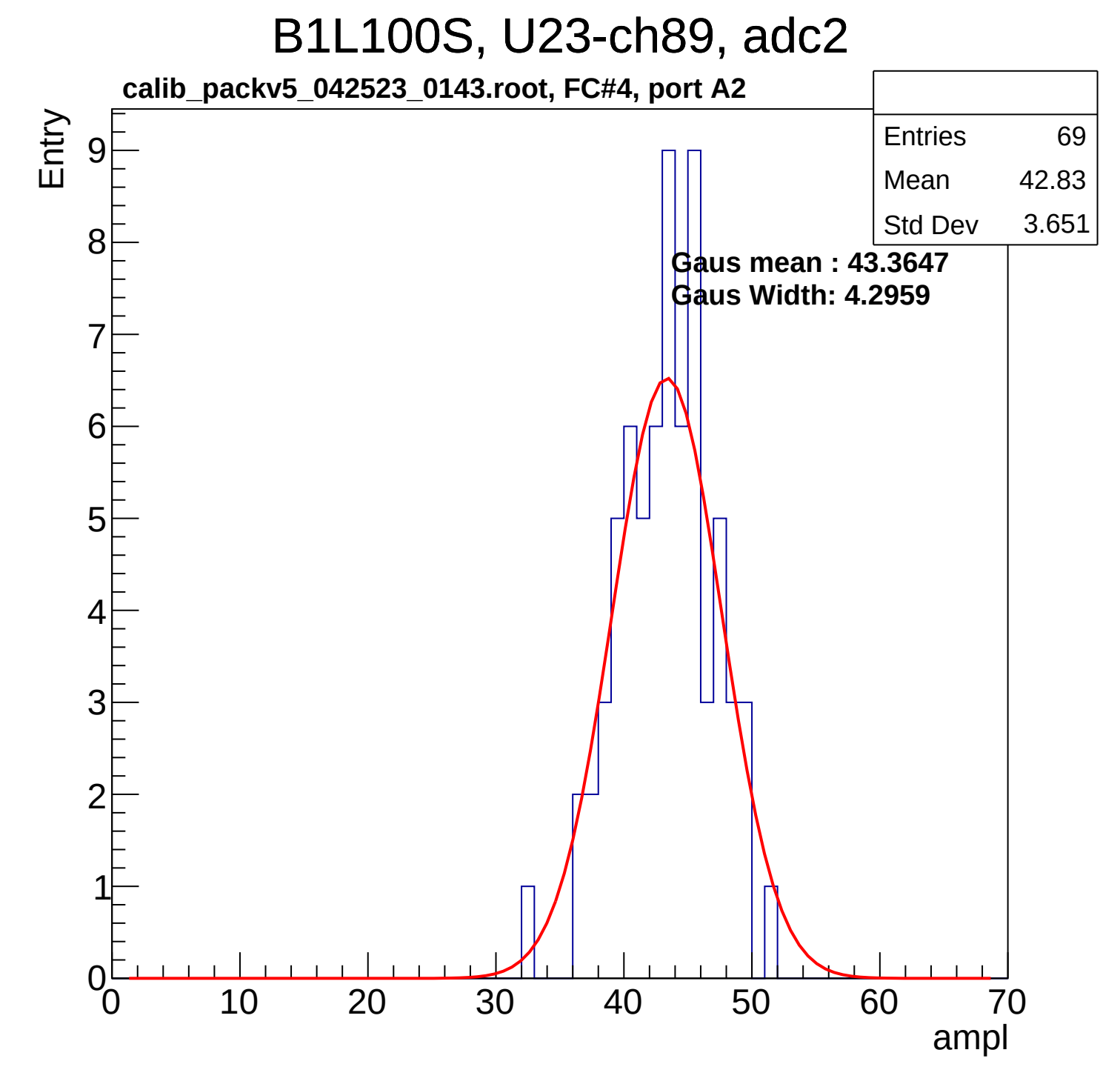
B1L100S, U23-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.83
Std Dev 3.651
Gaus mean : 43.3647
Gaus Width: 4.2959
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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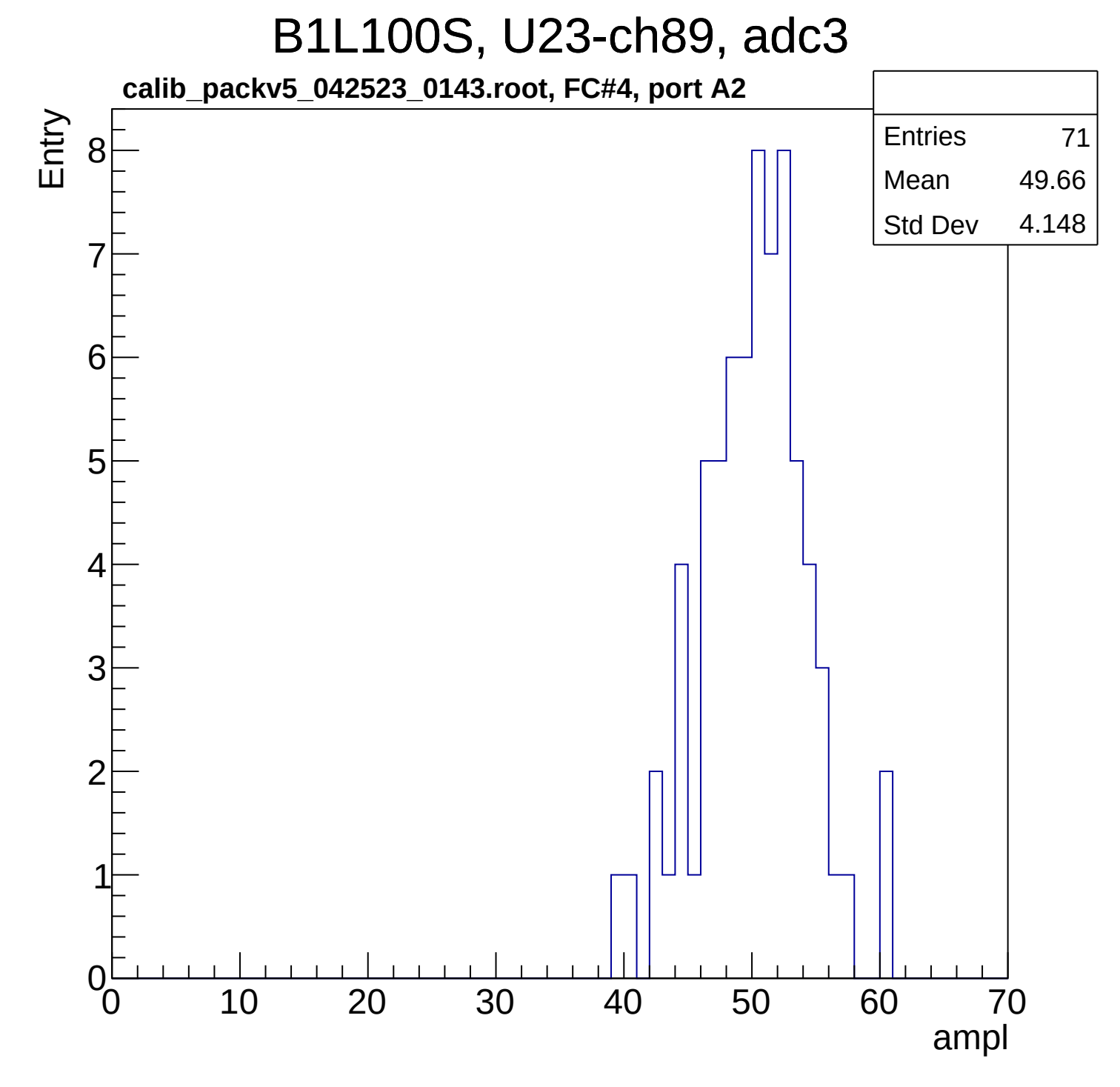
B1L100S, U23-ch89, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.66
Std Dev 4.148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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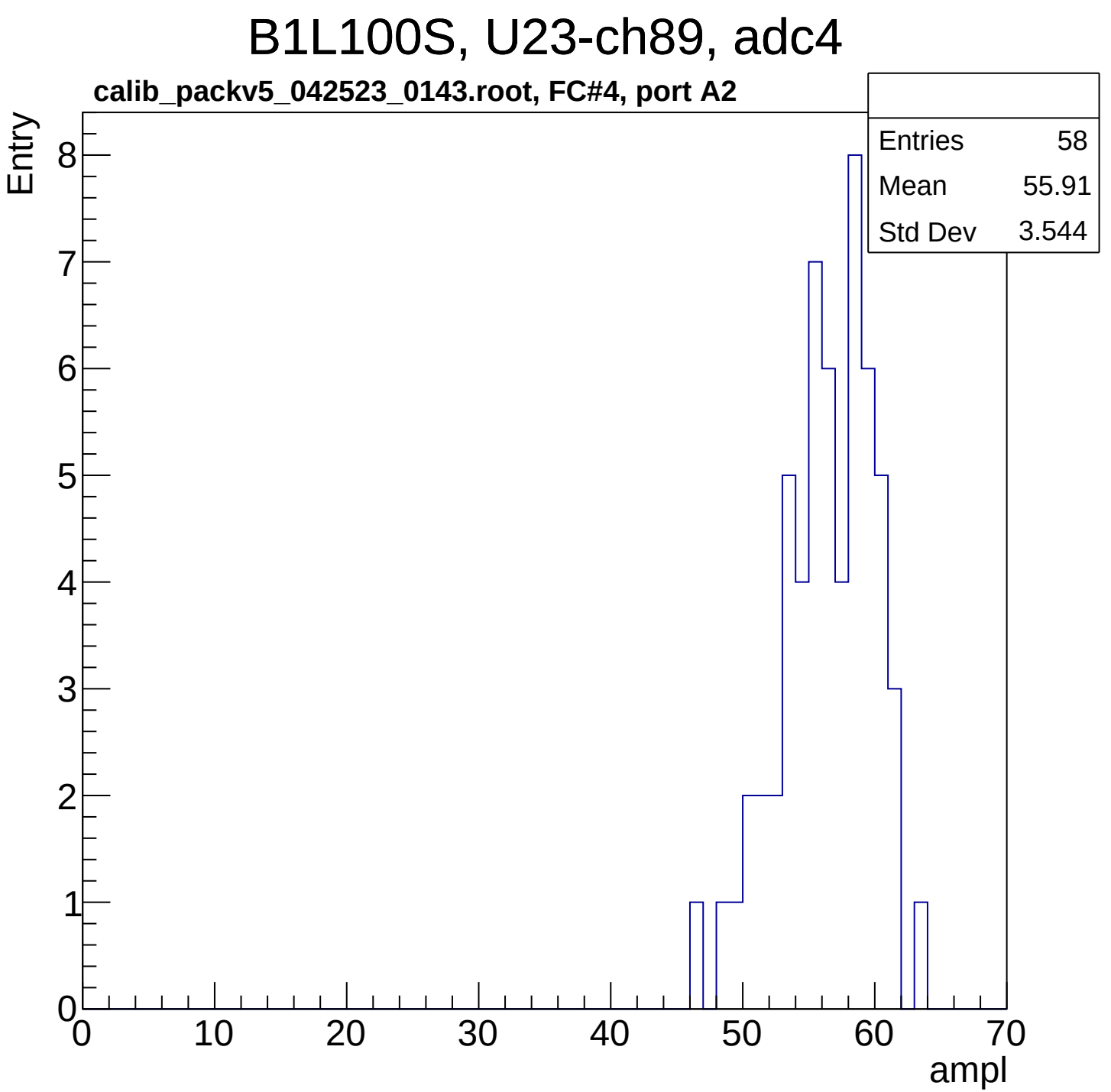
B1L100S, U23-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.91
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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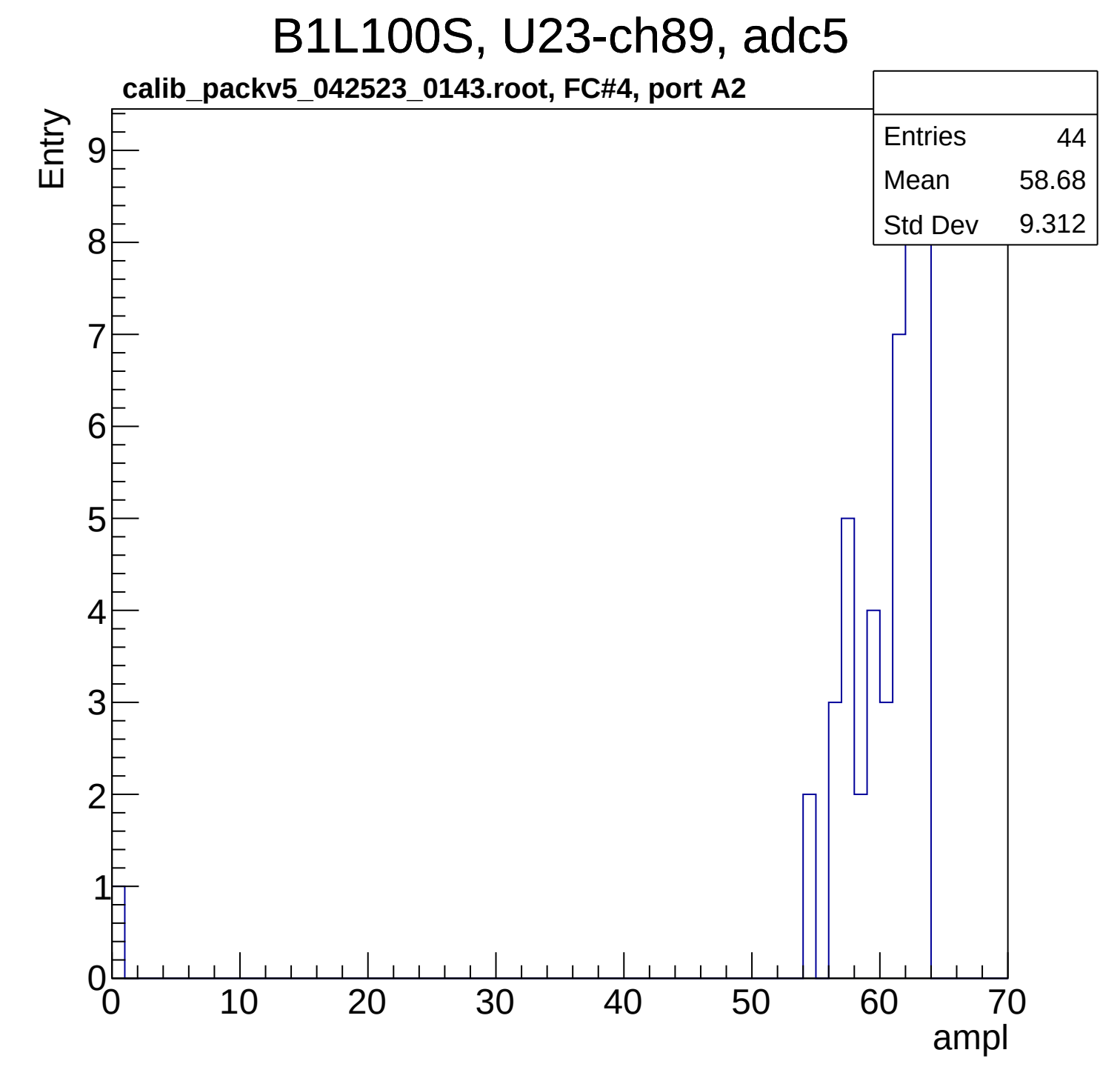
B1L100S, U23-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.68
Std Dev 9.312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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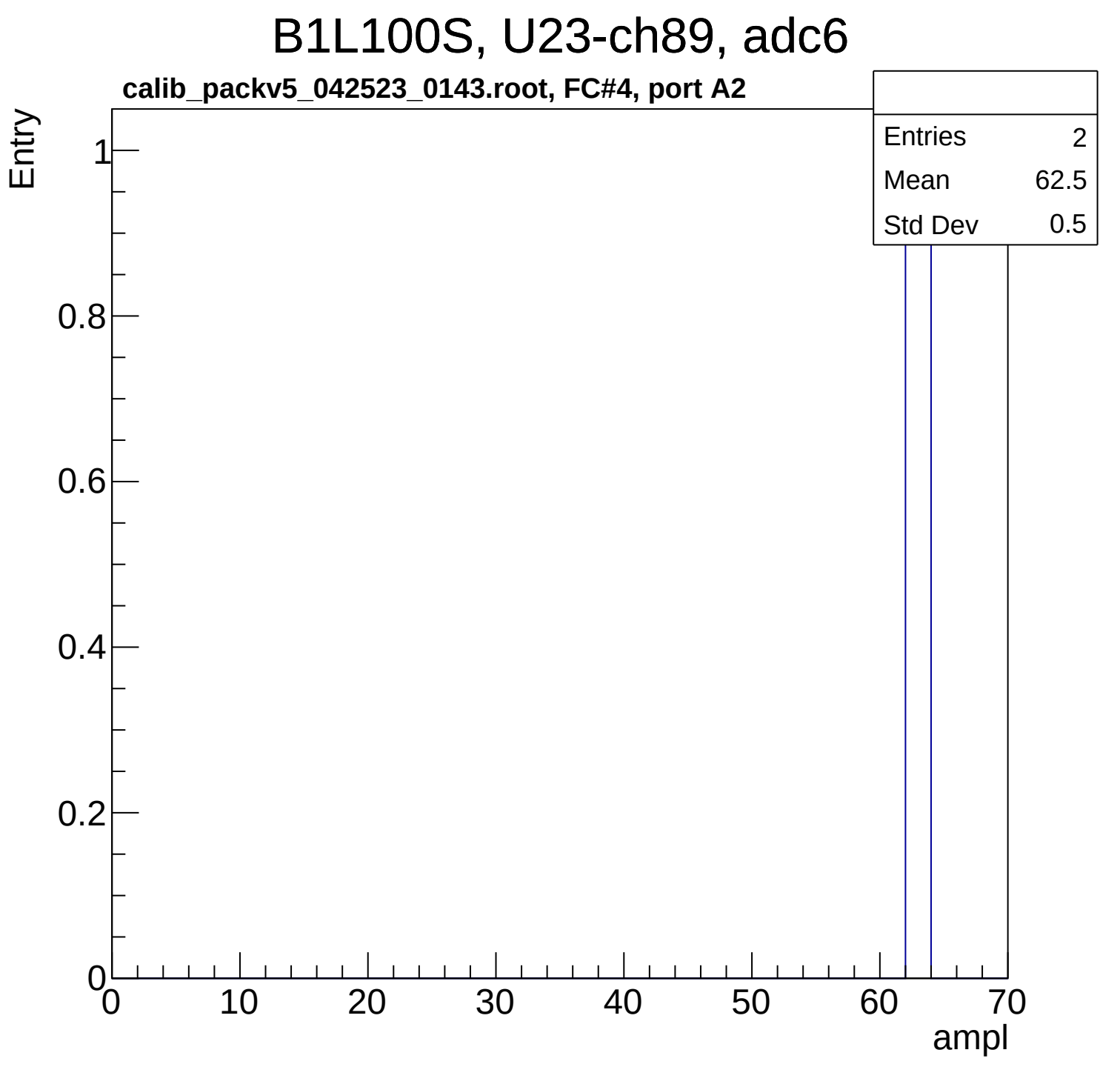
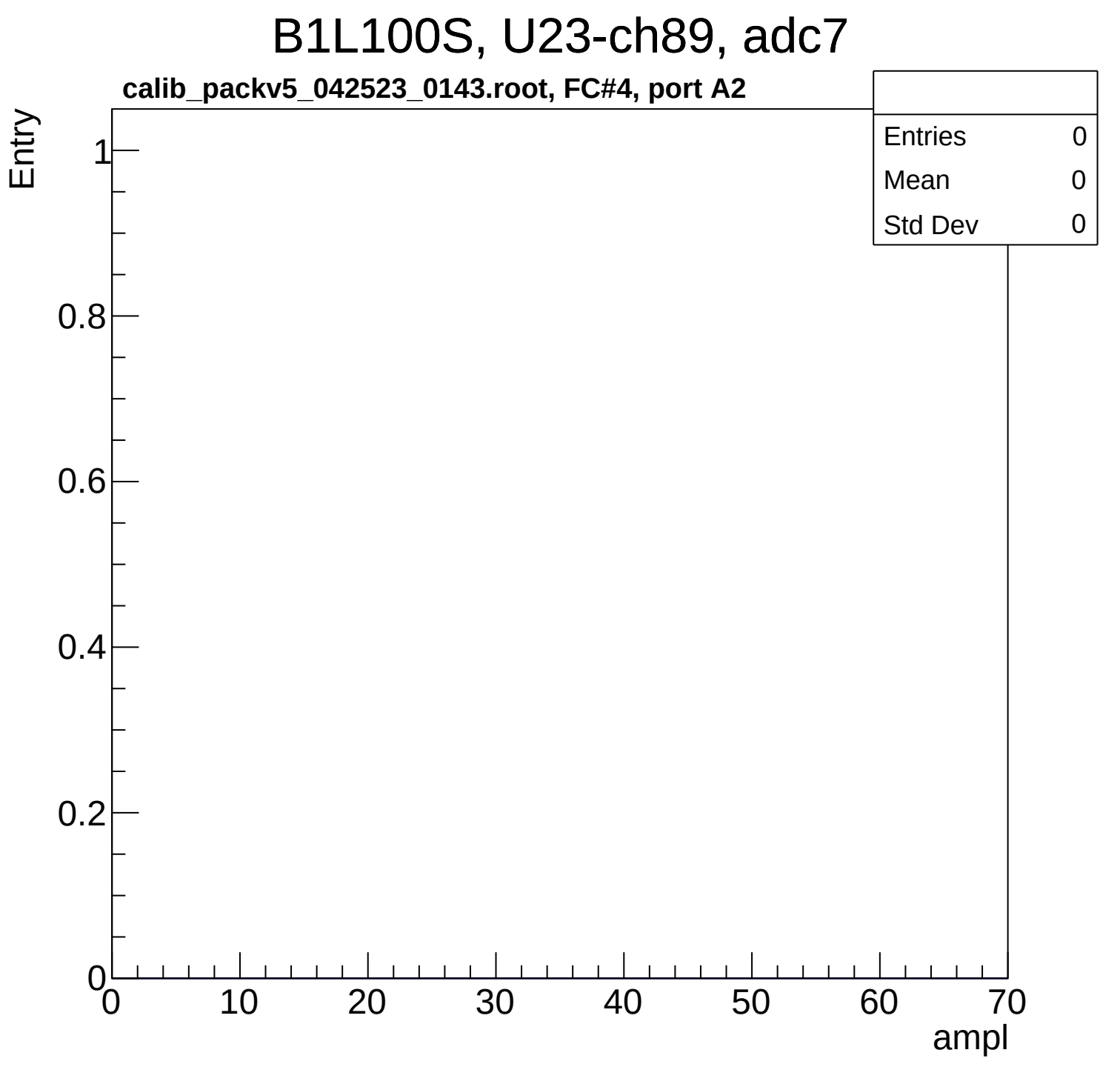
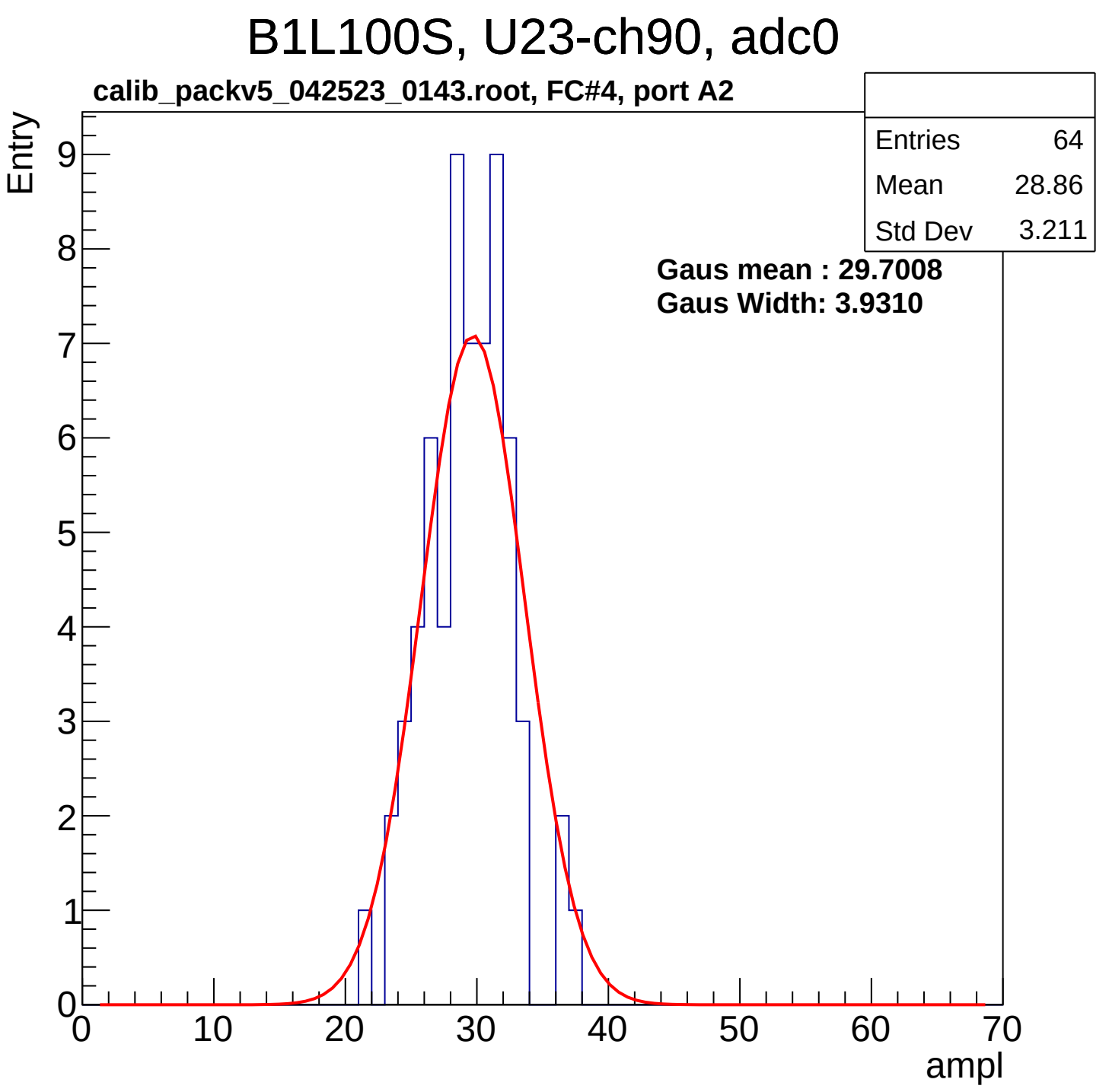
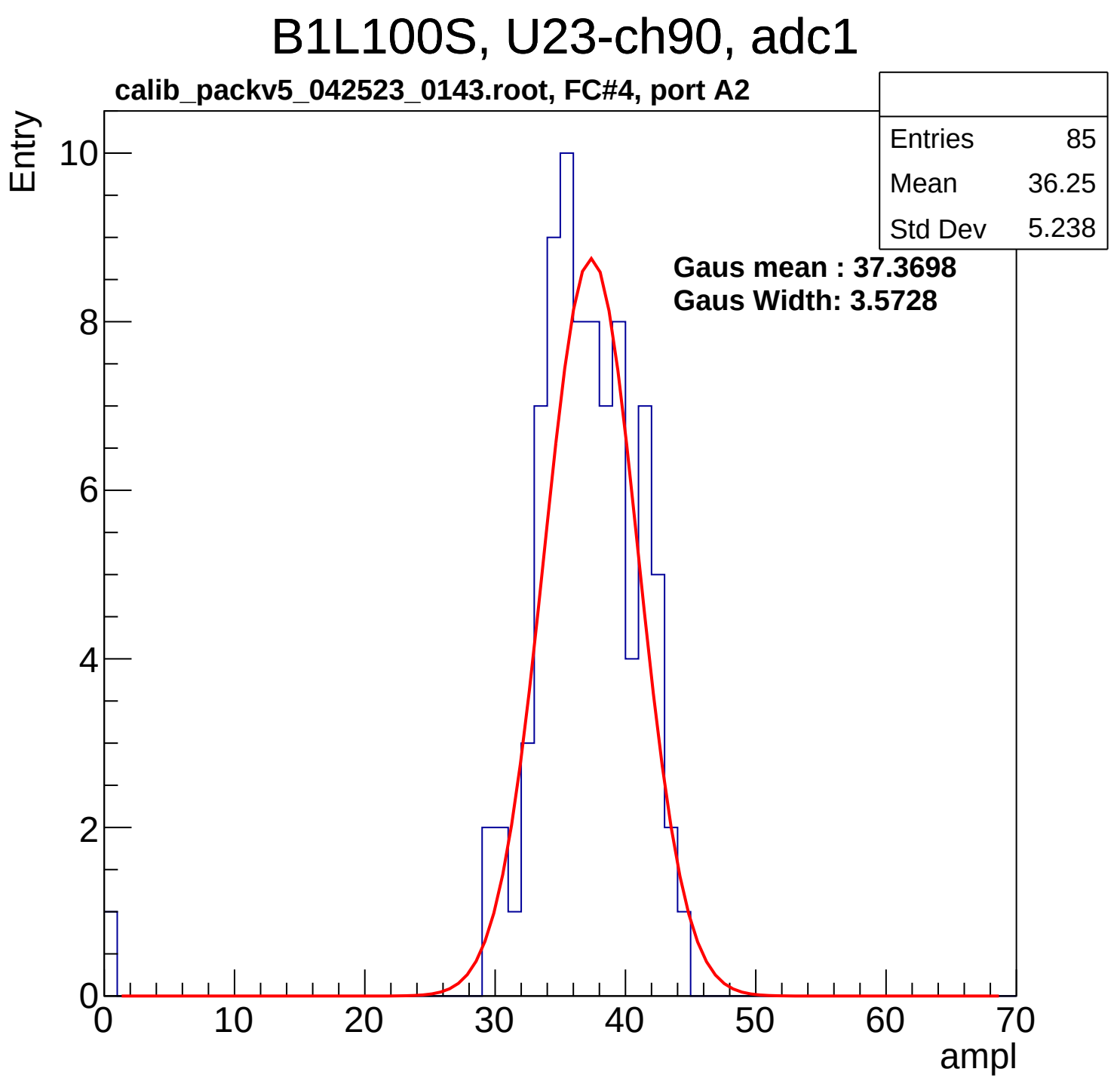
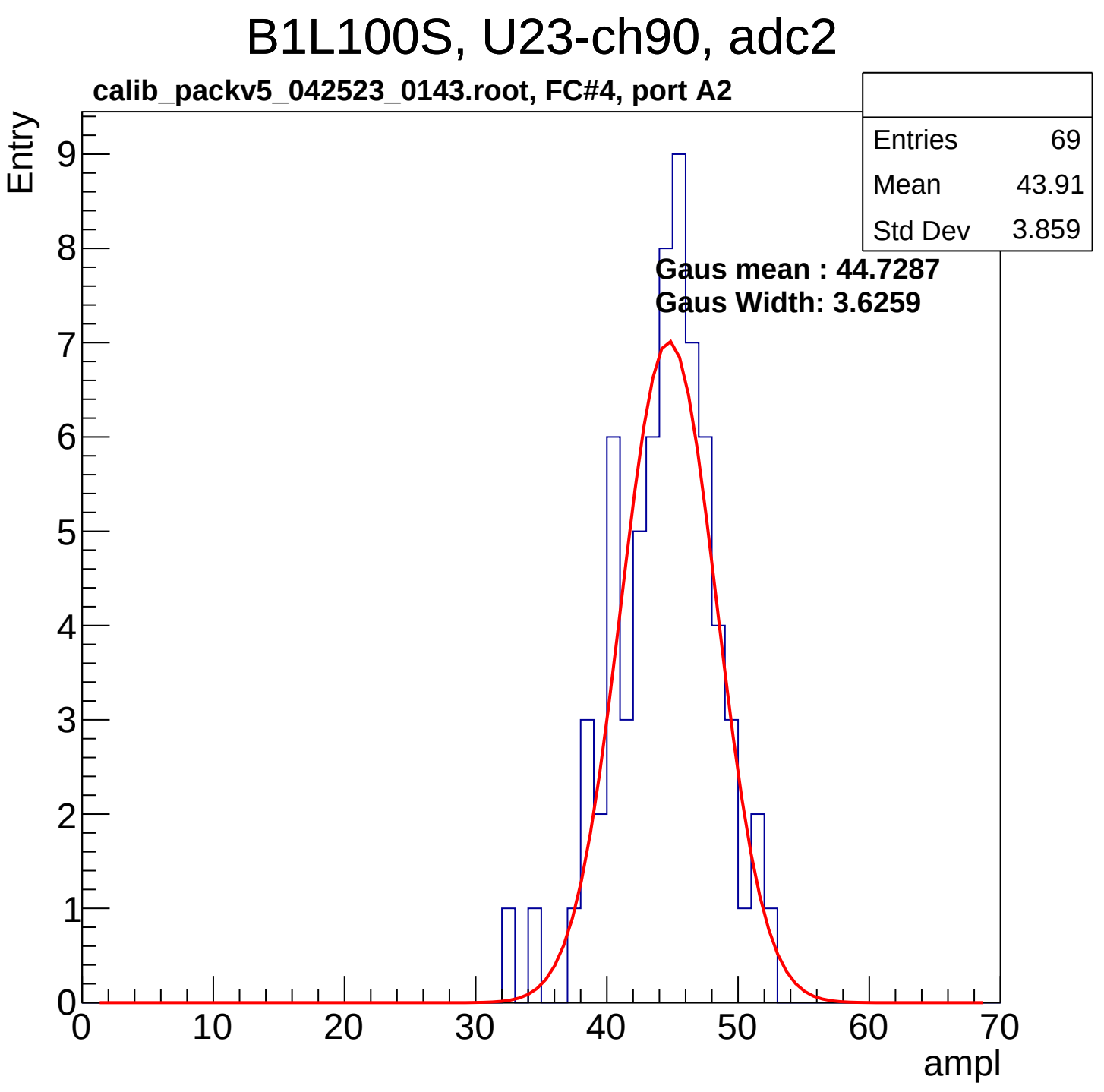
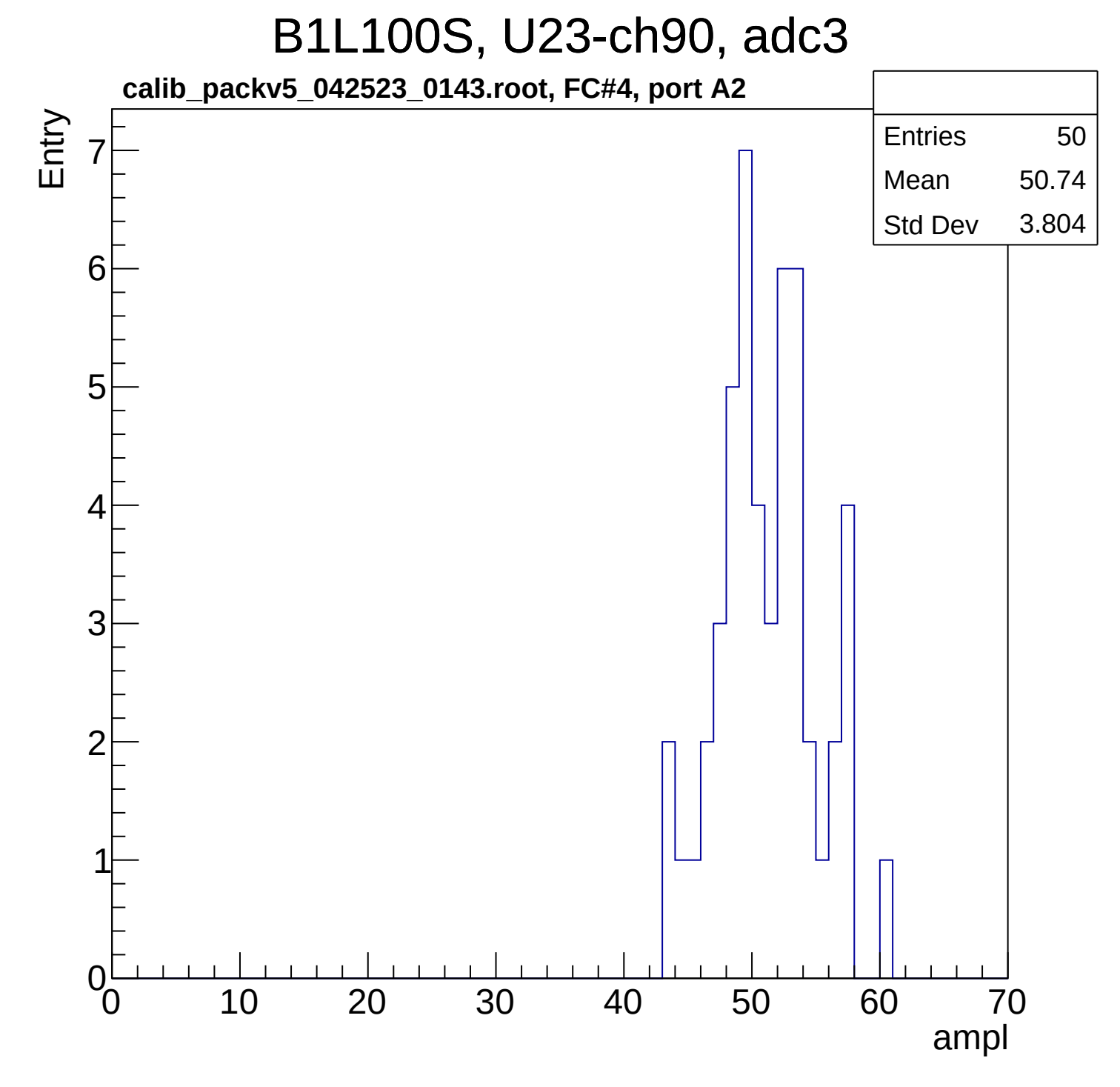

B1L100S, U23-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

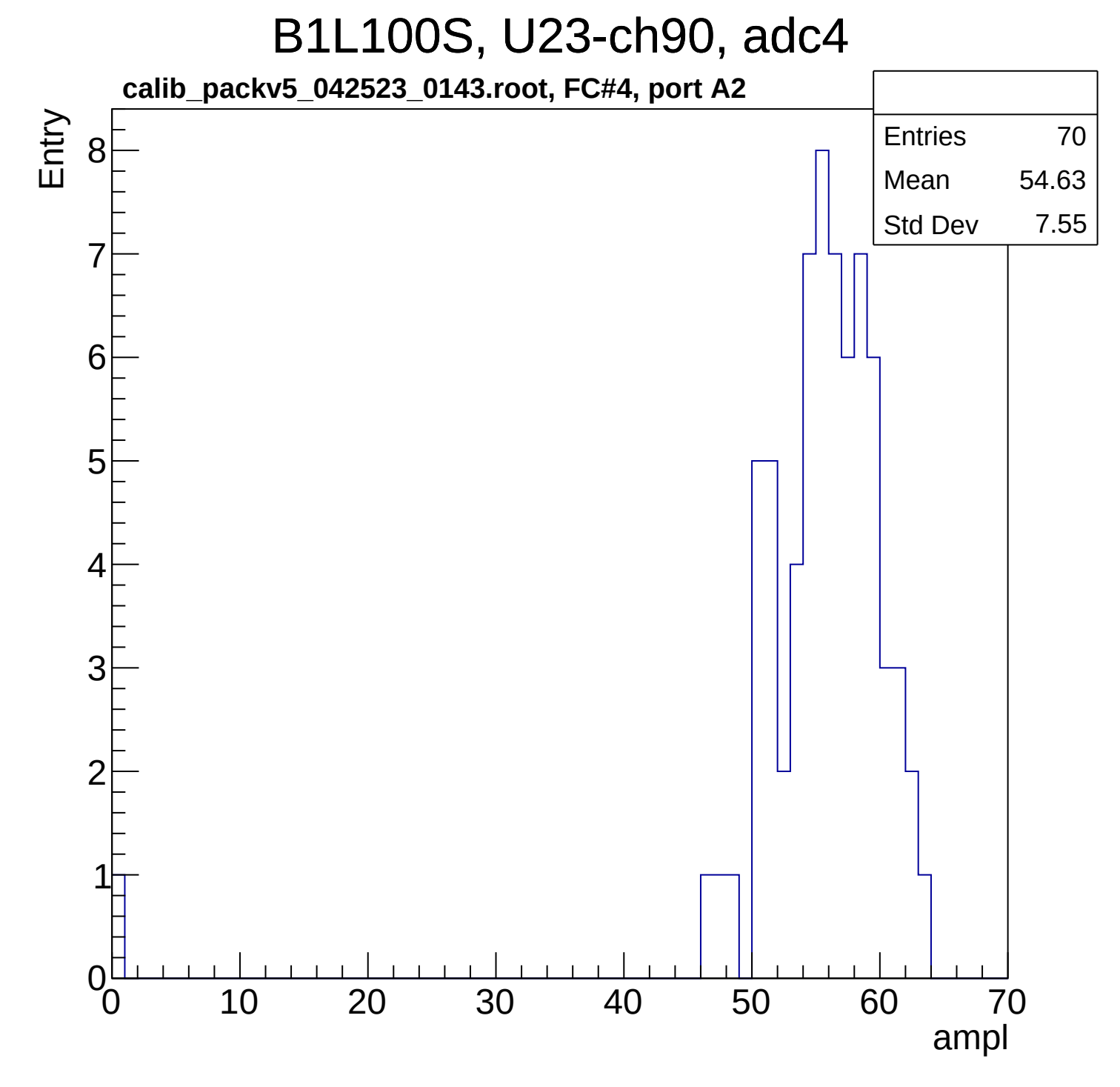

B1L100S, U23-ch89, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

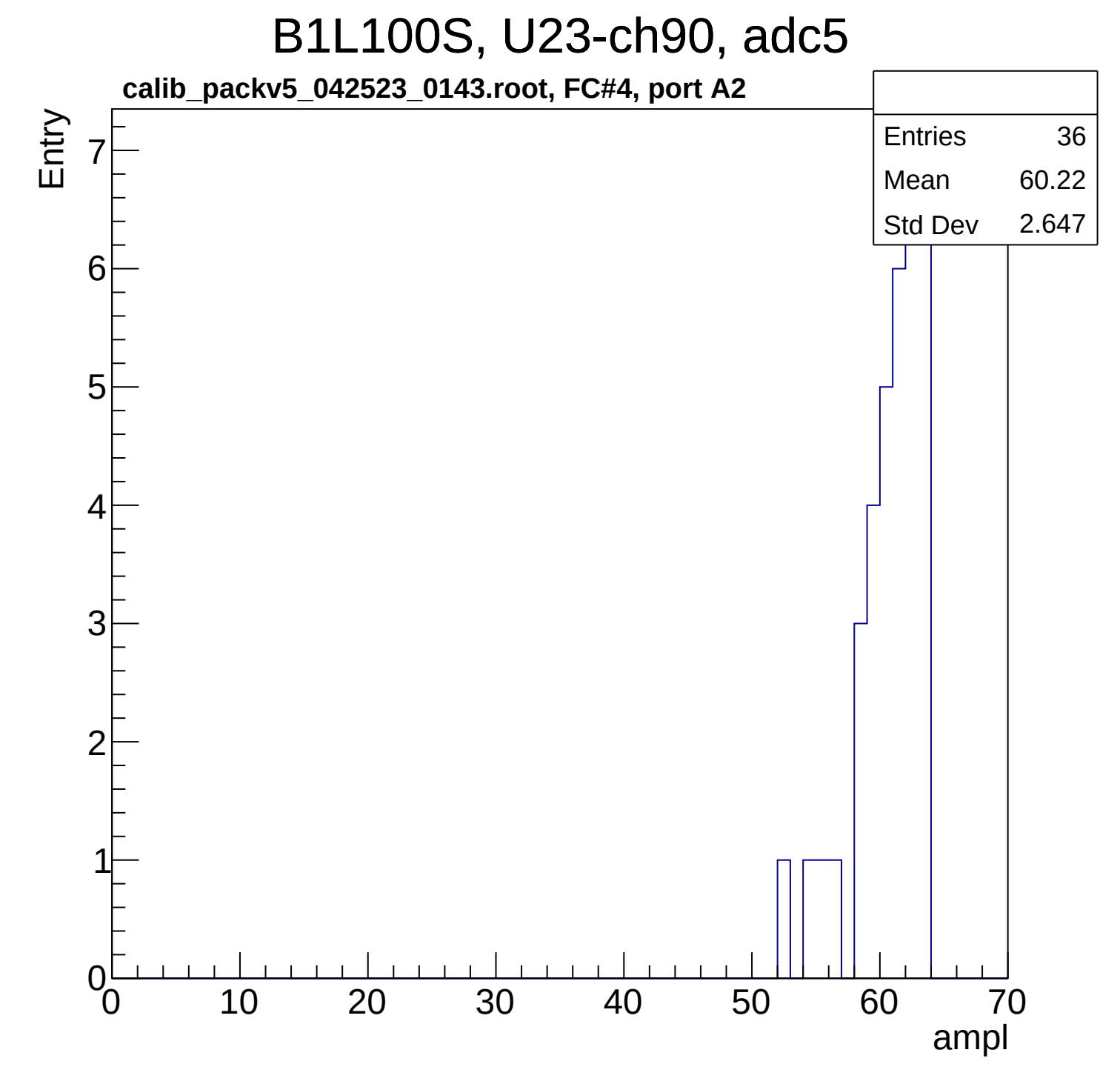

B1L100S, U23-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 28.86
Std Dev 3.211
Gaus mean : 29.7008
Gaus Width: 3.9310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

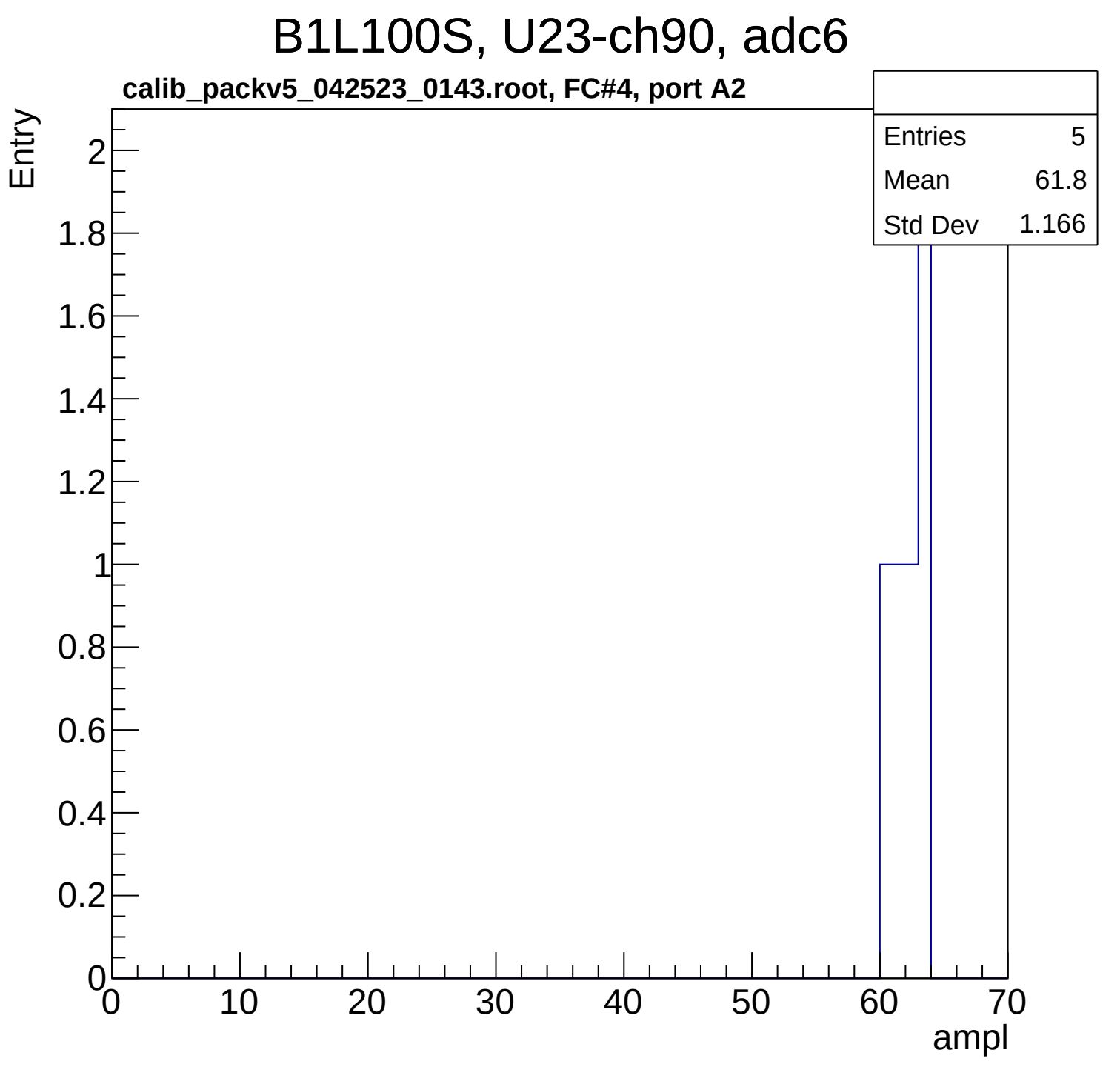

B1L100S, U23-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 36.25
Std Dev 5.238
Gaus mean : 37.3698
Gaus Width: 3.5728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

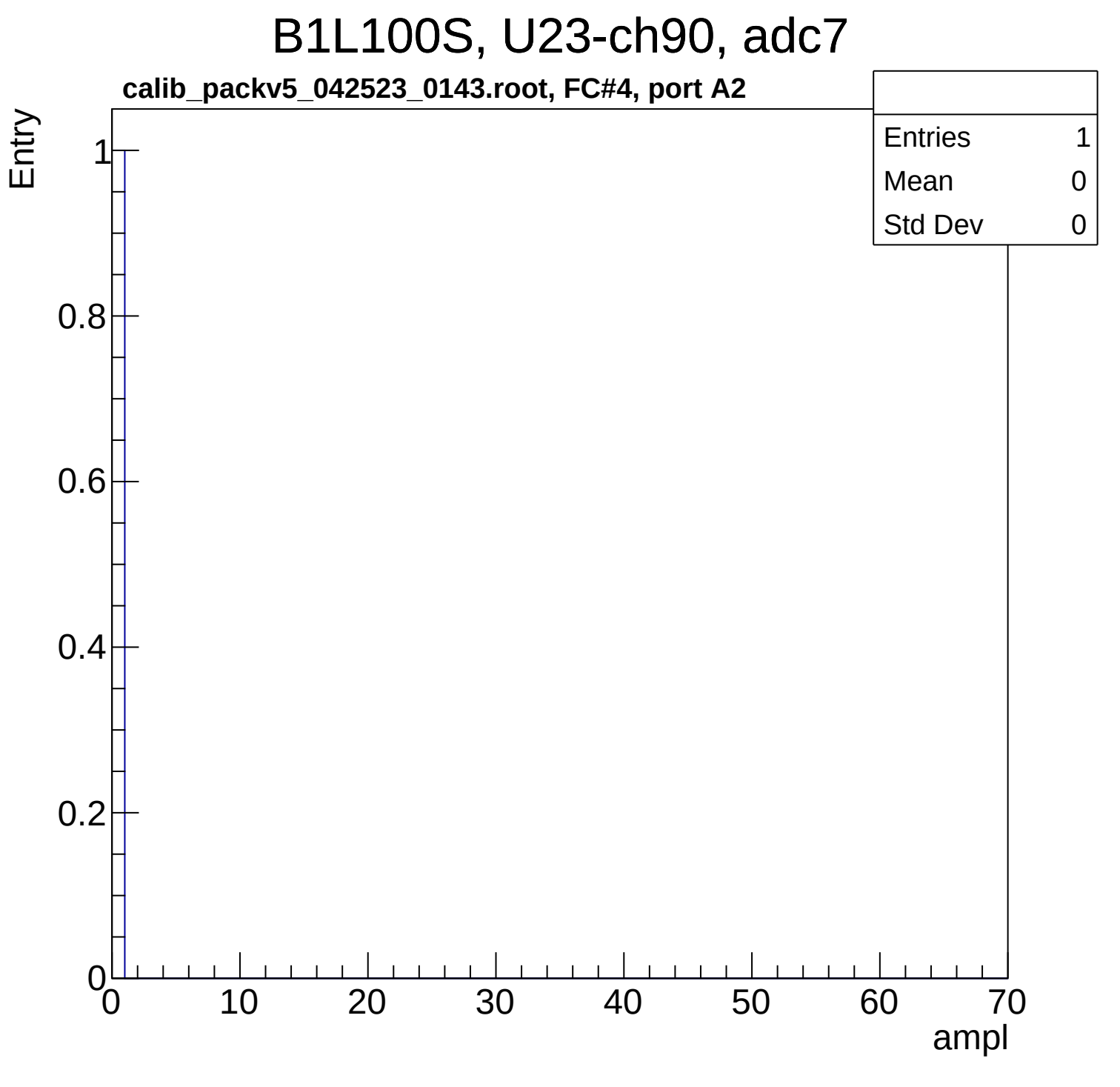

B1L100S, U23-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.91
Std Dev 3.859
Gaus mean : 44.7287
Gaus Width: 3.6259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

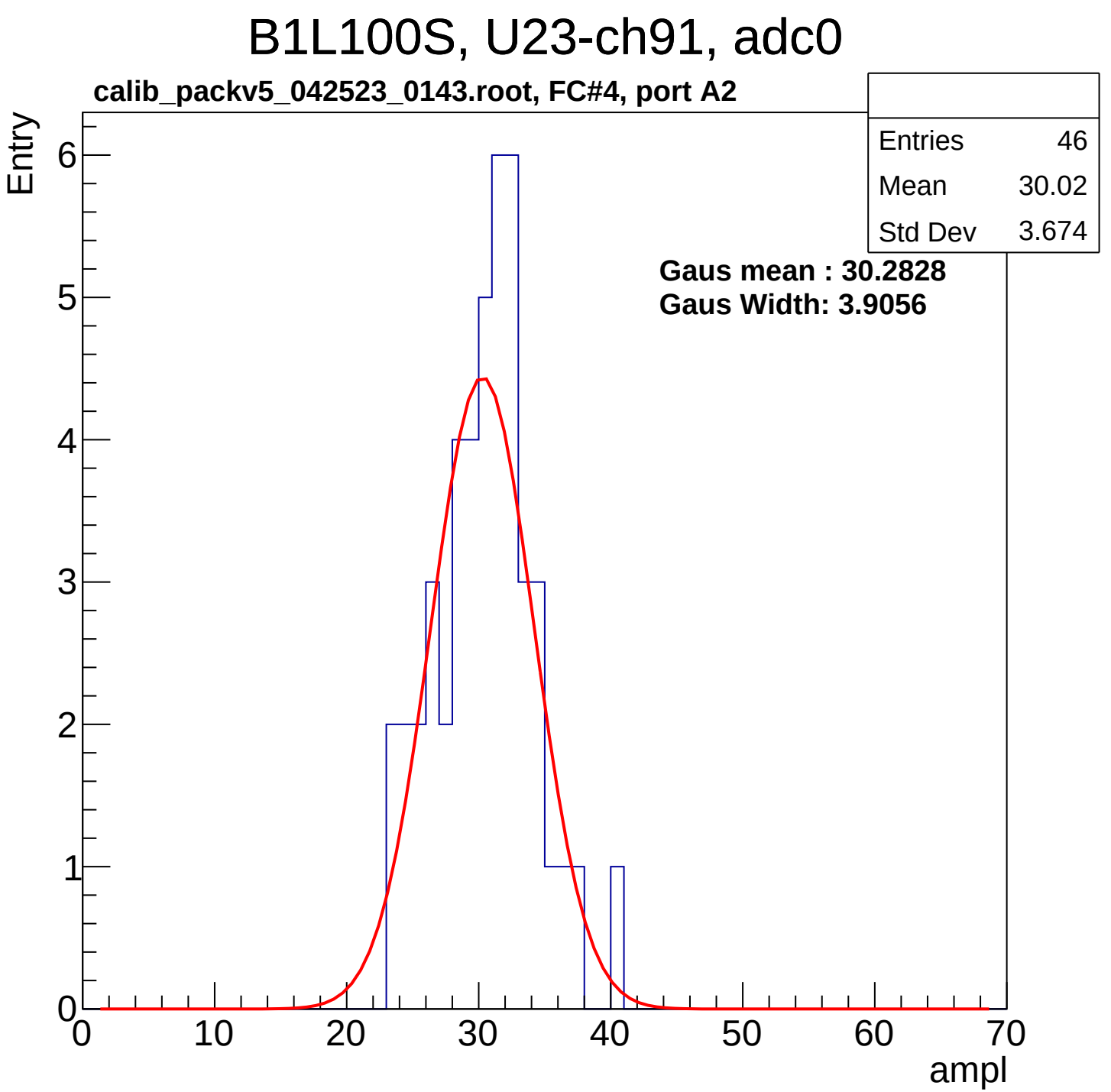

B1L100S, U23-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 50.74
Std Dev 3.804
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

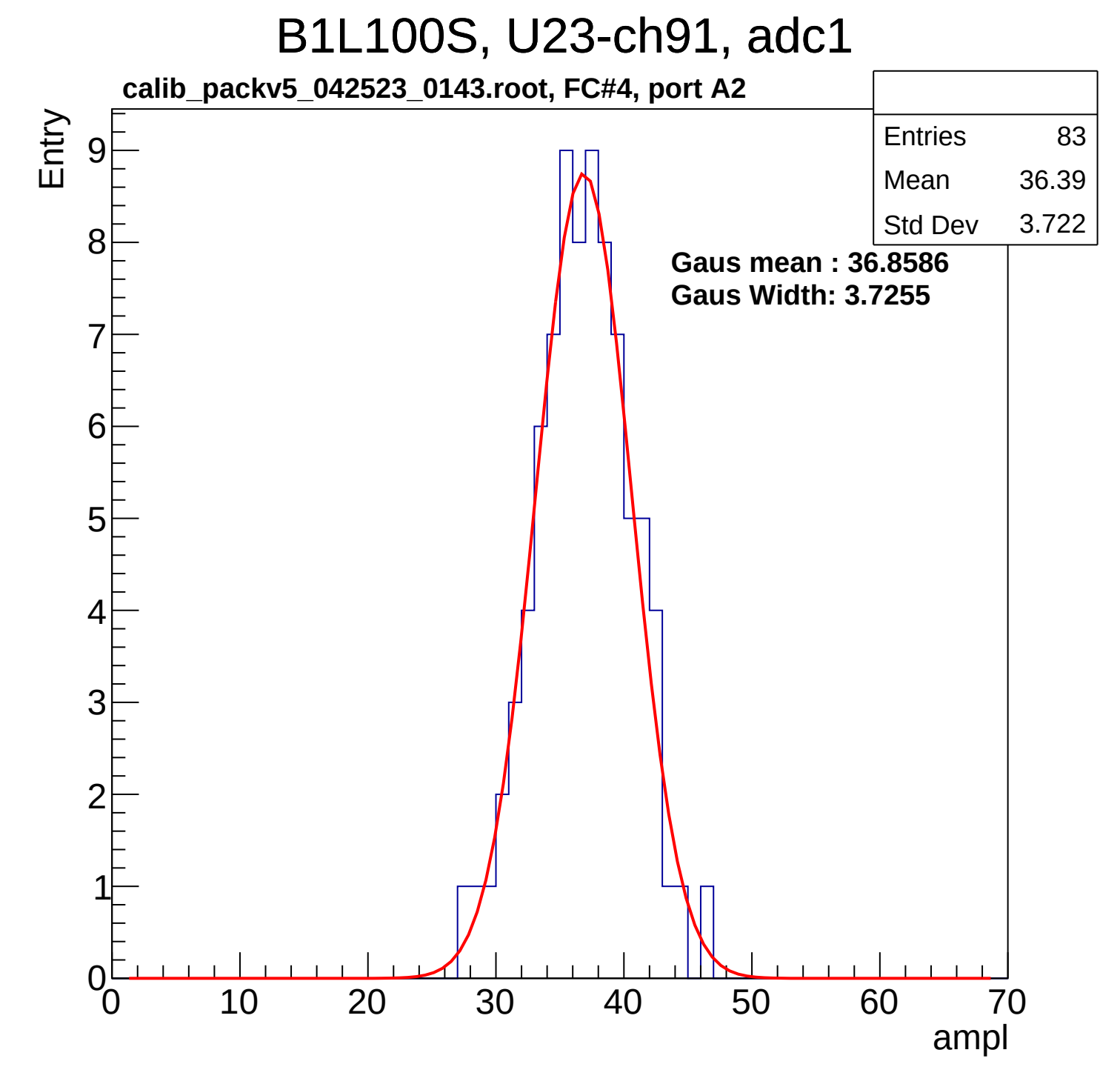

B1L100S, U23-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 54.63
Std Dev 7.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

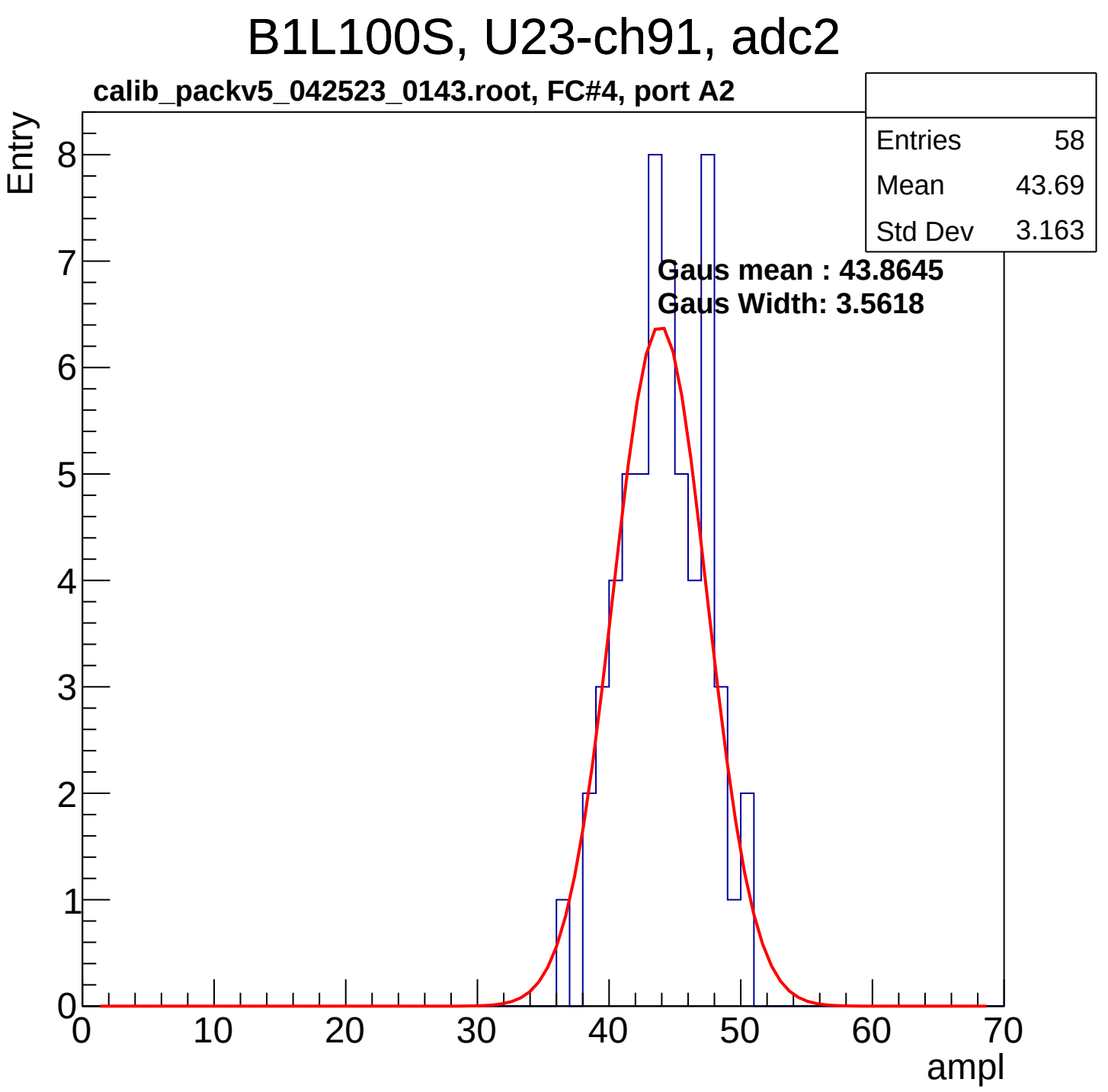

B1L100S, U23-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.22
Std Dev 2.647
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch90, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 30.02
Std Dev 3.674
Gaus mean : 30.2828
Gaus Width: 3.9056
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.39
Std Dev 3.722
Gaus mean : 36.8586
Gaus Width: 3.7255
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.69
Std Dev 3.163
Gaus mean : 43.8645
Gaus Width: 3.5618
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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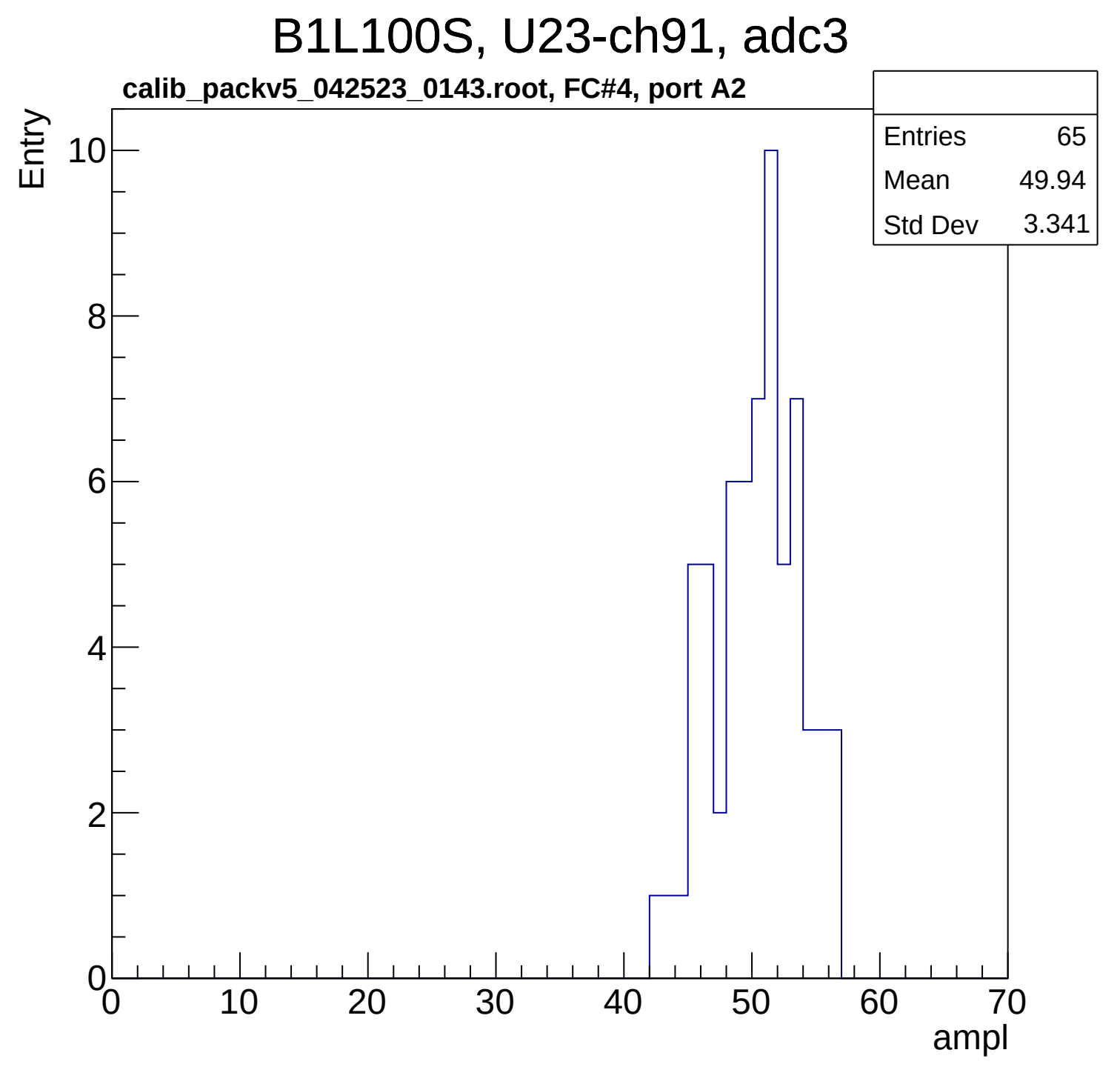
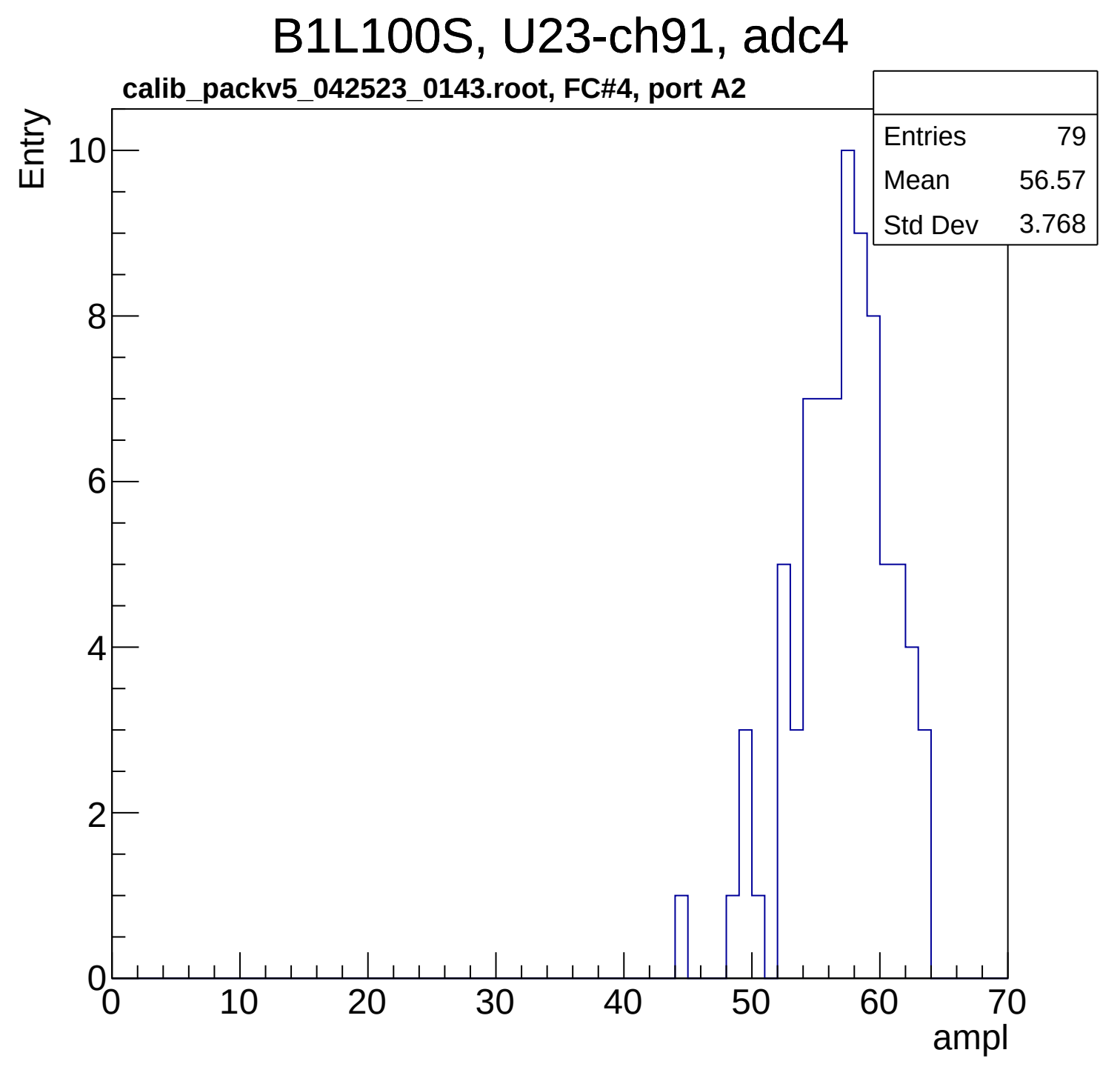
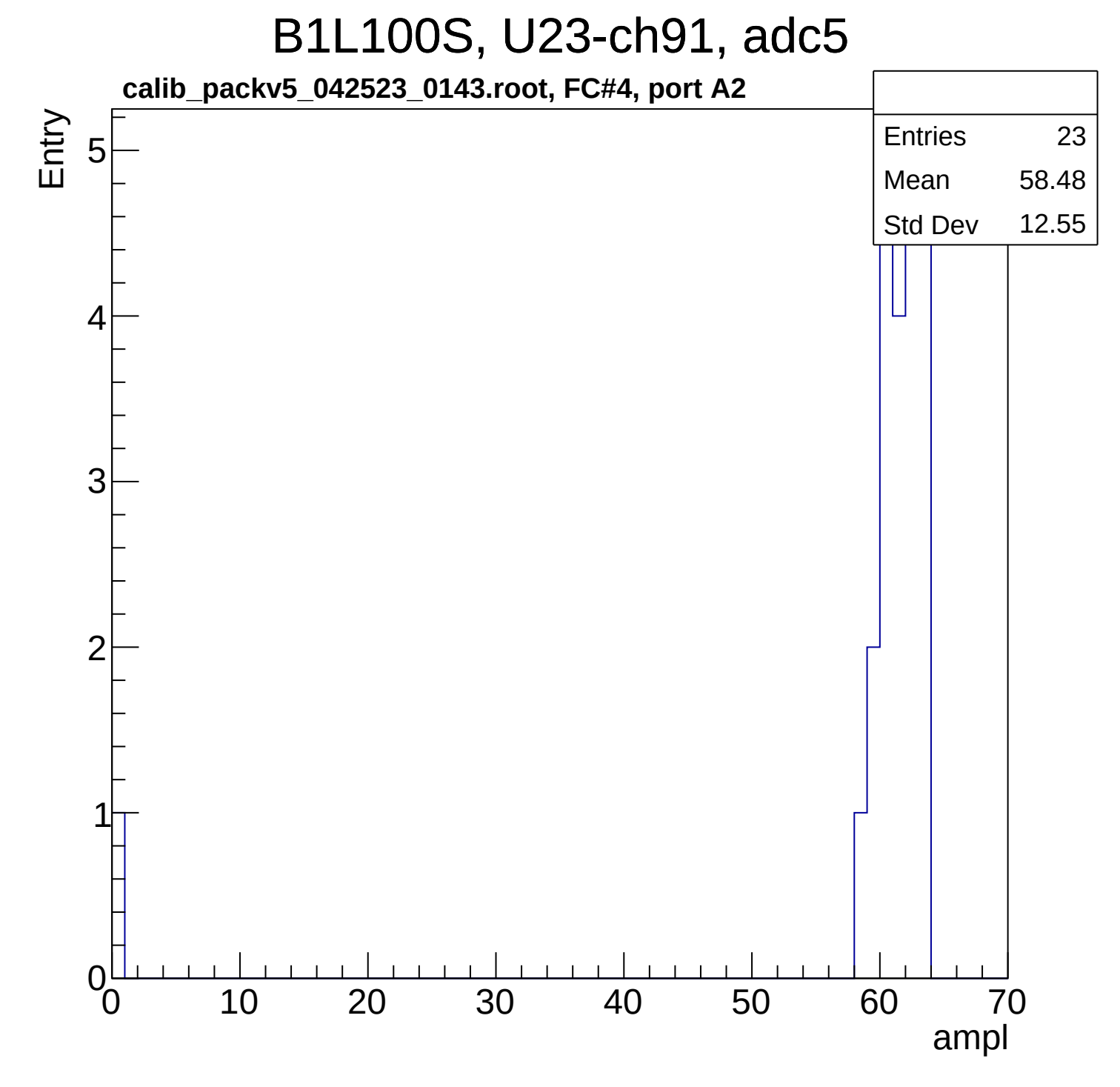
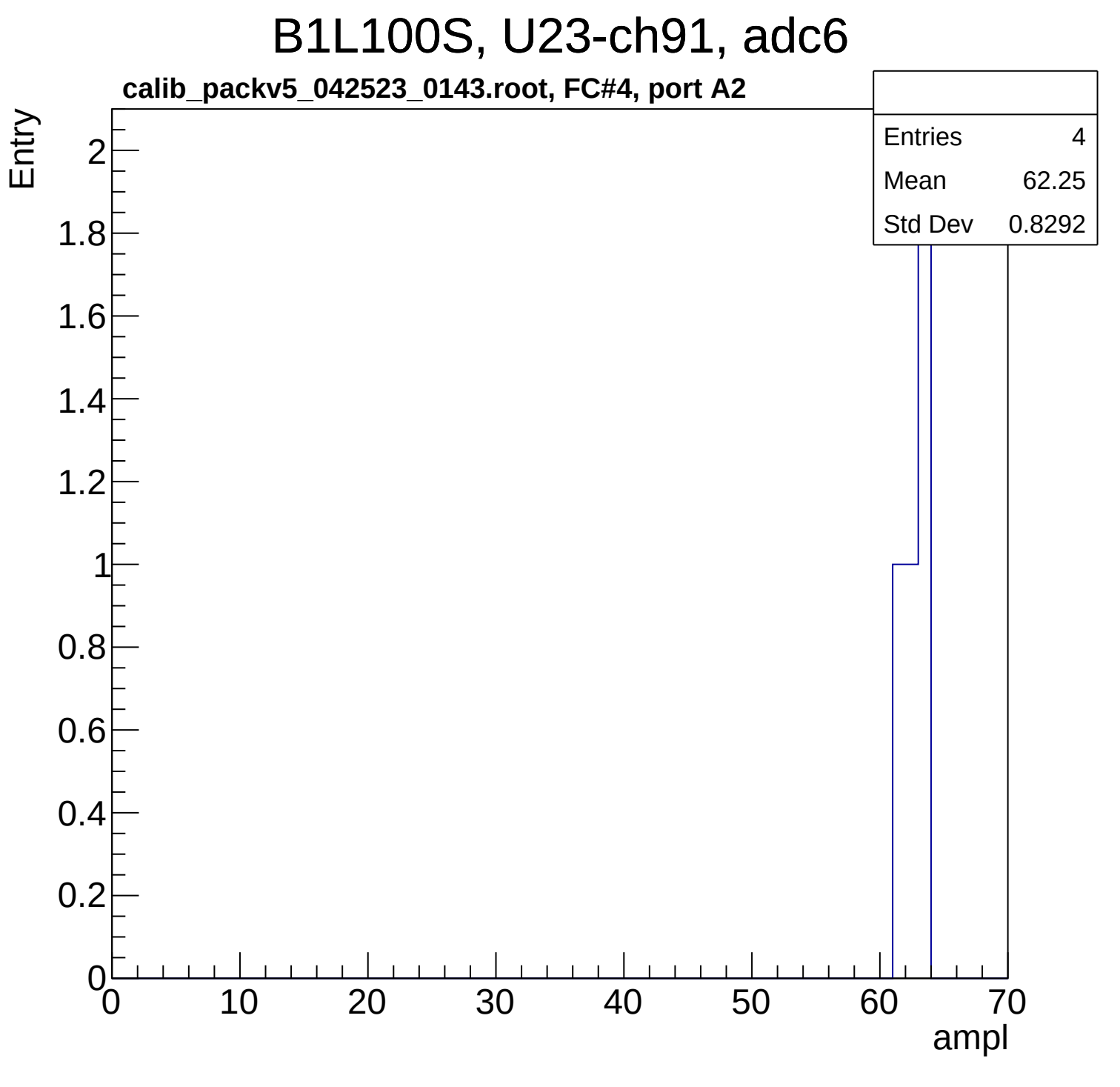
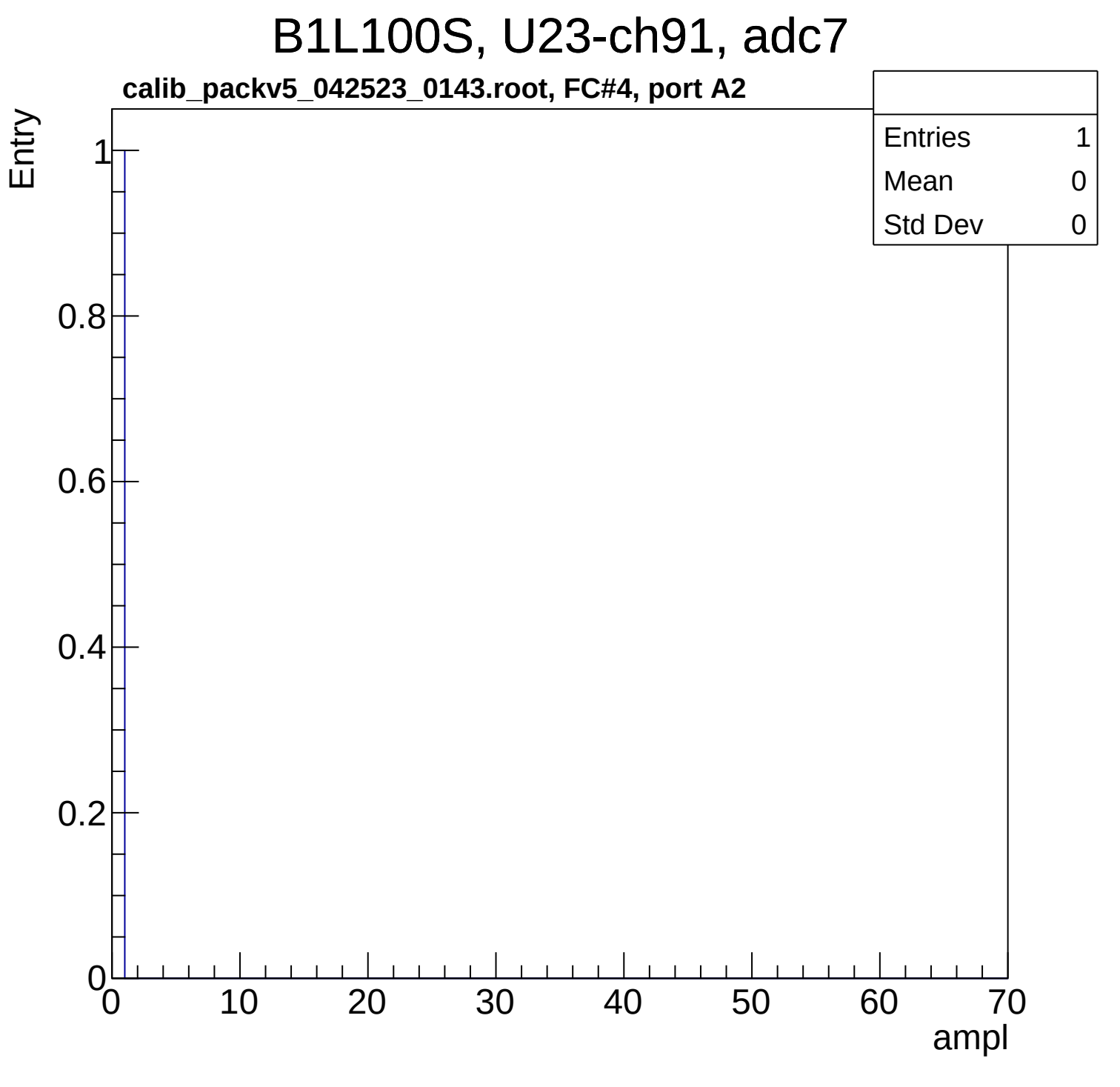
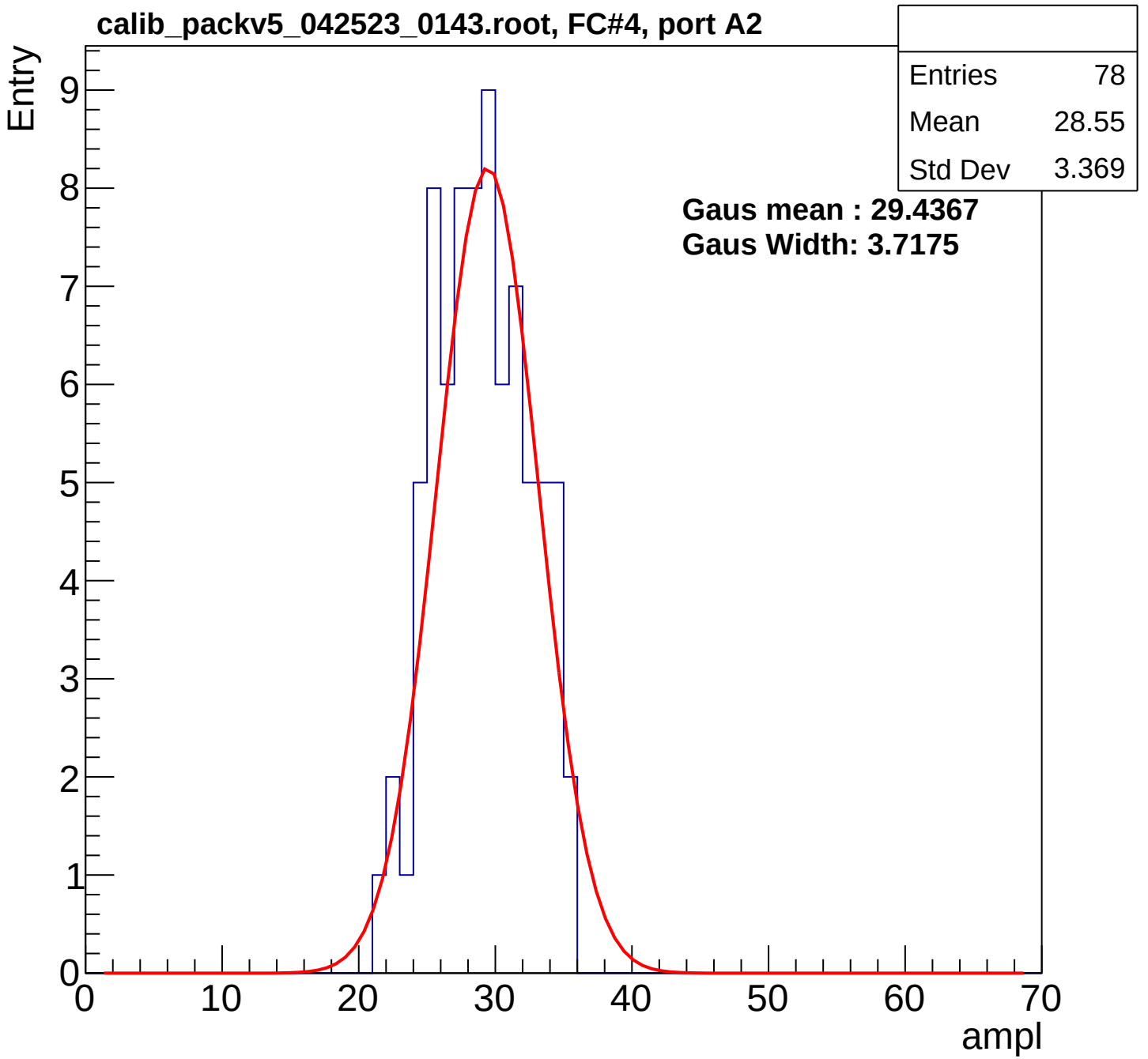

B1L100S, U23-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.94
Std Dev 3.341
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

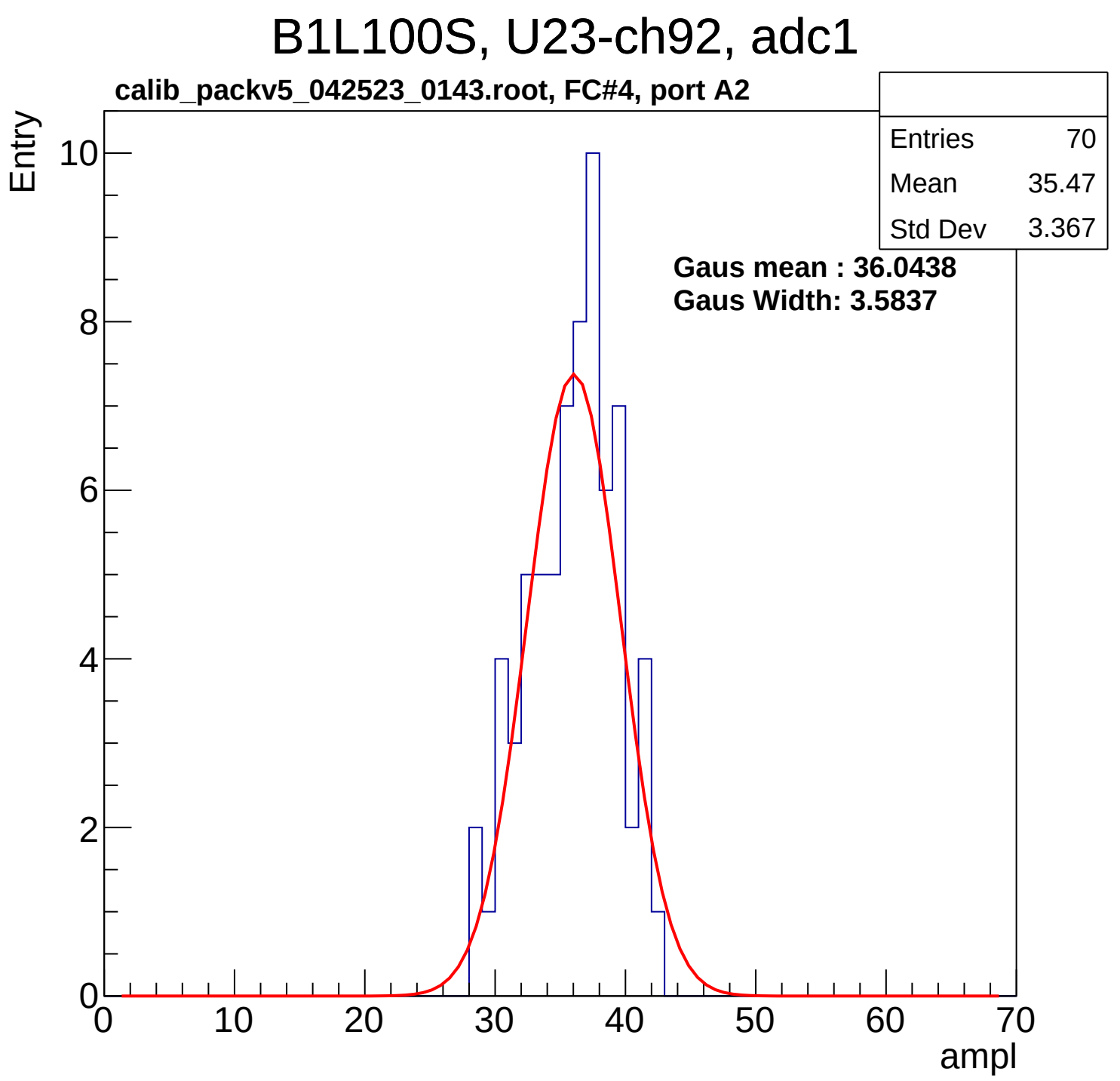

B1L100S, U23-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 56.57
Std Dev 3.768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

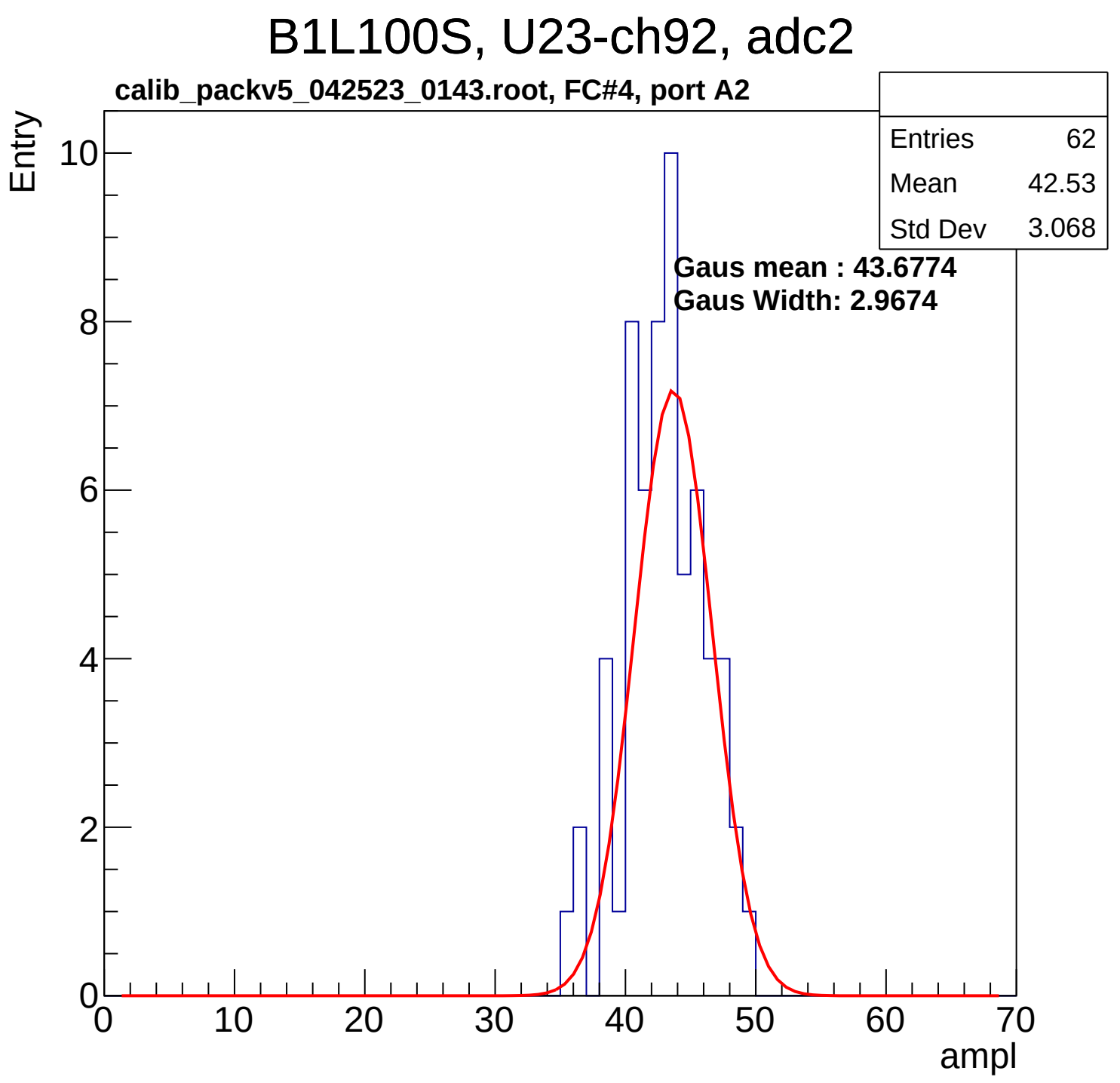

B1L100S, U23-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 58.48
Std Dev 12.55
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

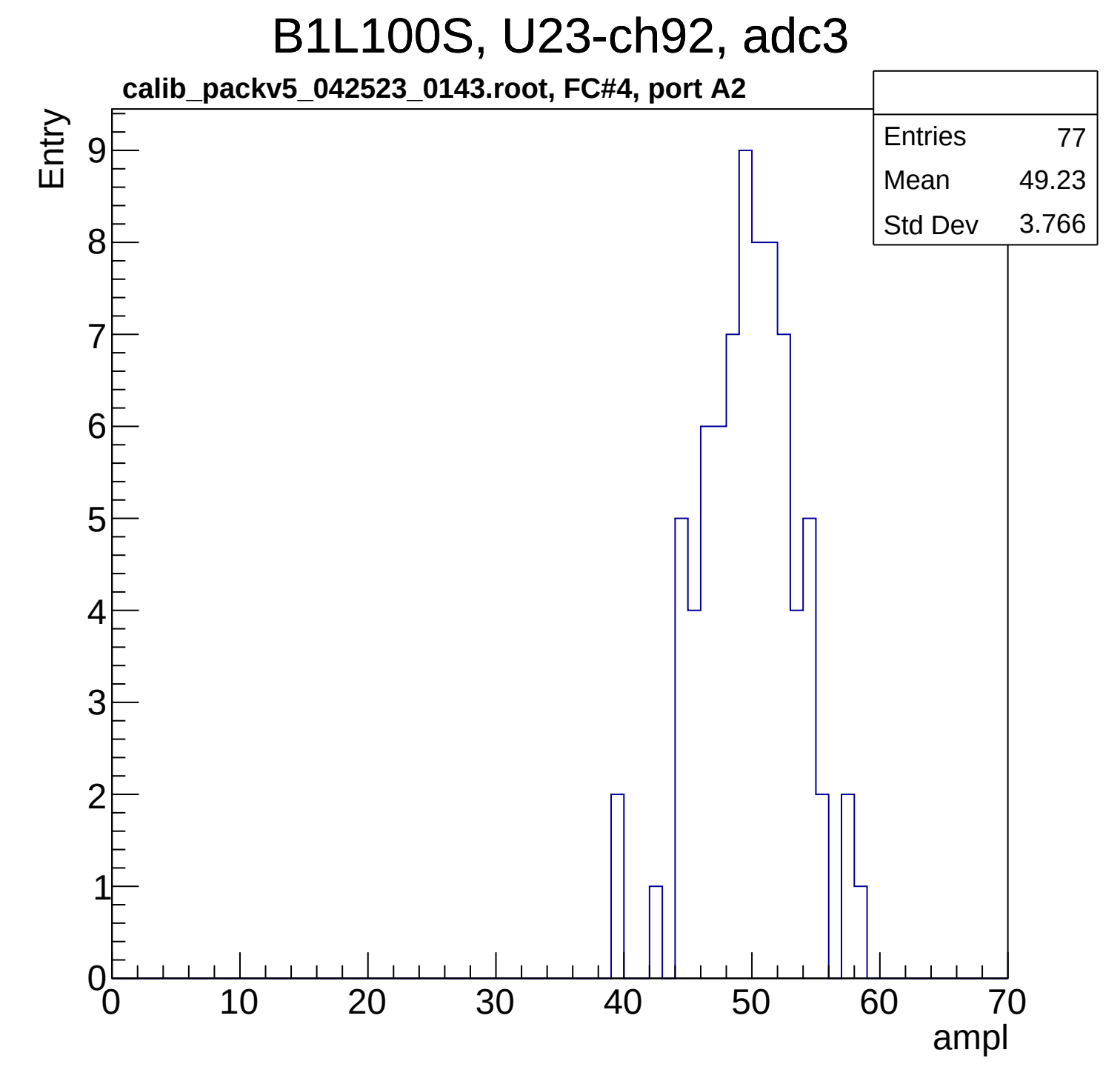

B1L100S, U23-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 28.55
Std Dev 3.369
Gaus mean : 29.4367
Gaus Width: 3.7175
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U23-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.47
Std Dev 3.367
Gaus mean : 36.0438
Gaus Width: 3.5837
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.53
Std Dev 3.068
Gaus mean : 43.6774
Gaus Width: 2.9674
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch92, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 49.23
Std Dev 3.766
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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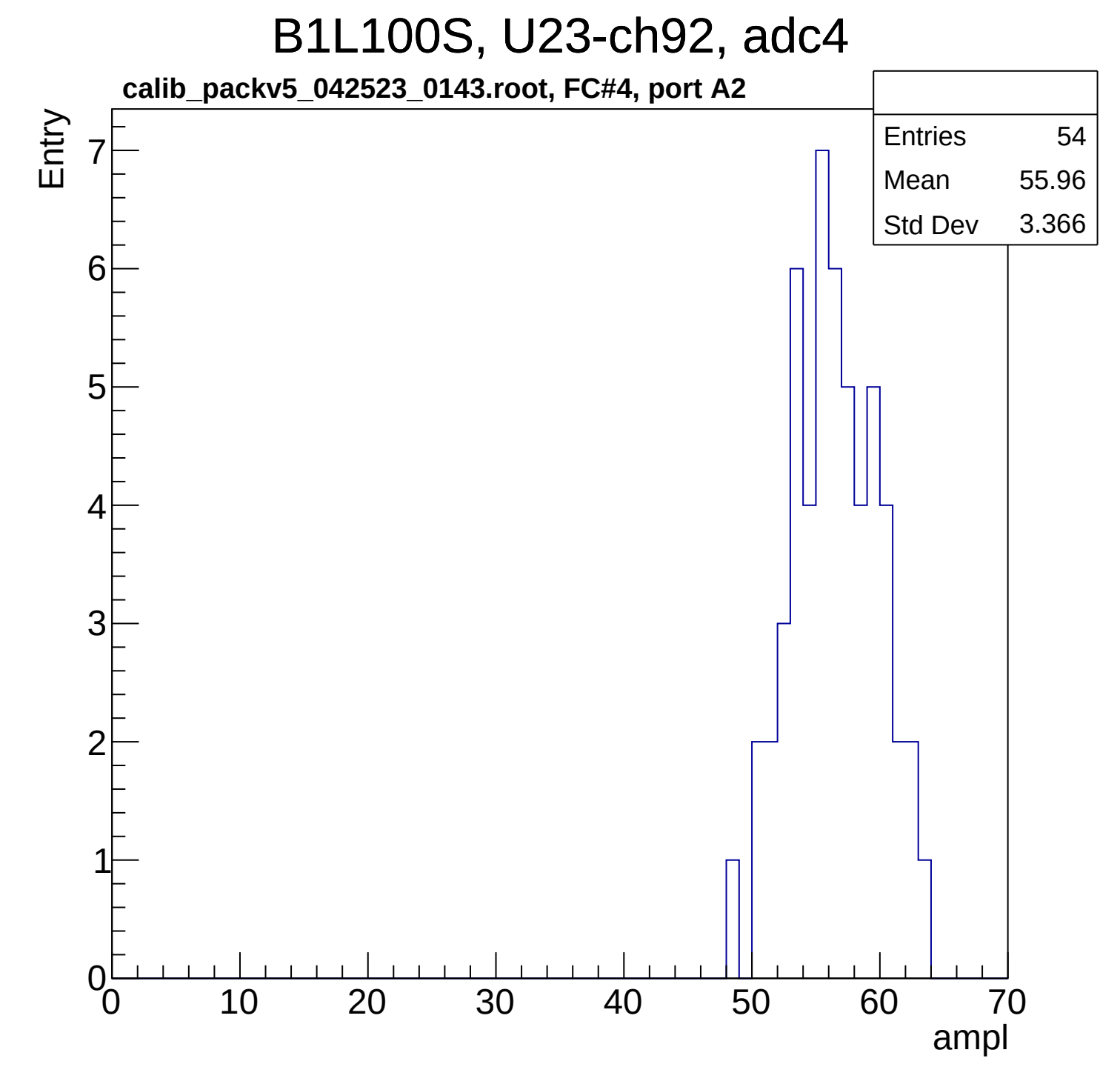
B1L100S, U23-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.96
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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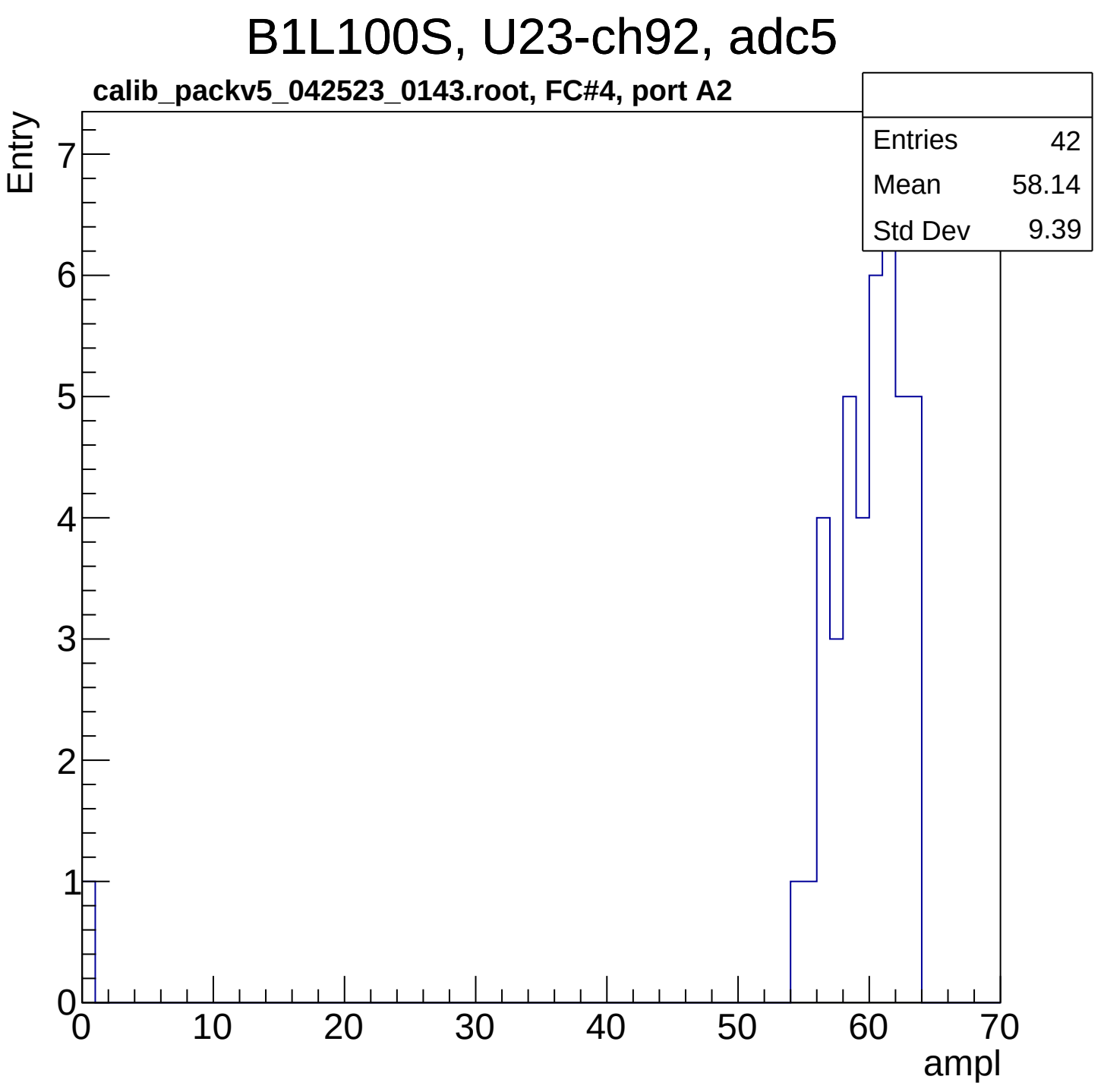
B1L100S, U23-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.14
Std Dev 9.39
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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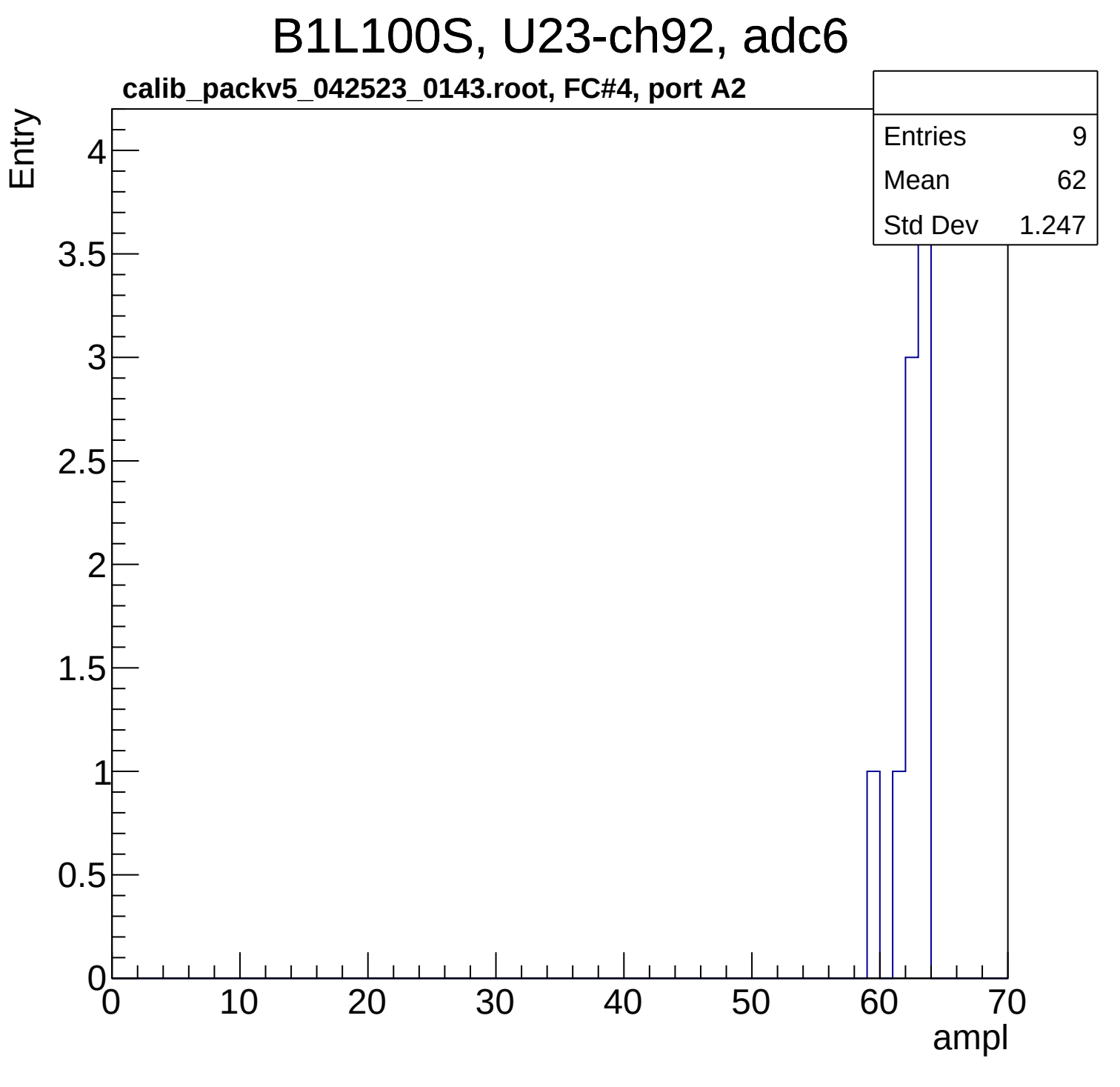
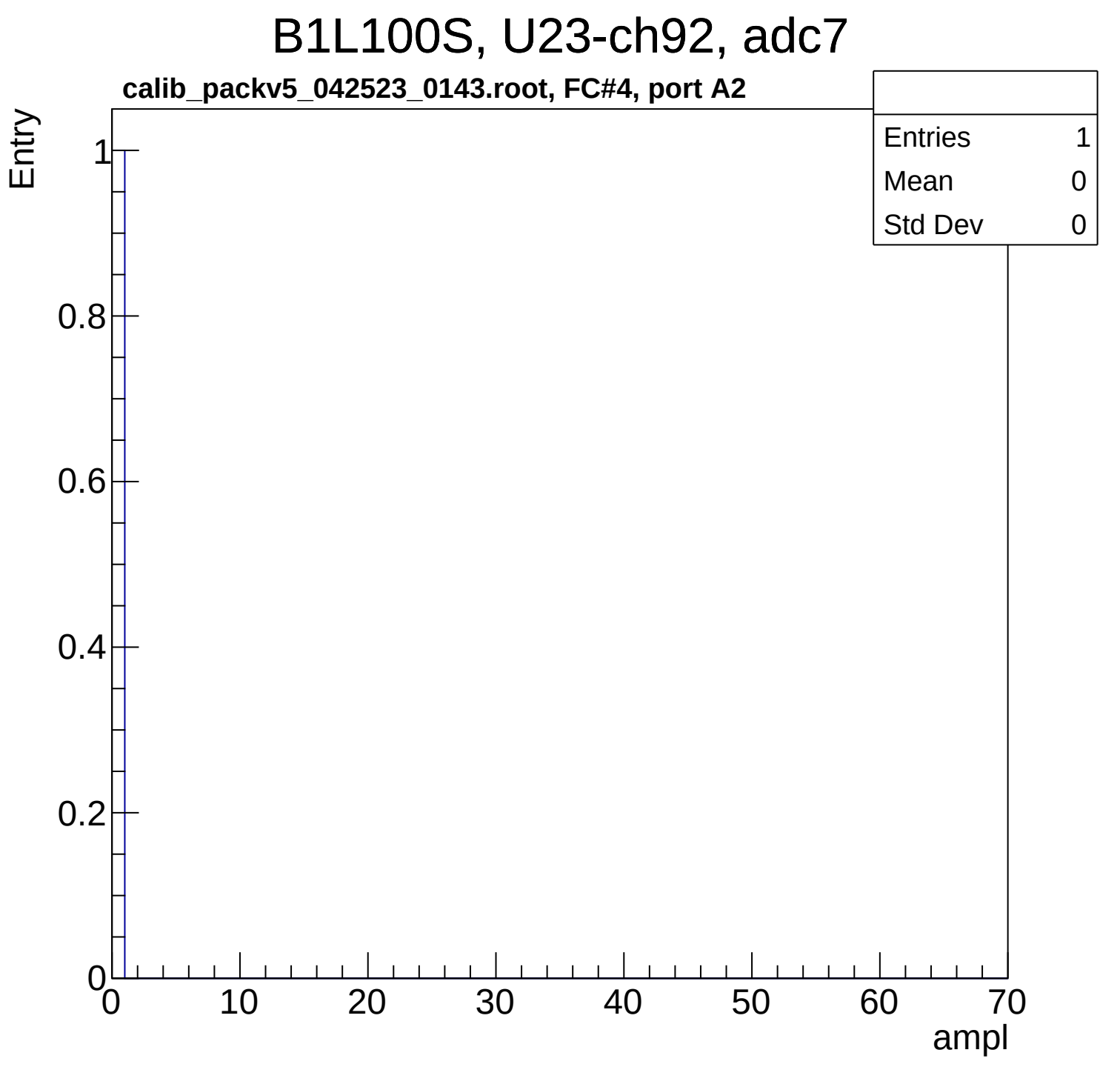

B1L100S, U23-ch92, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62
Std Dev 1.247
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U23-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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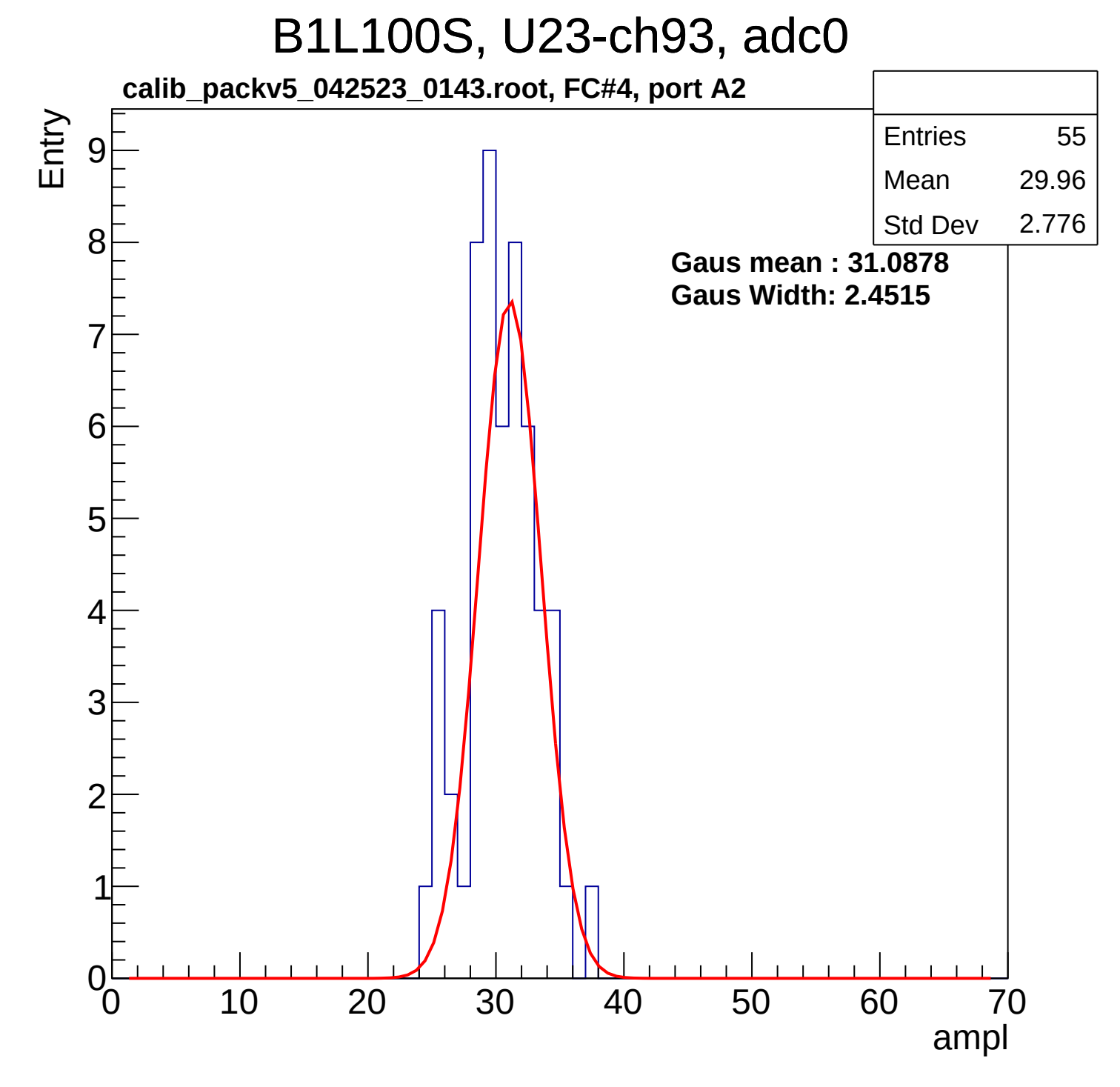
70
ampl


B1L100S, U23-ch93, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 29.96
Std Dev 2.776
Gaus mean : 31.0878
Gaus Width: 2.4515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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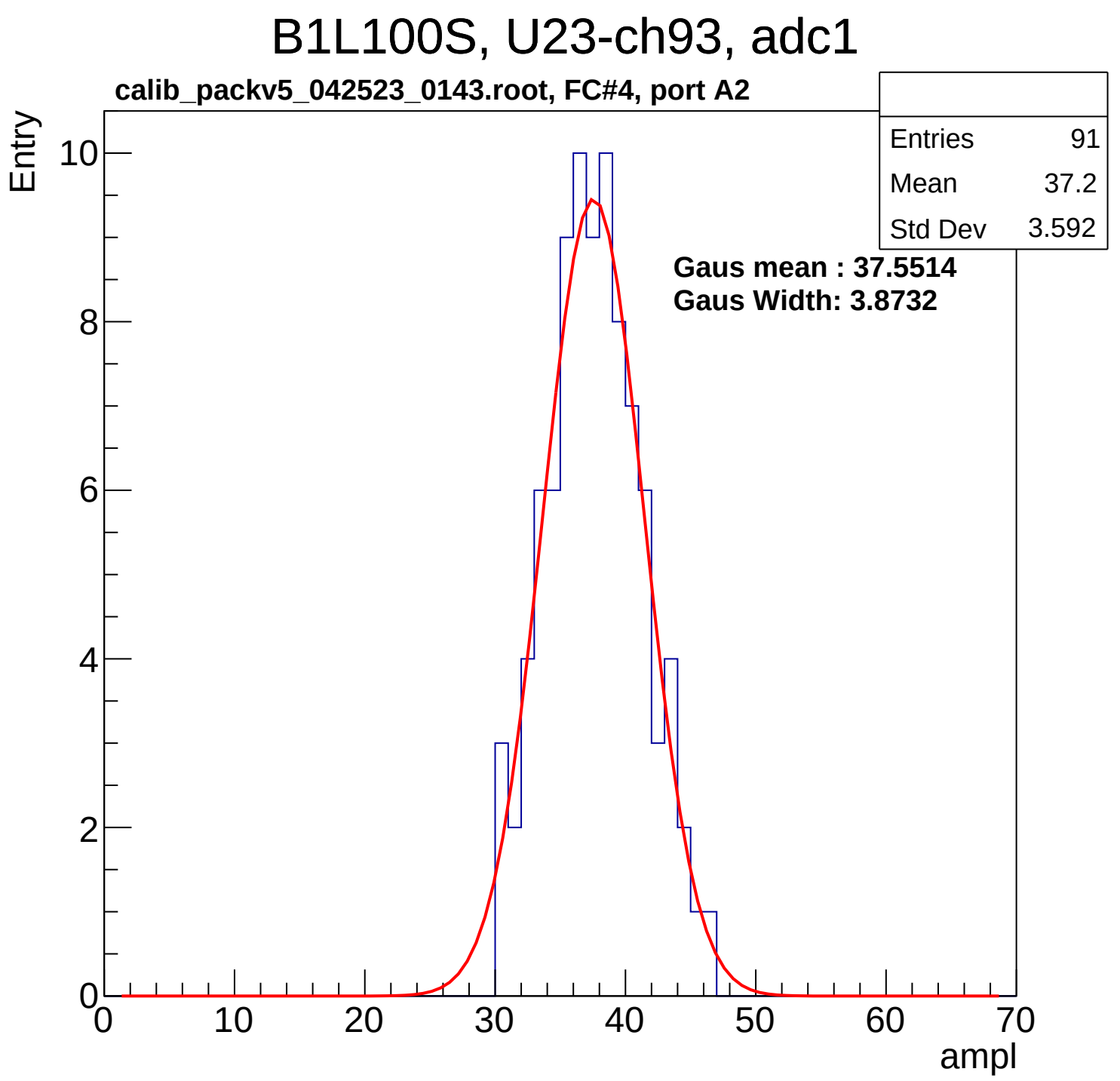
B1L100S, U23-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 37.2
Std Dev 3.592
Gaus mean : 37.5514
Gaus Width: 3.8732
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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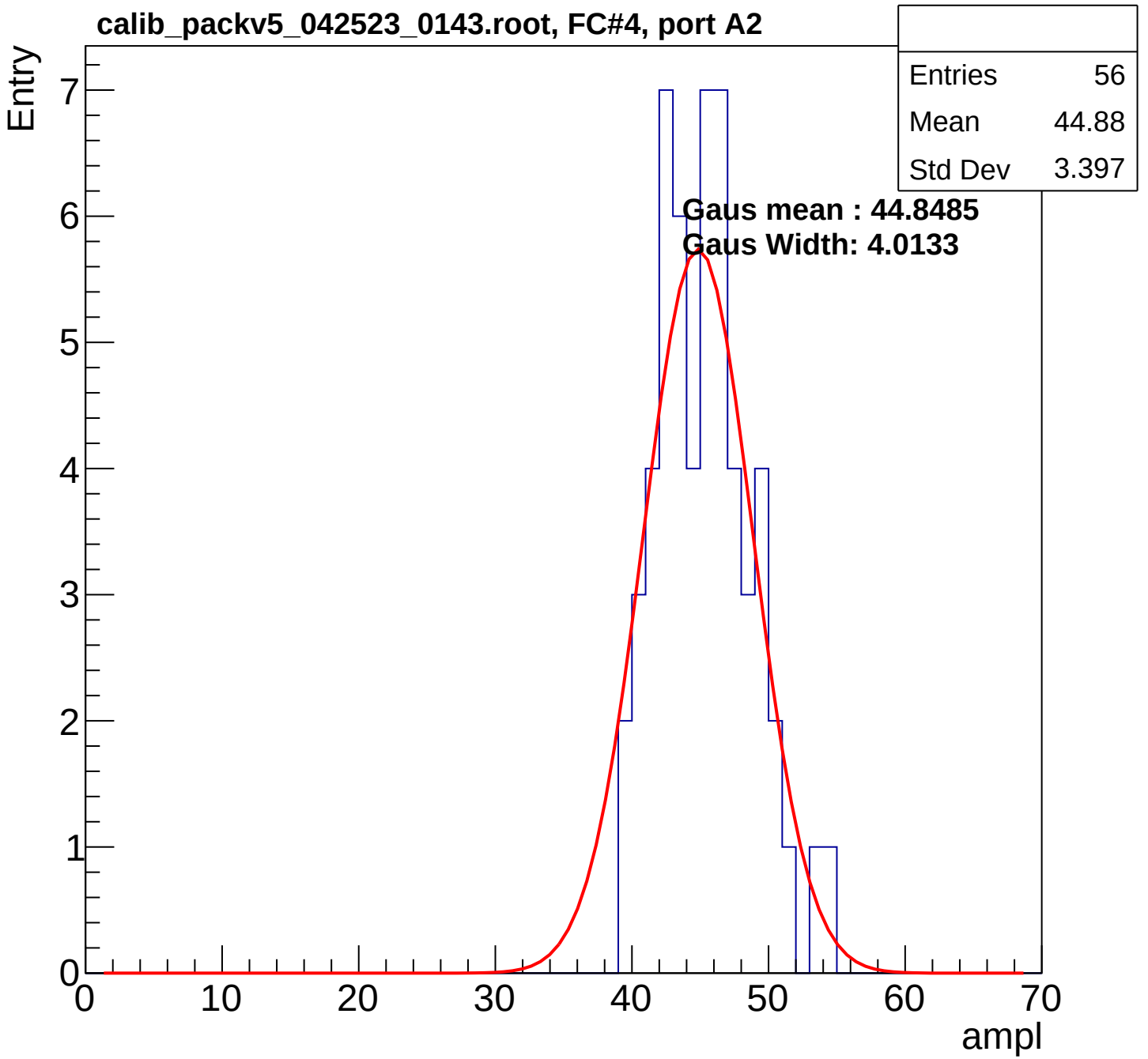
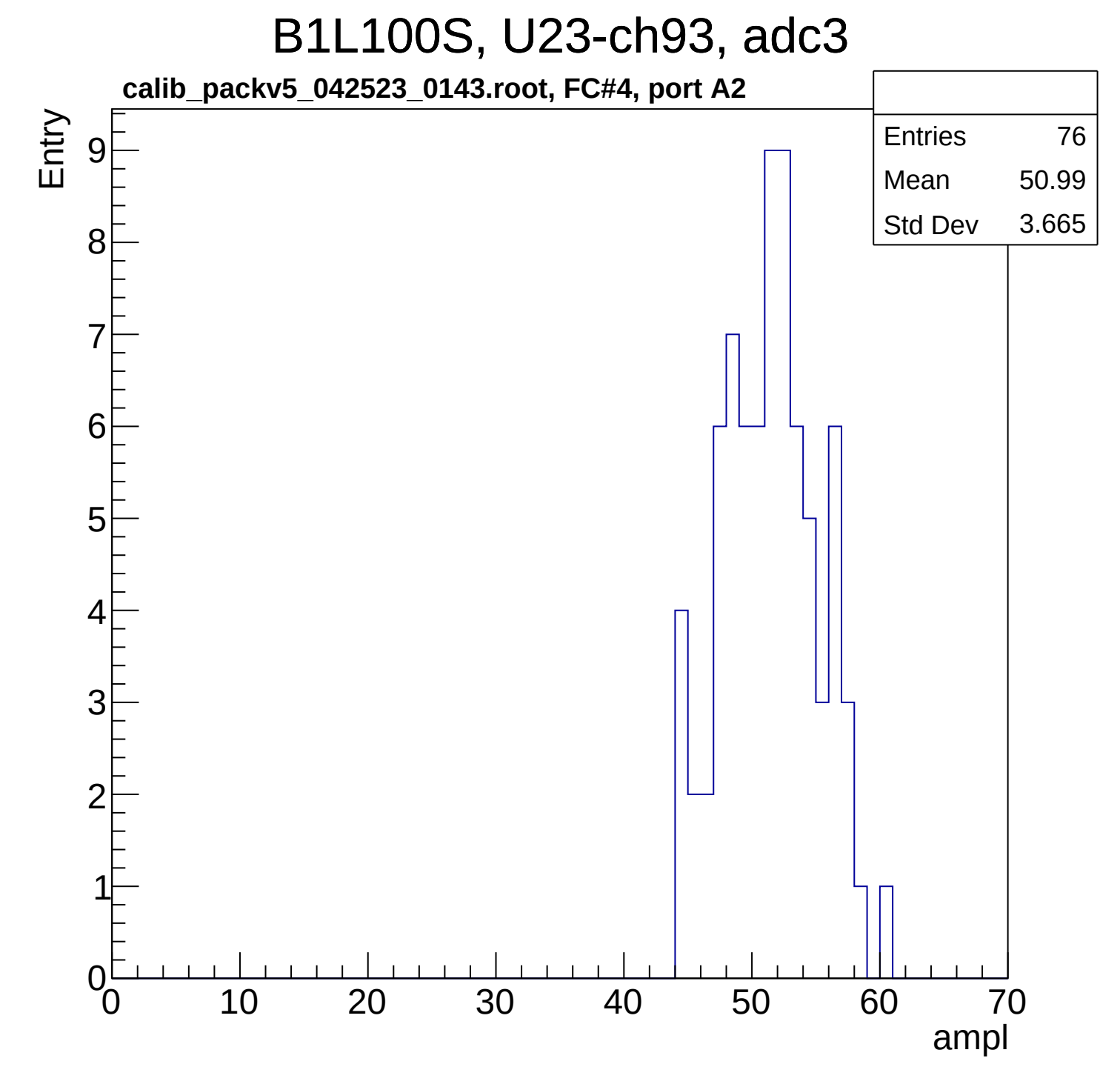
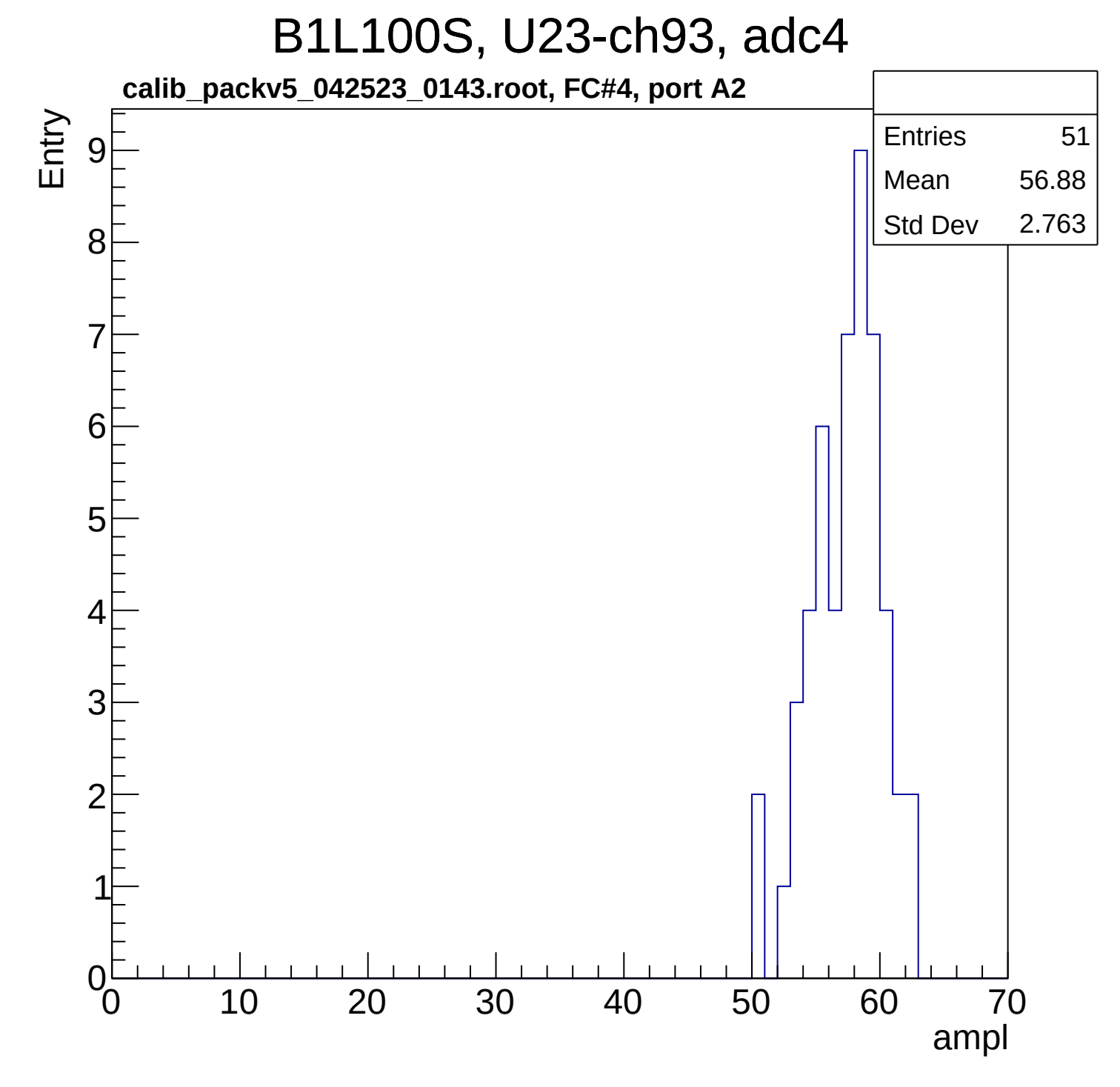

B1L100S, U23-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.88
Std Dev 3.397
Gaus mean : 44.8485
Gaus Width: 4.0133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch93, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 50.99
Std Dev 3.665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.88
Std Dev 2.763
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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50
60
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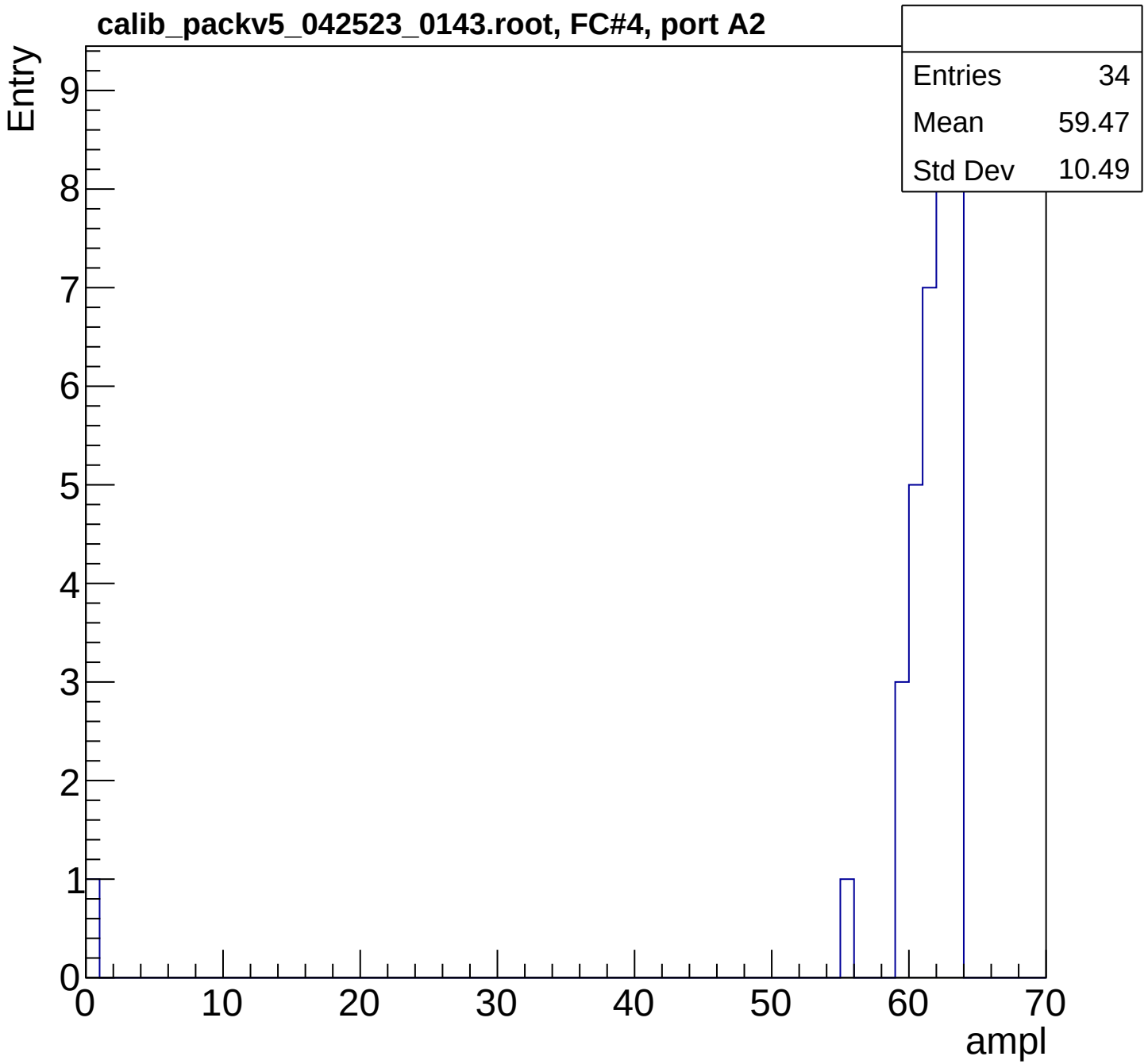
B1L100S, U23-ch93, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59.47
Std Dev 10.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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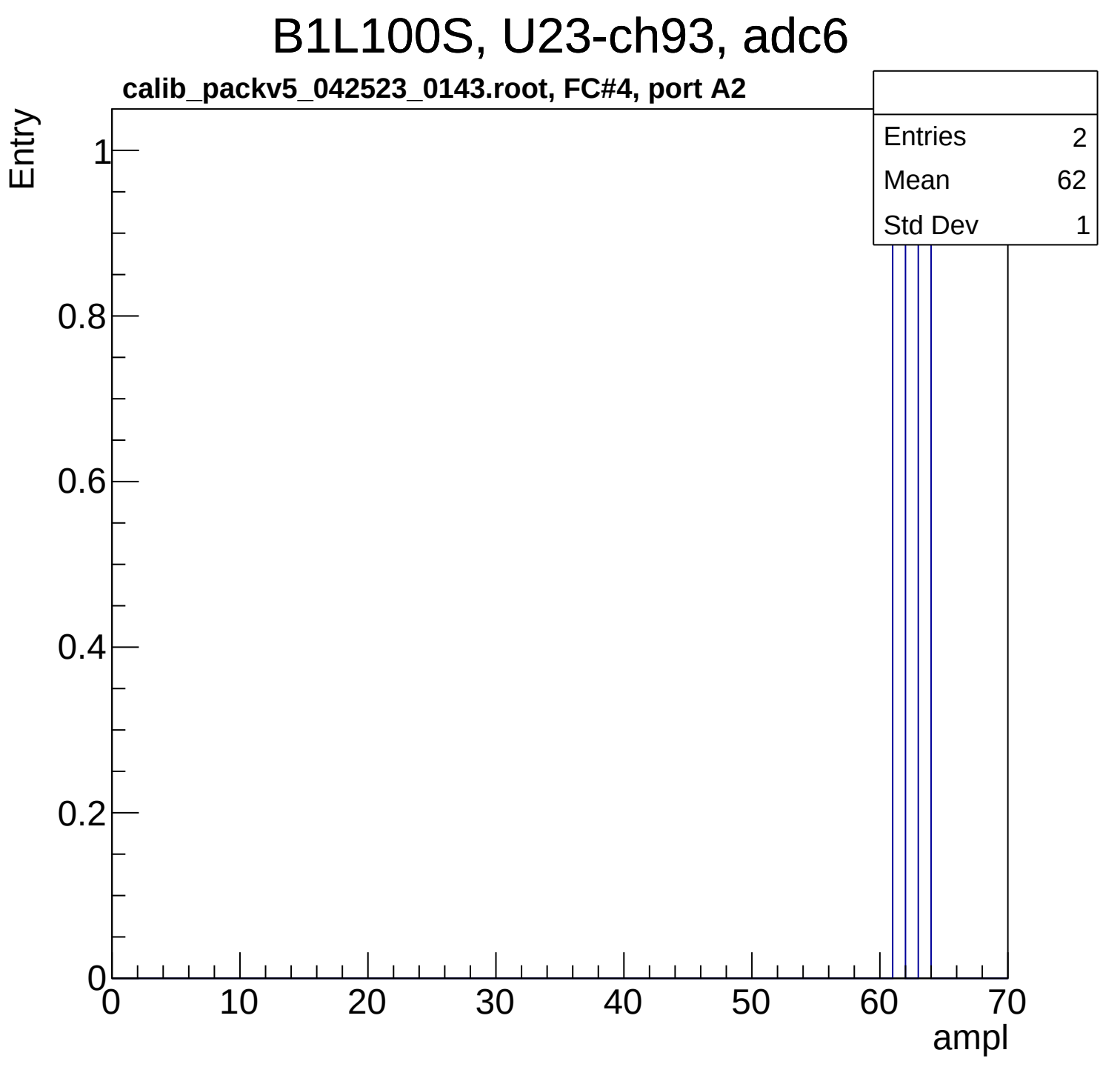
B1L100S, U23-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
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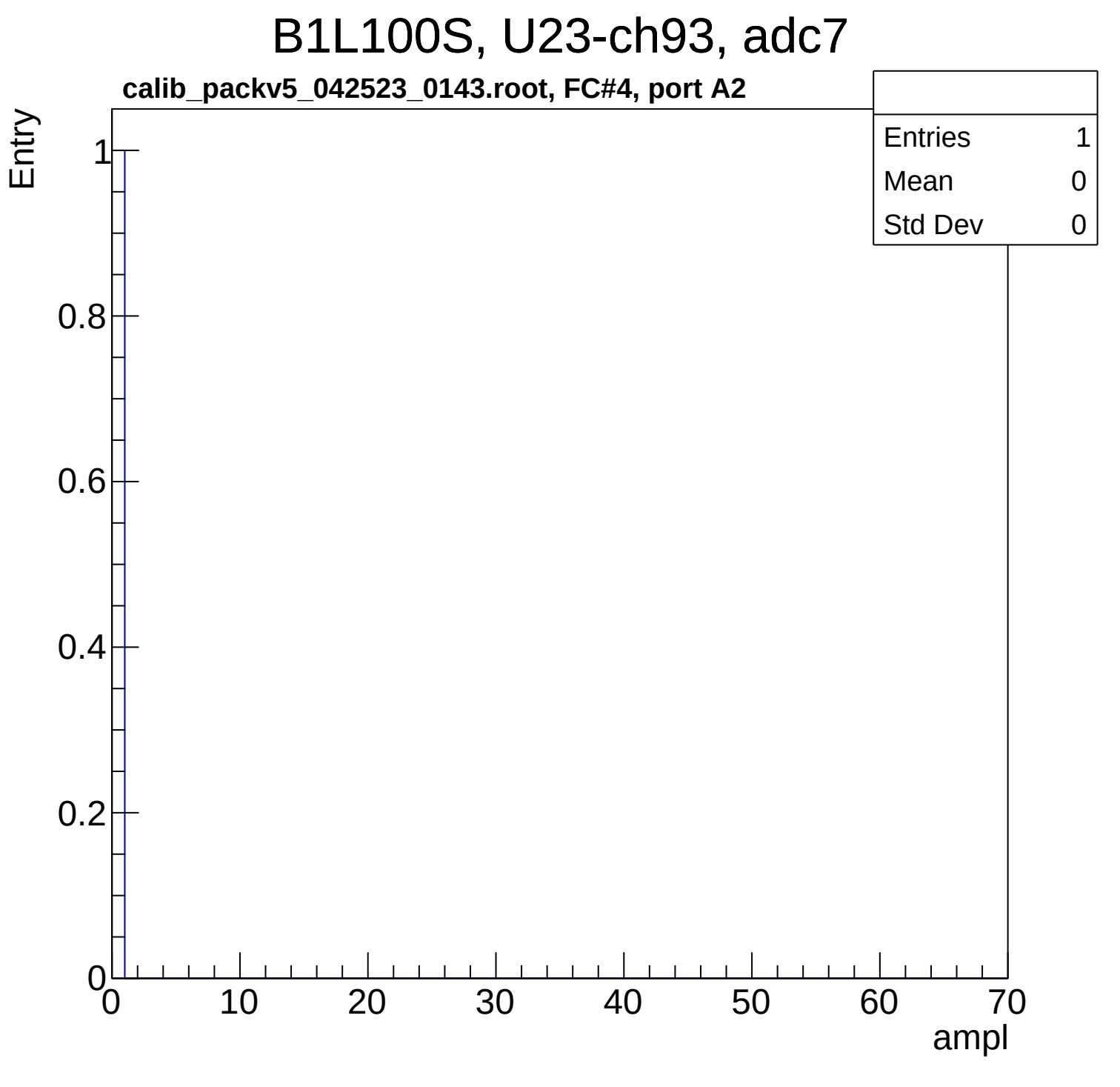
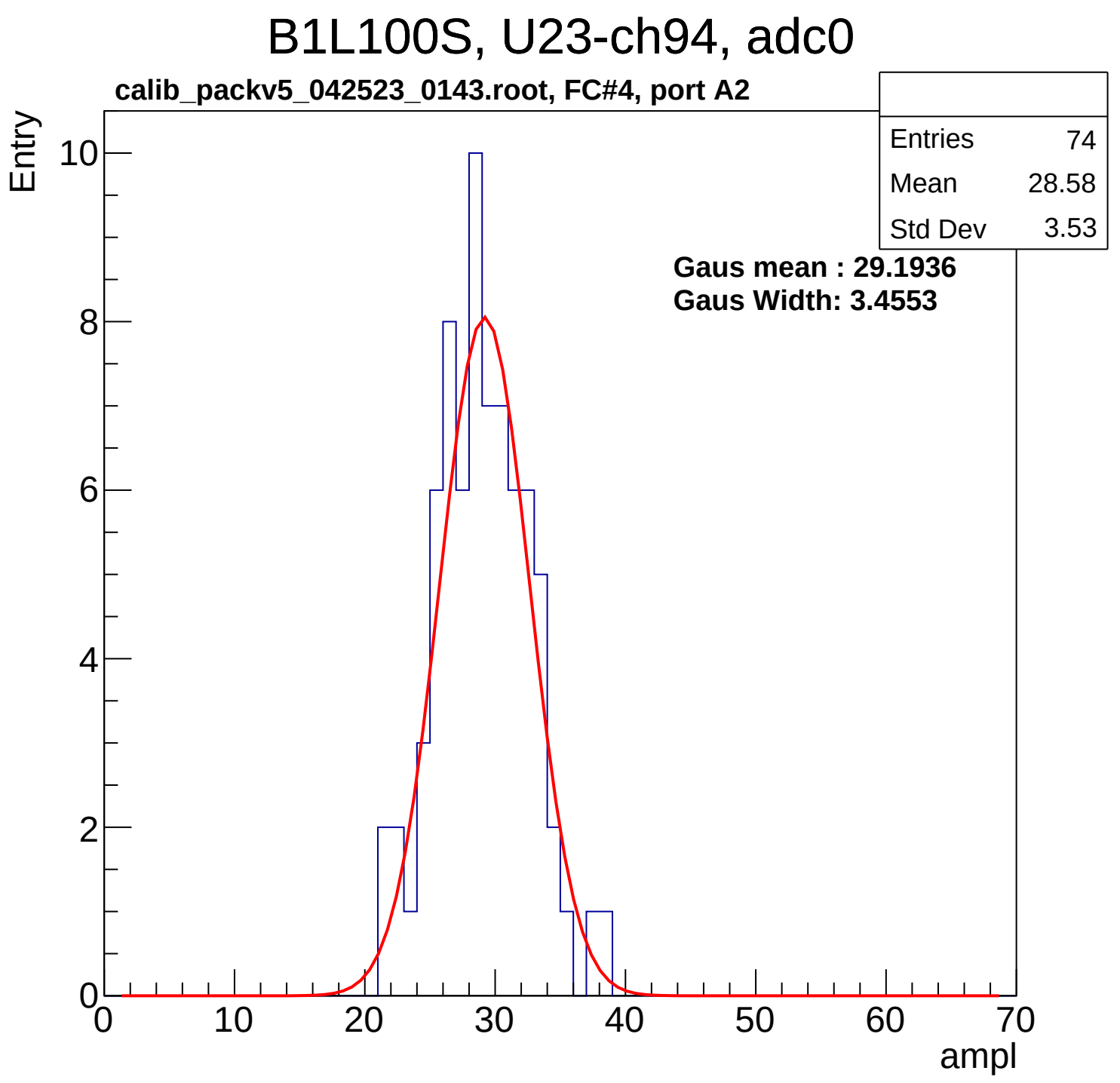
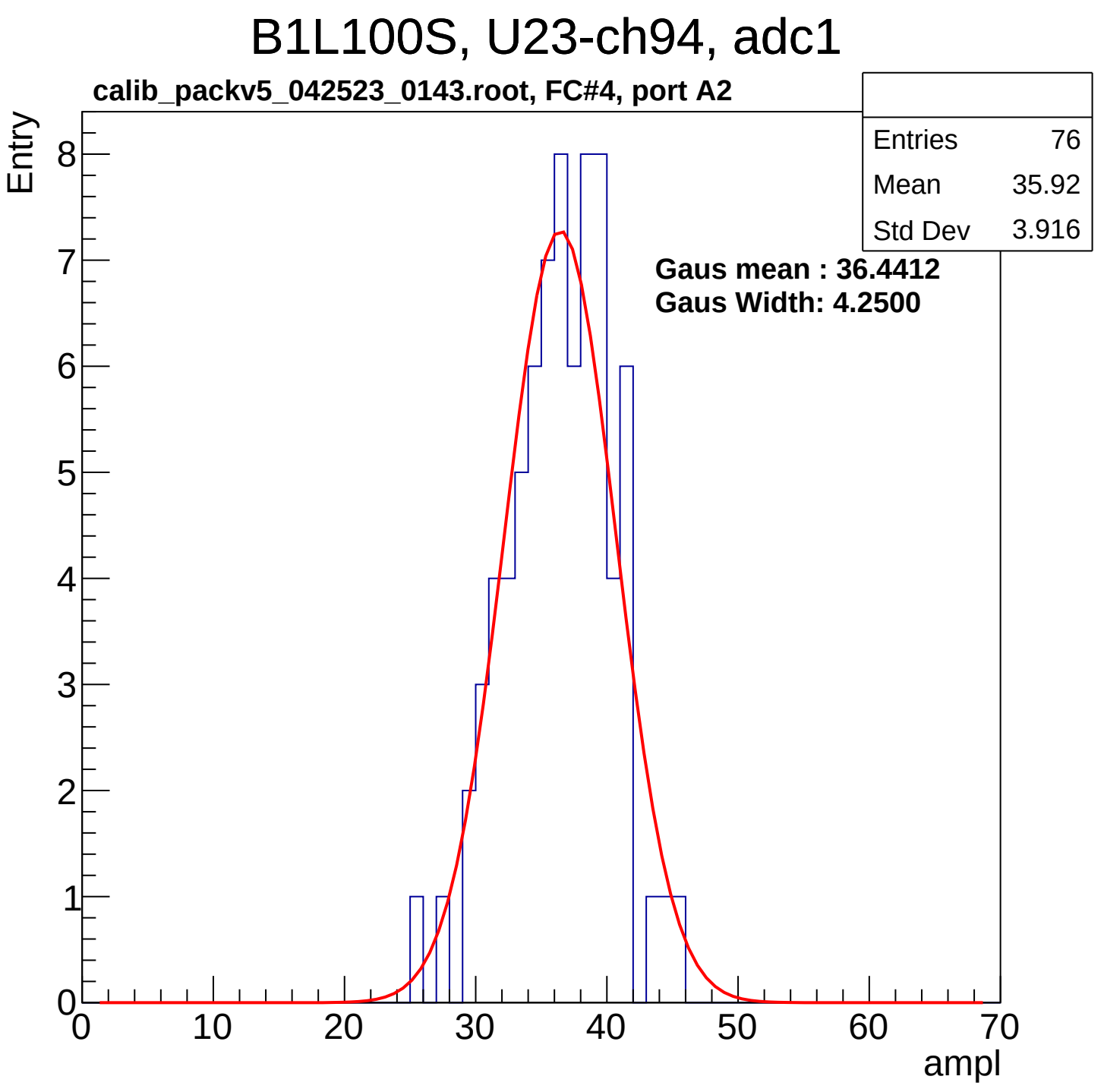
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch93, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.58
Std Dev 3.53
Gaus mean : 29.1936
Gaus Width: 3.4553
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.92
Std Dev 3.916
Gaus mean : 36.4412
Gaus Width: 4.2500
Entry
ampl
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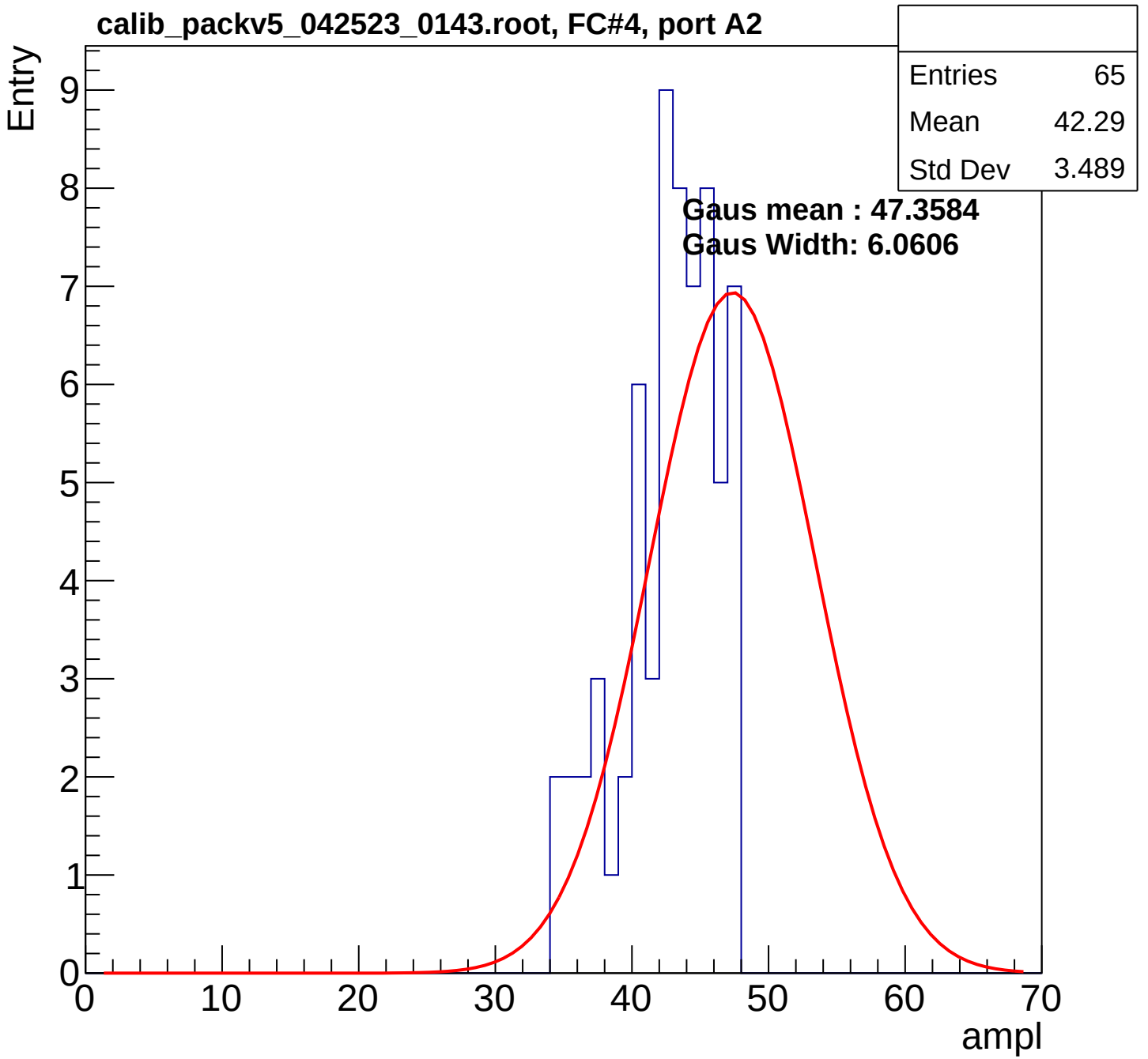
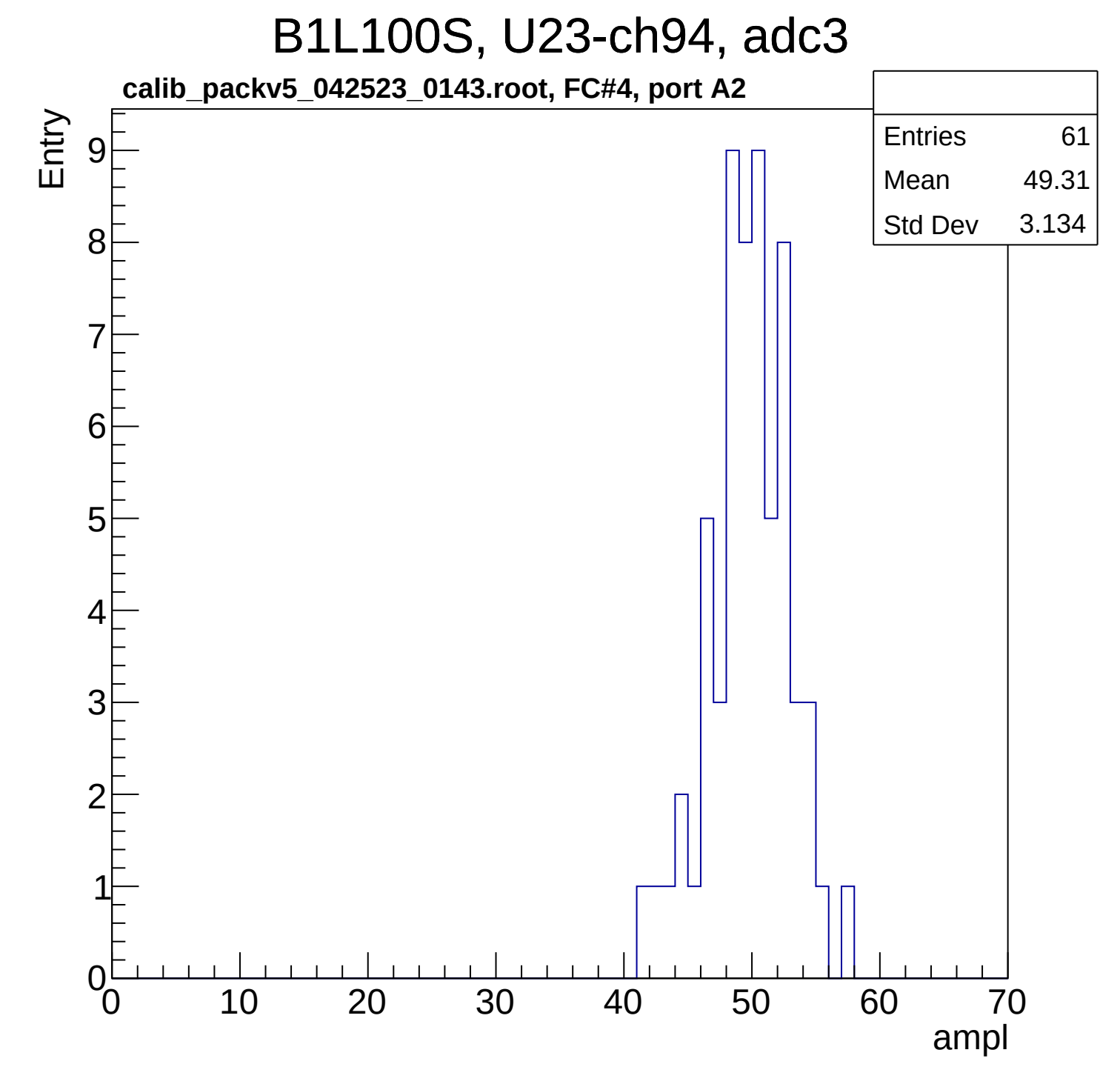
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.29
Std Dev 3.489
Gaus mean : 47.3584
Gaus Width: 6.0606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.31
Std Dev 3.134
Entry
ampl
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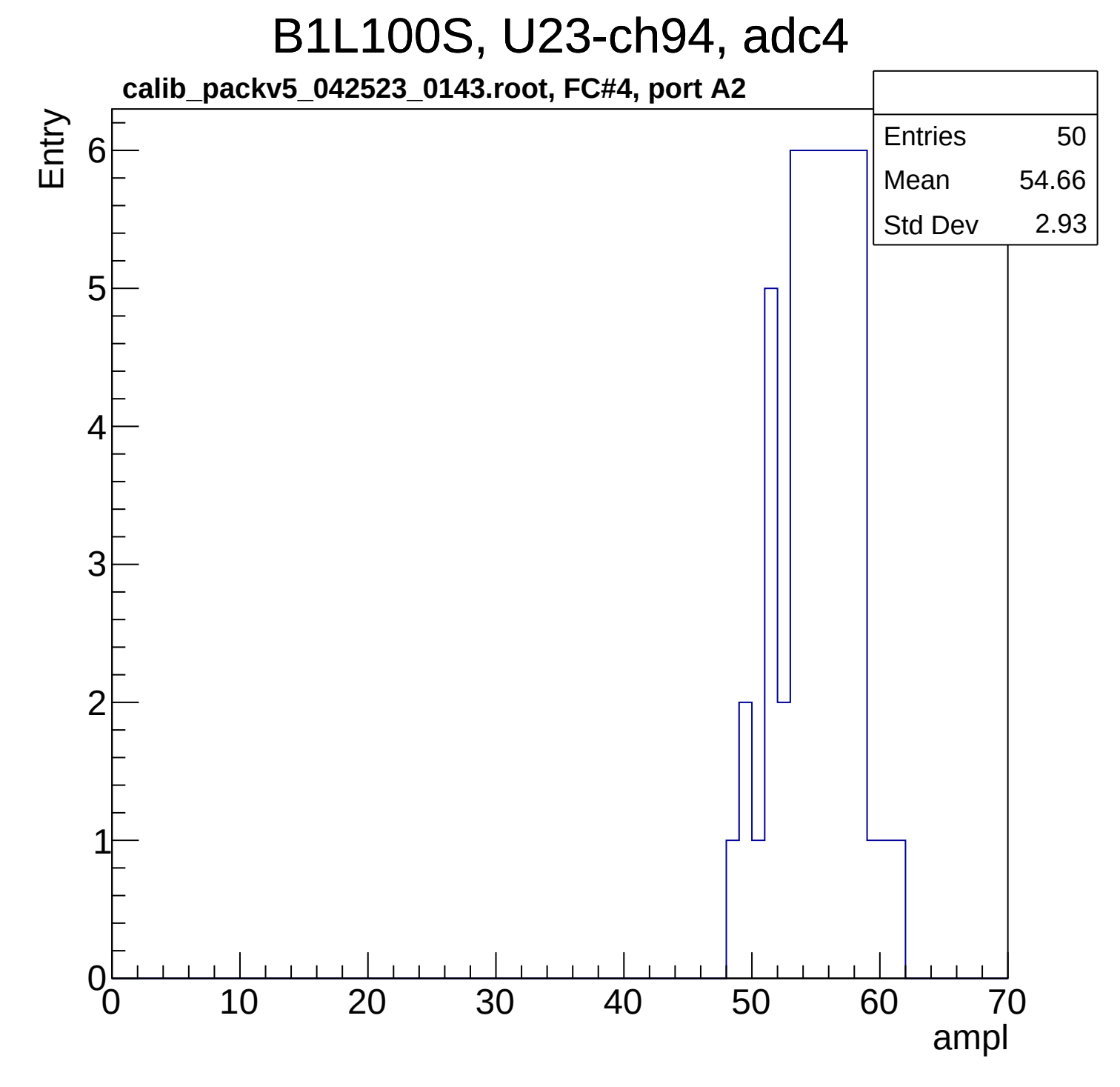
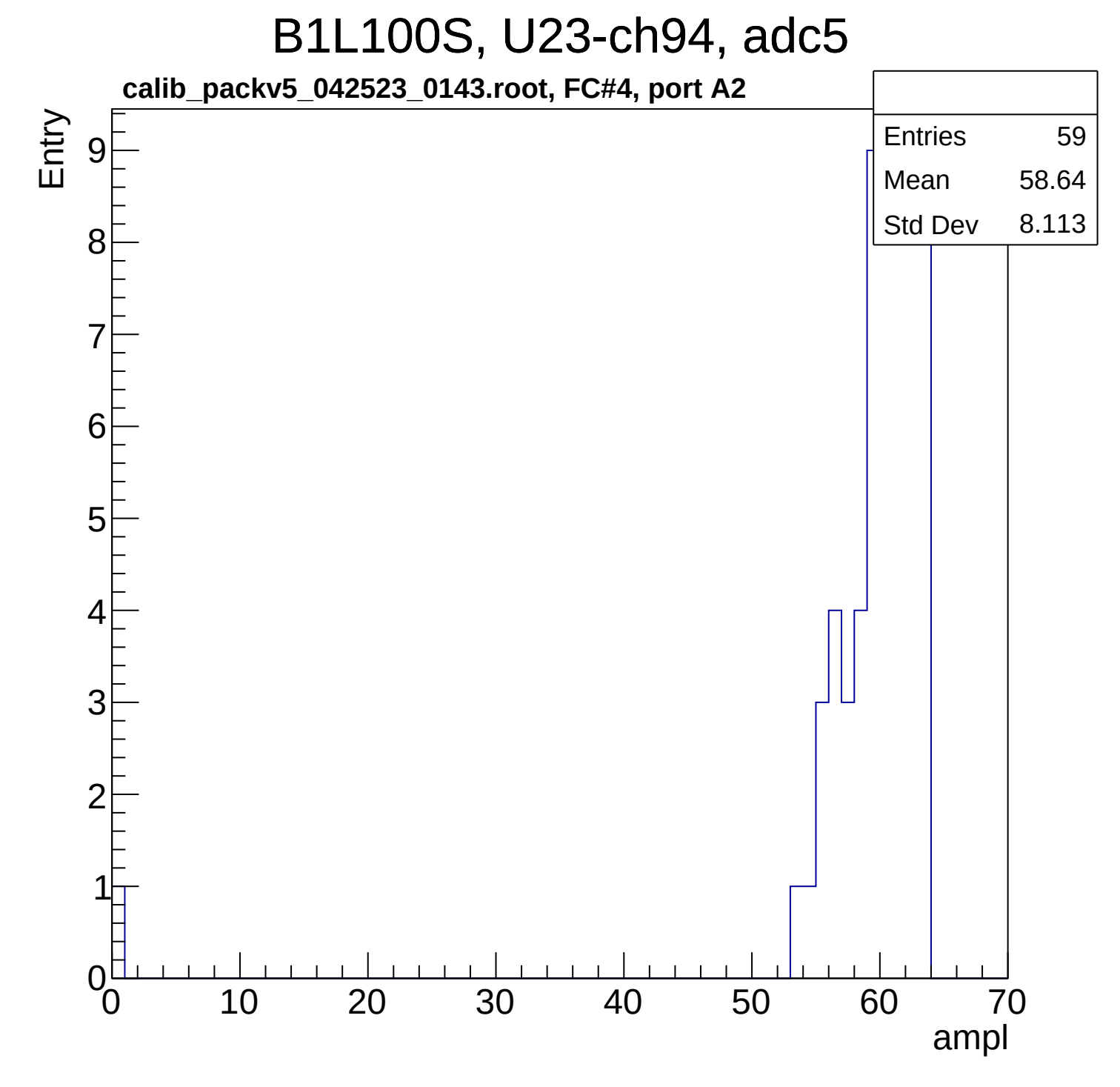
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.66
Std Dev 2.93
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch94, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.64
Std Dev 8.113
Entry
ampl
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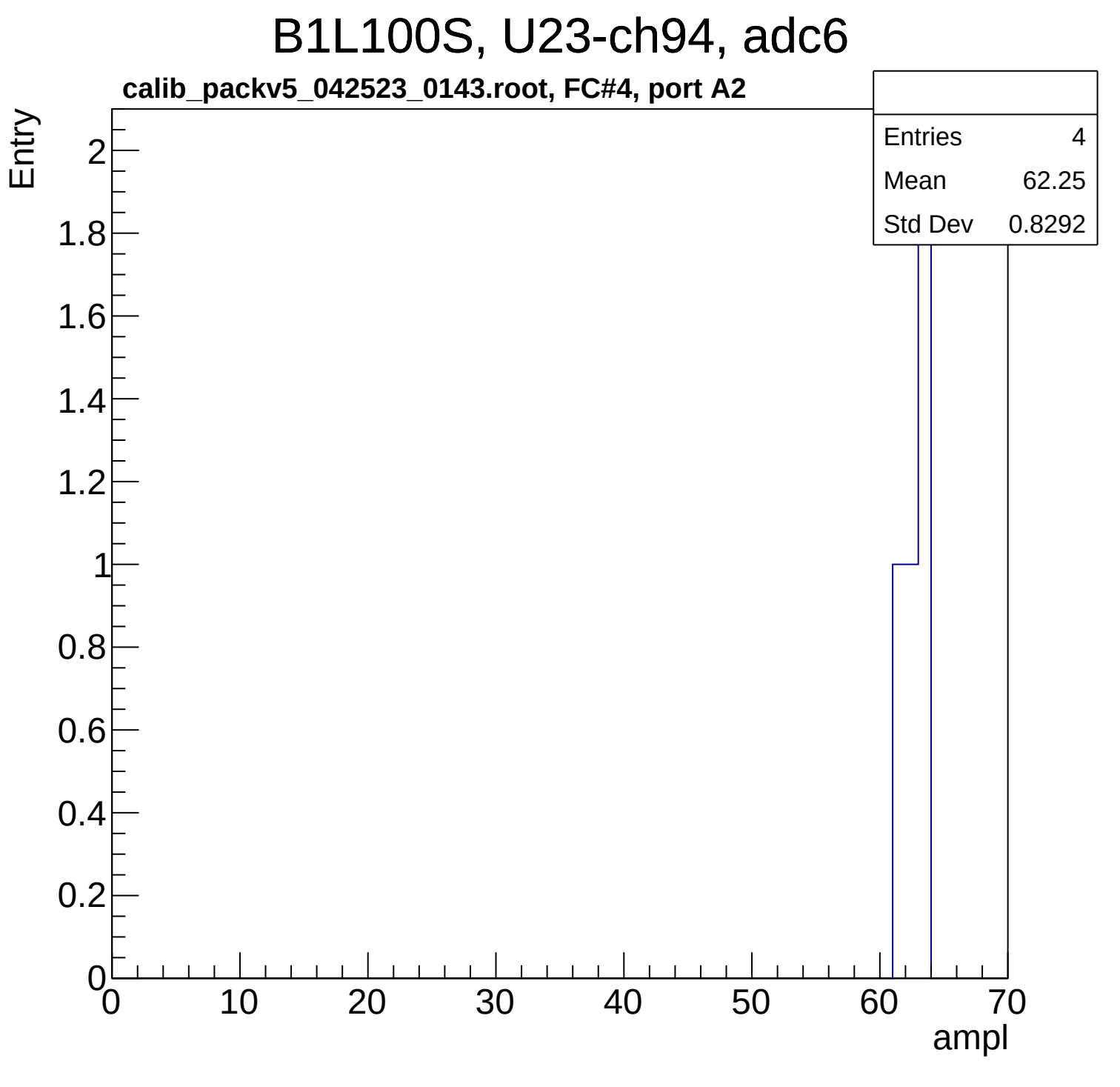
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch94, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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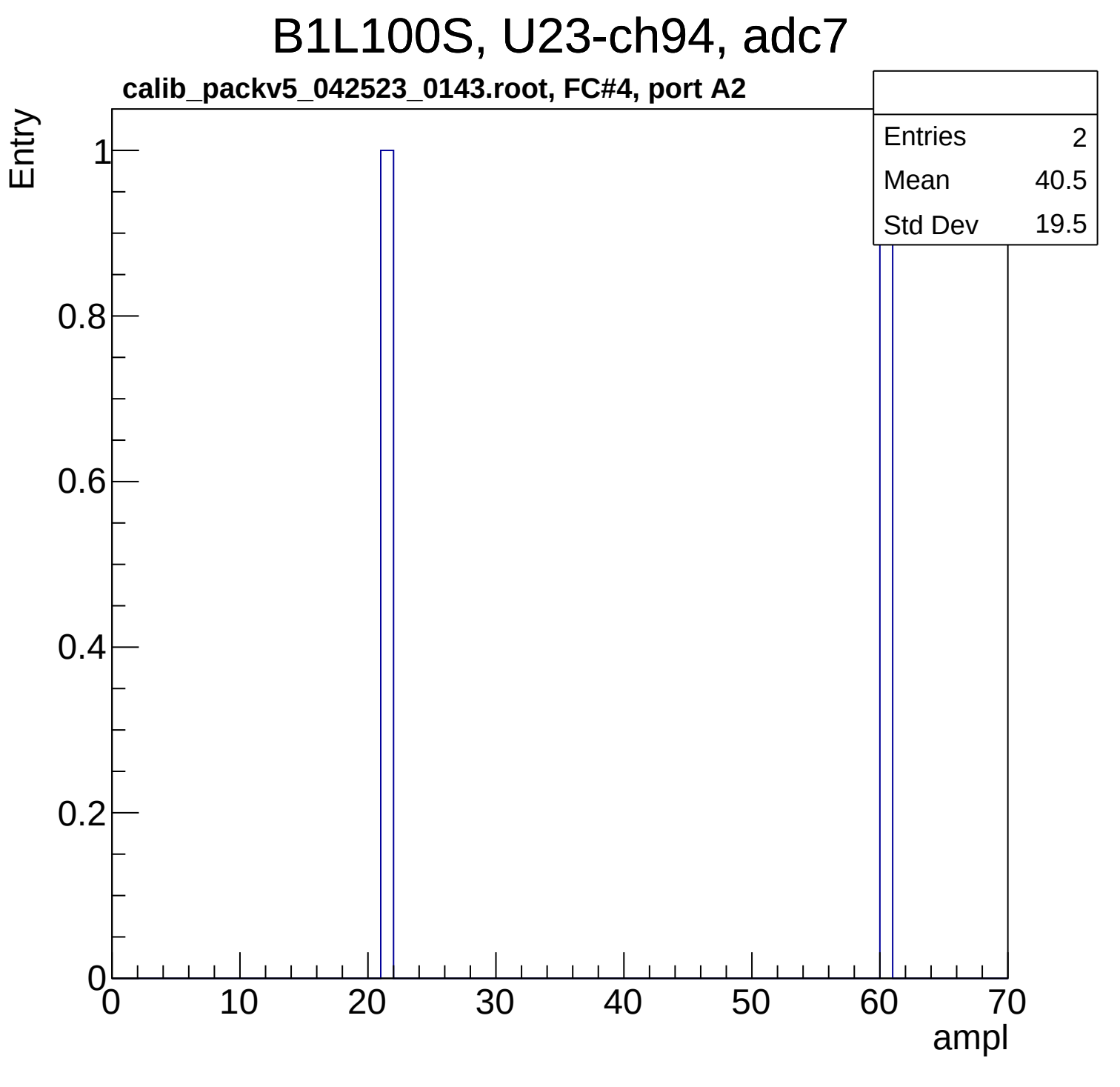
B1L100S, U23-ch94, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 40.5
Std Dev 19.5
Entry
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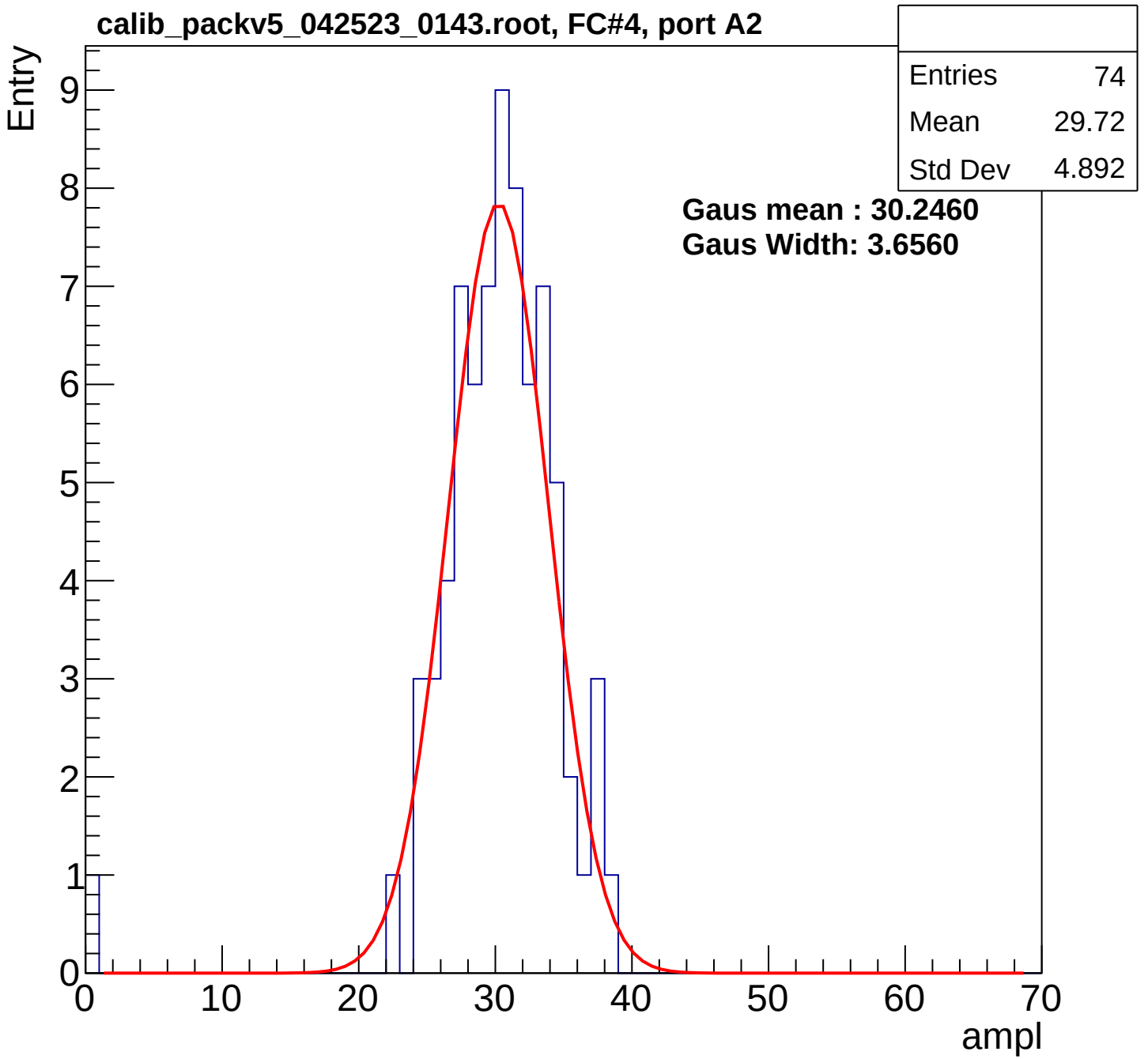
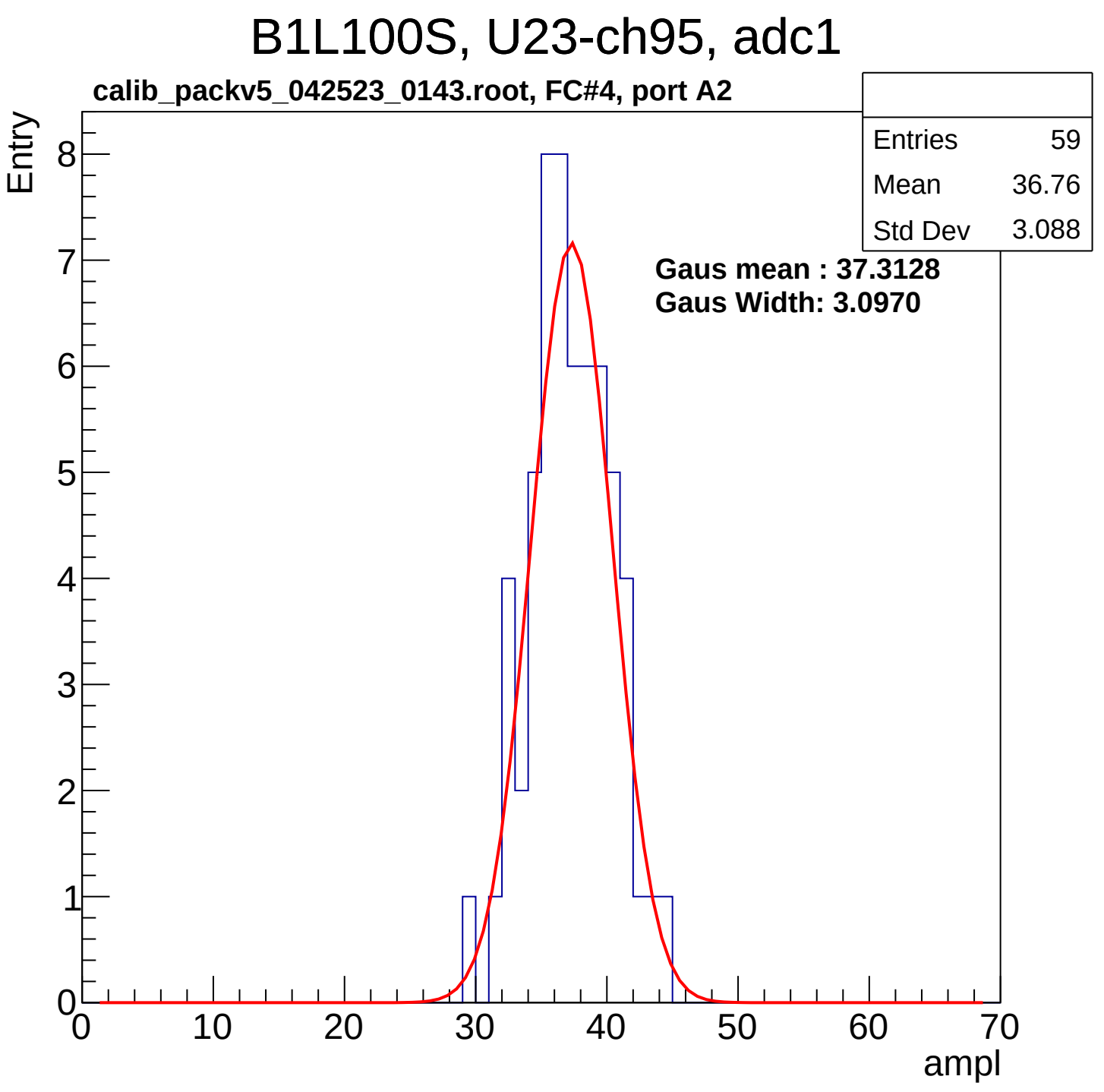
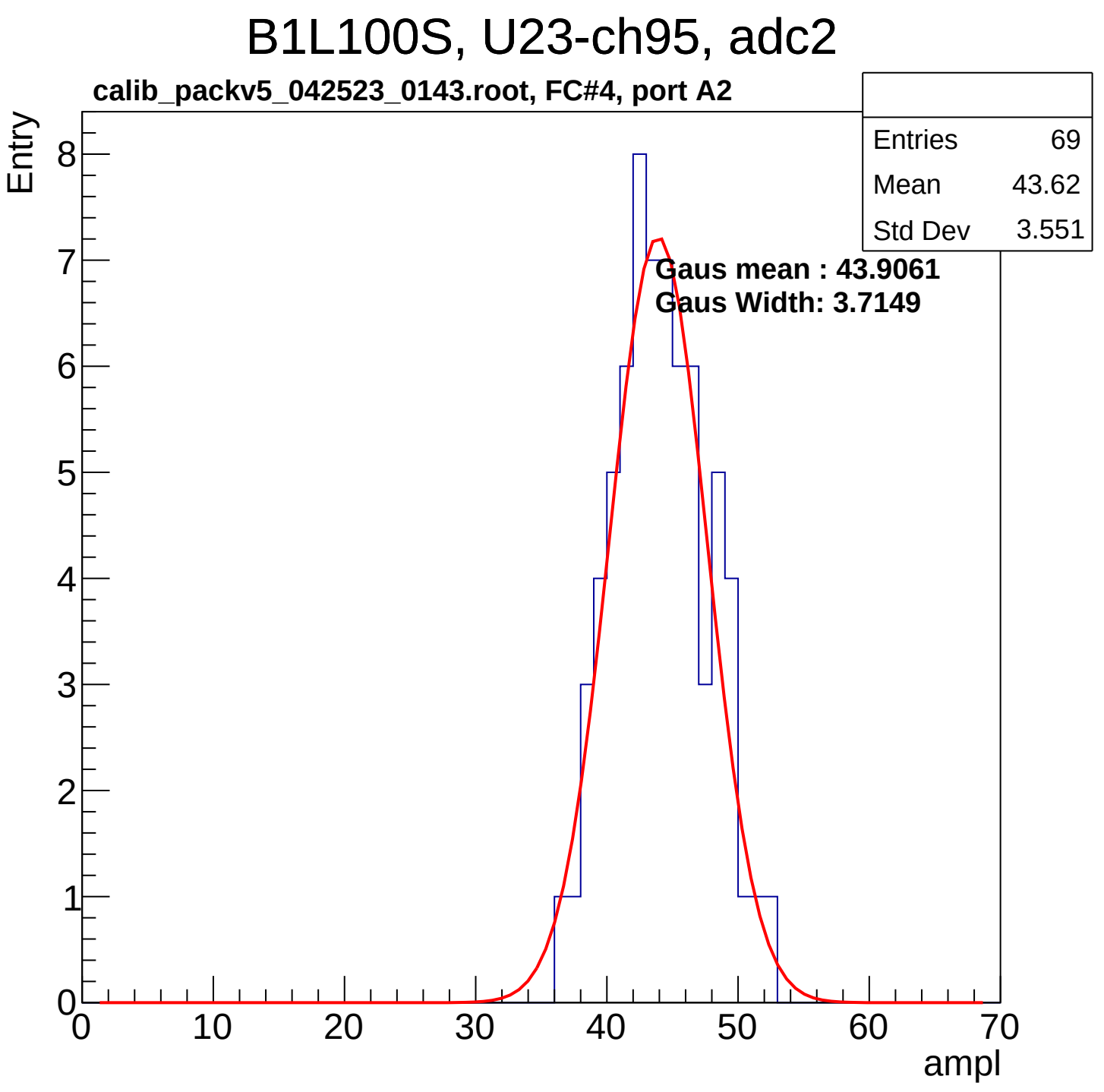
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.72
Std Dev 4.892
Gaus mean : 30.2460
Gaus Width: 3.6560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.76
Std Dev 3.088
Gaus mean : 37.3128
Gaus Width: 3.0970
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.62
Std Dev 3.551
Gaus mean : 43.9061
Gaus Width: 3.7149
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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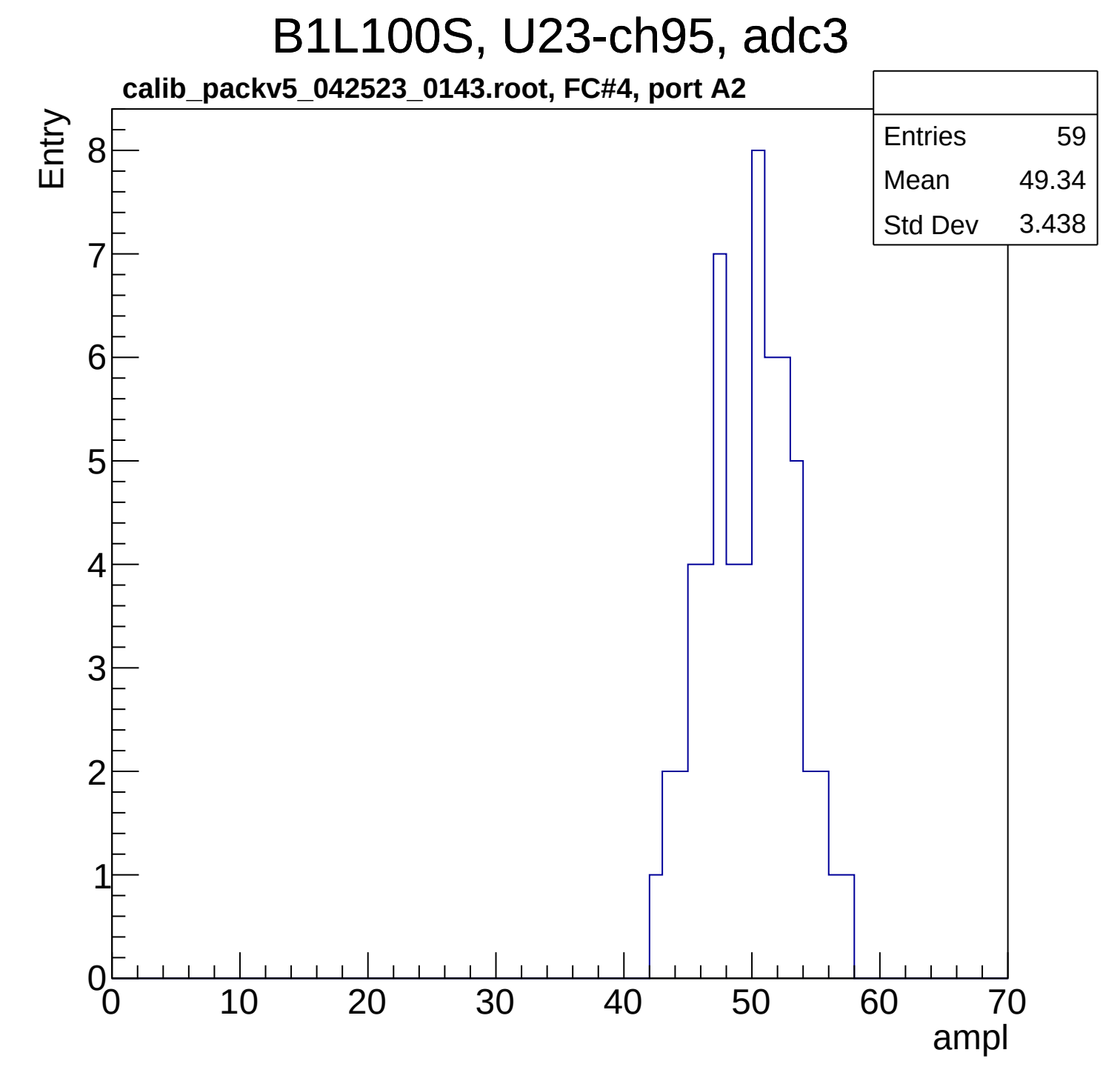
60
70


B1L100S, U23-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 49.34
Std Dev 3.438
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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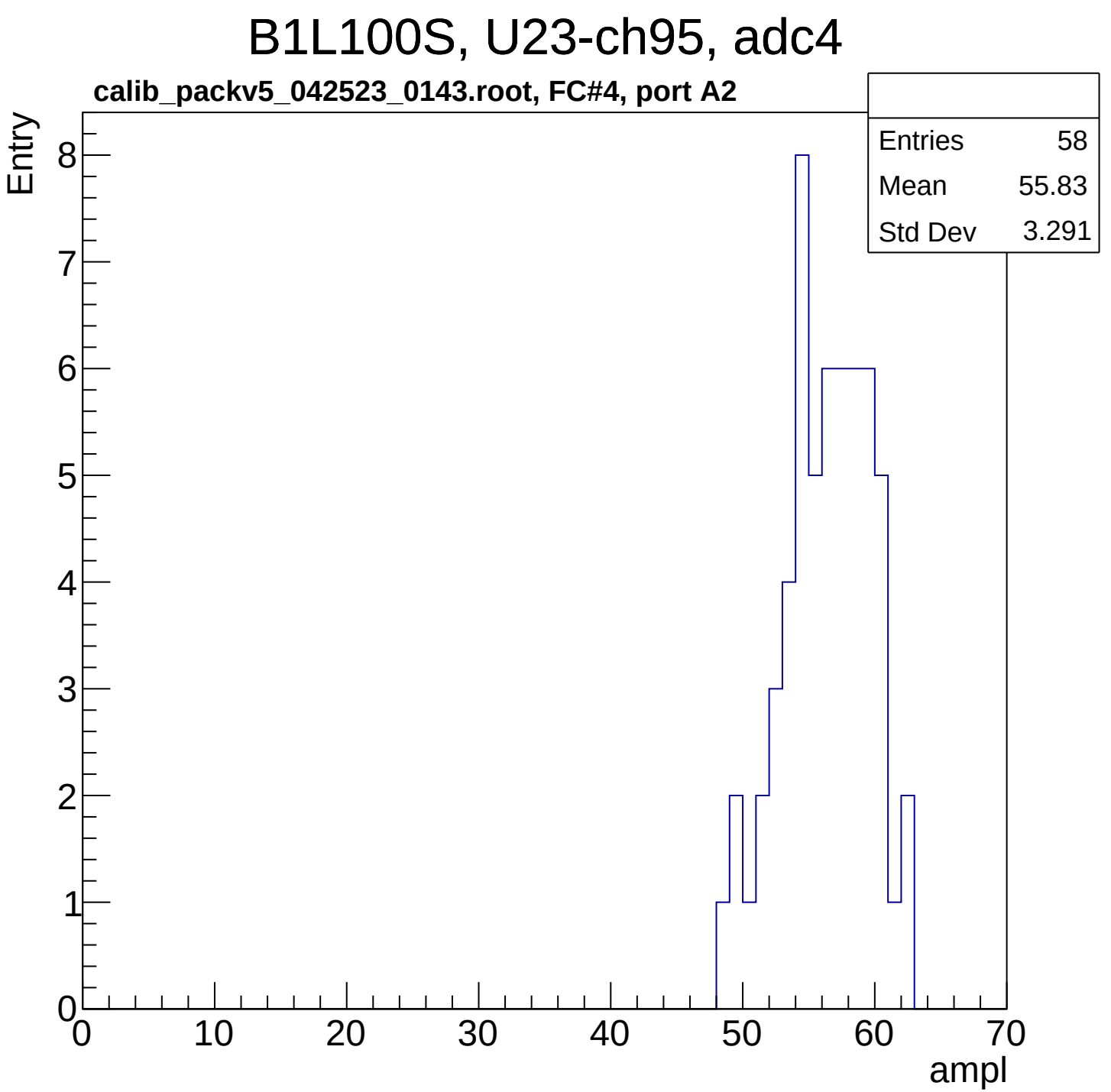
40
50
60
70


B1L100S, U23-ch95, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.83
Std Dev 3.291
Entry
ampl
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1
2
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5
6
7
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0
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40
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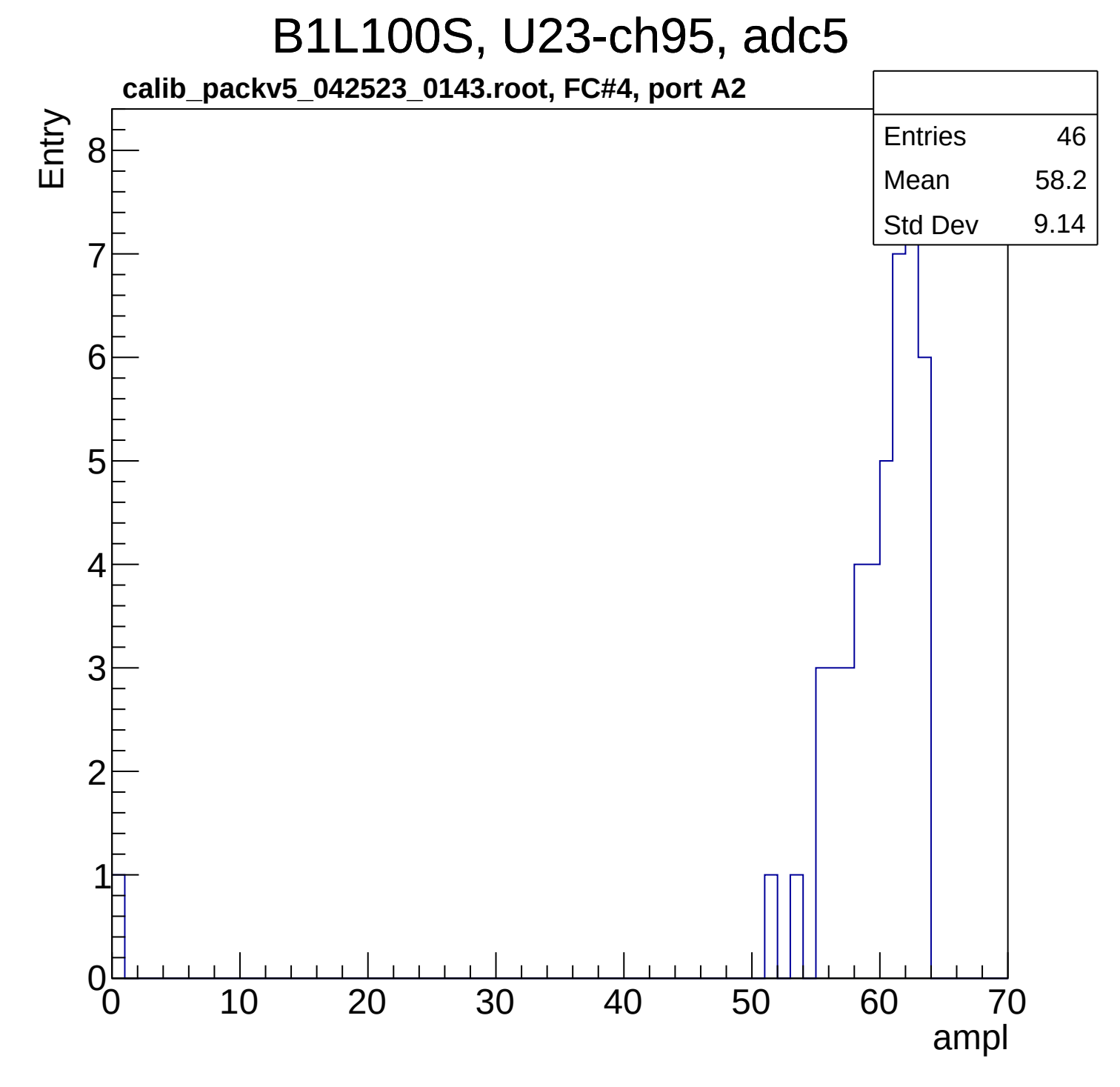
60
70


B1L100S, U23-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.2
Std Dev 9.14
Entry
ampl
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1
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3
4
5
6
7
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0
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60
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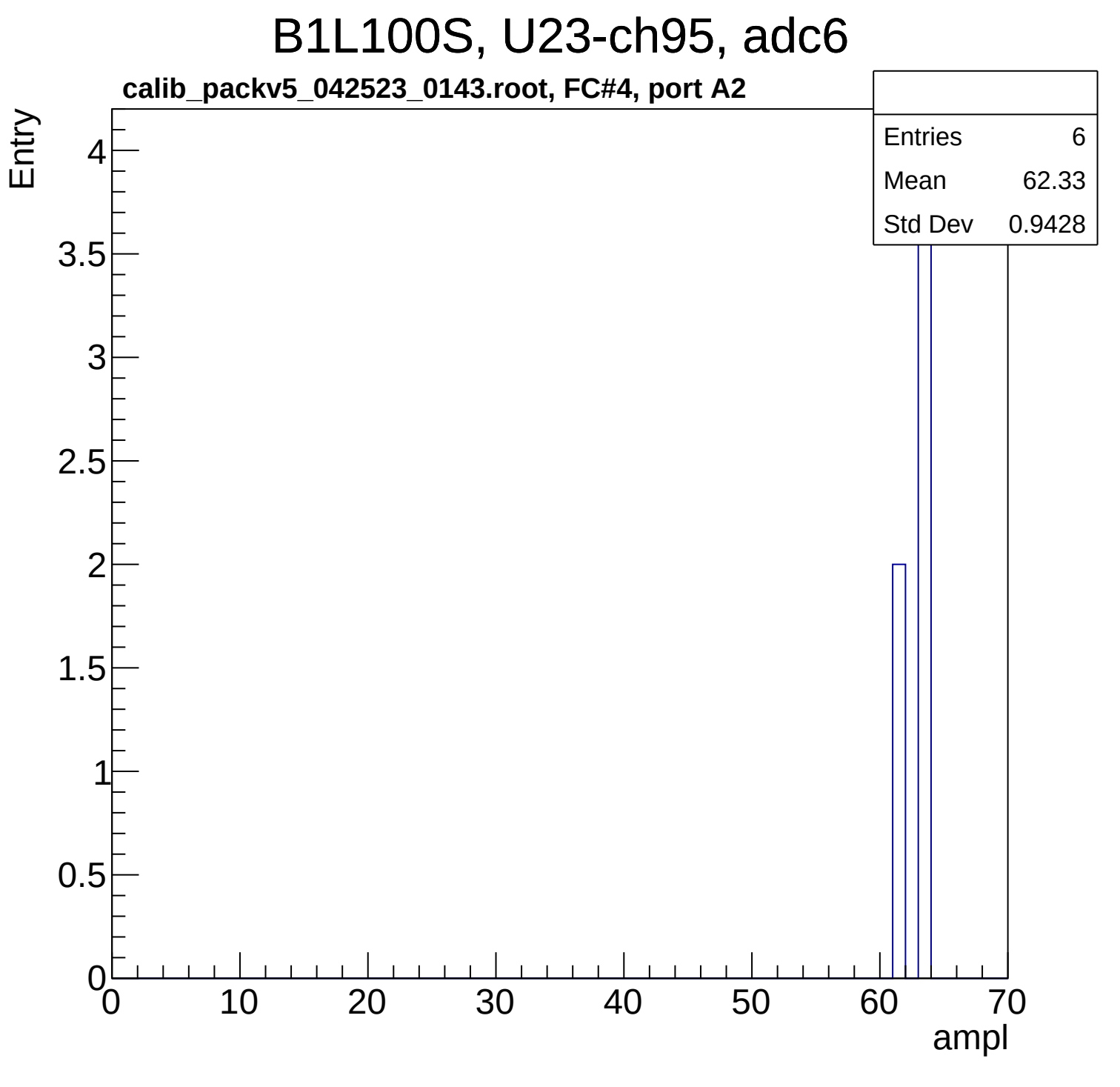
B1L100S, U23-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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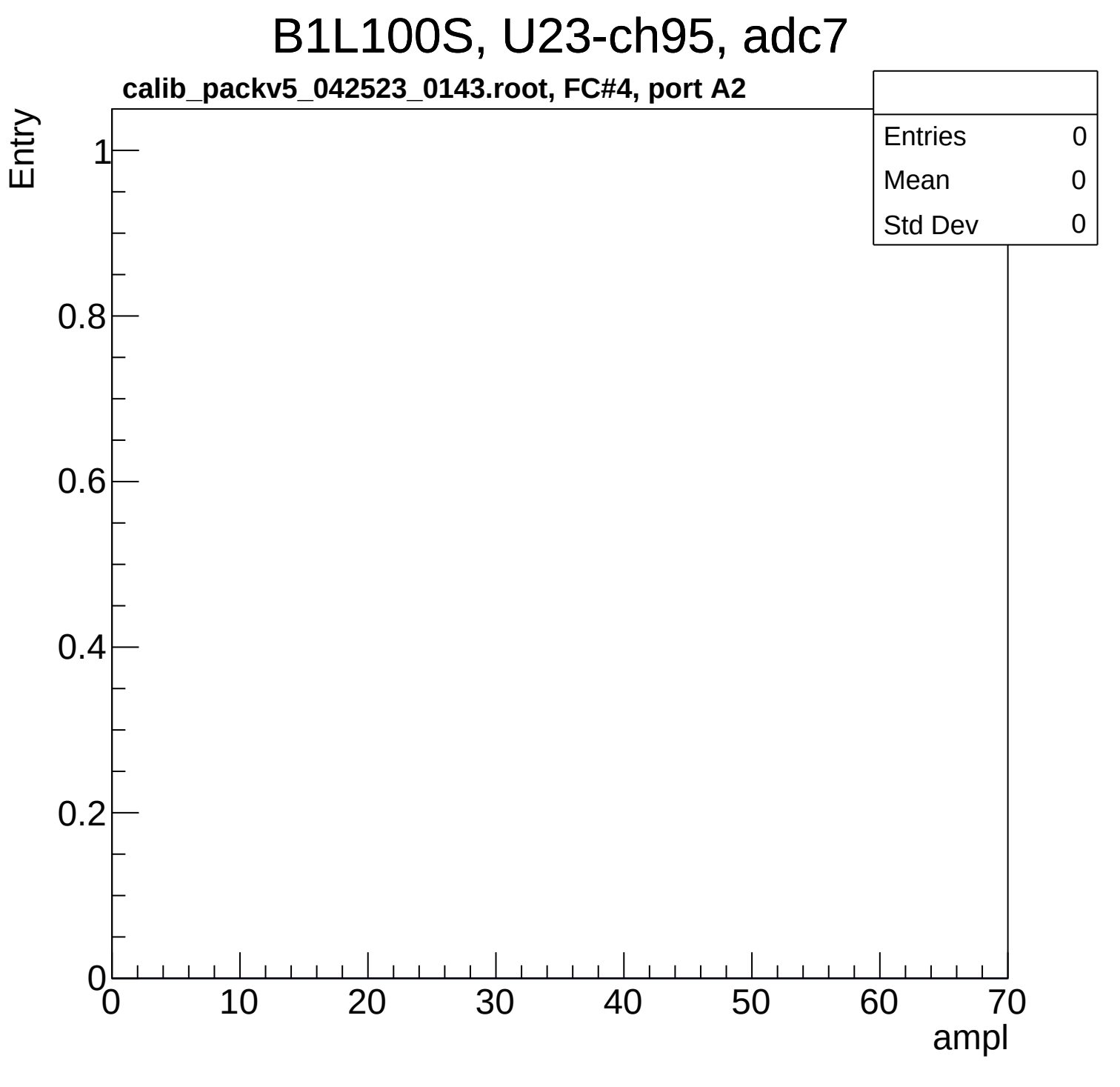
B1L100S, U23-ch95, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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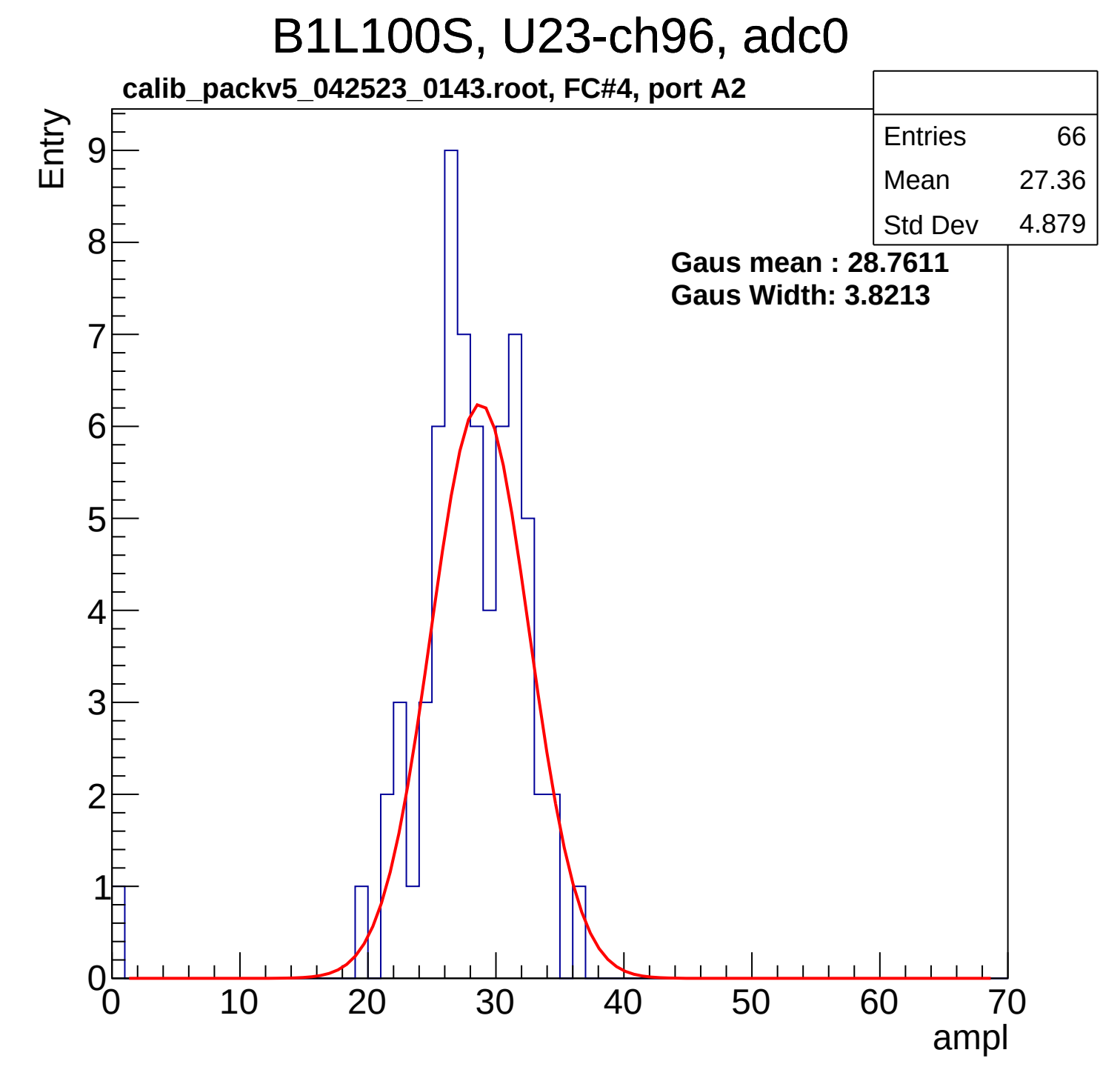
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 27.36
Std Dev 4.879
Gaus mean : 28.7611
Gaus Width: 3.8213
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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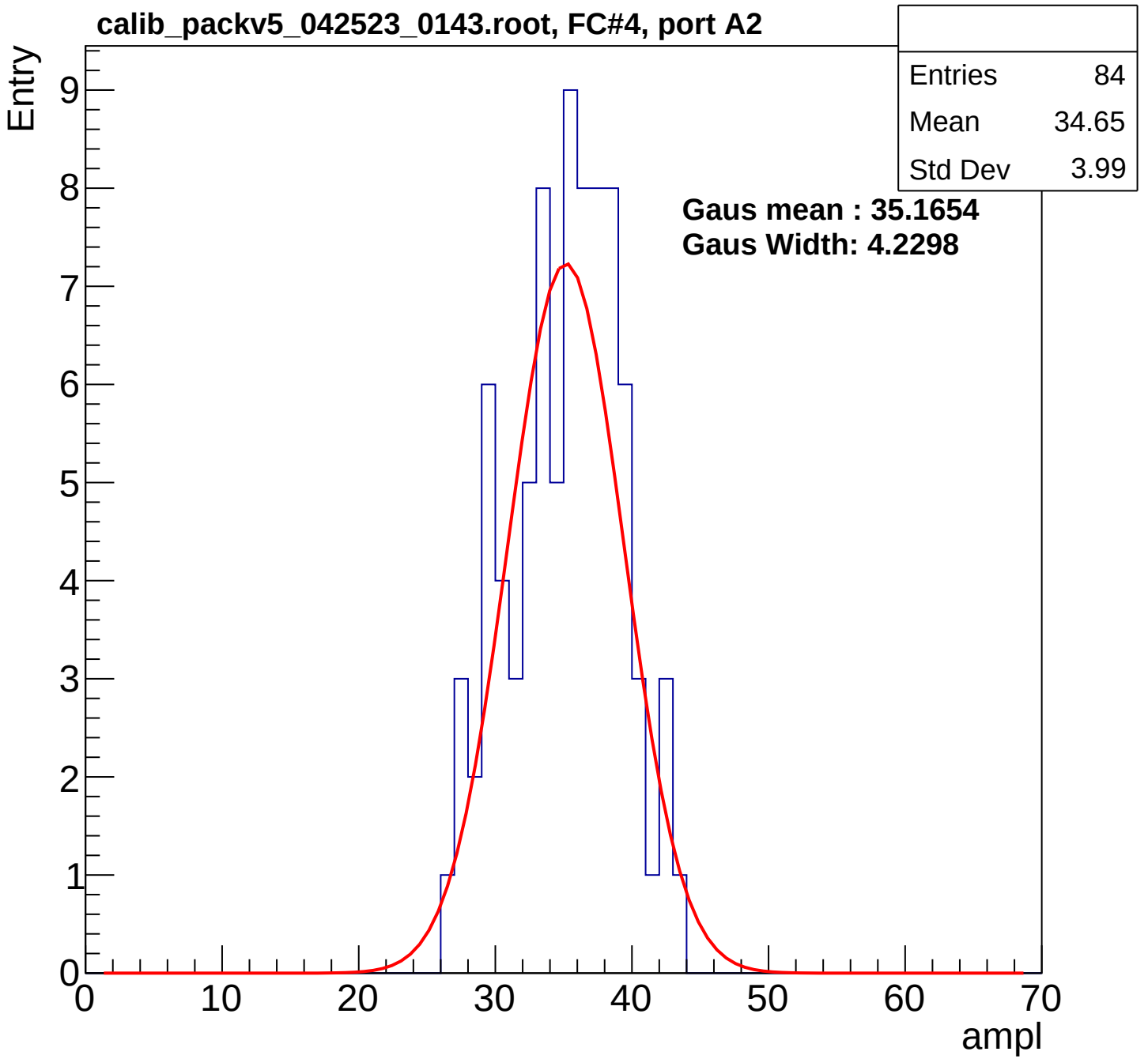
B1L100S, U23-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 34.65
Std Dev 3.99
Gaus mean : 35.1654
Gaus Width: 4.2298
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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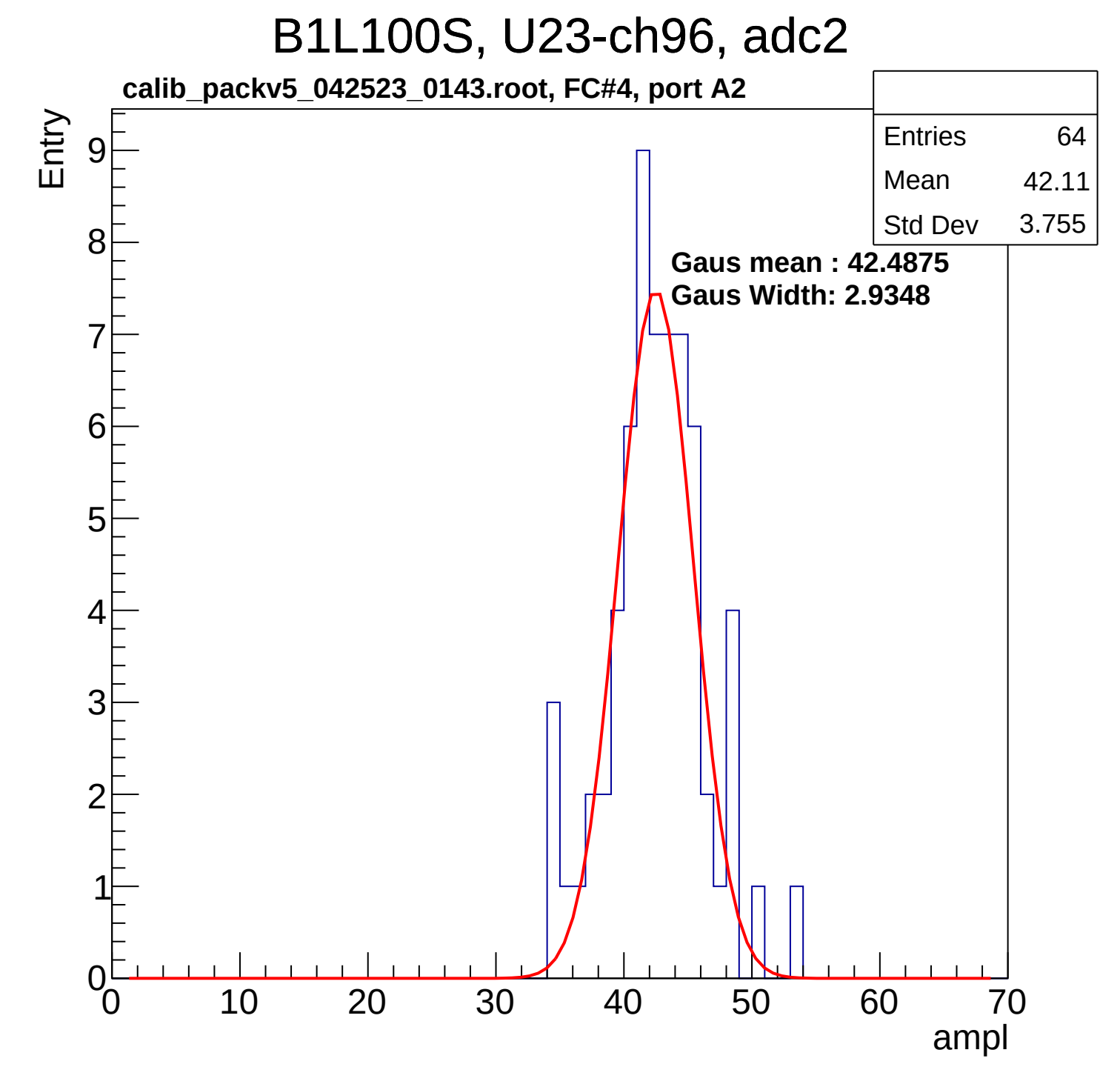
B1L100S, U23-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.11
Std Dev 3.755
Gaus mean : 42.4875
Gaus Width: 2.9348
Entry
ampl
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2
3
4
5
6
7
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0
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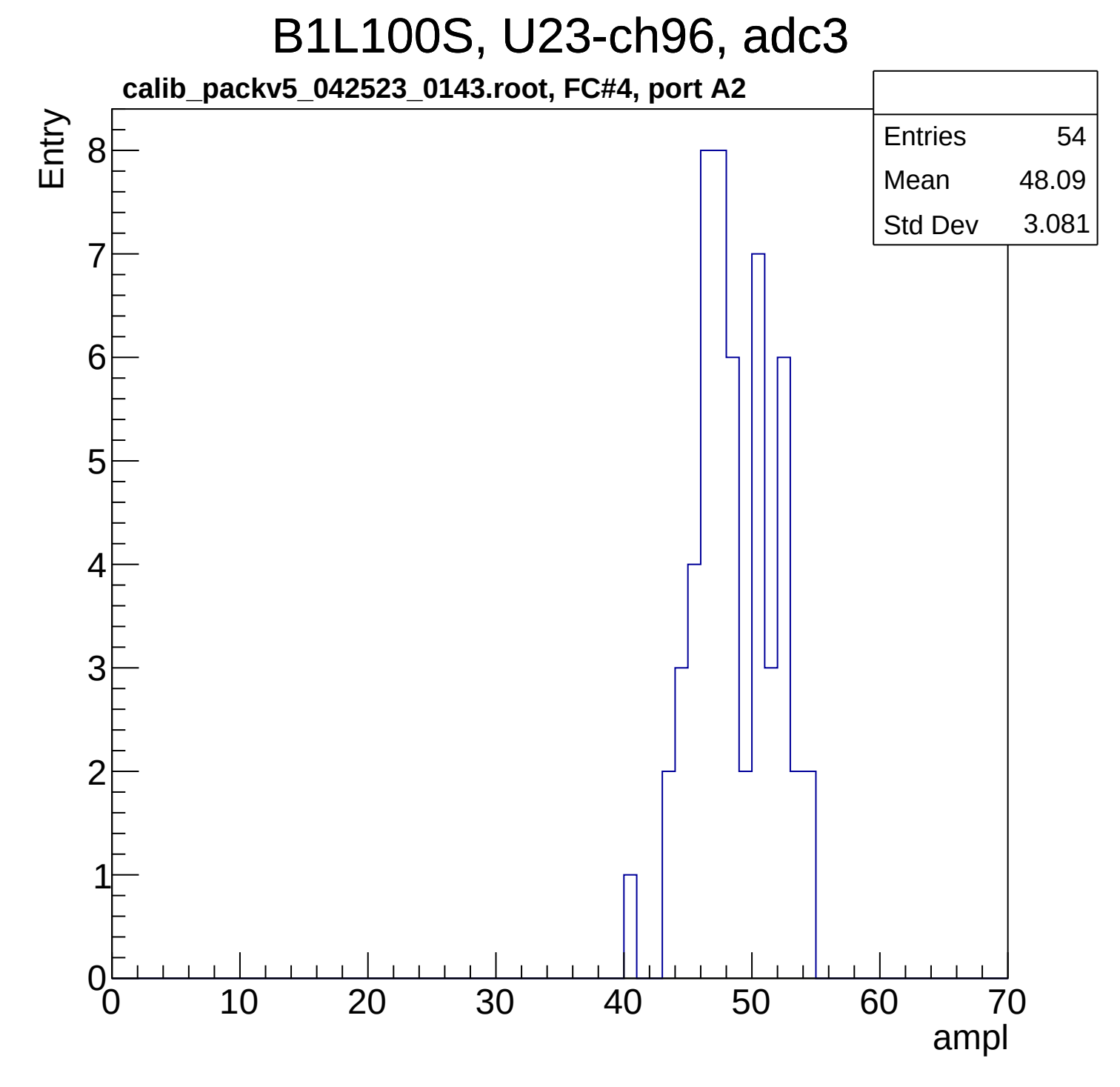
B1L100S, U23-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 48.09
Std Dev 3.081
Entry
ampl
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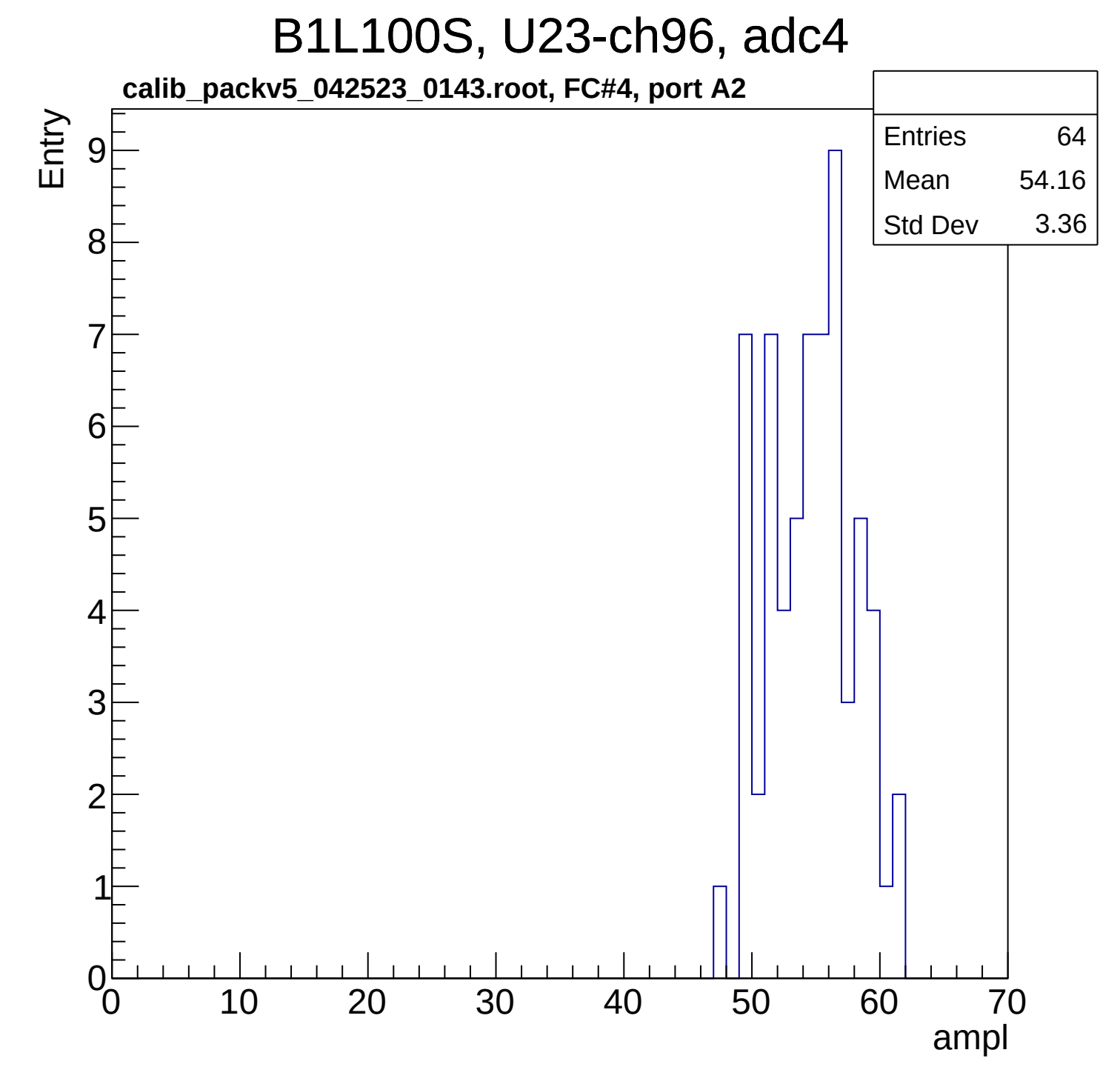
B1L100S, U23-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.16
Std Dev 3.36
Entry
ampl
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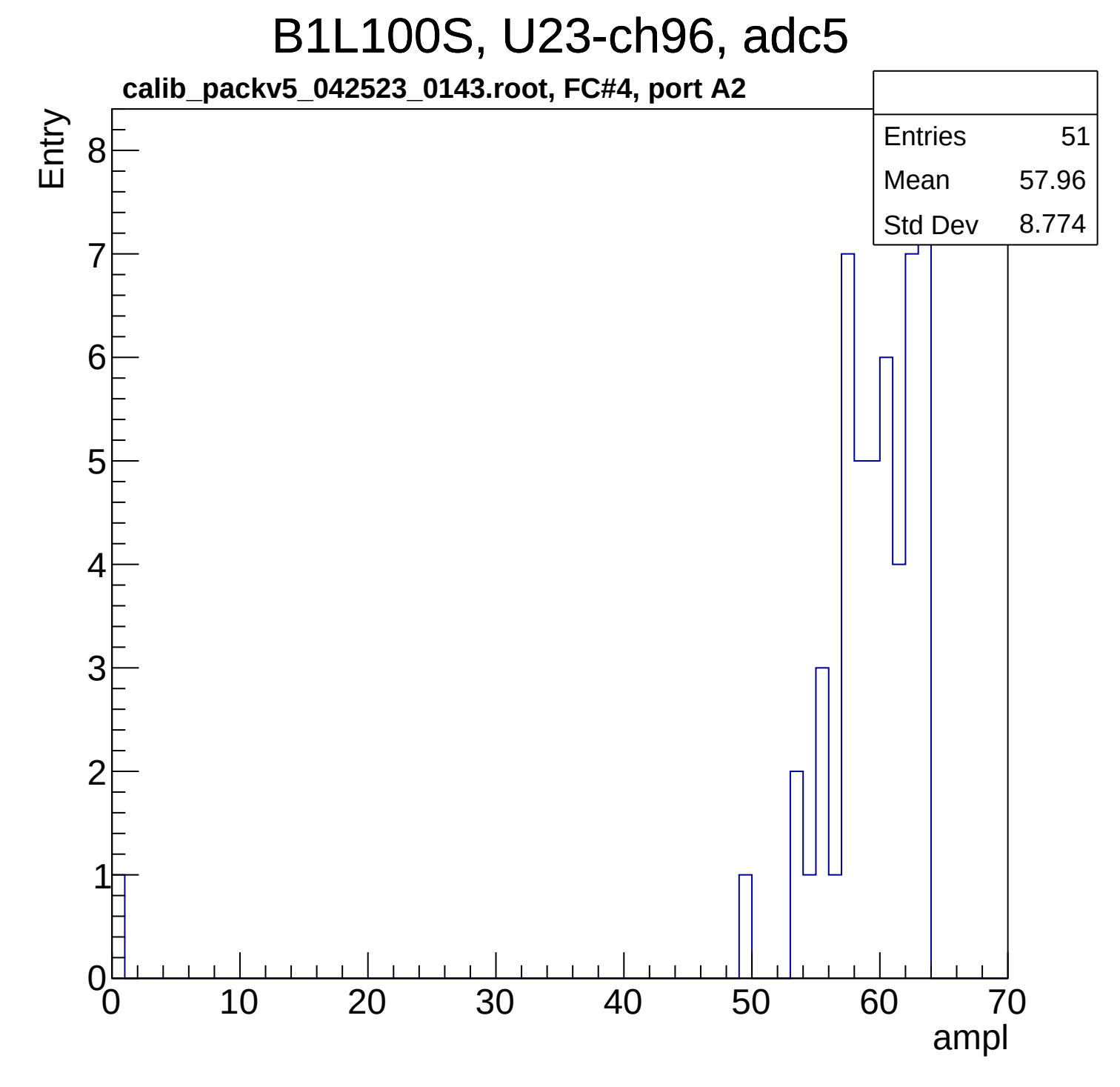
B1L100S, U23-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.96
Std Dev 8.774
Entry
ampl
0
1
2
3
4
5
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7
8
0
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20
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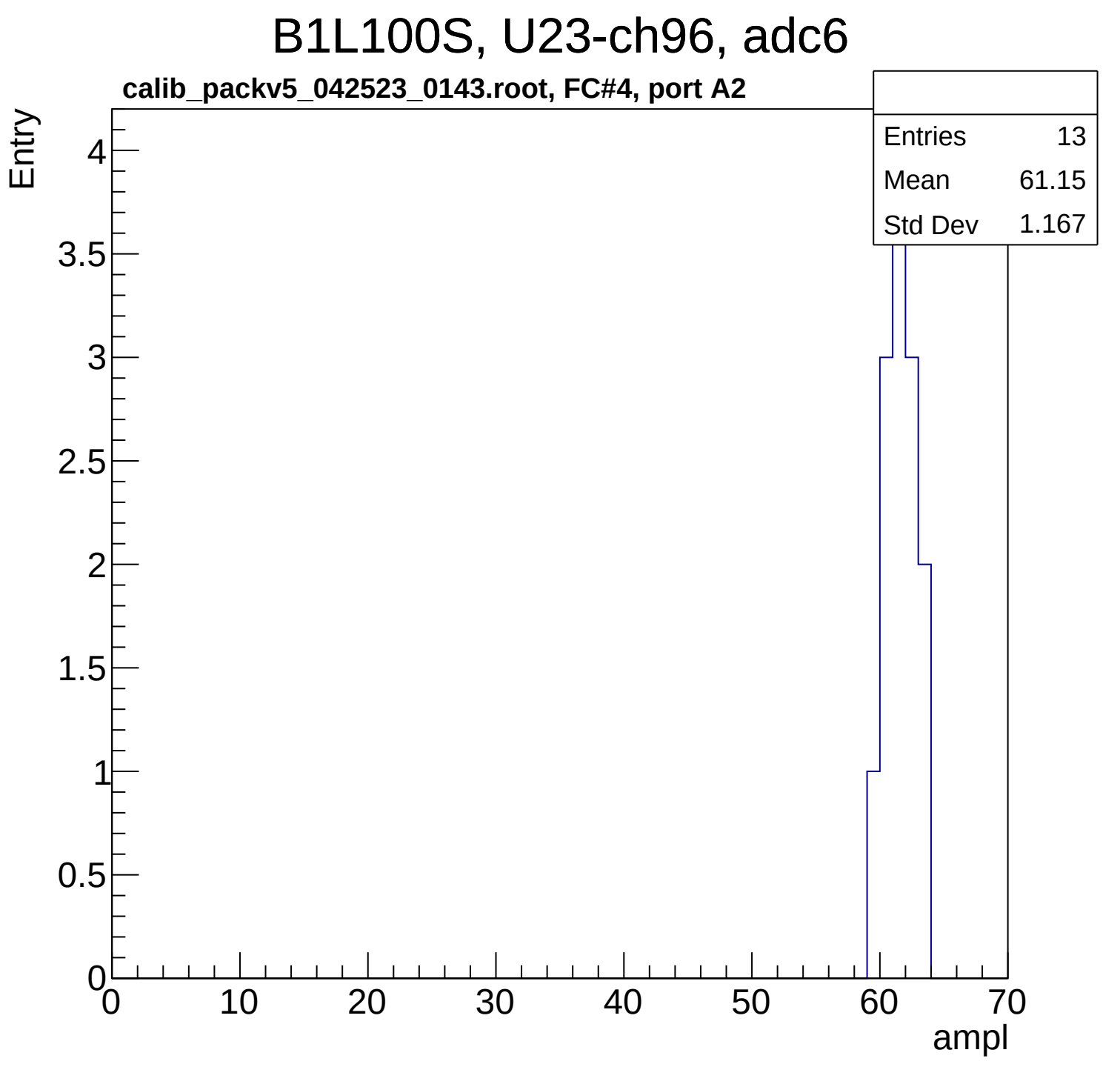
40
50
60
70


B1L100S, U23-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 61.15
Std Dev 1.167
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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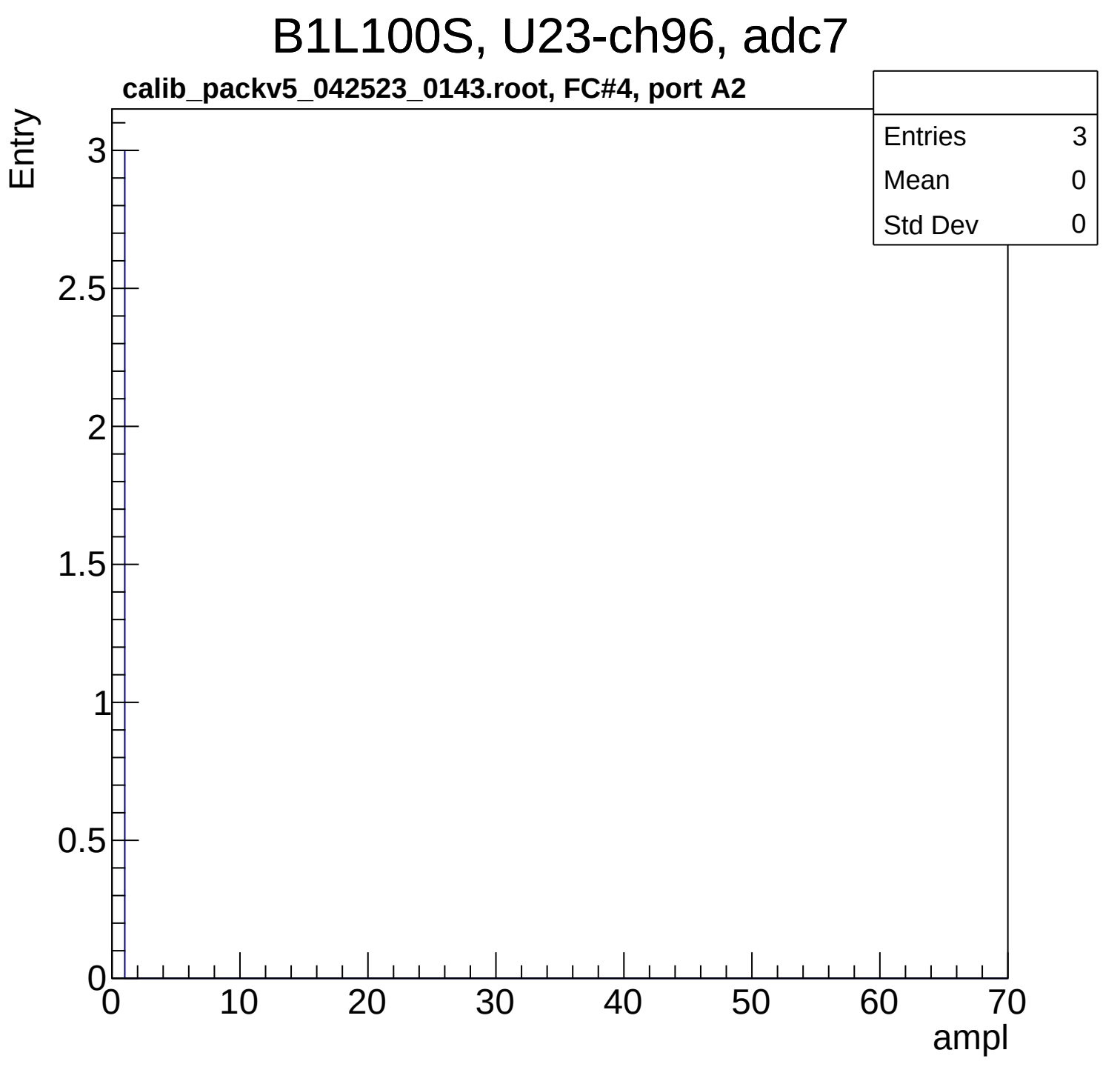
B1L100S, U23-ch96, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
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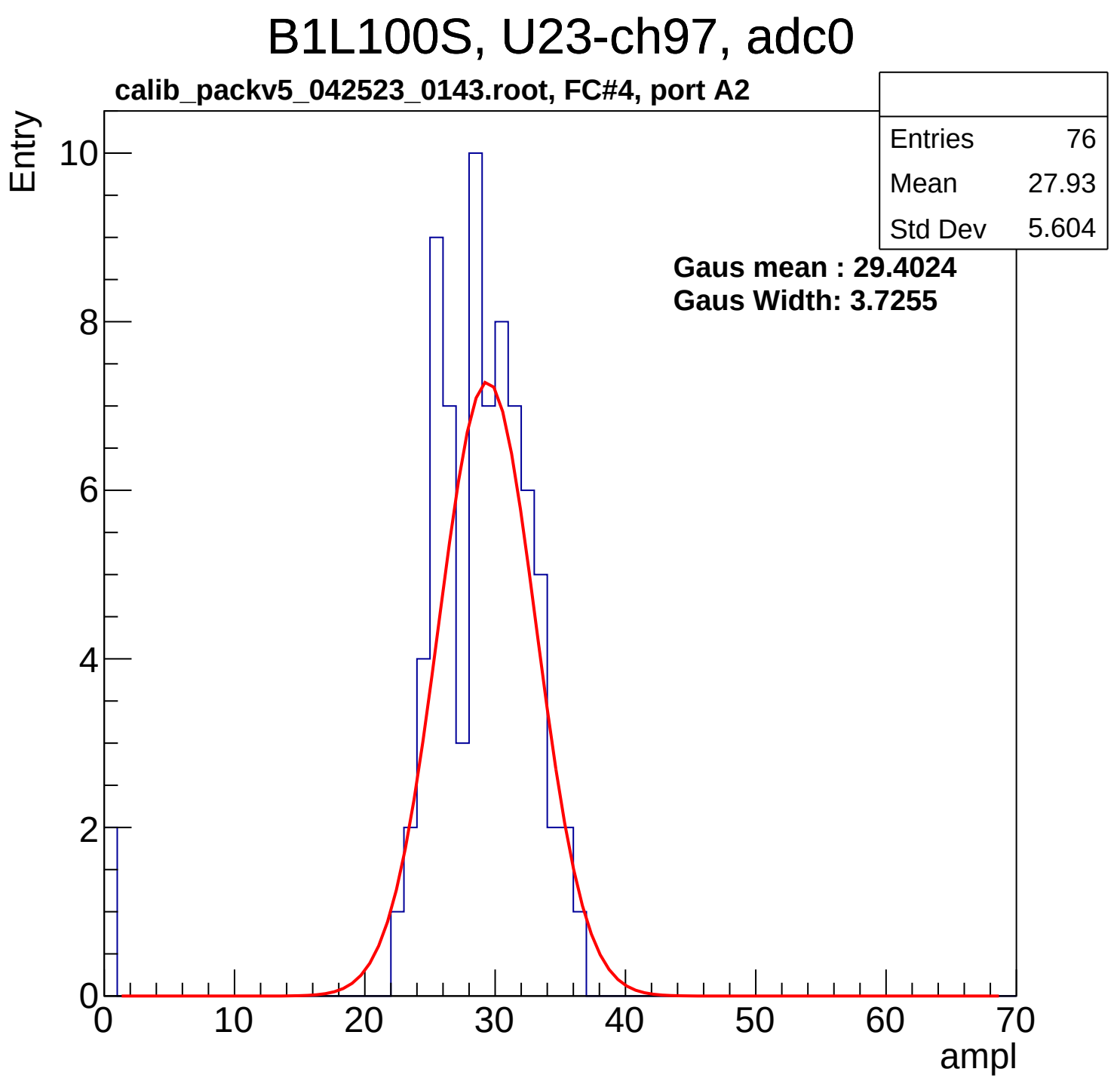
30
40
50
60
70
ampl


B1L100S, U23-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 27.93
Std Dev 5.604
Gaus mean : 29.4024
Gaus Width: 3.7255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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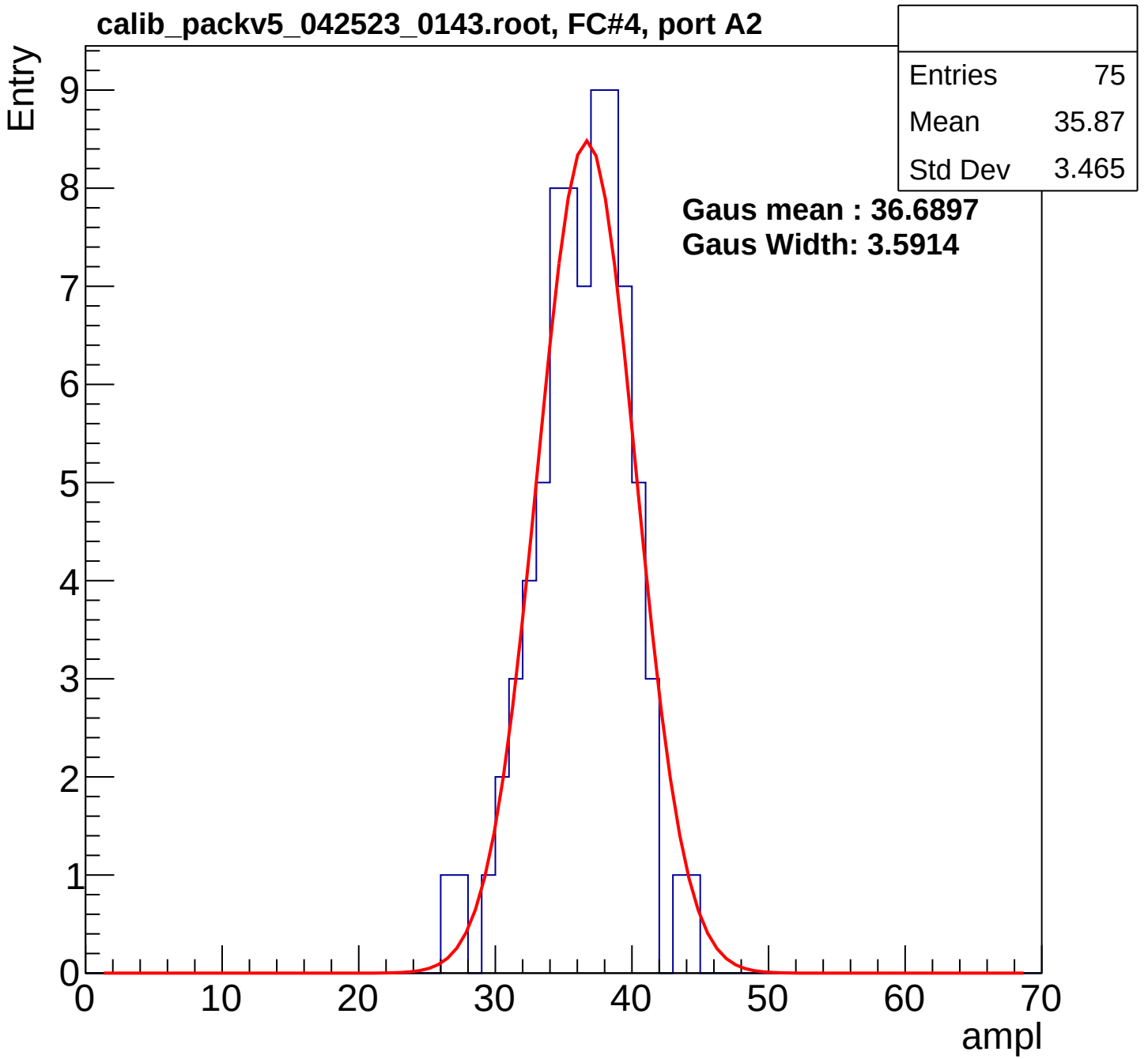
B1L100S, U23-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.87
Std Dev 3.465
Gaus mean : 36.6897
Gaus Width: 3.5914
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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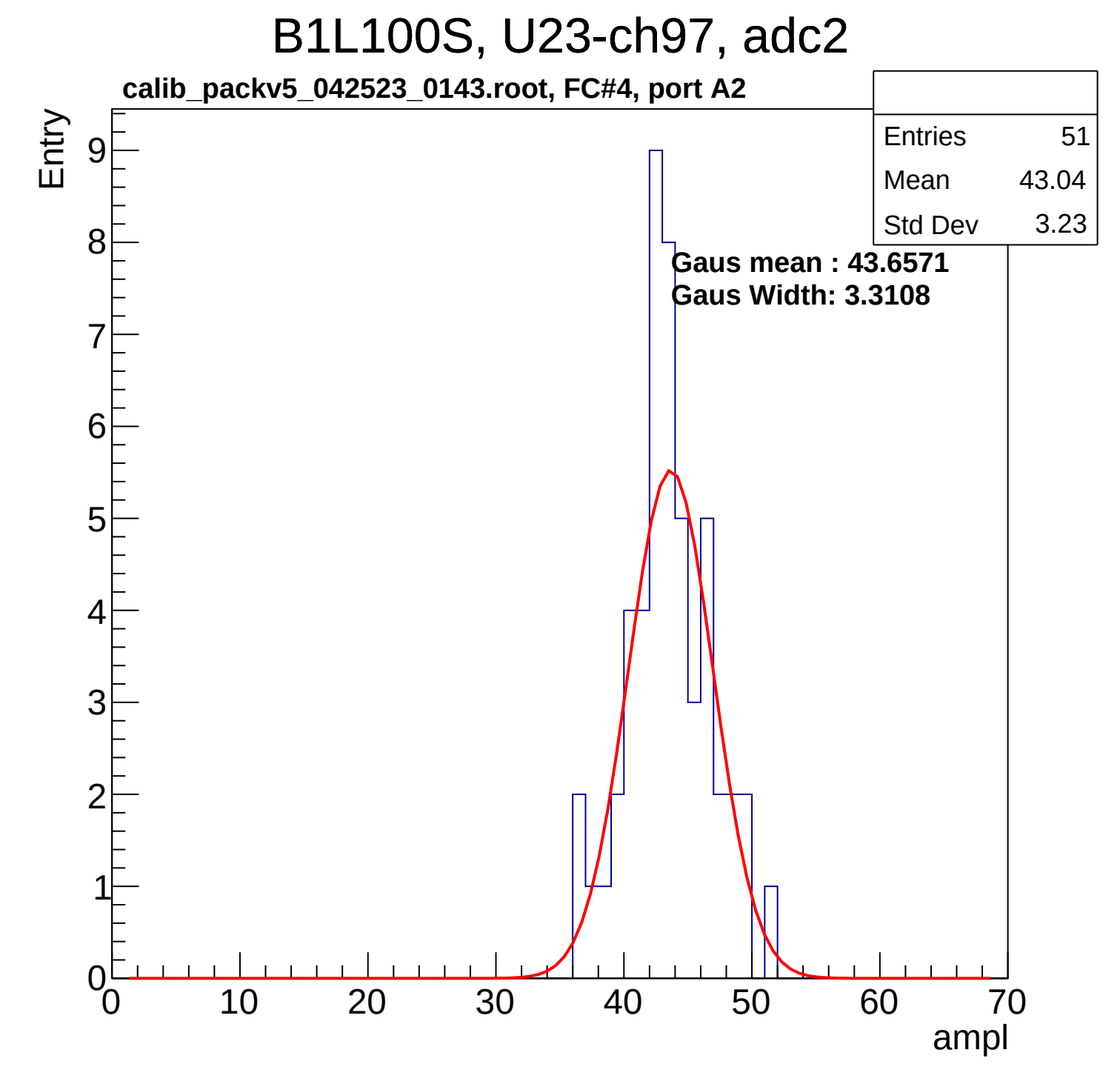
B1L100S, U23-ch97, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 43.04
Std Dev 3.23
Gaus mean : 43.6571
Gaus Width: 3.3108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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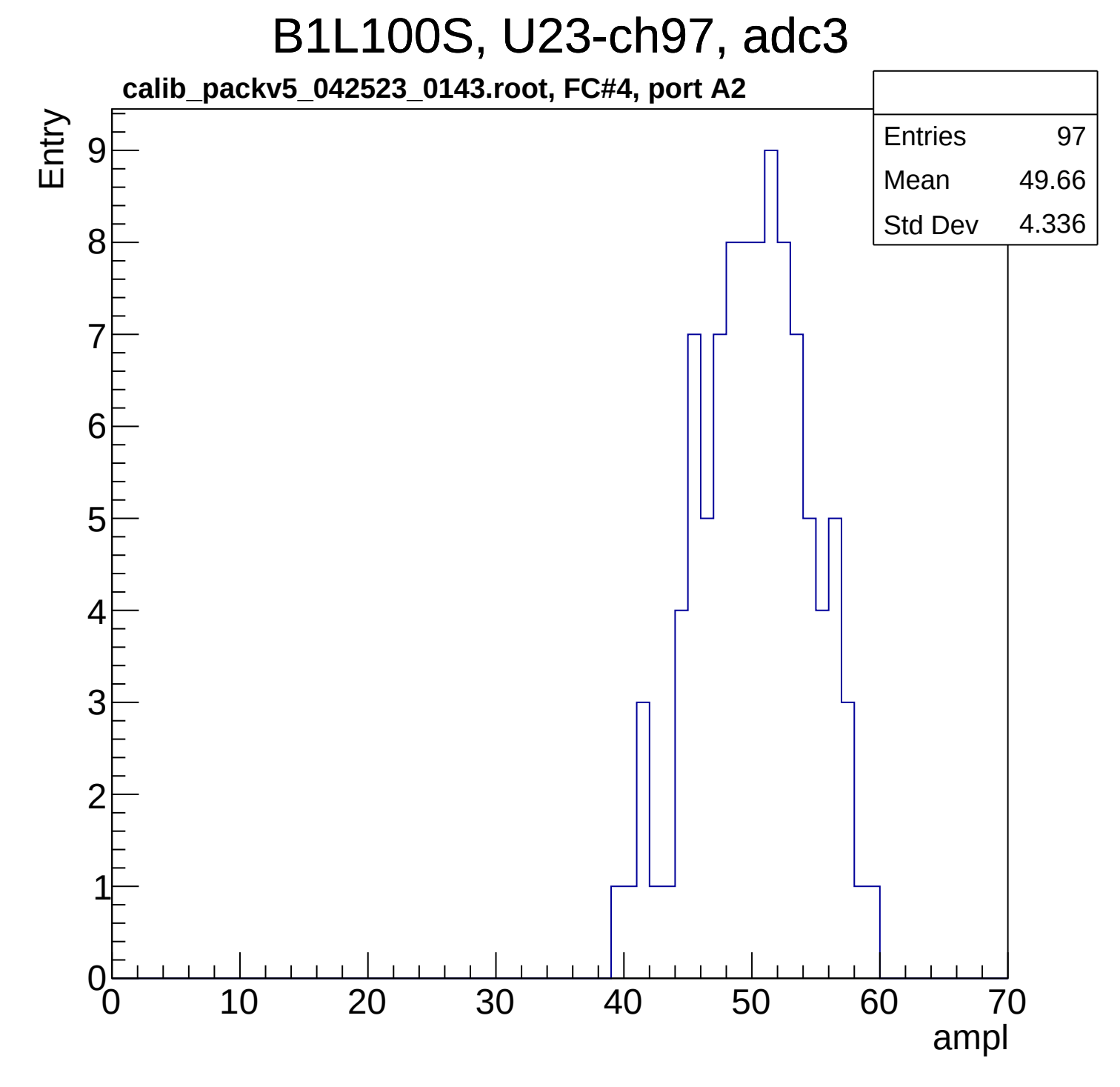
B1L100S, U23-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 97
Mean 49.66
Std Dev 4.336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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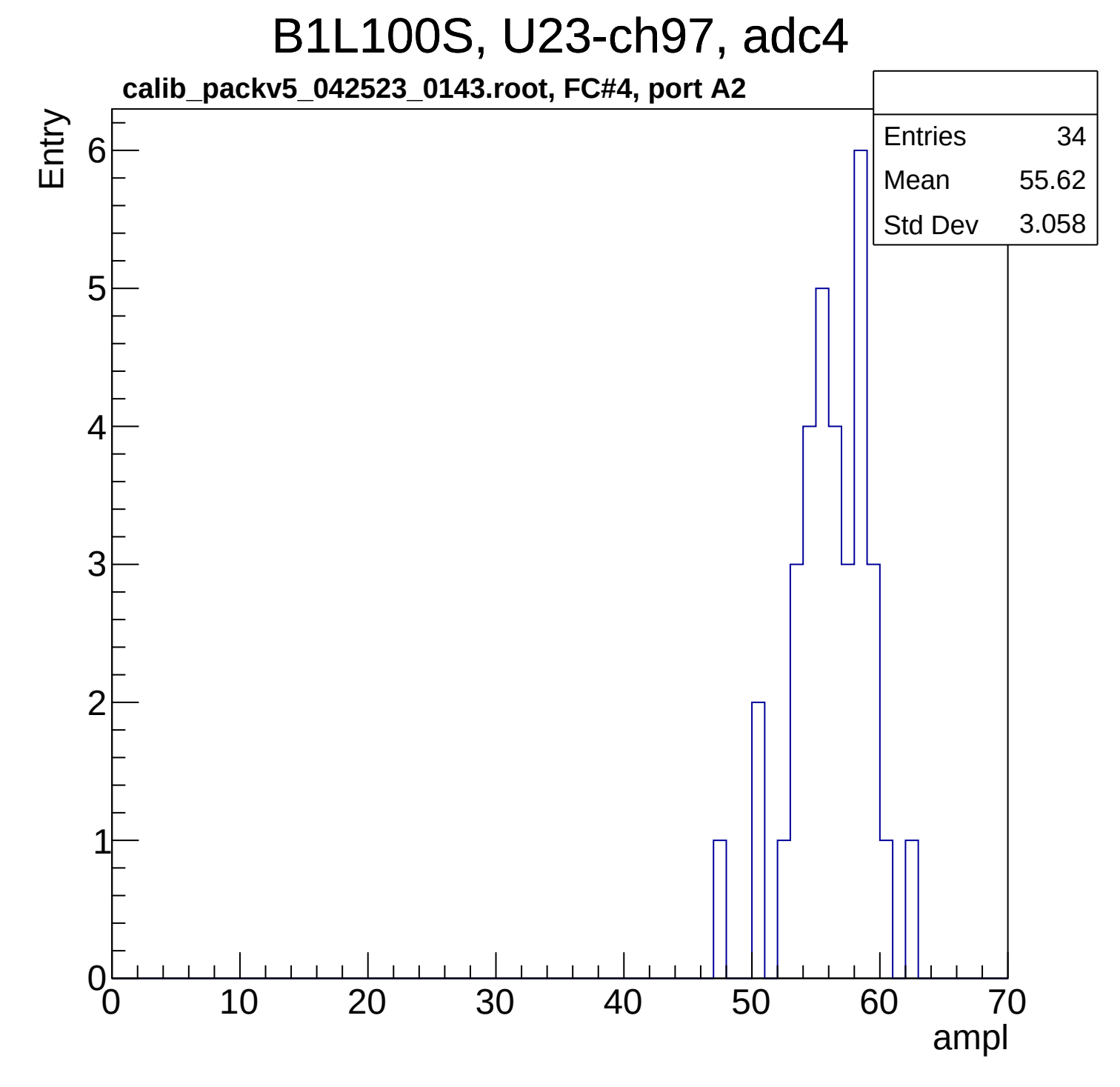
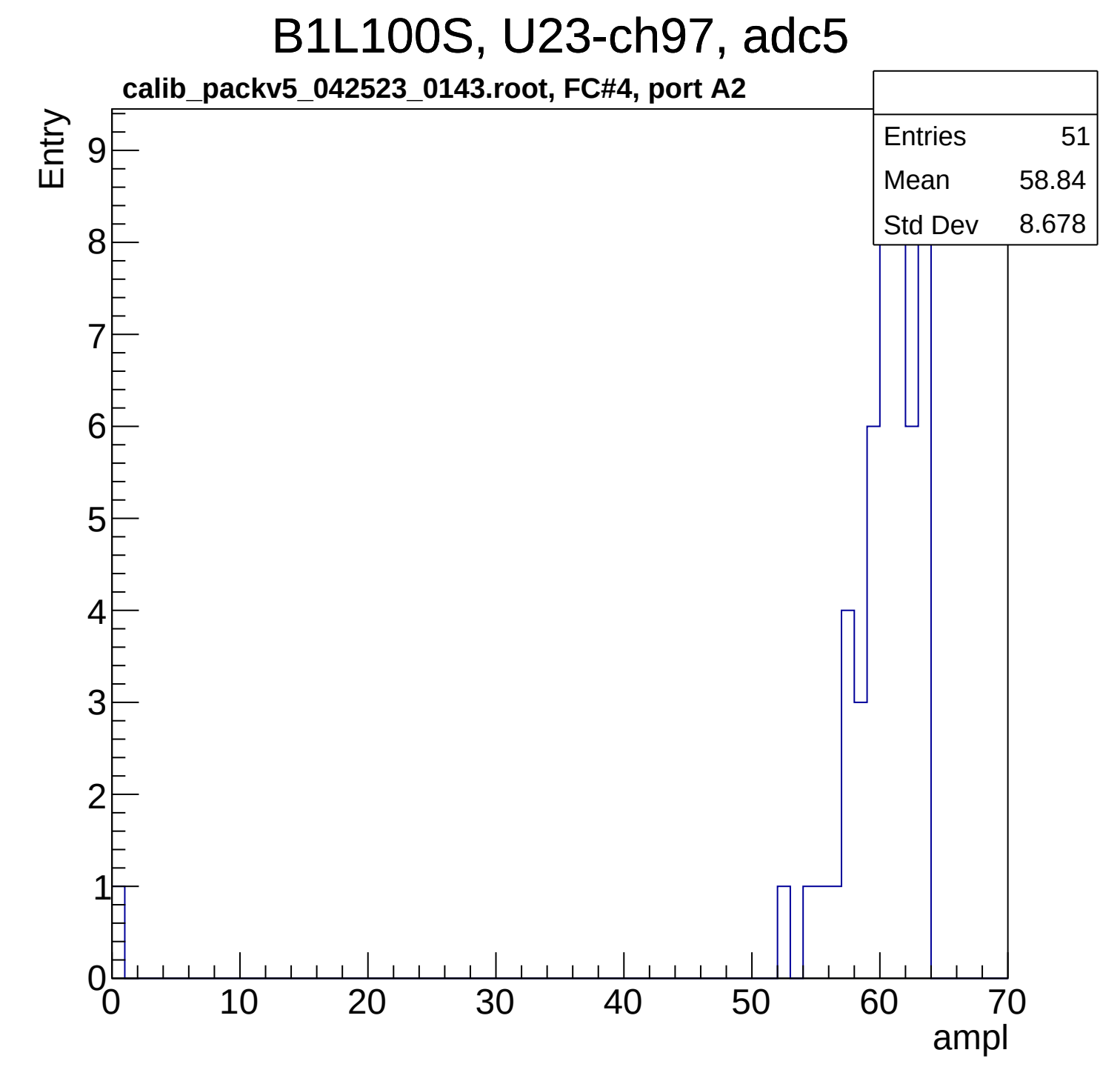

B1L100S, U23-ch97, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 55.62
Std Dev 3.058
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.84
Std Dev 8.678
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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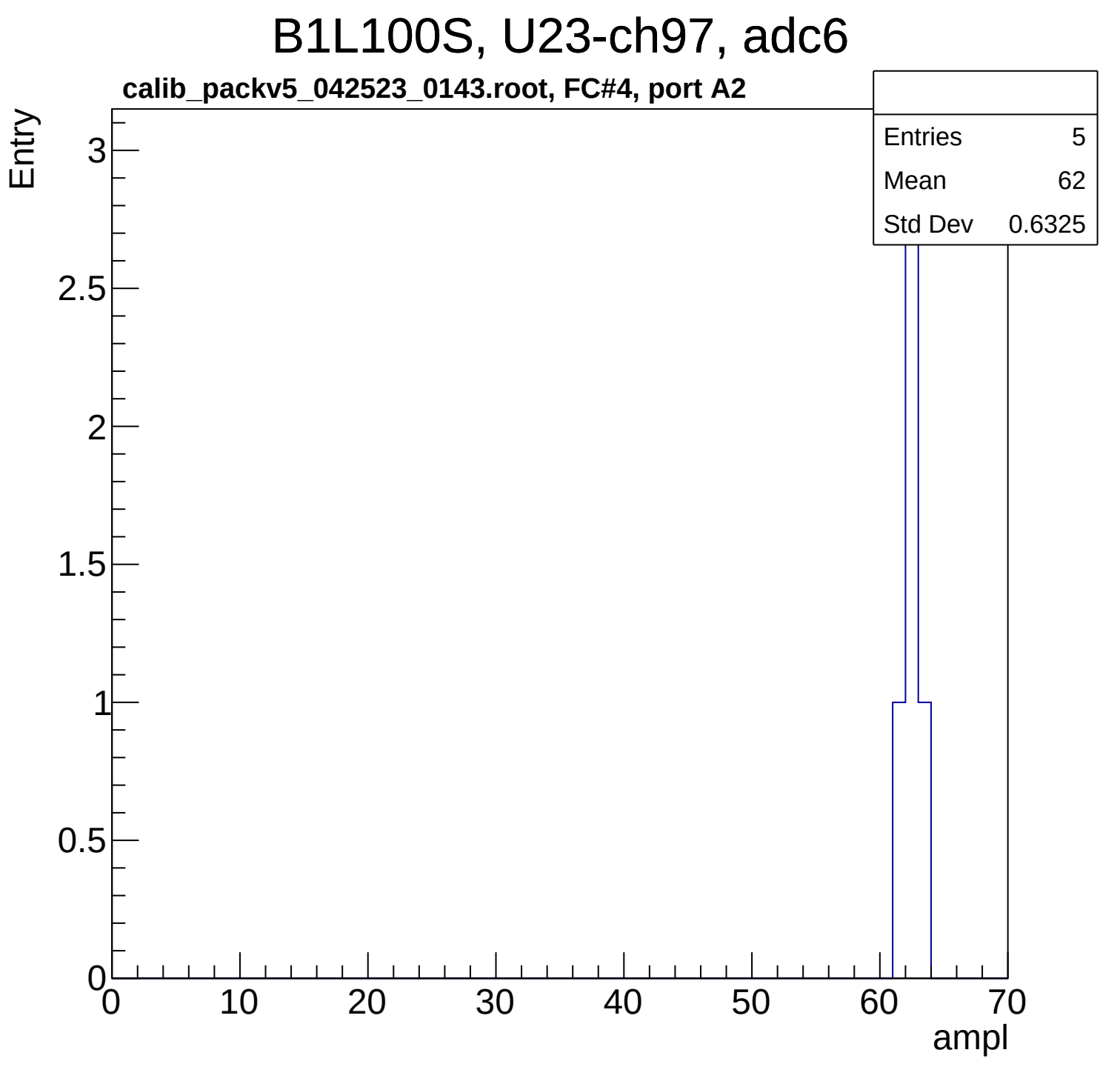
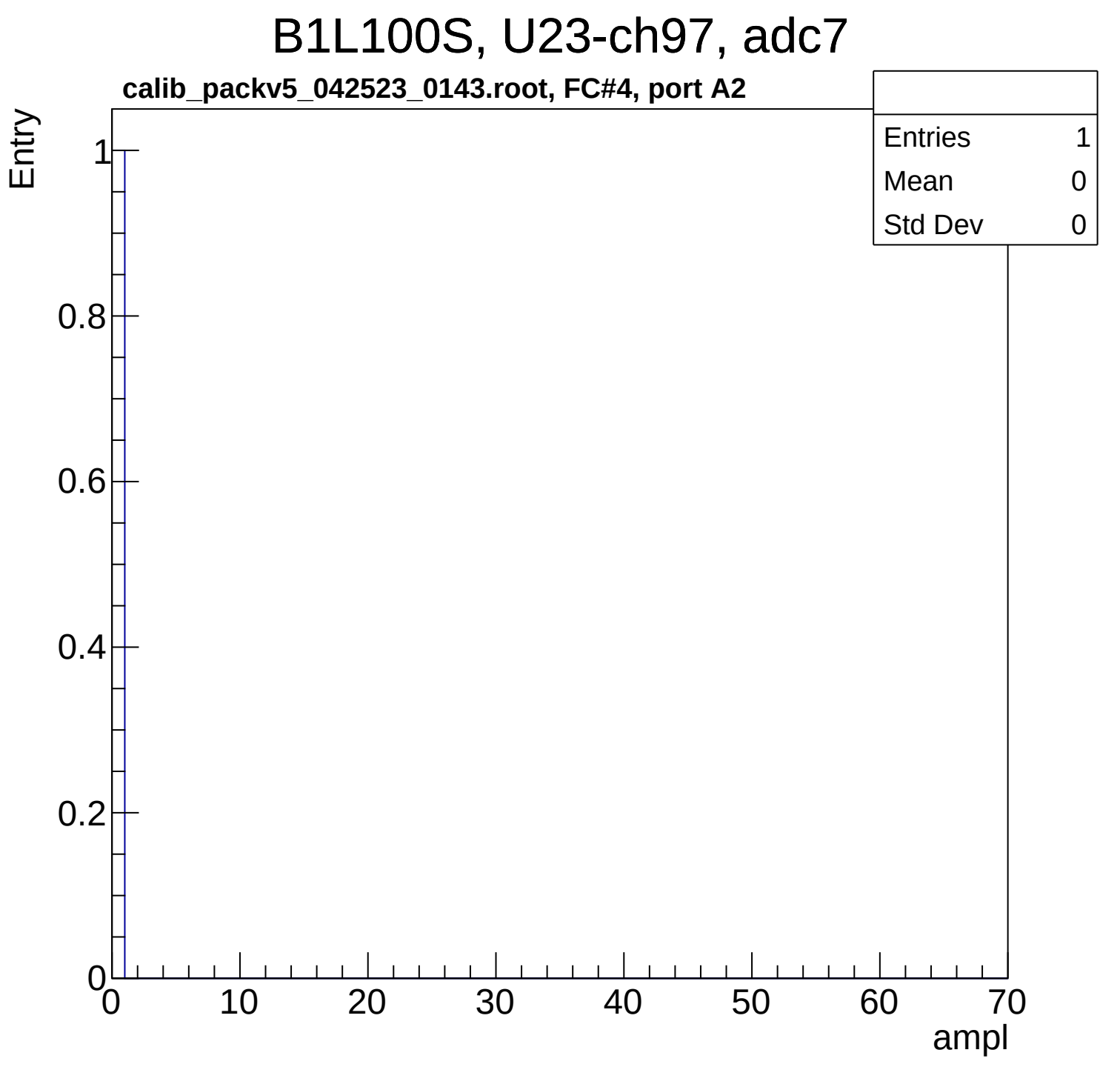
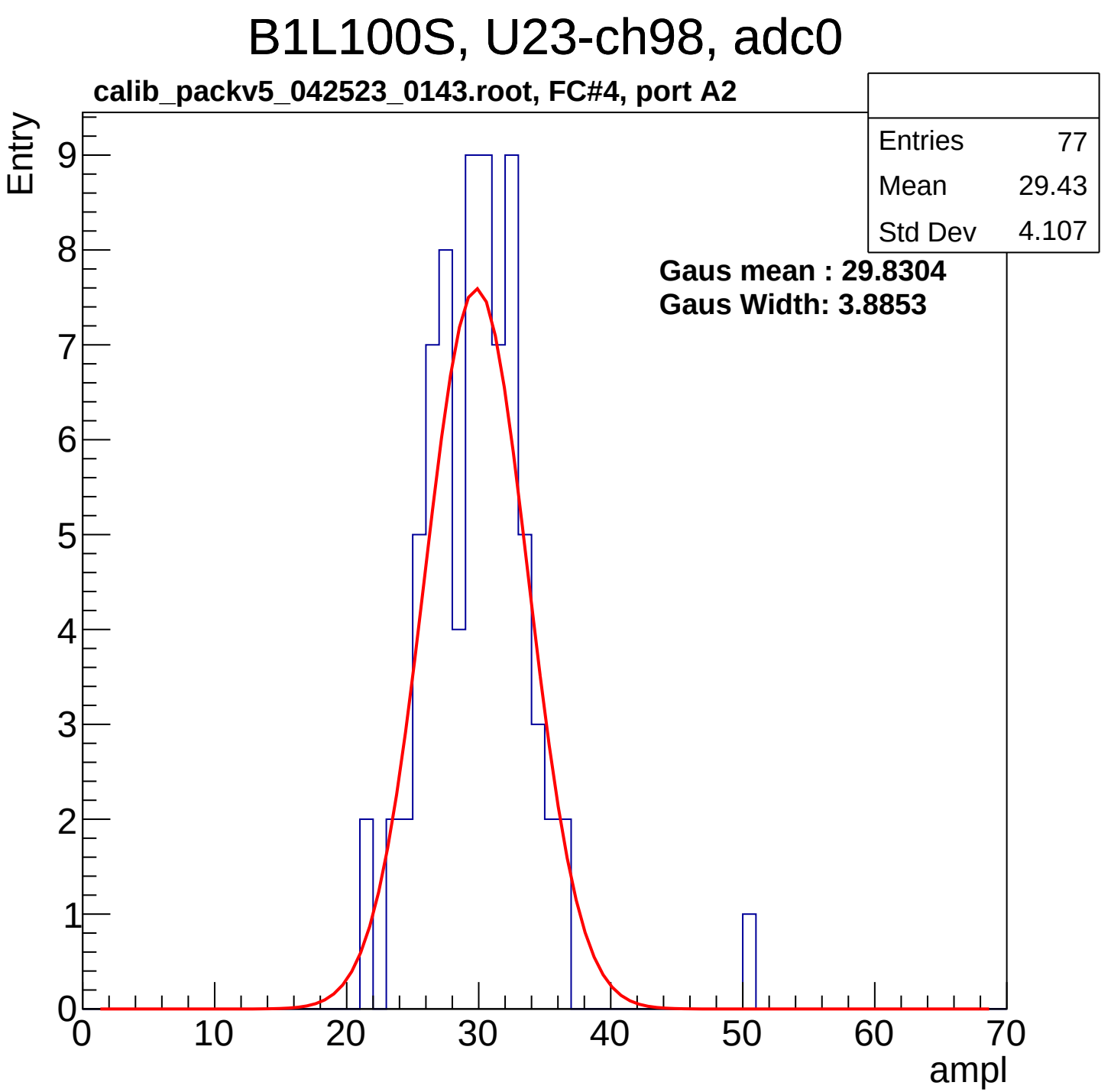
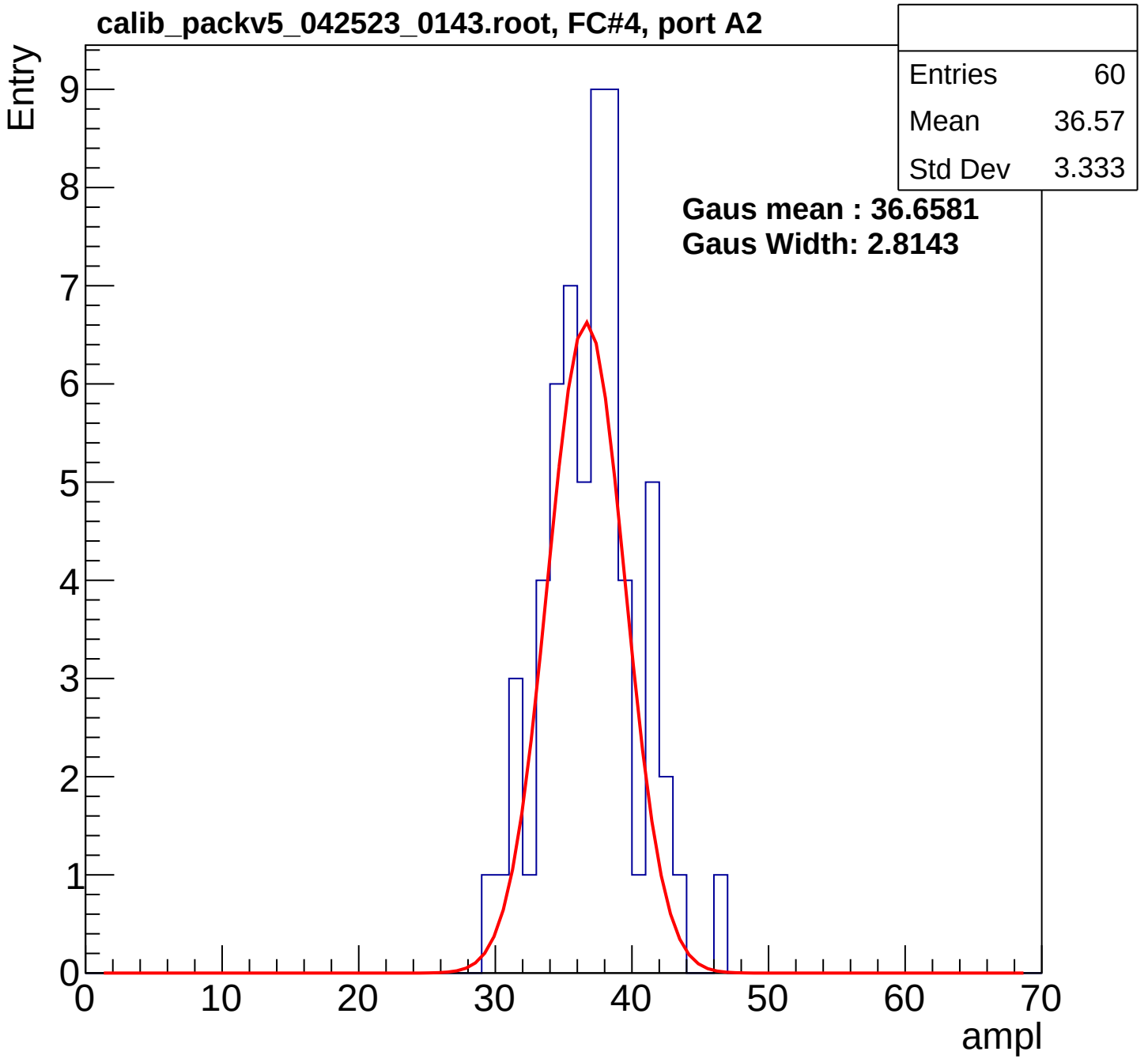

B1L100S, U23-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.43
Std Dev 4.107
Gaus mean : 29.8304
Gaus Width: 3.8853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.57
Std Dev 3.333
Gaus mean : 36.6581
Gaus Width: 2.8143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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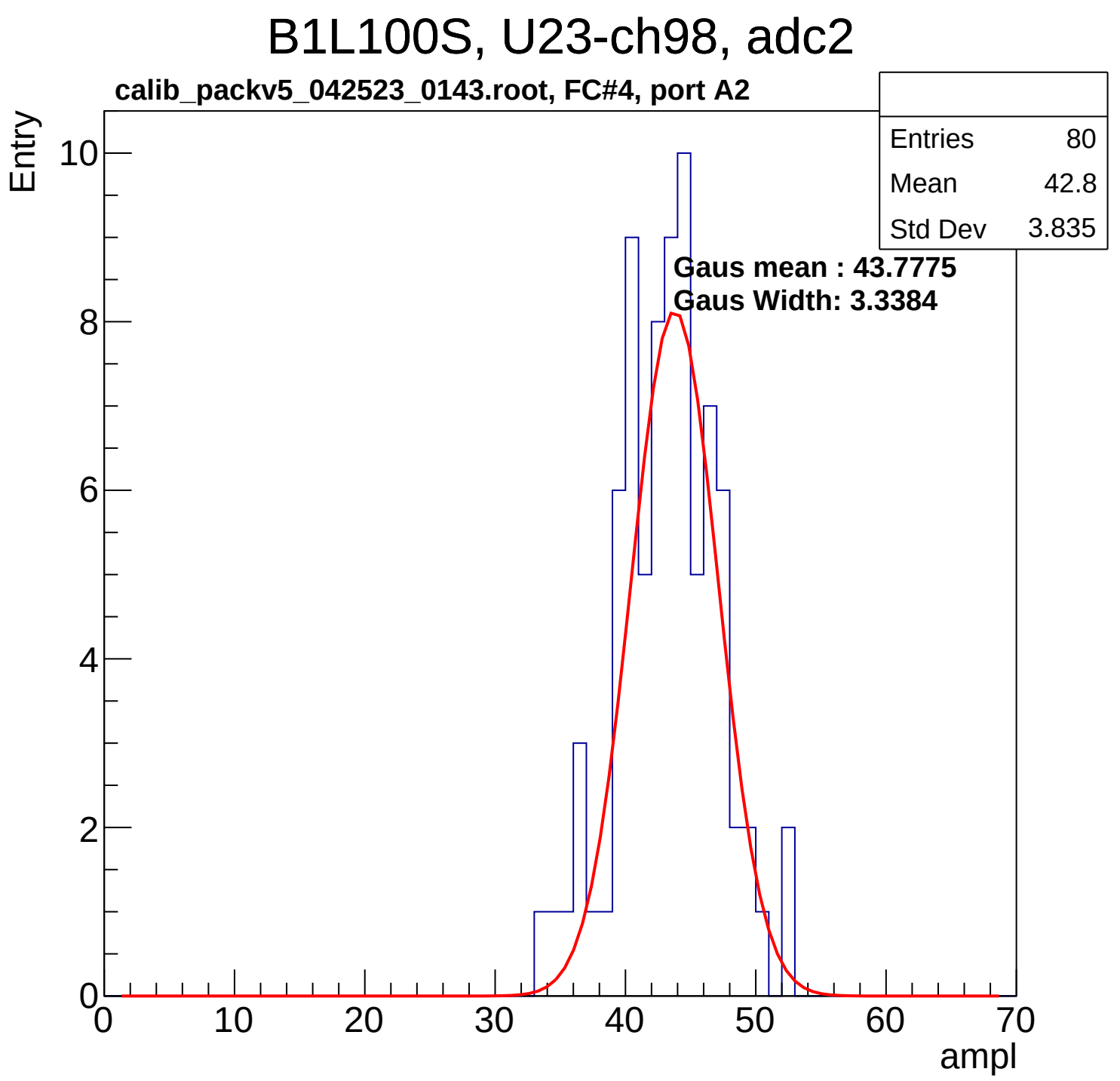
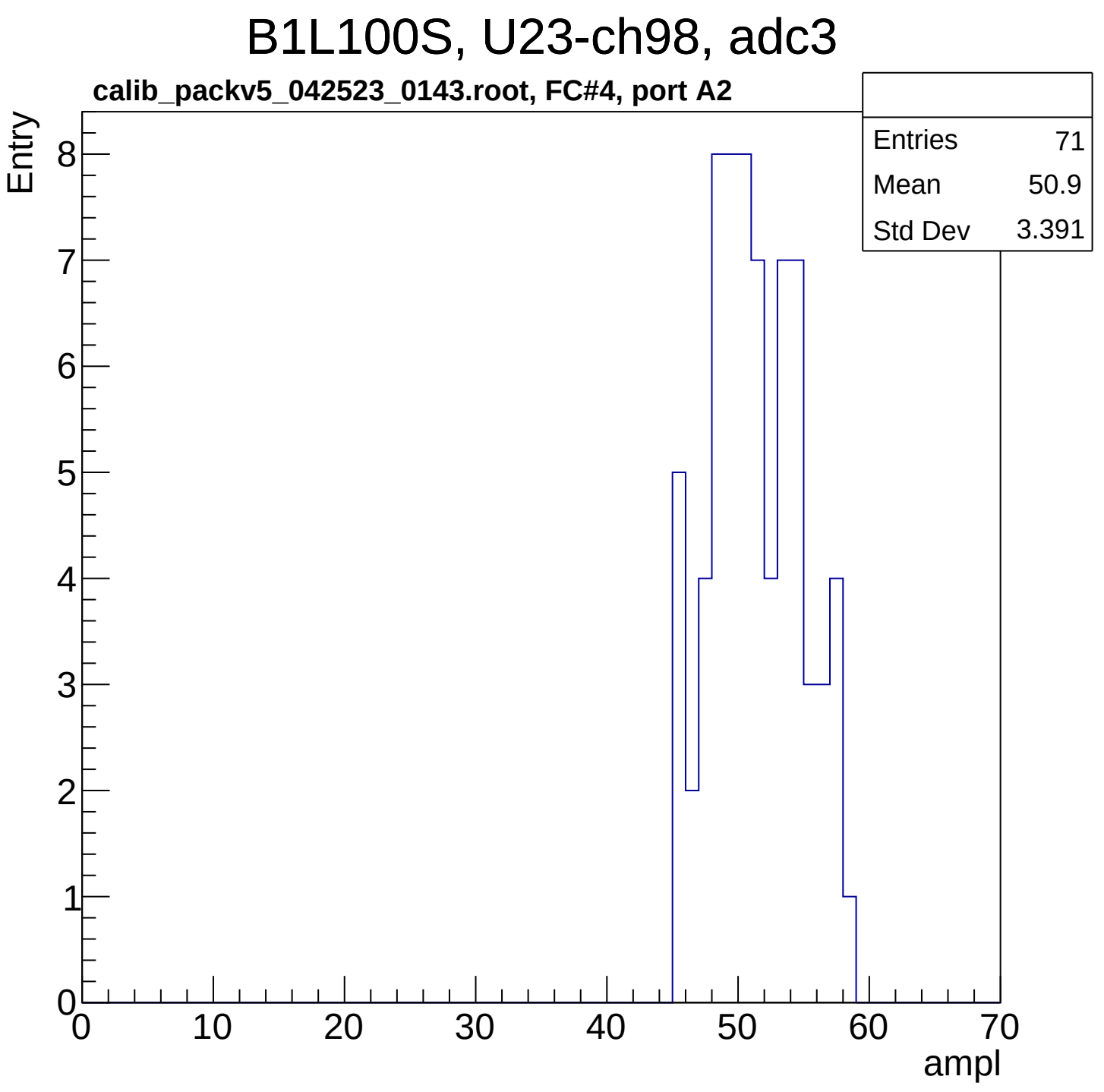

B1L100S, U23-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 42.8
Std Dev 3.835
Gaus mean : 43.7775
Gaus Width: 3.3384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch98, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.9
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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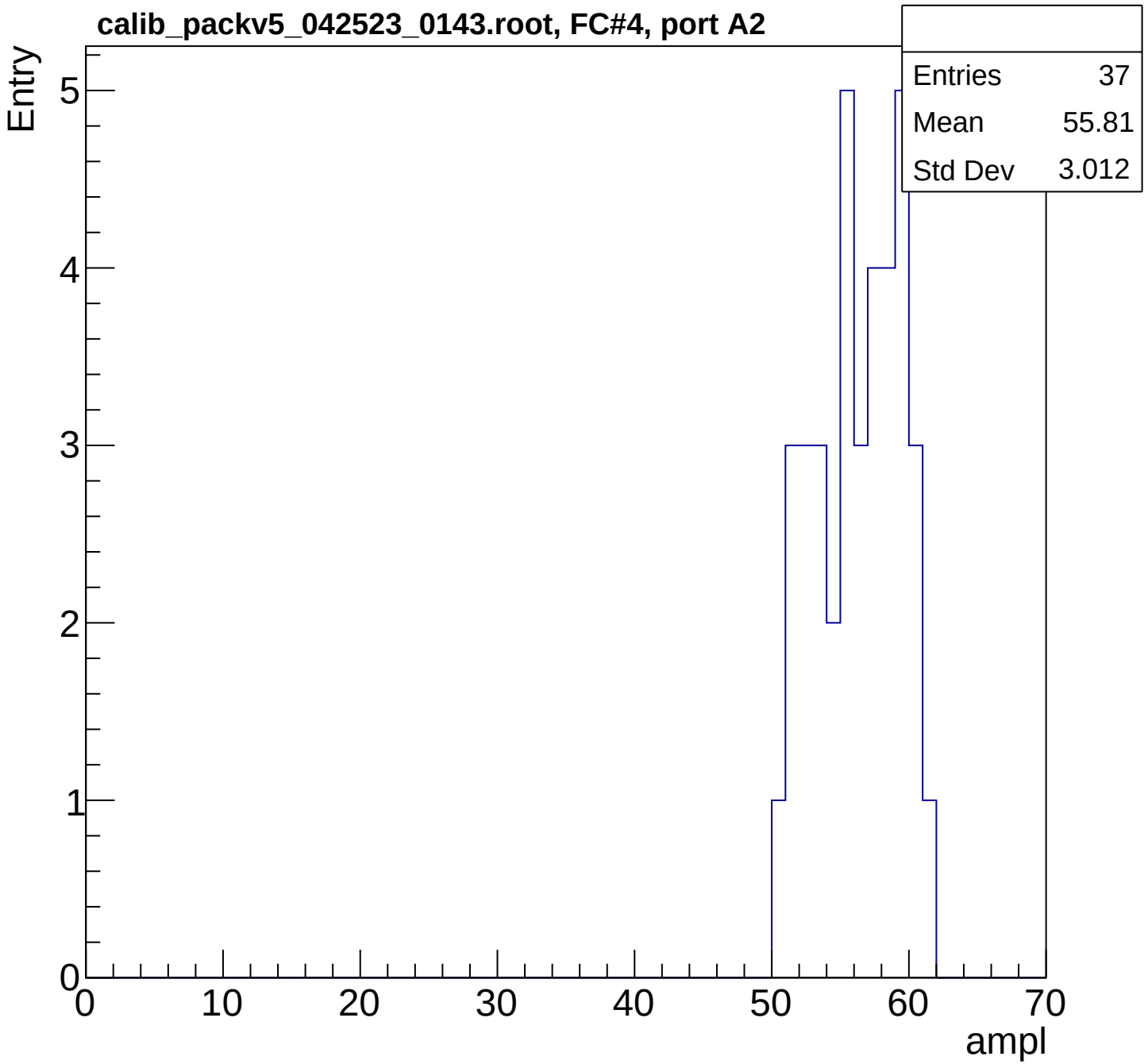
B1L100S, U23-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 55.81
Std Dev 3.012
Entry
ampl
0
1
2
3
4
5
0
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20
30
40
50
60
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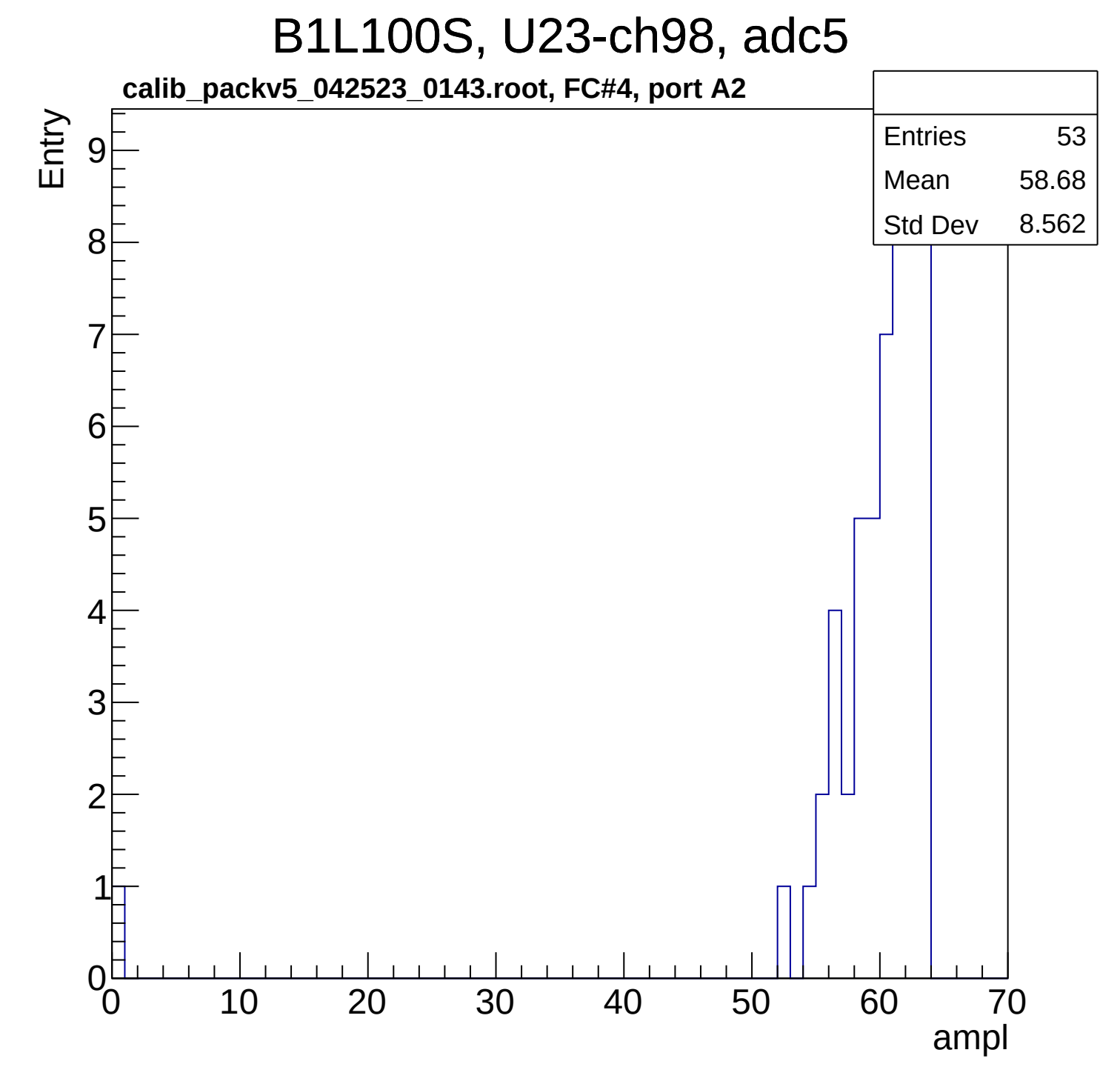
B1L100S, U23-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.68
Std Dev 8.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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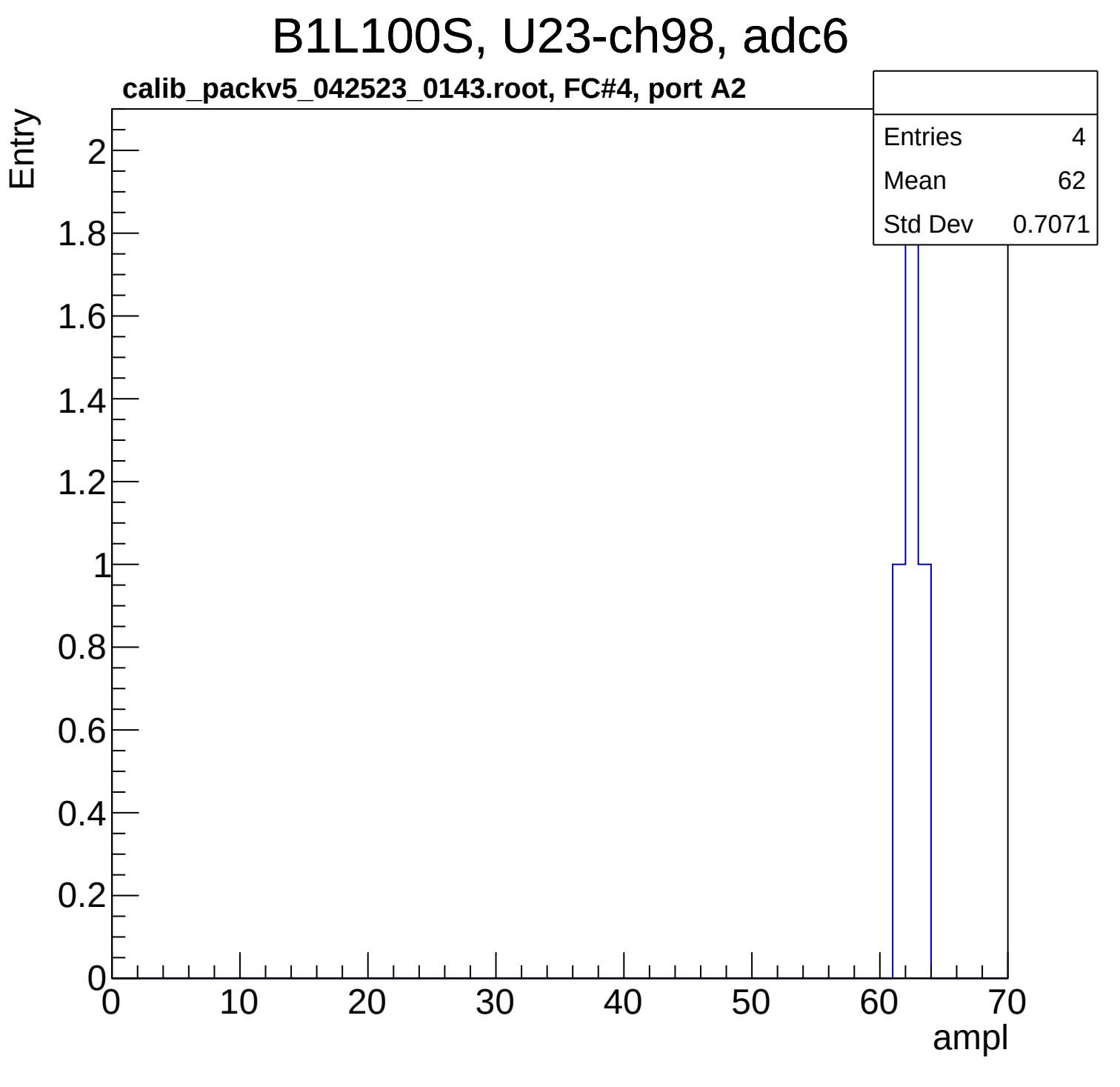
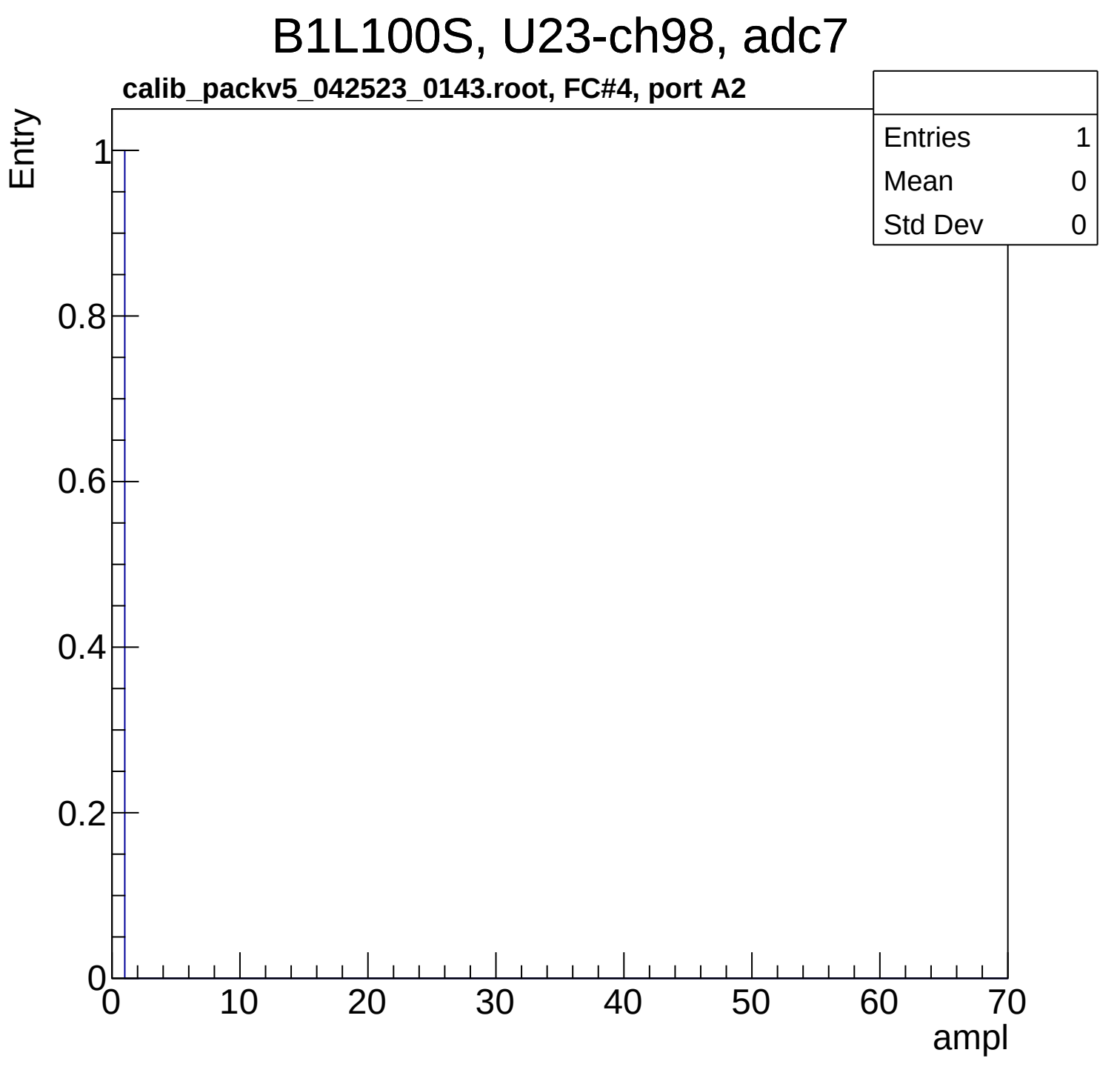
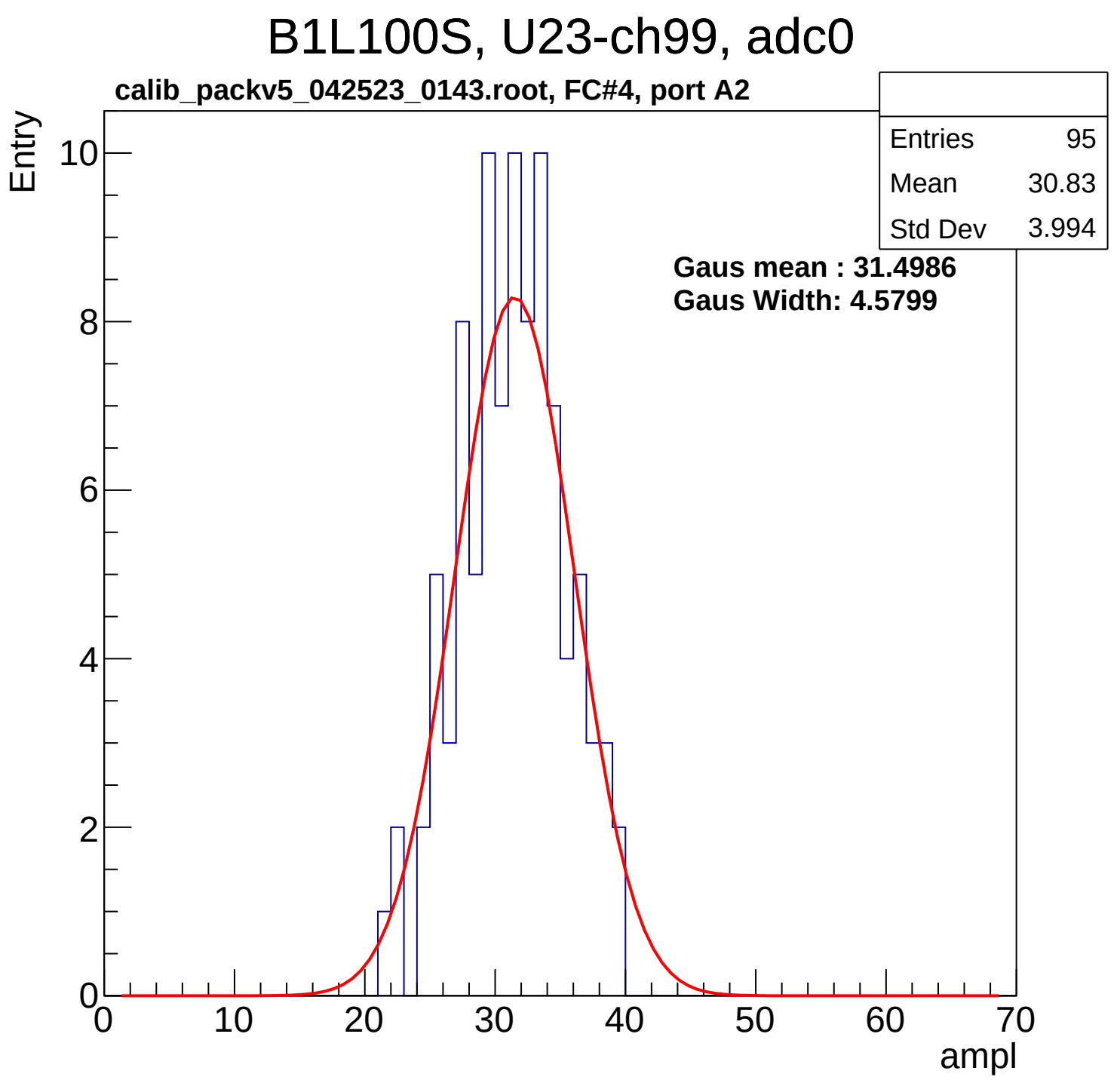
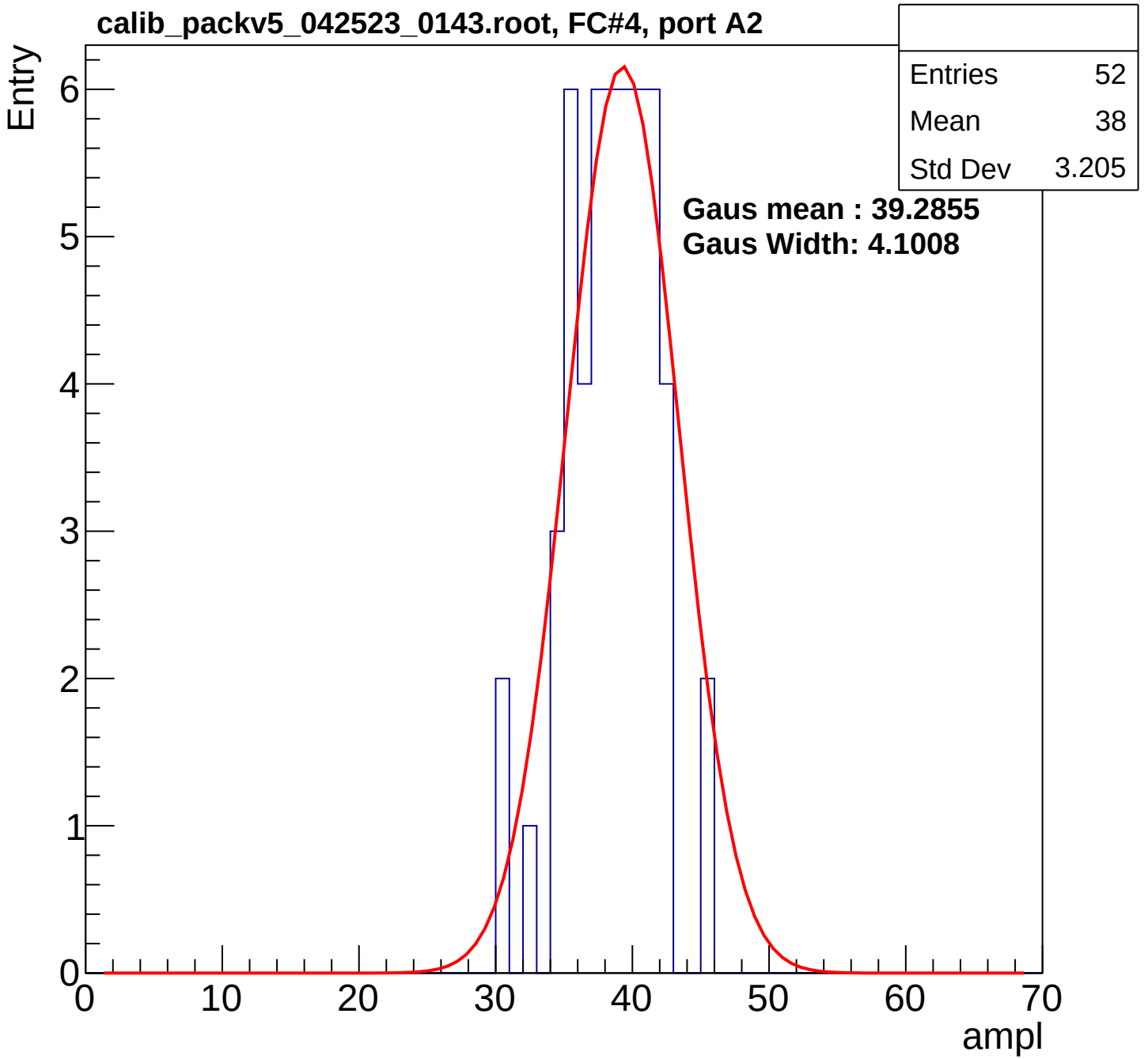
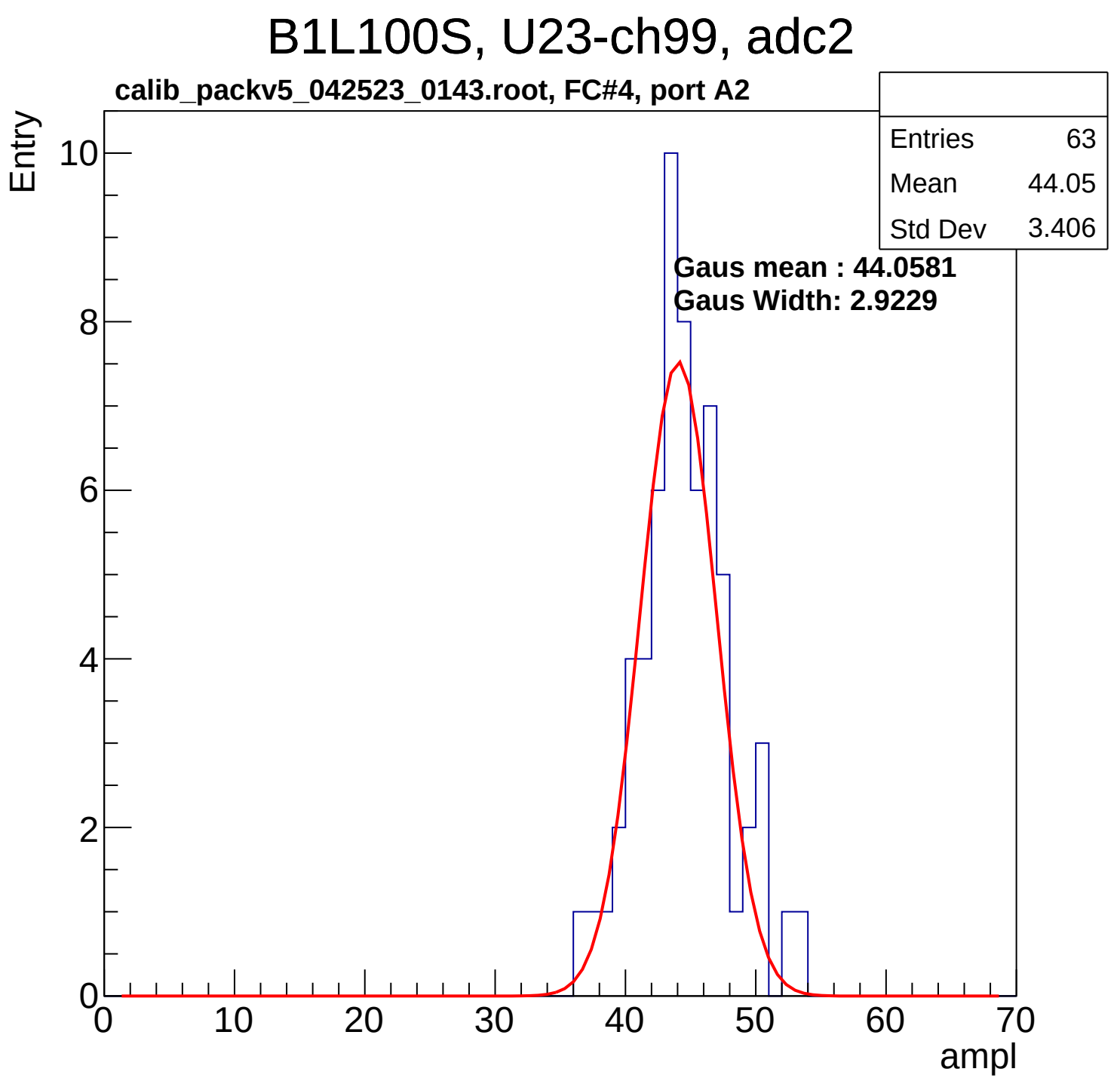
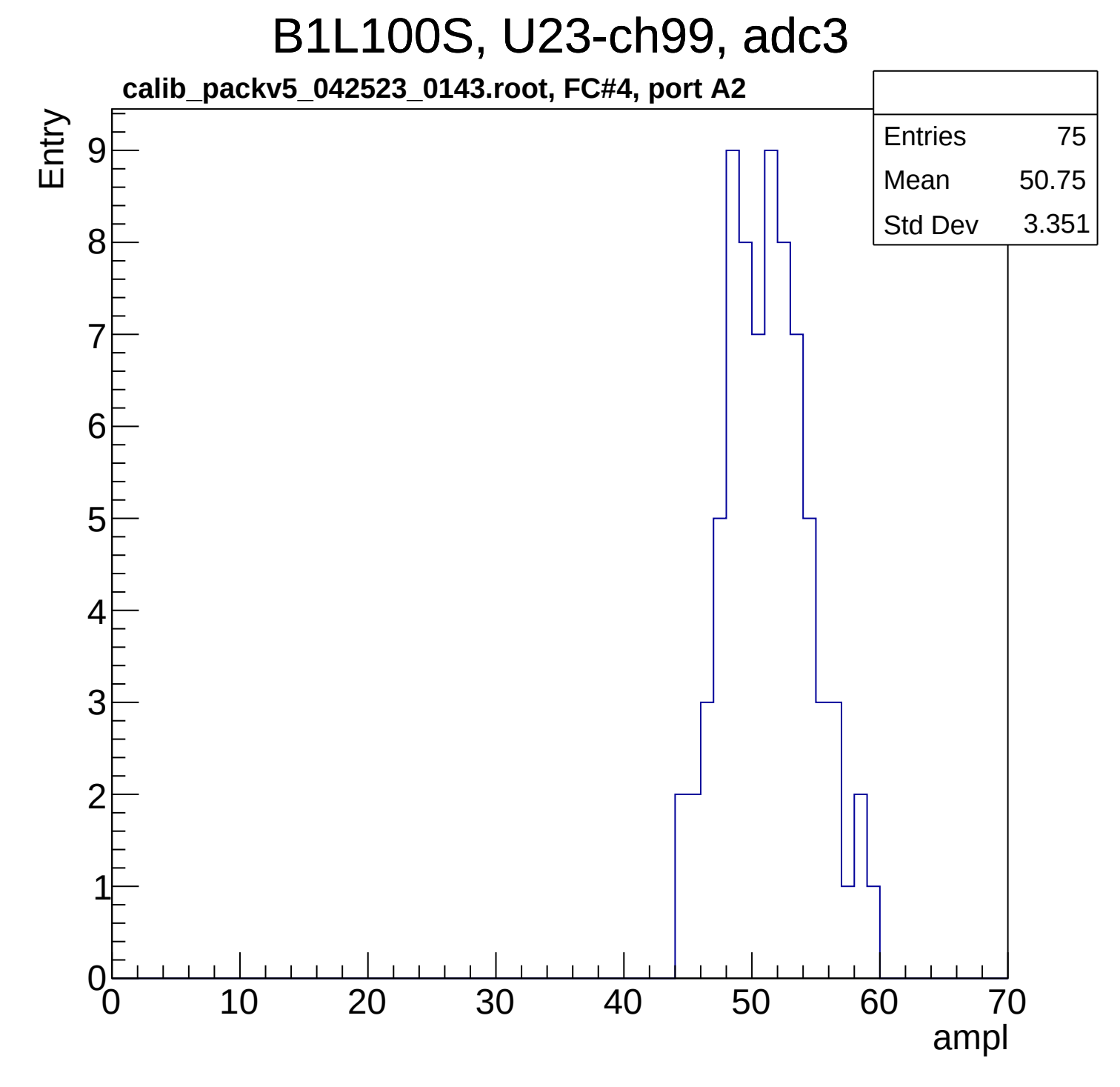

B1L100S, U23-ch98, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 30.83
Std Dev 3.994
Gaus mean : 31.4986
Gaus Width: 4.5799
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 38
Std Dev 3.205
Gaus mean : 39.2855
Gaus Width: 4.1008
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U23-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.05
Std Dev 3.406
Gaus mean : 44.0581
Gaus Width: 2.9229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch99, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.75
Std Dev 3.351
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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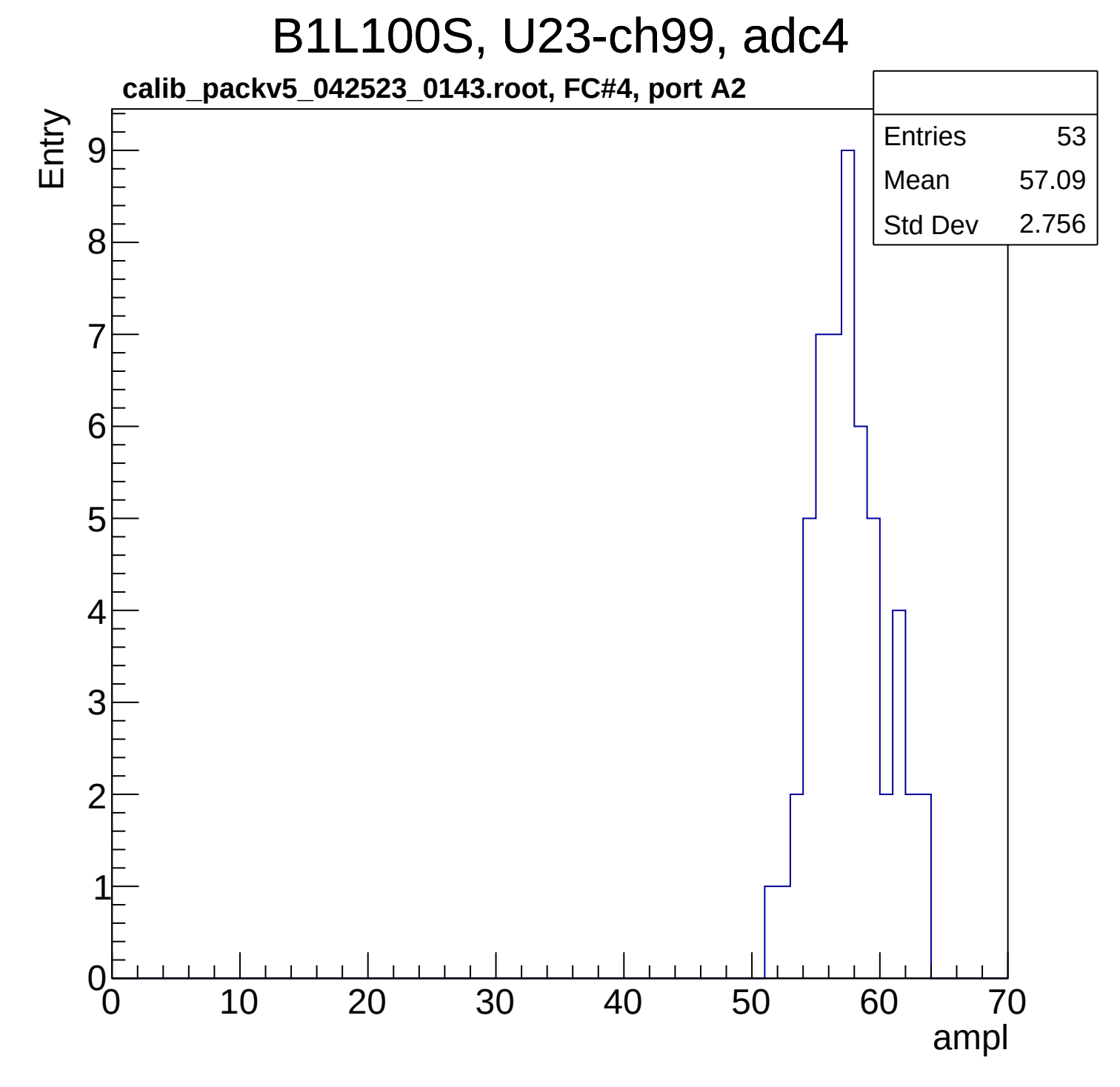
B1L100S, U23-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 57.09
Std Dev 2.756
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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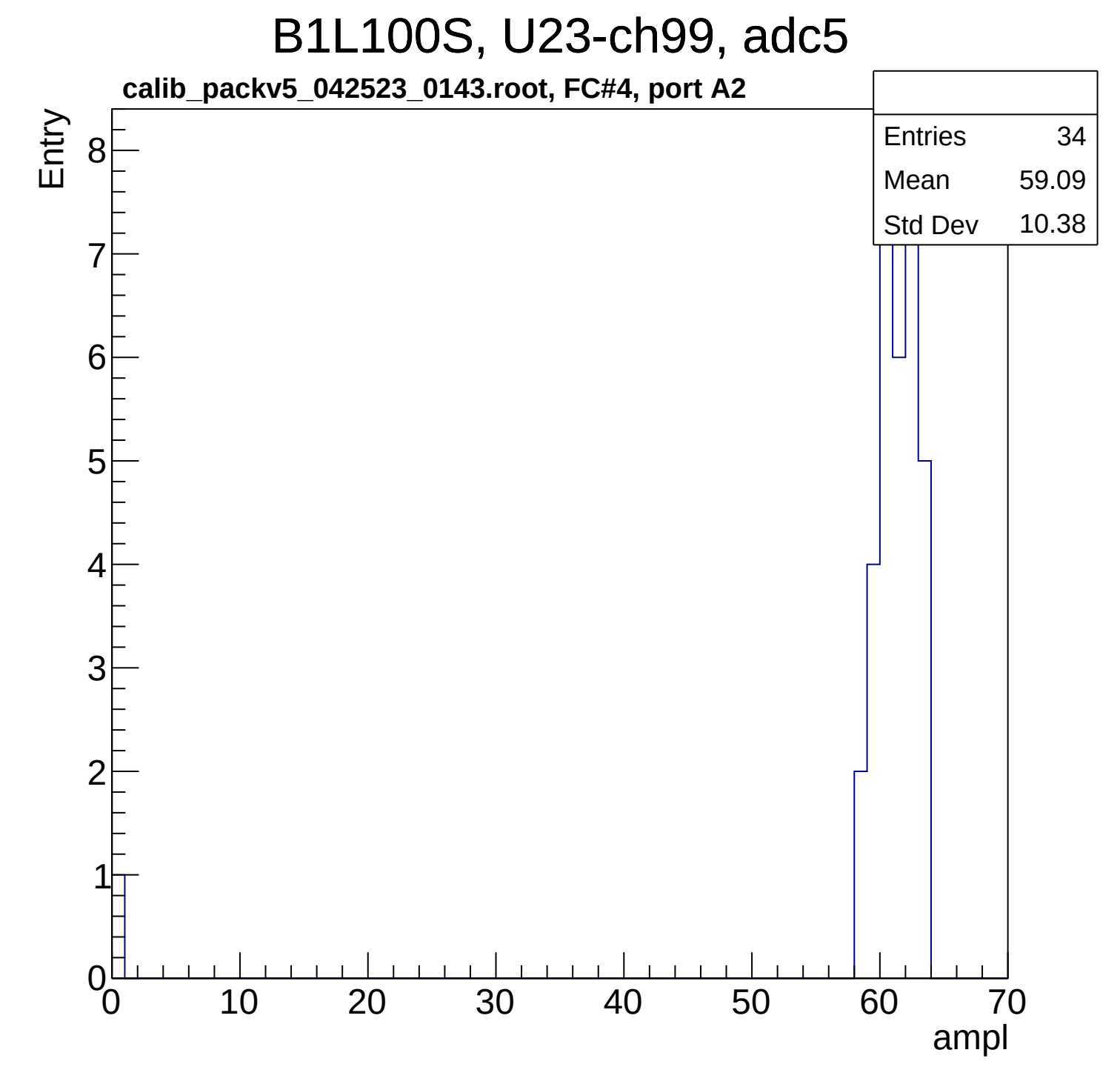
50
60
70


B1L100S, U23-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59.09
Std Dev 10.38
Entry
ampl
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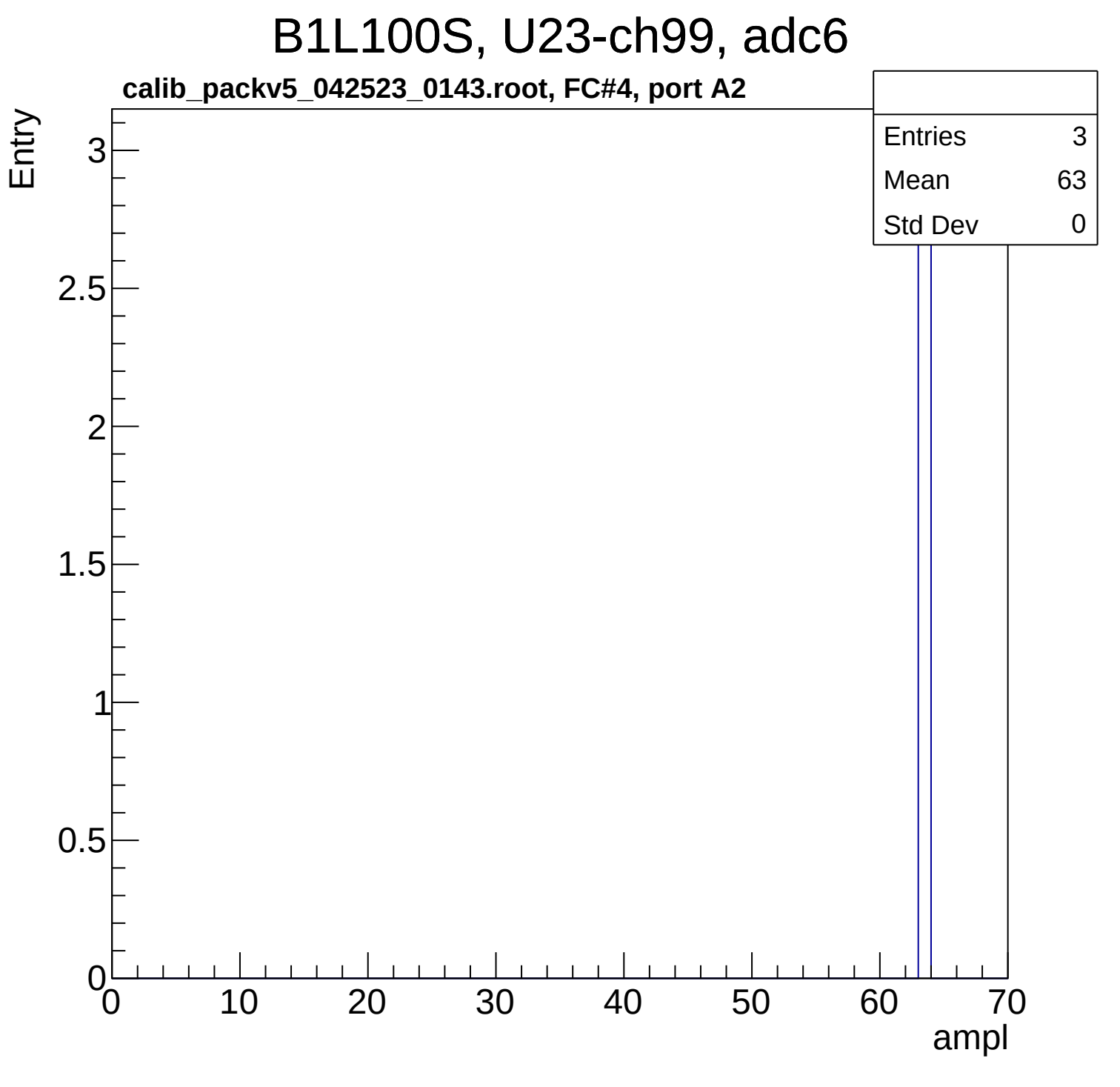
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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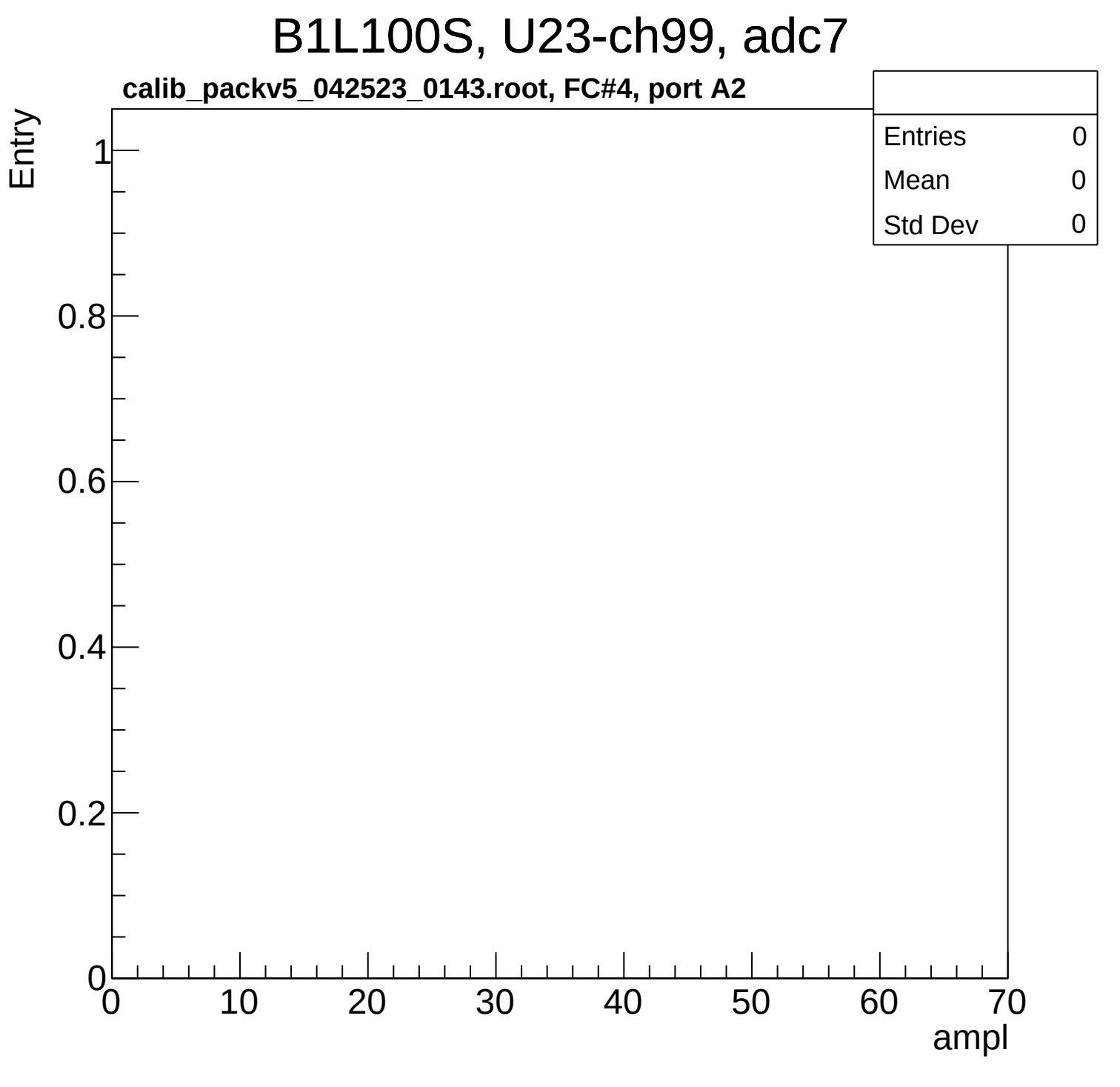
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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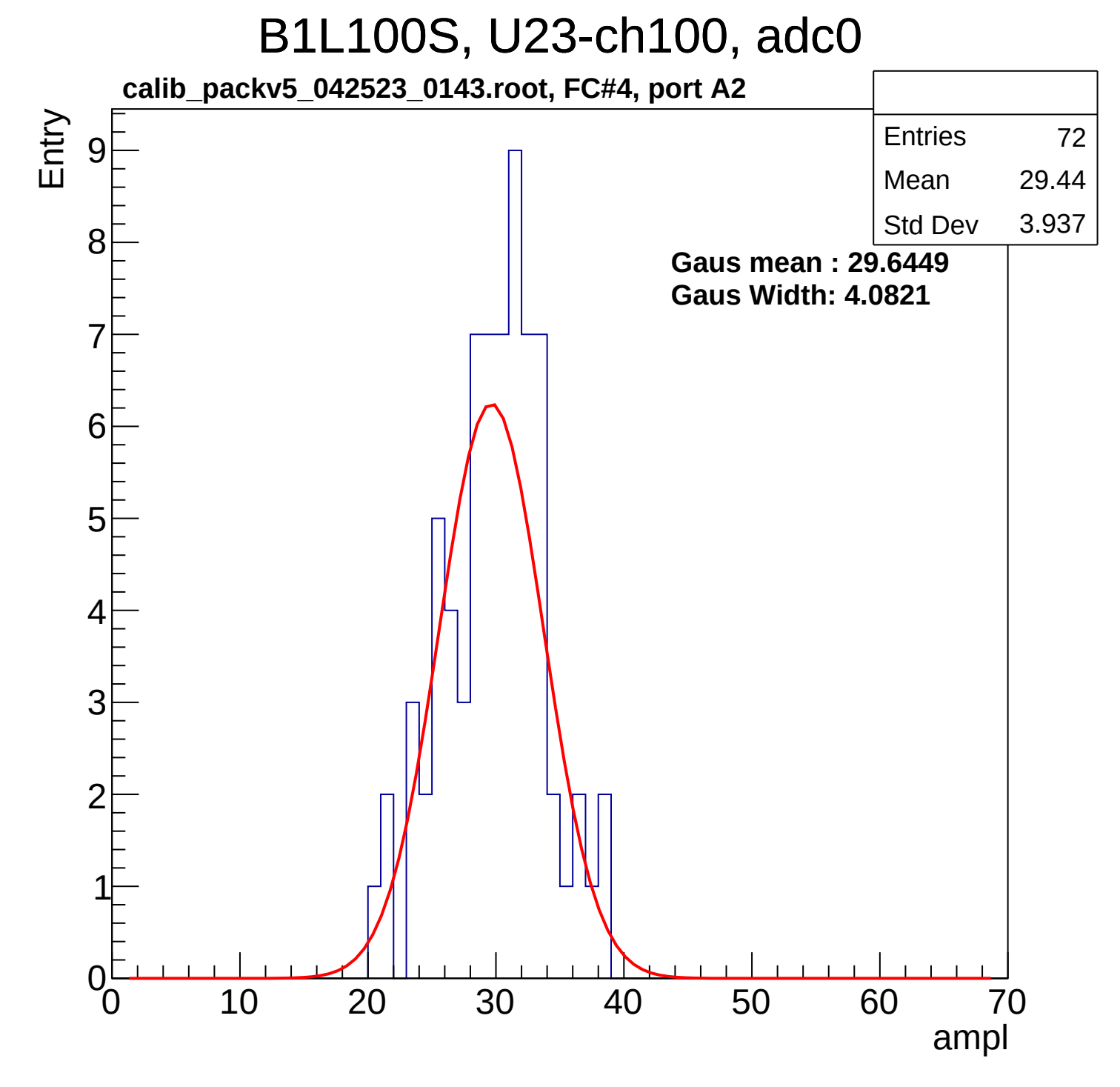
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch100, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.44
Std Dev 3.937
Gaus mean : 29.6449
Gaus Width: 4.0821
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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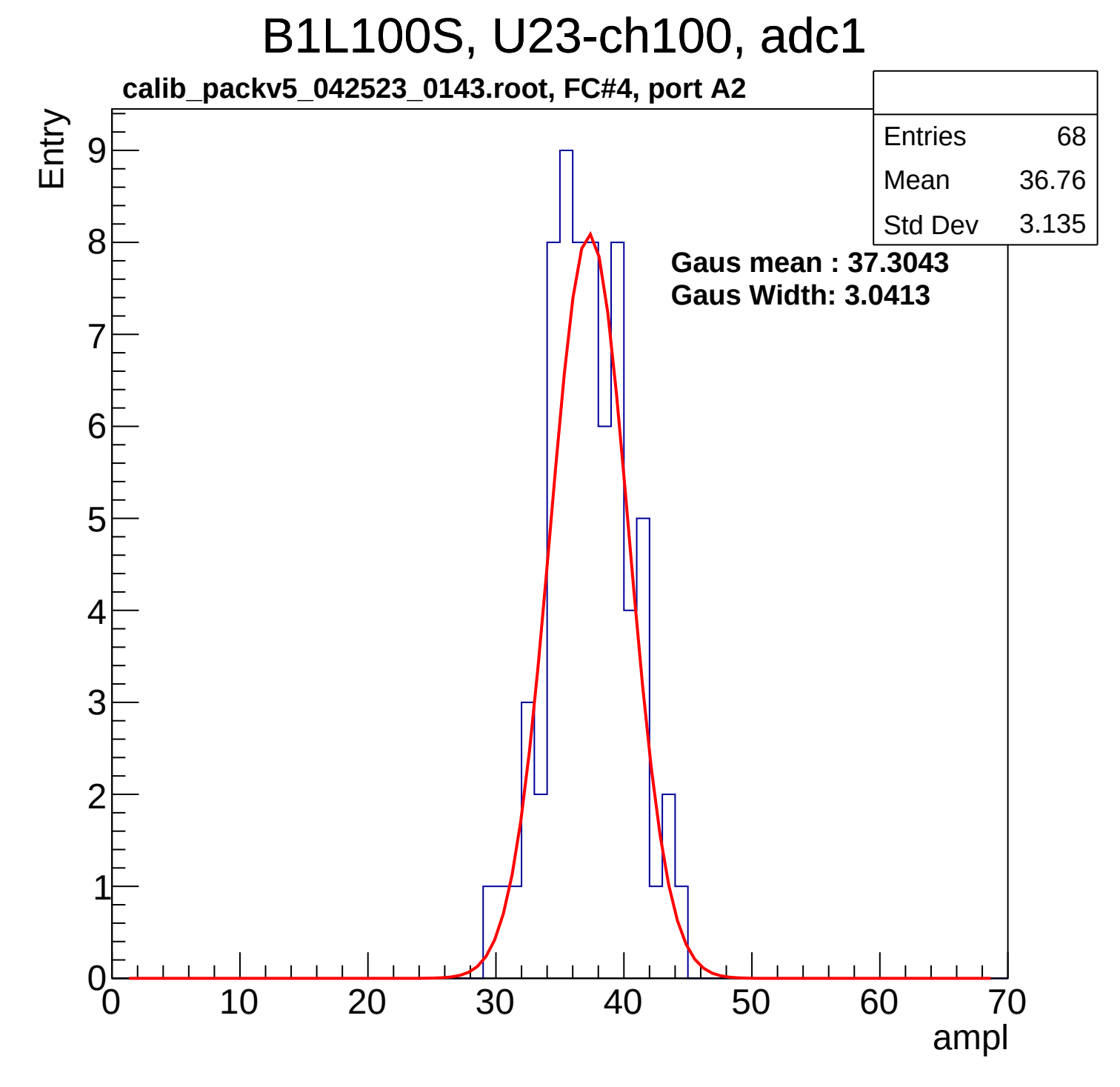
B1L100S, U23-ch100, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.76
Std Dev 3.135
Gaus mean : 37.3043
Gaus Width: 3.0413
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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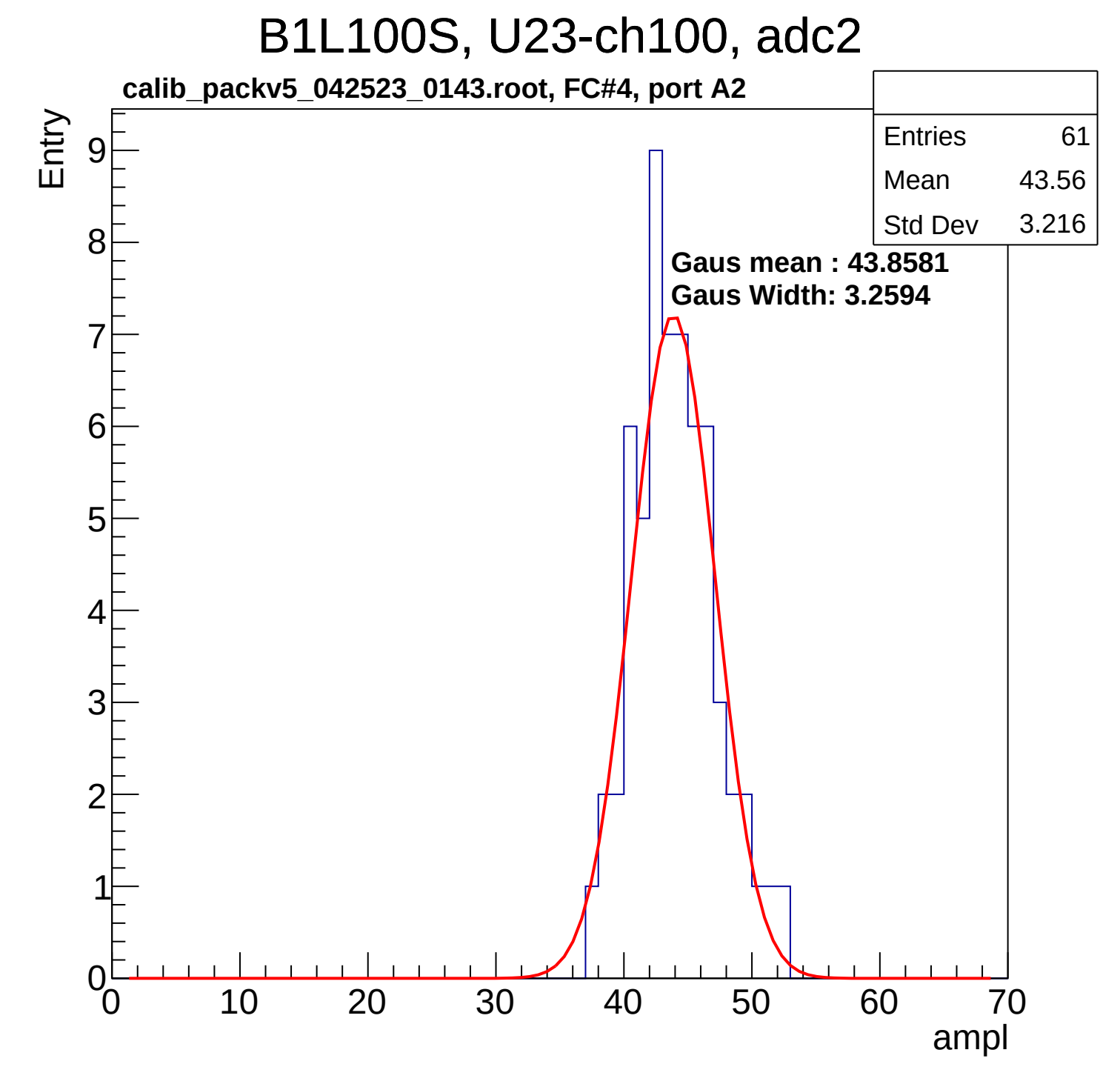
B1L100S, U23-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.56
Std Dev 3.216
Gaus mean : 43.8581
Gaus Width: 3.2594
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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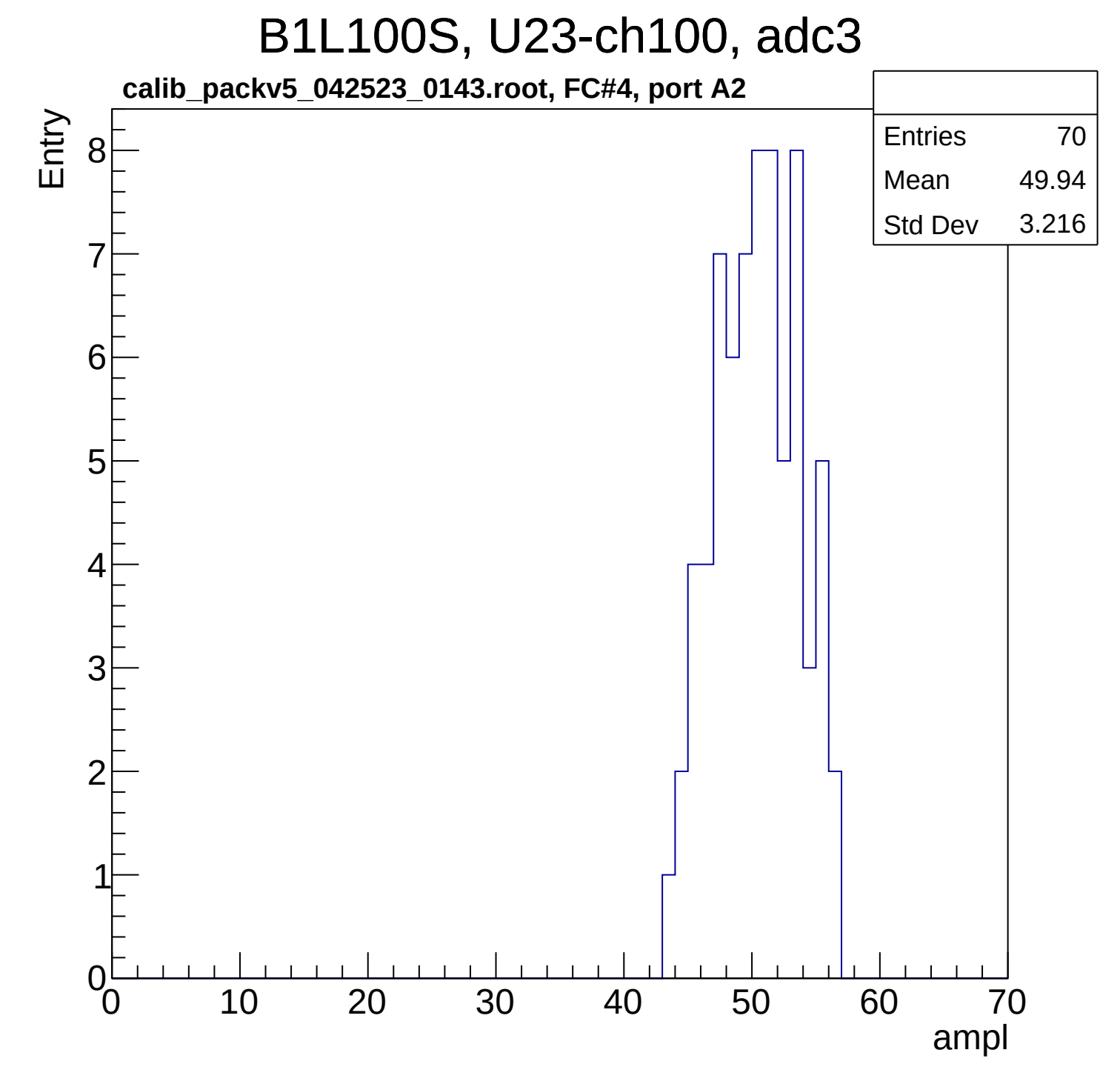
B1L100S, U23-ch100, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.94
Std Dev 3.216
Entry
ampl
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2
3
4
5
6
7
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0
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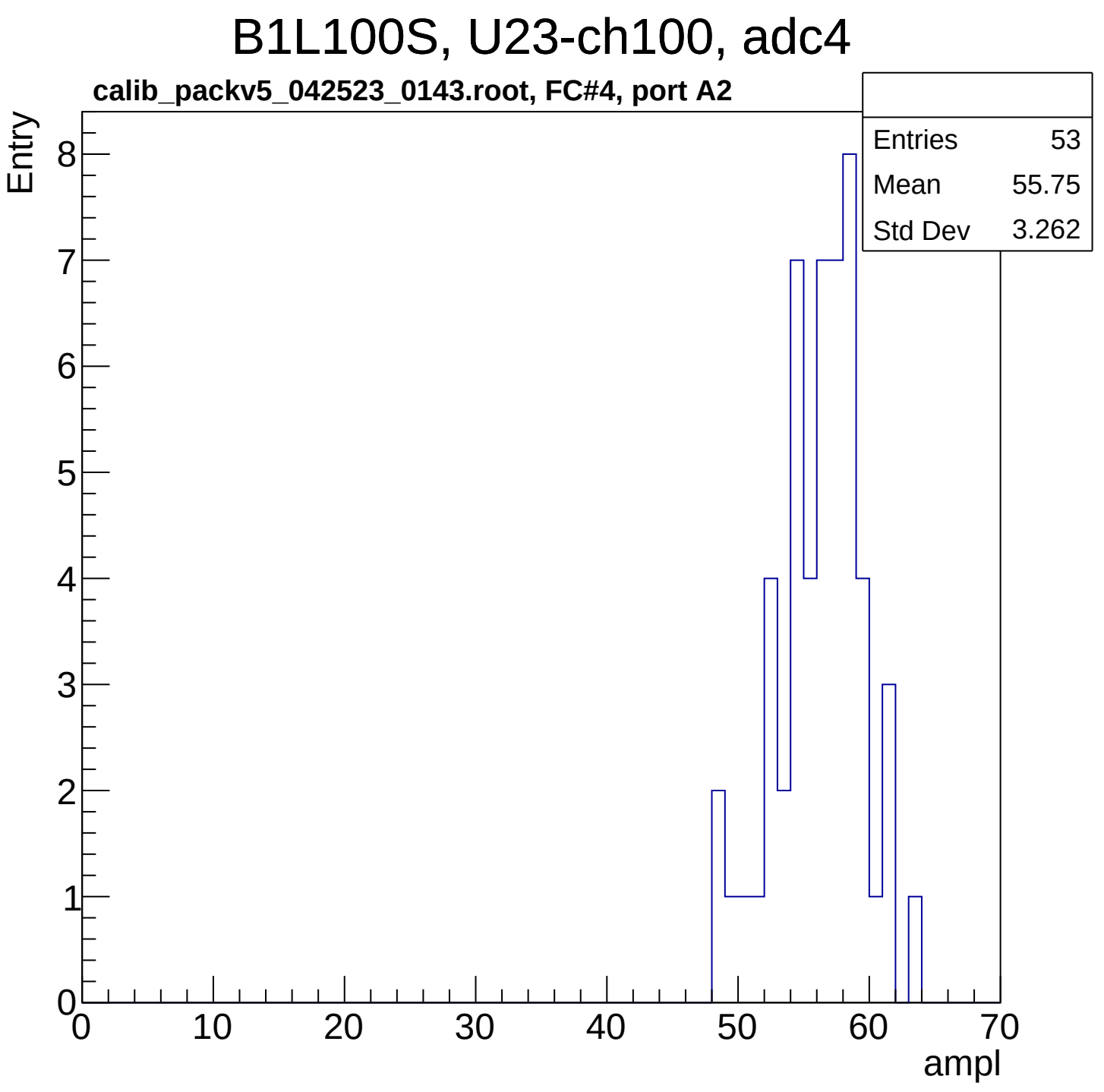
20
30
40
50
60
70


B1L100S, U23-ch100, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.75
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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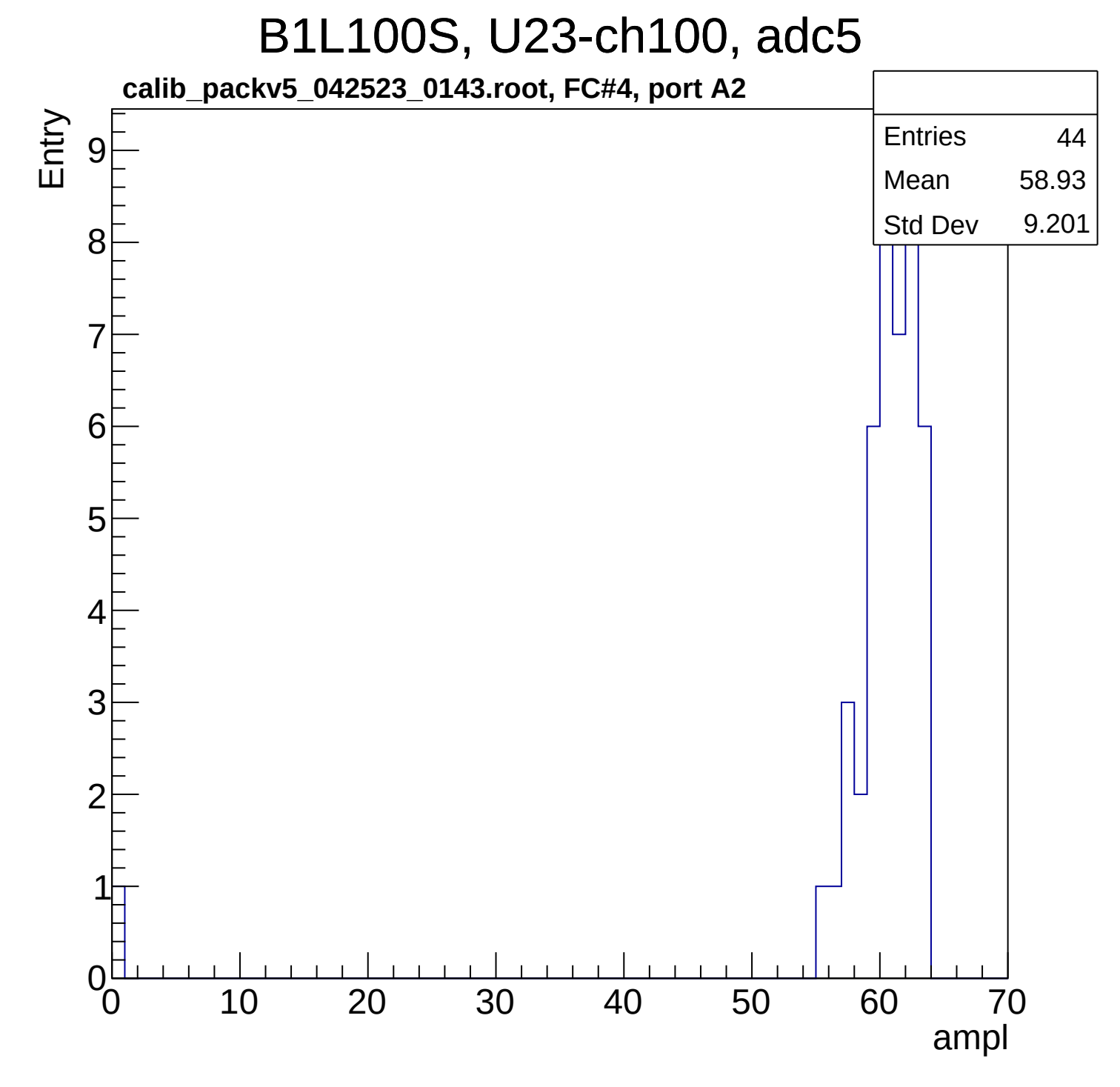
40
50
60
70


B1L100S, U23-ch100, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.93
Std Dev 9.201
Entry
ampl
0
1
2
3
4
5
6
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9
0
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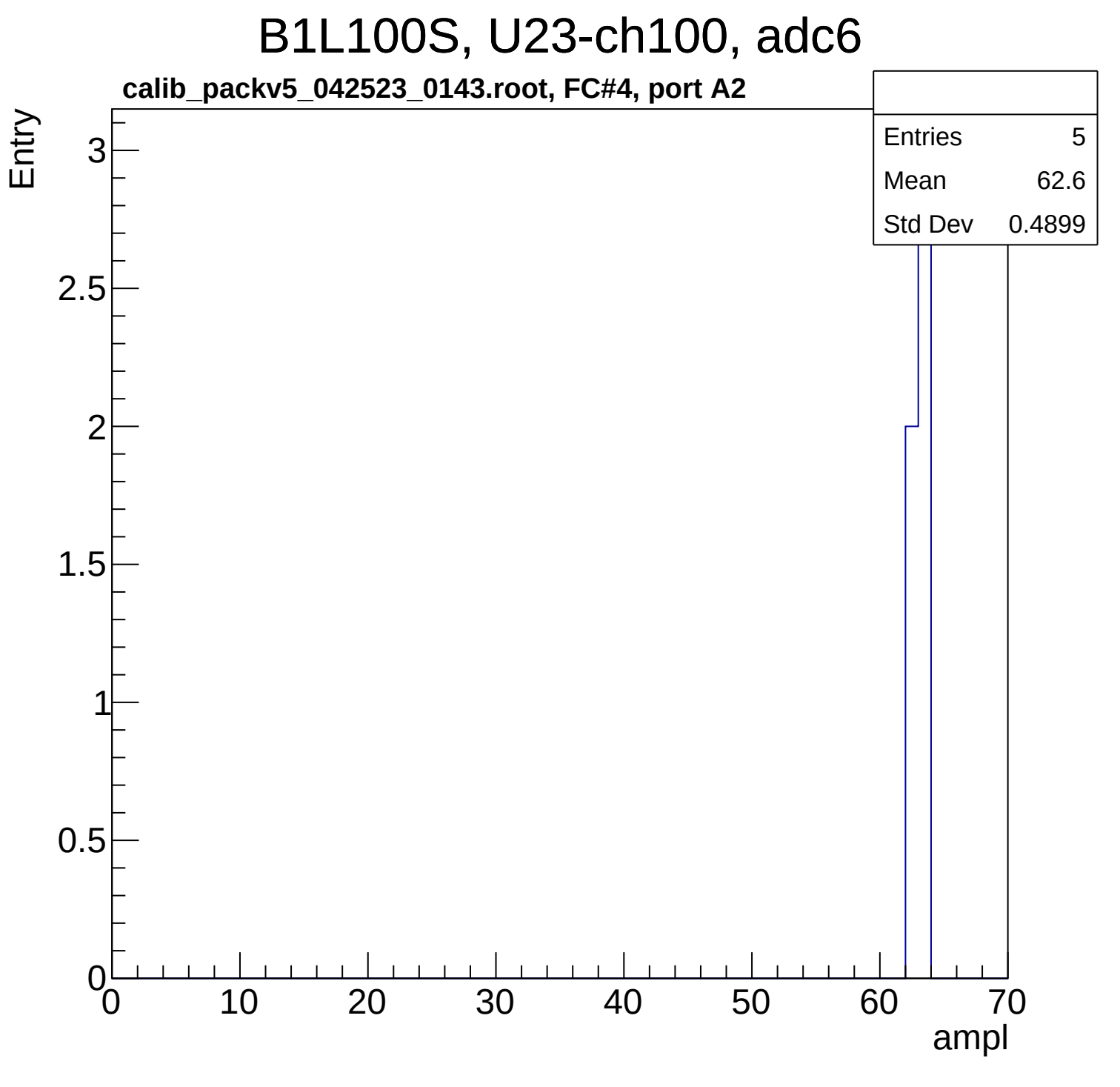
B1L100S, U23-ch100, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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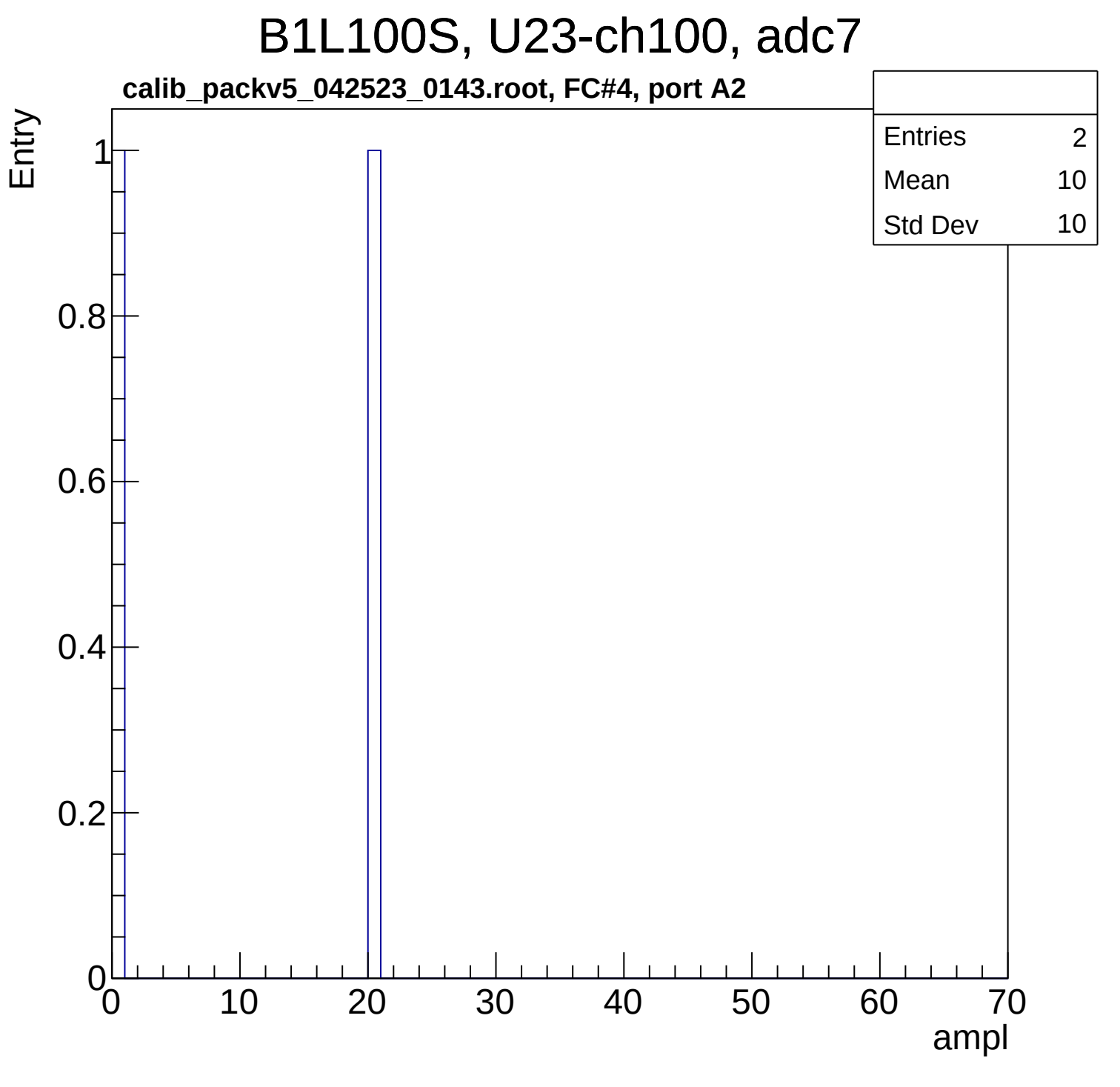
B1L100S, U23-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
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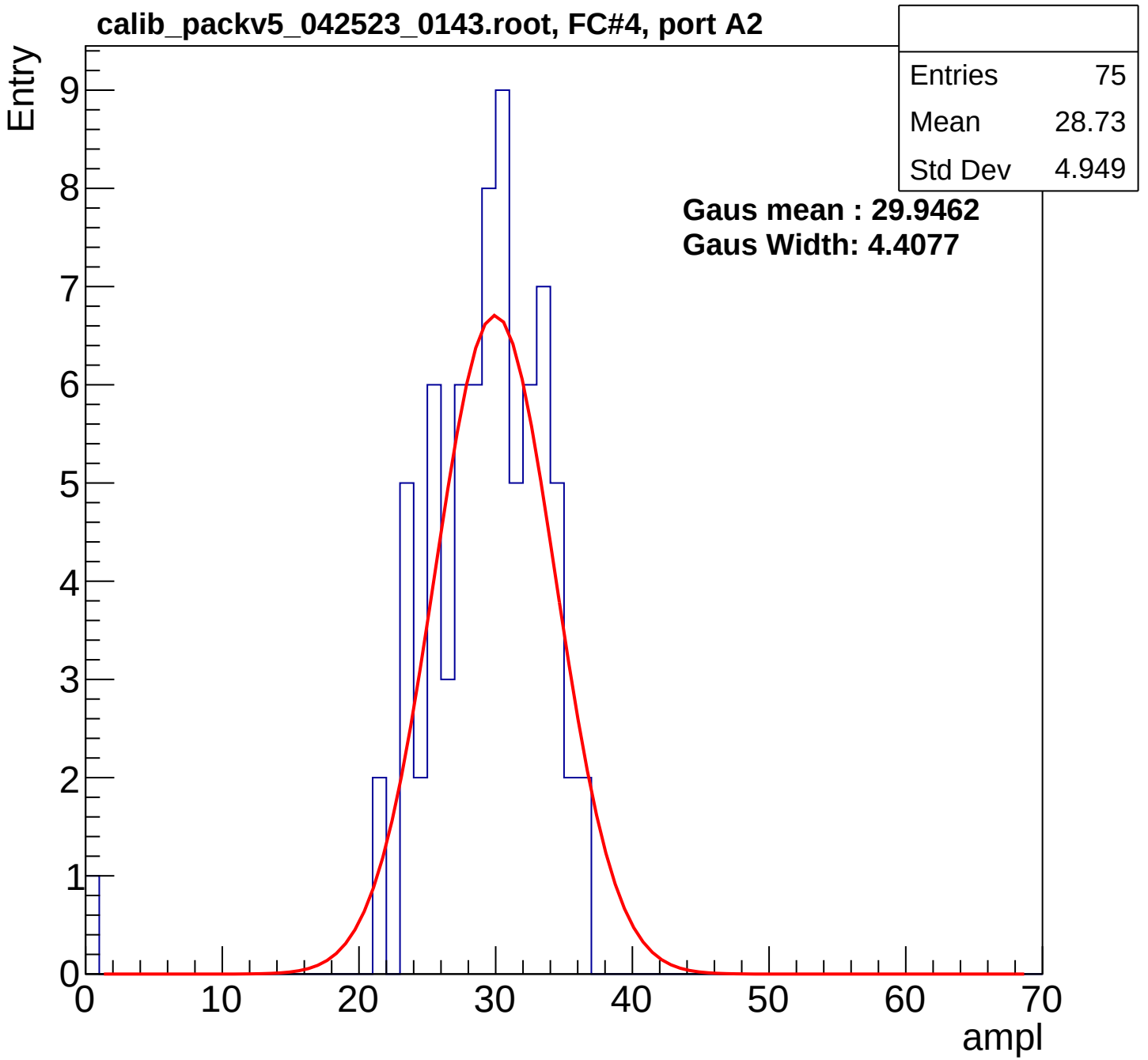
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.73
Std Dev 4.949
Gaus mean : 29.9462
Gaus Width: 4.4077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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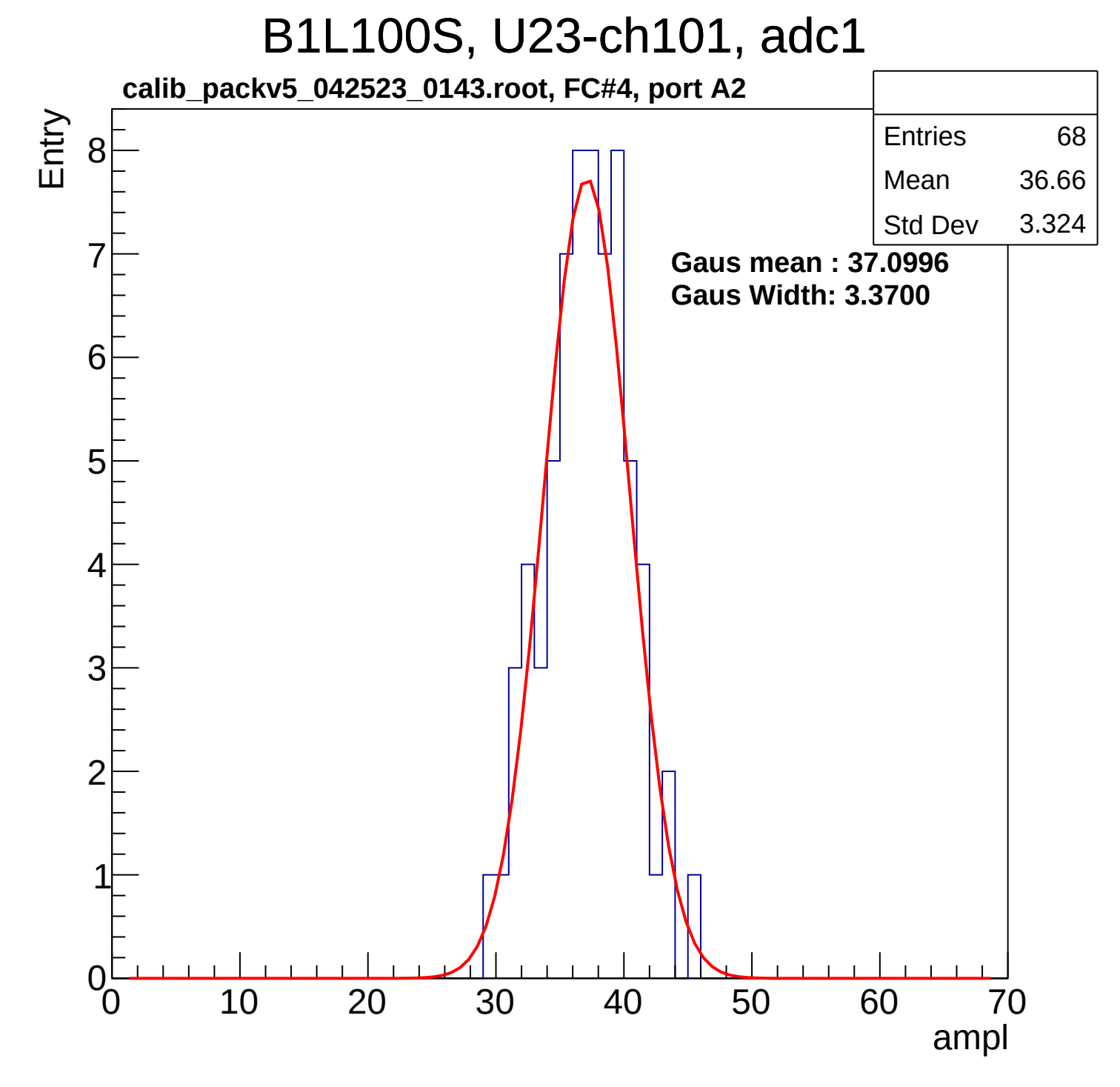
B1L100S, U23-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.66
Std Dev 3.324
Gaus mean : 37.0996
Gaus Width: 3.3700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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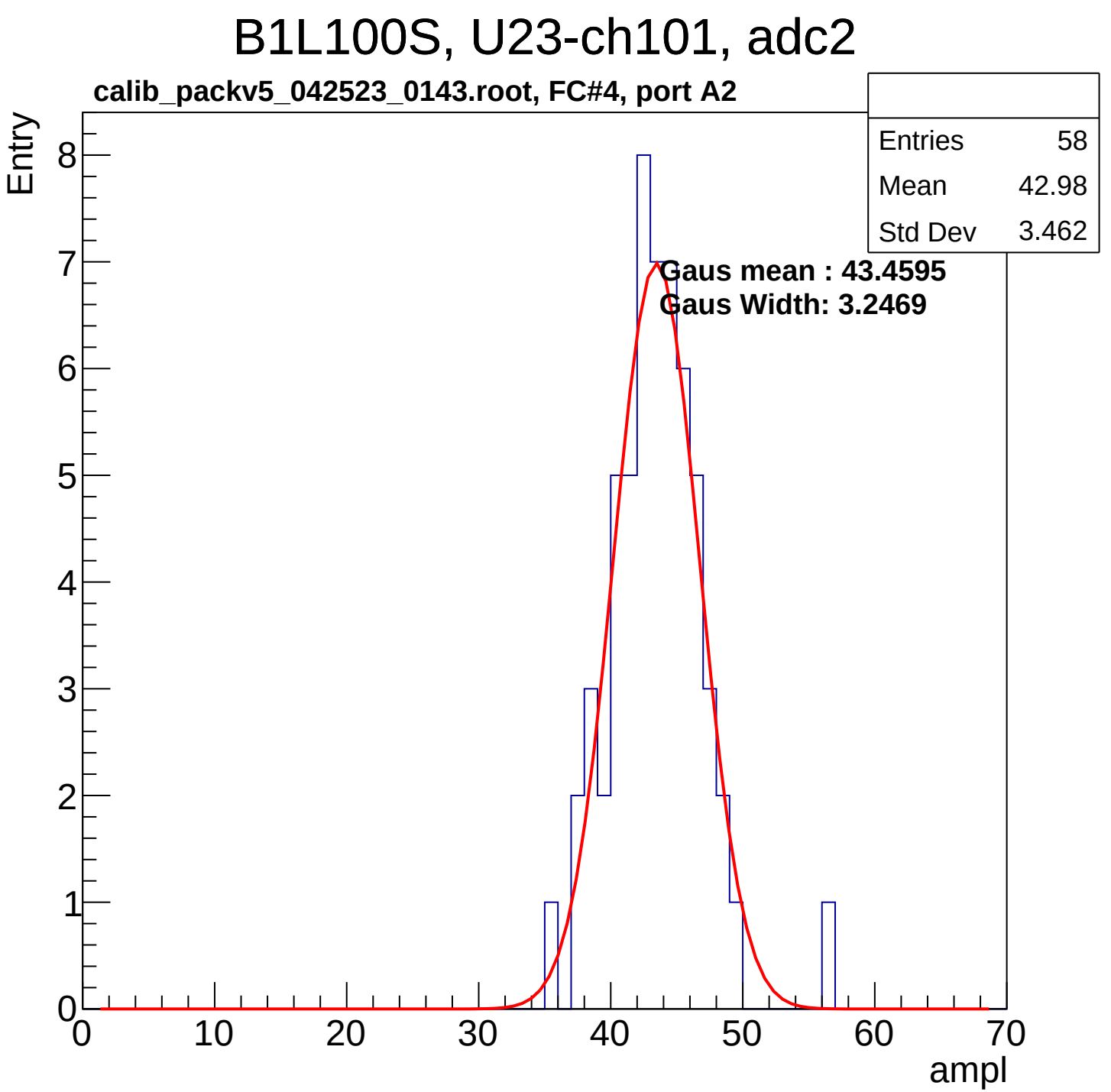
B1L100S, U23-ch101, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 42.98
Std Dev 3.462
Gaus mean : 43.4595
Gaus Width: 3.2469
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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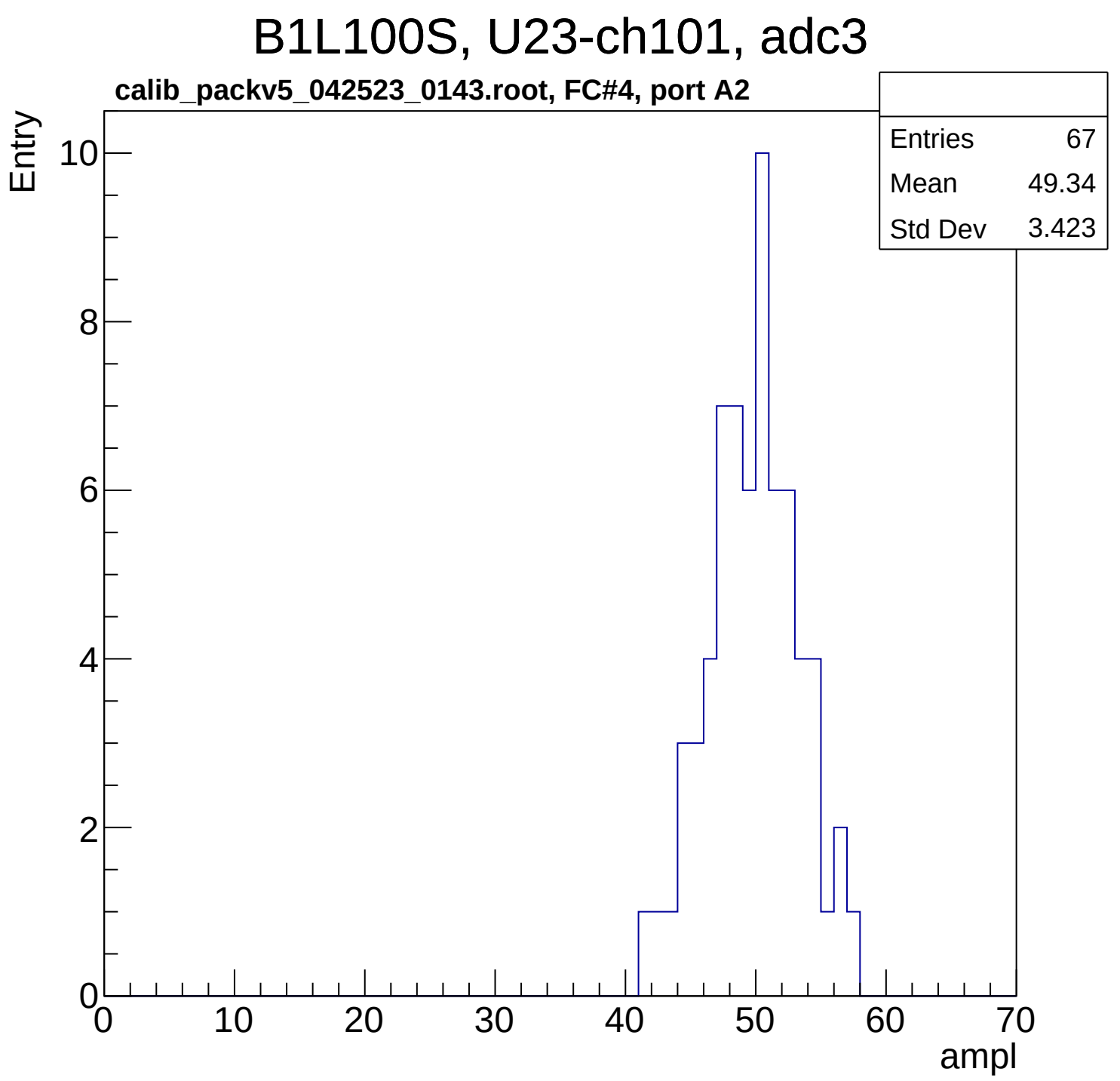
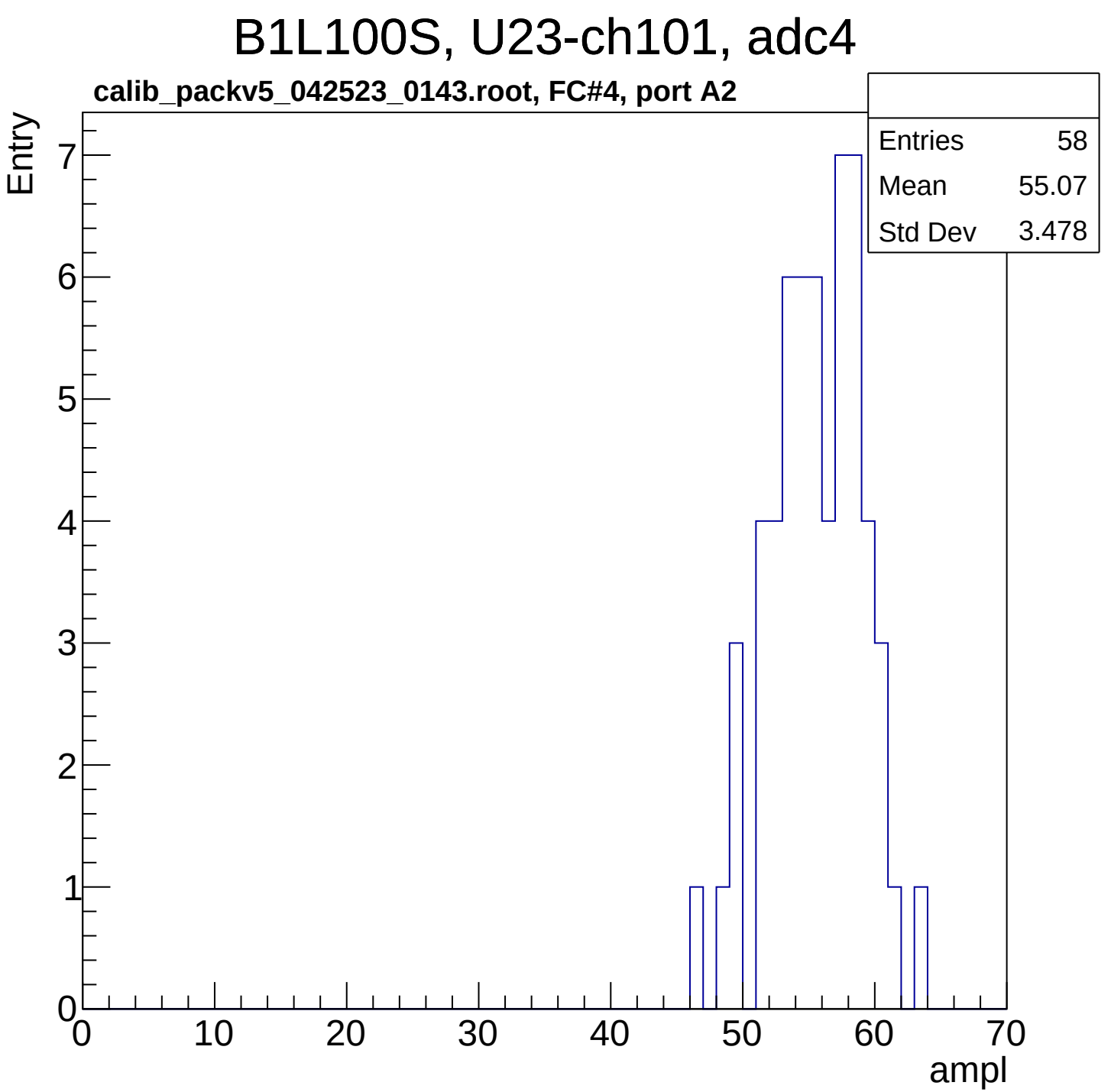

B1L100S, U23-ch101, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.34
Std Dev 3.423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch101, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.07
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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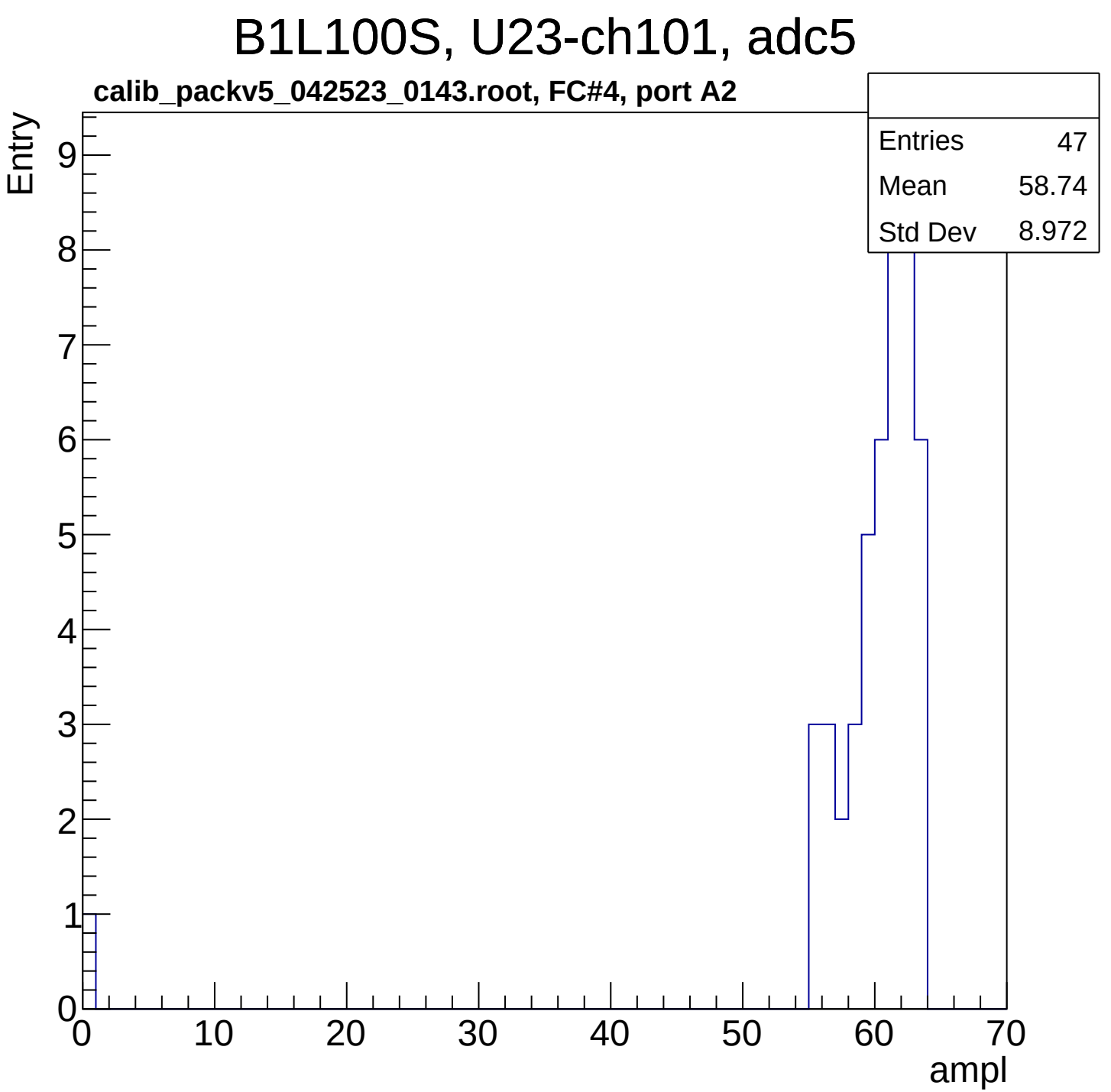
B1L100S, U23-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.74
Std Dev 8.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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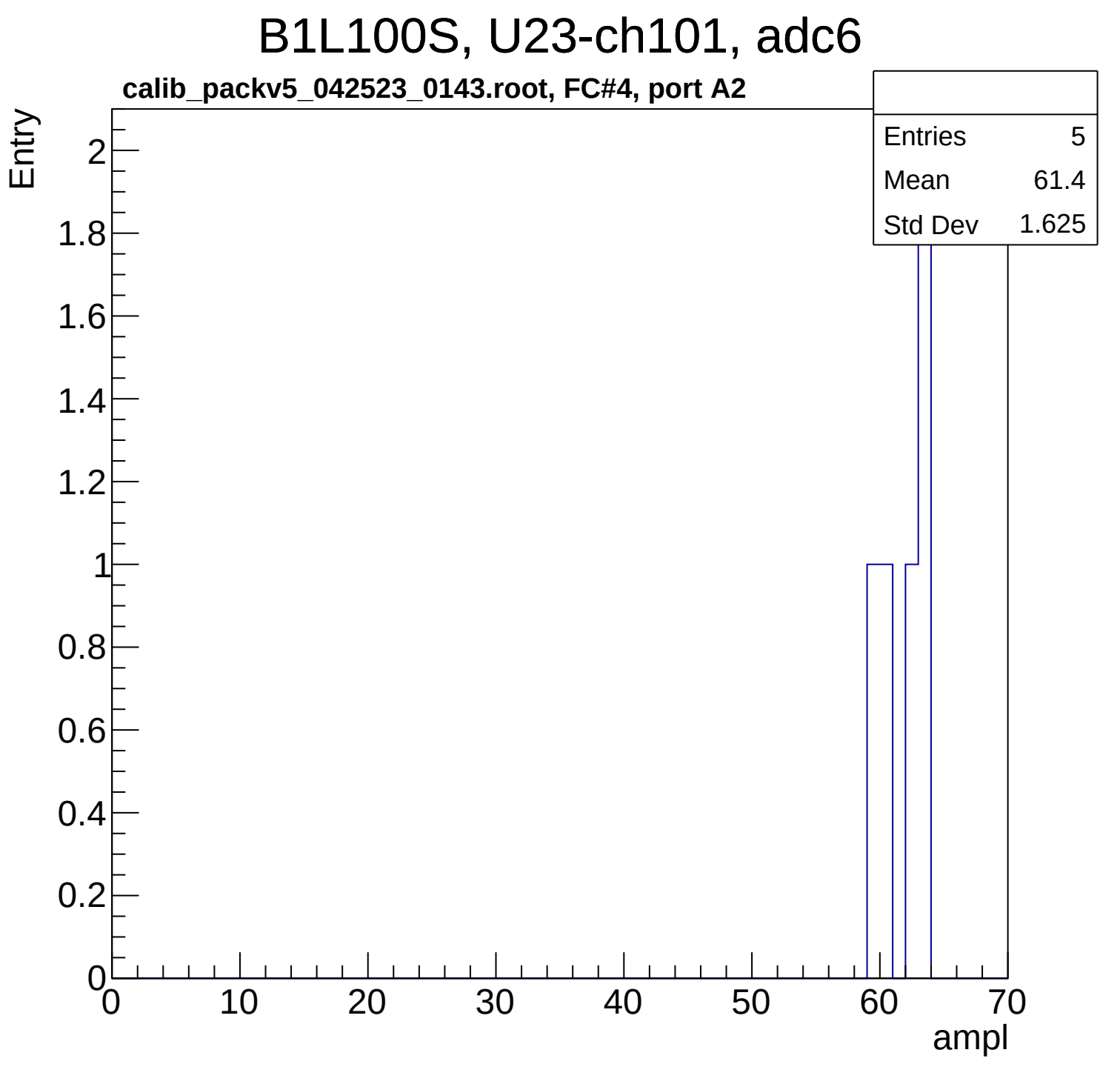
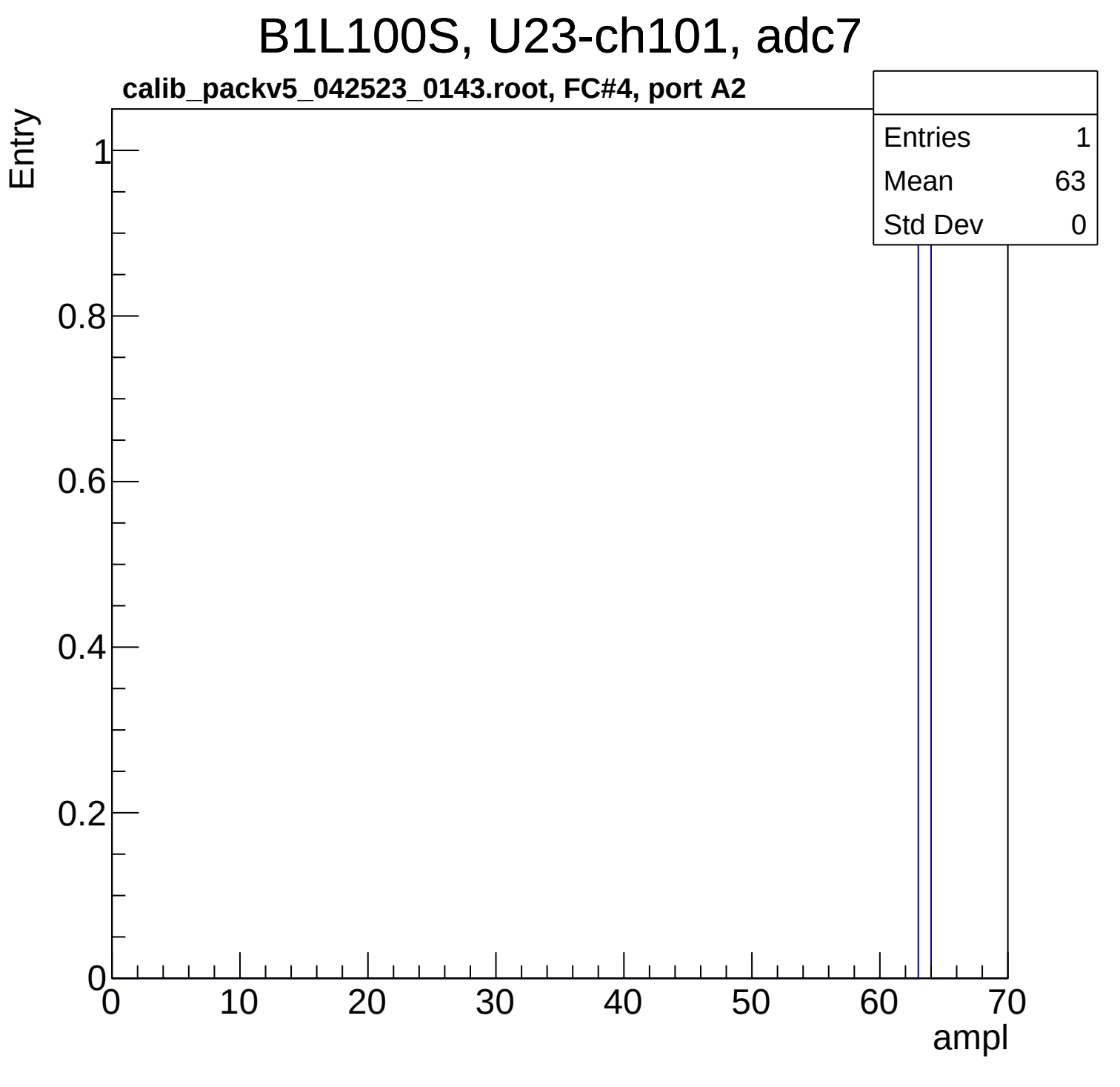
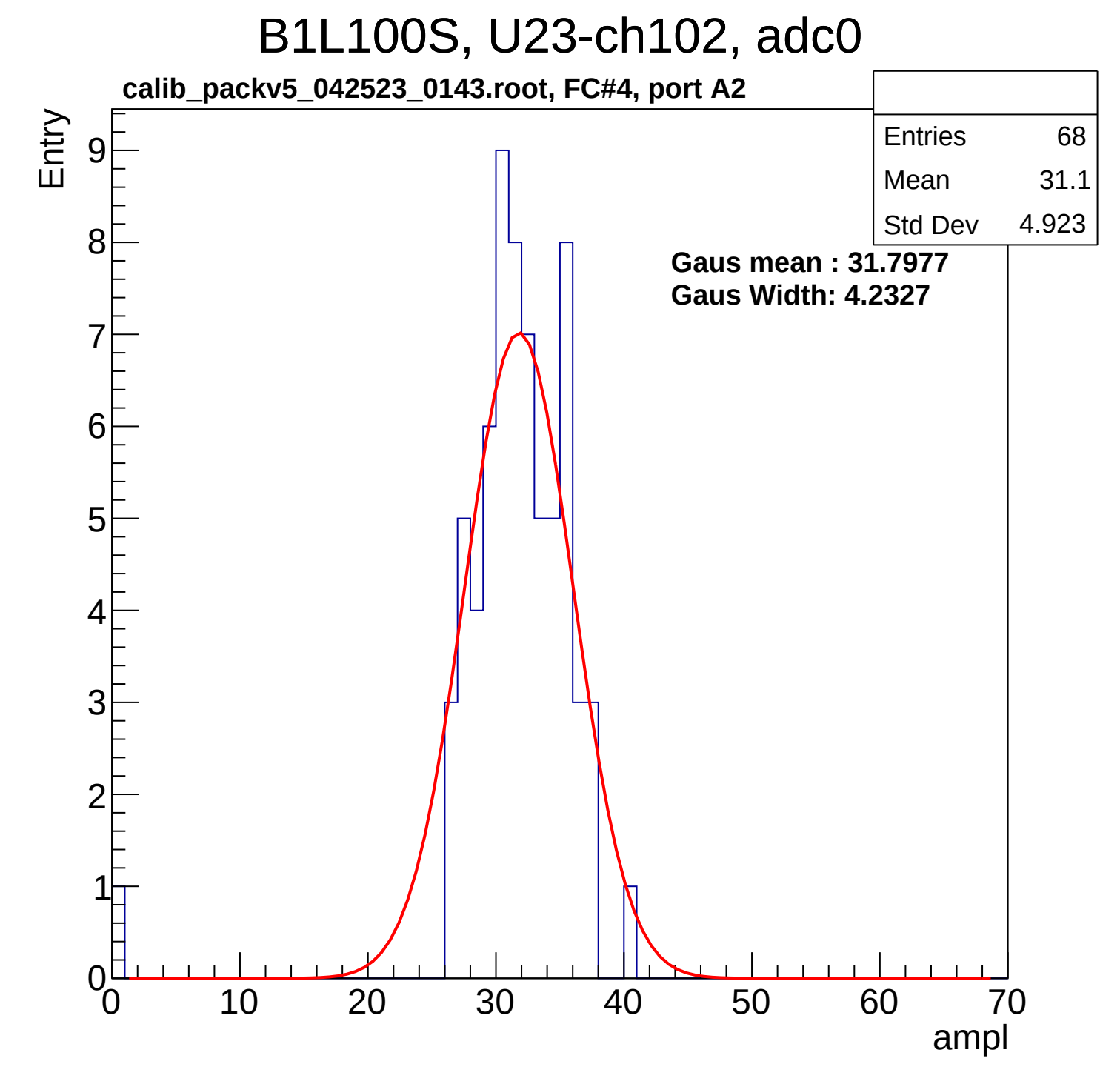
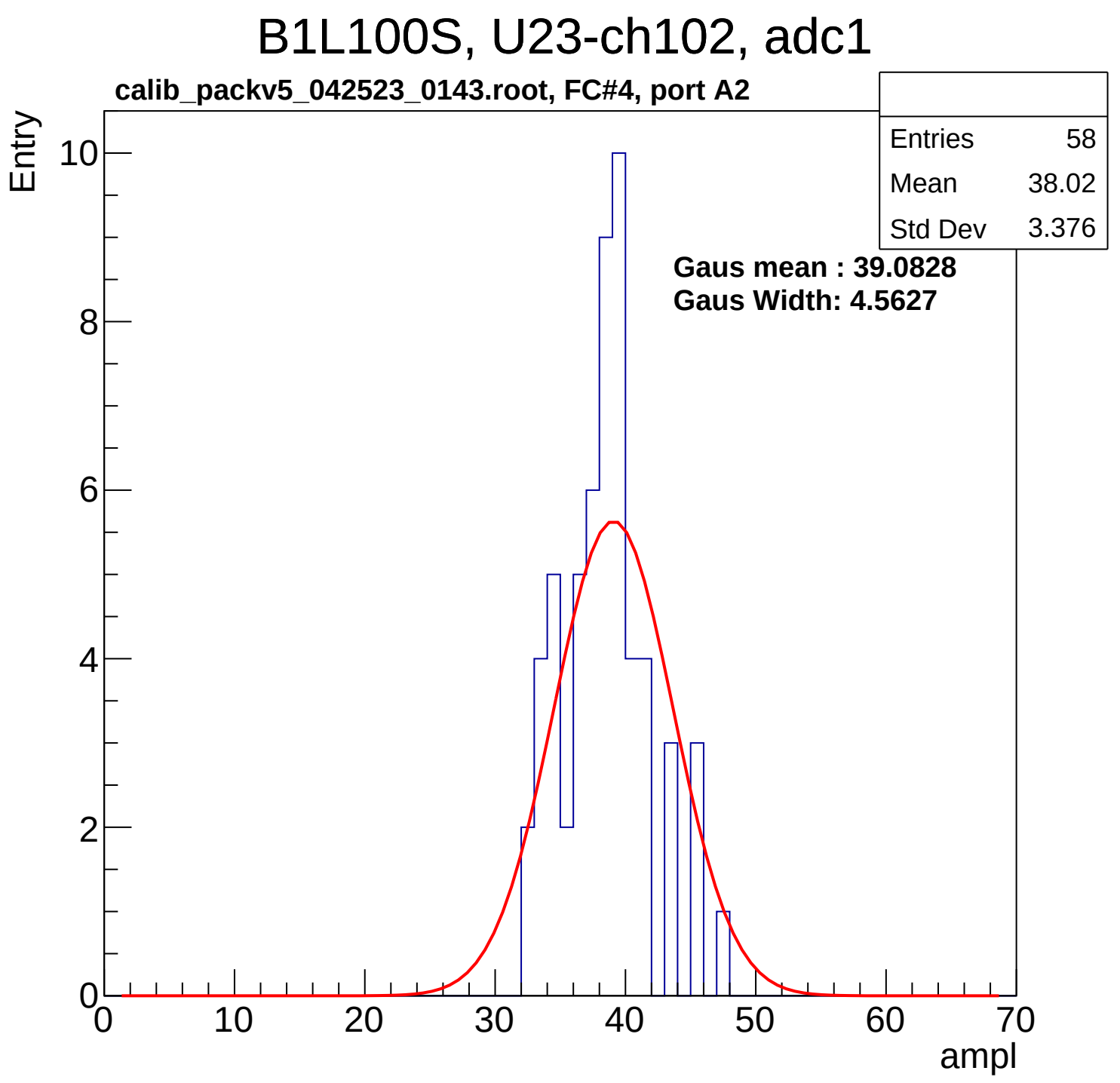
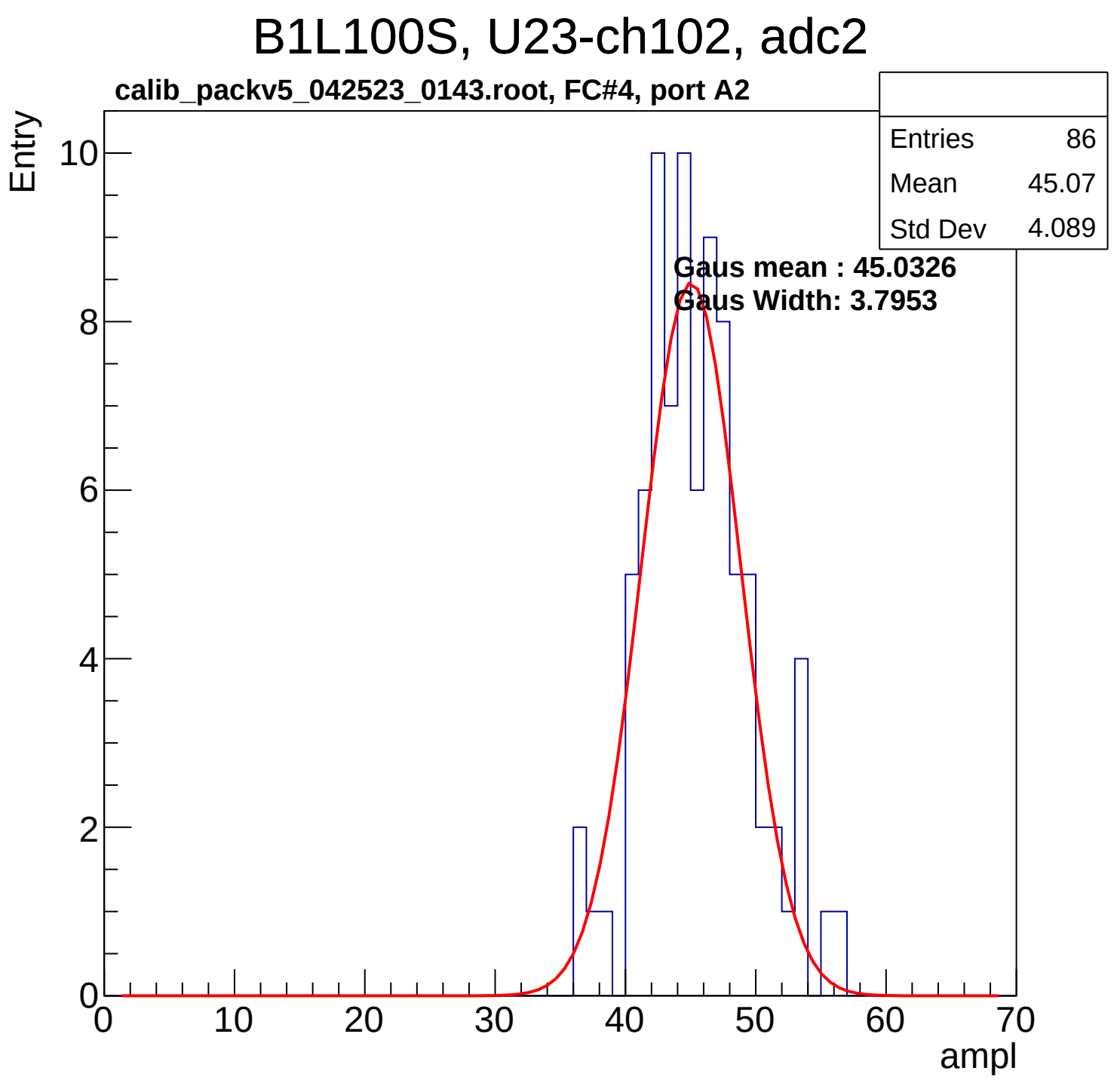
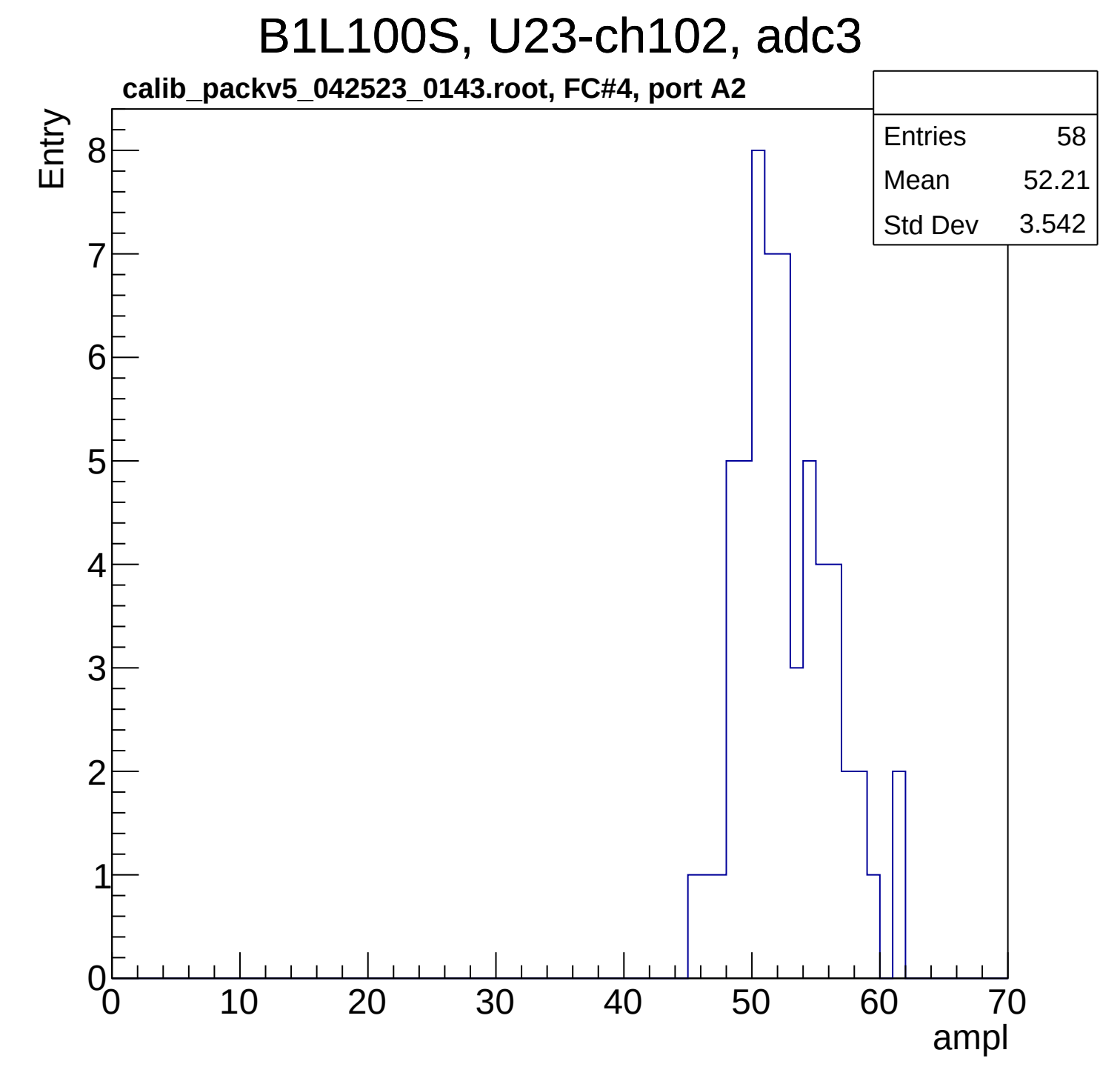

B1L100S, U23-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 1.625
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 31.1
Std Dev 4.923
Gaus mean : 31.7977
Gaus Width: 4.2327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 38.02
Std Dev 3.376
Gaus mean : 39.0828
Gaus Width: 4.5627
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 45.07
Std Dev 4.089
Gaus mean : 45.0326
Gaus Width: 3.7953
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch102, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 52.21
Std Dev 3.542
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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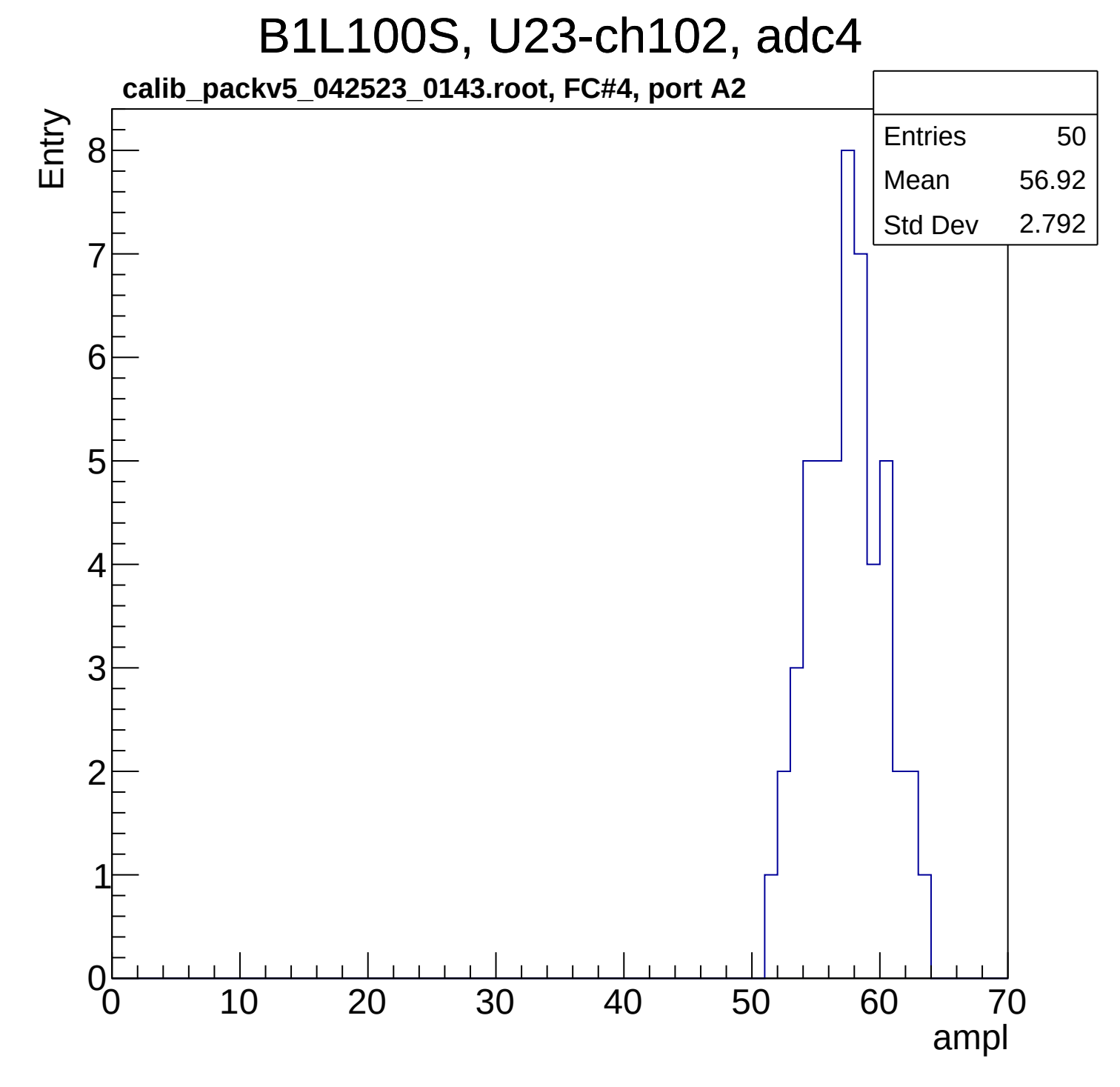
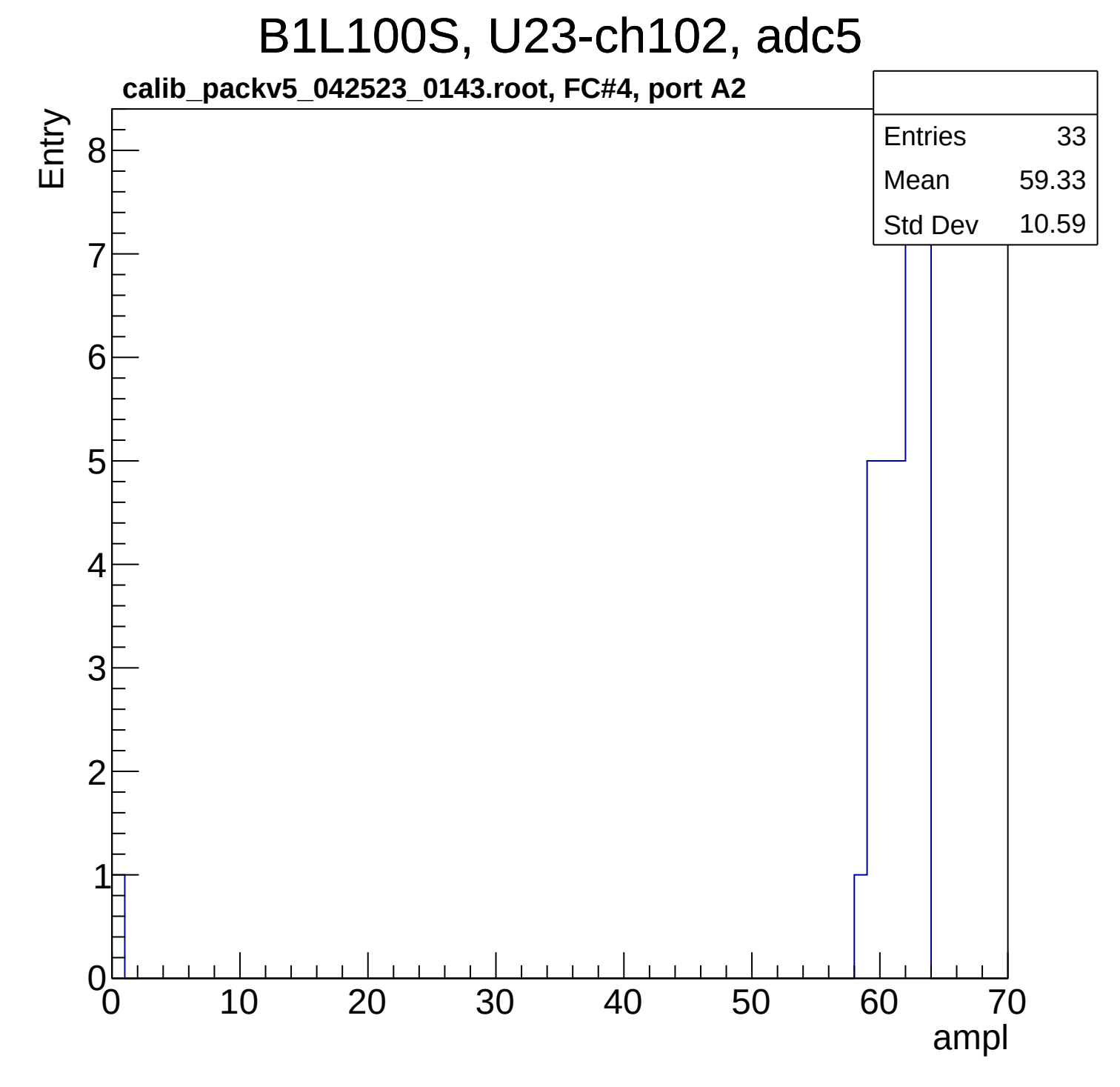
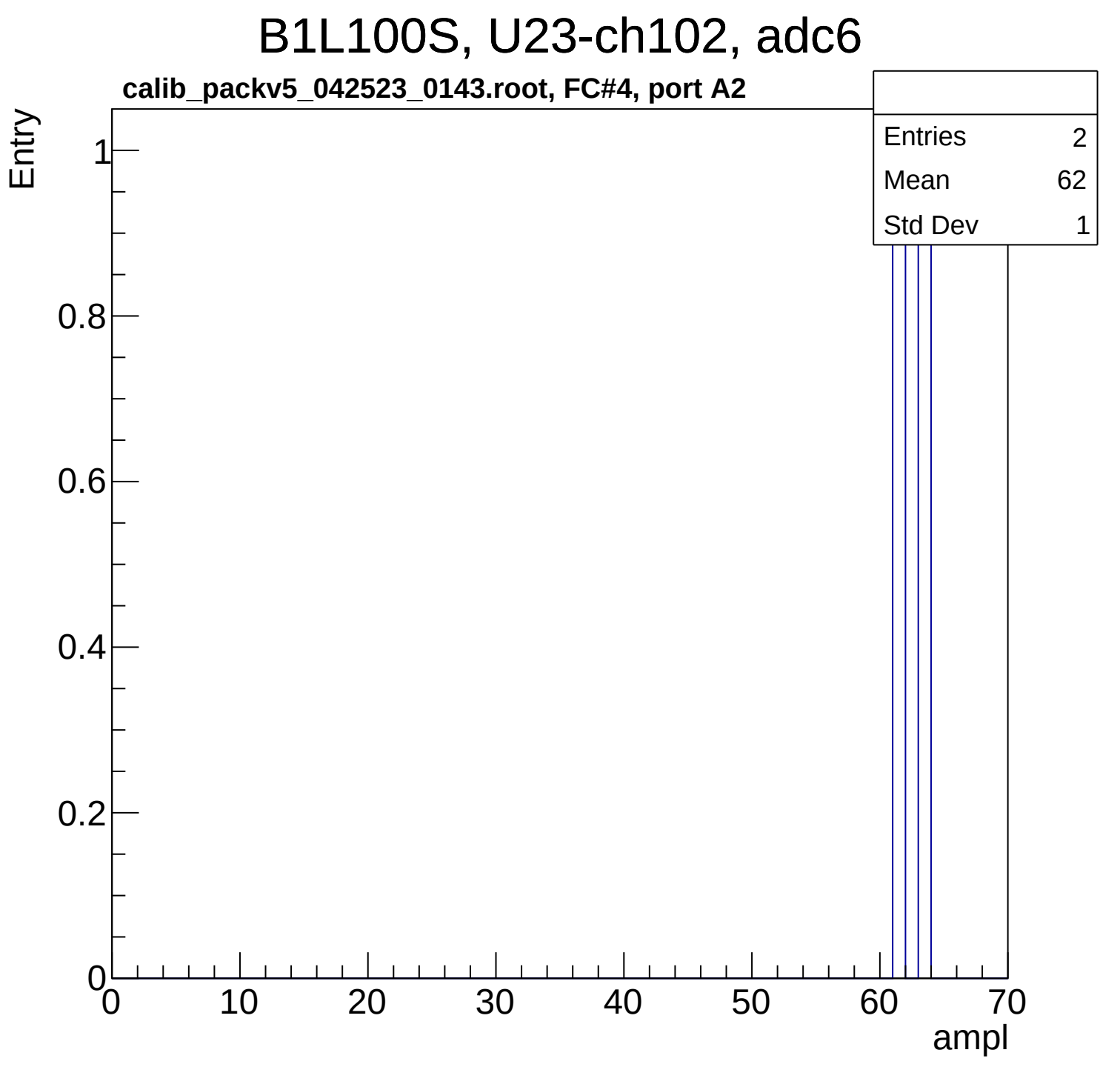
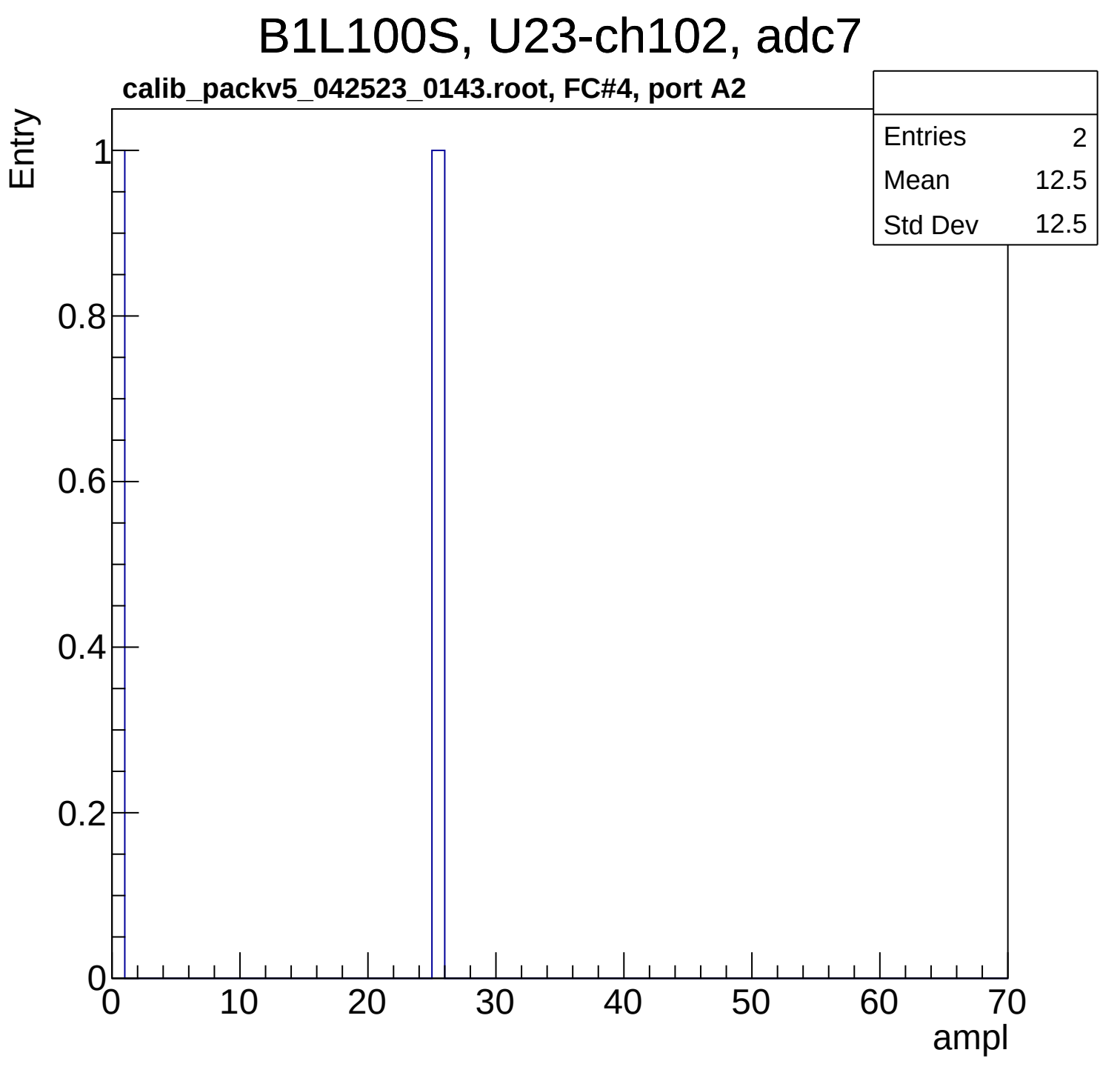

B1L100S, U23-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 56.92
Std Dev 2.792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch102, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.33
Std Dev 10.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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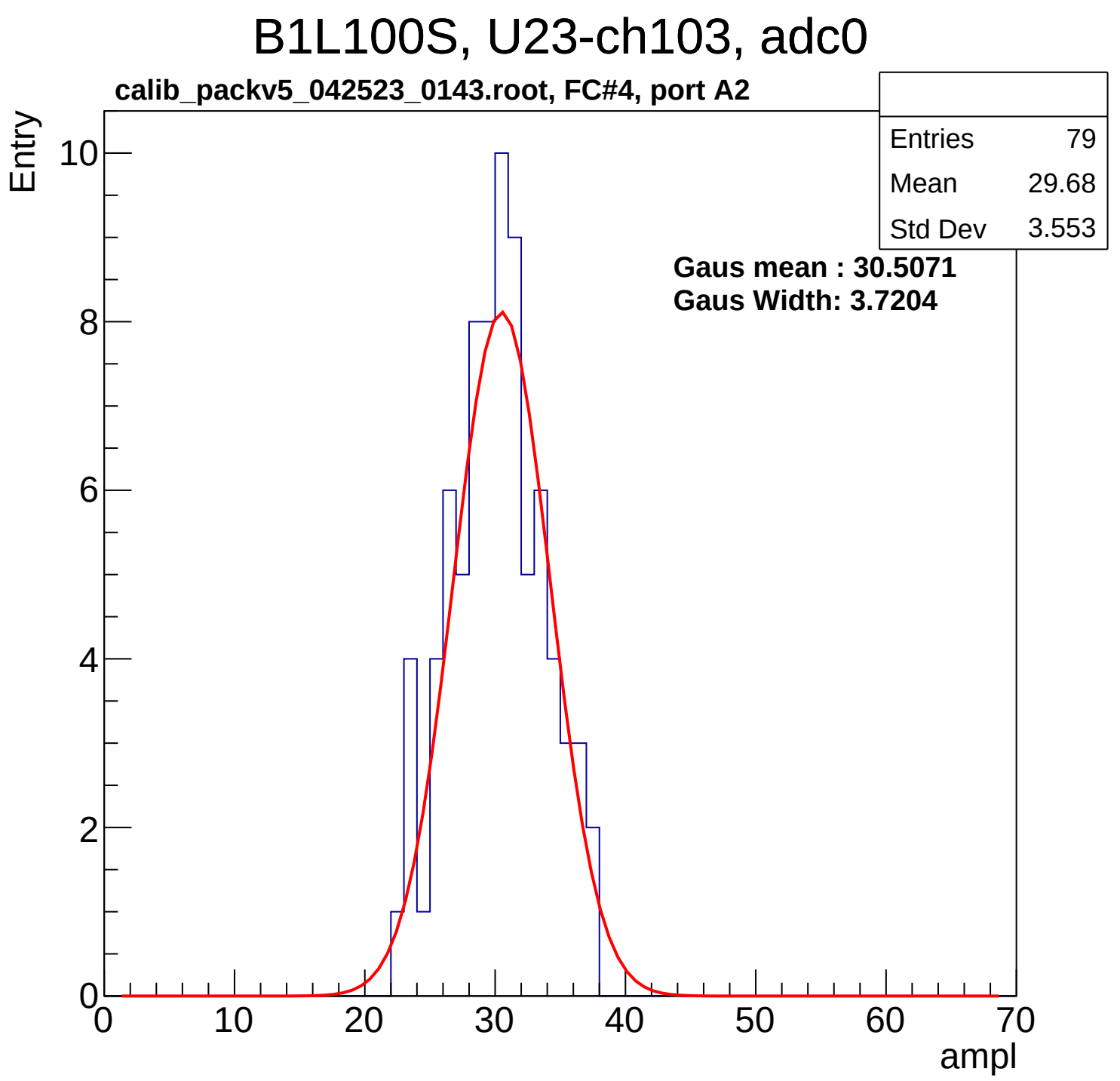
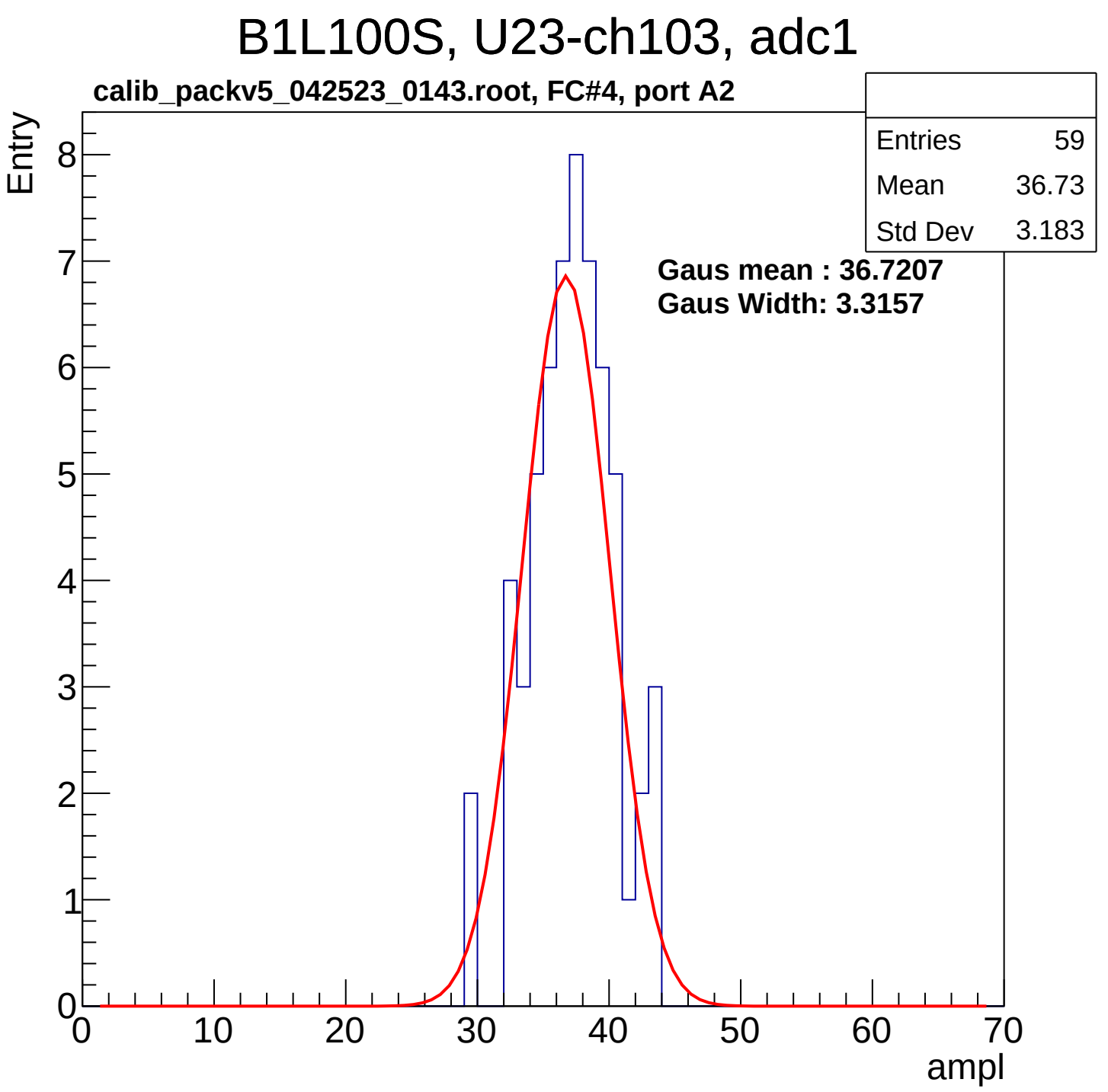
50
60
70
ampl


B1L100S, U23-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.68
Std Dev 3.553
Gaus mean : 30.5071
Gaus Width: 3.7204
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.73
Std Dev 3.183
Gaus mean : 36.7207
Gaus Width: 3.3157
Entry
ampl
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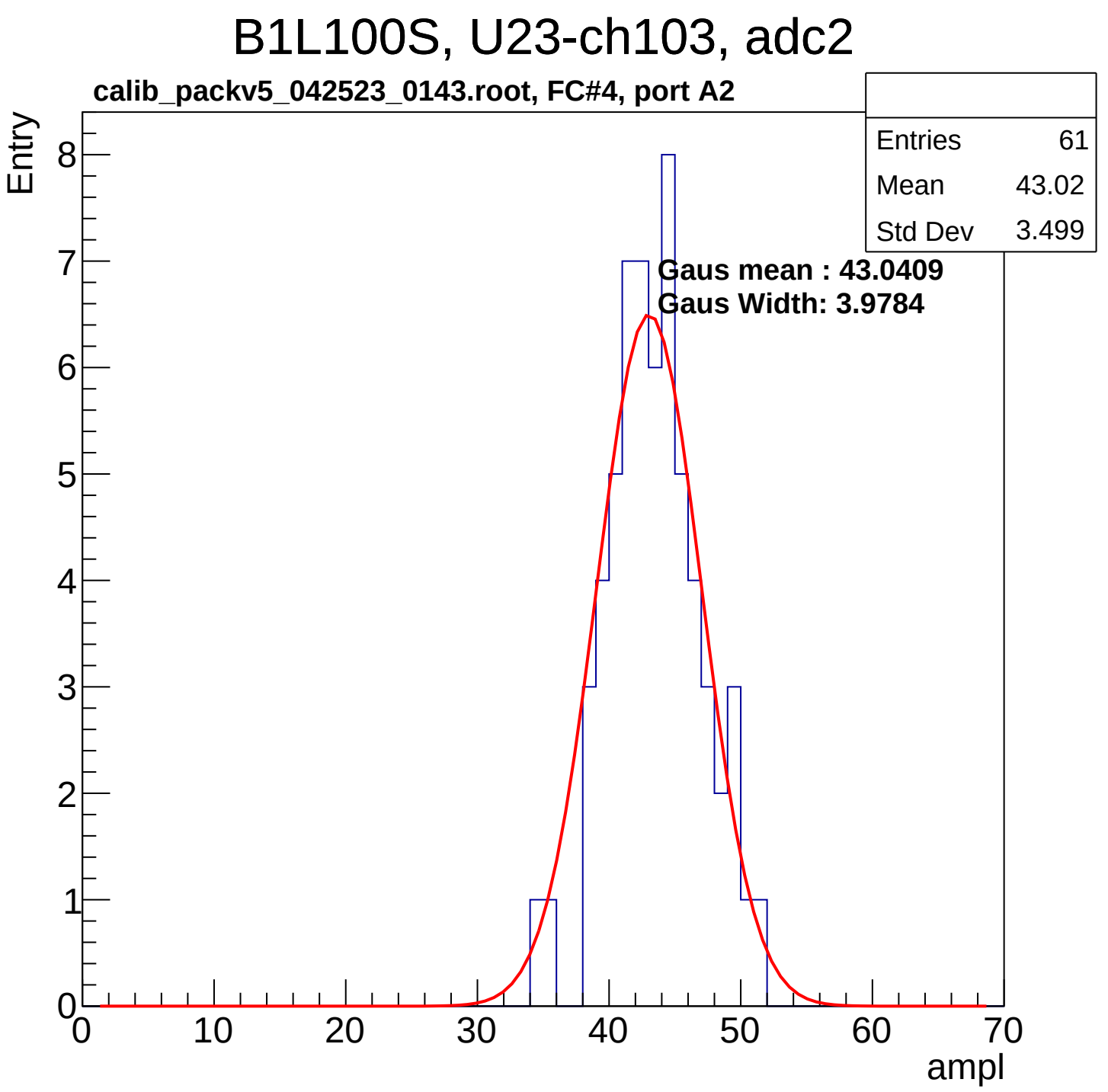
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.02
Std Dev 3.499
Gaus mean : 43.0409
Gaus Width: 3.9784
Entry
ampl
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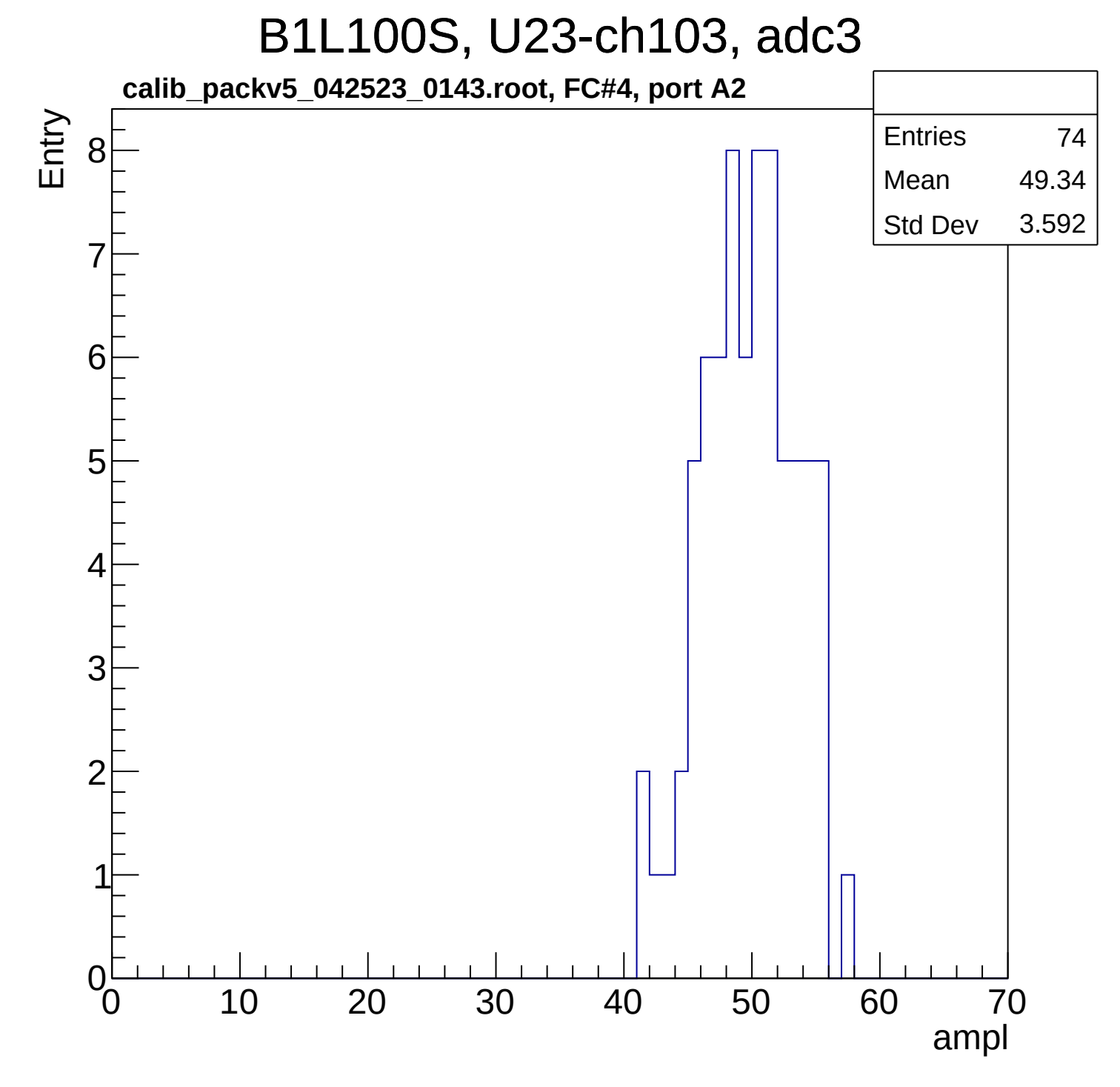
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch103, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.34
Std Dev 3.592
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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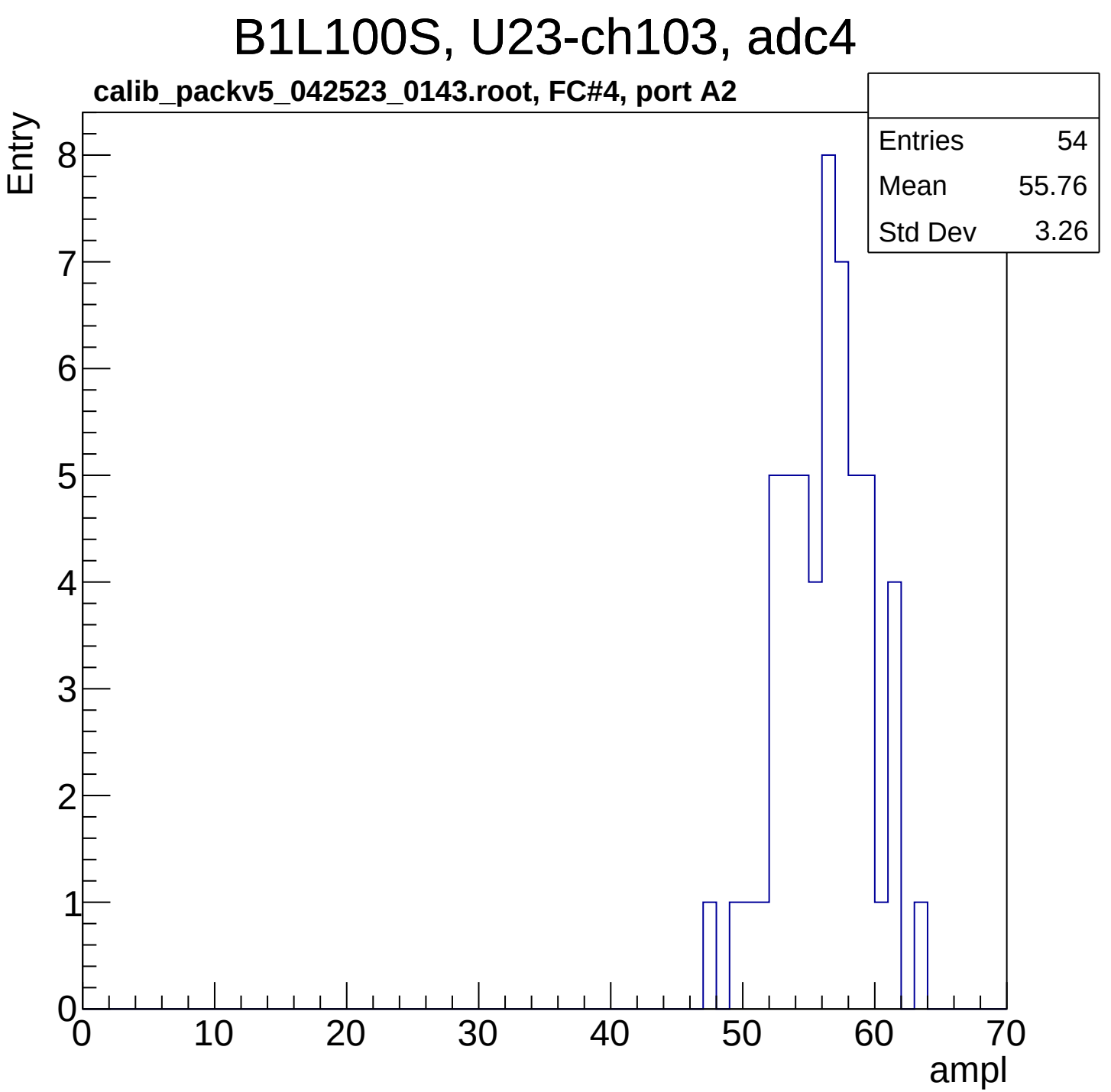
60
70


B1L100S, U23-ch103, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.76
Std Dev 3.26
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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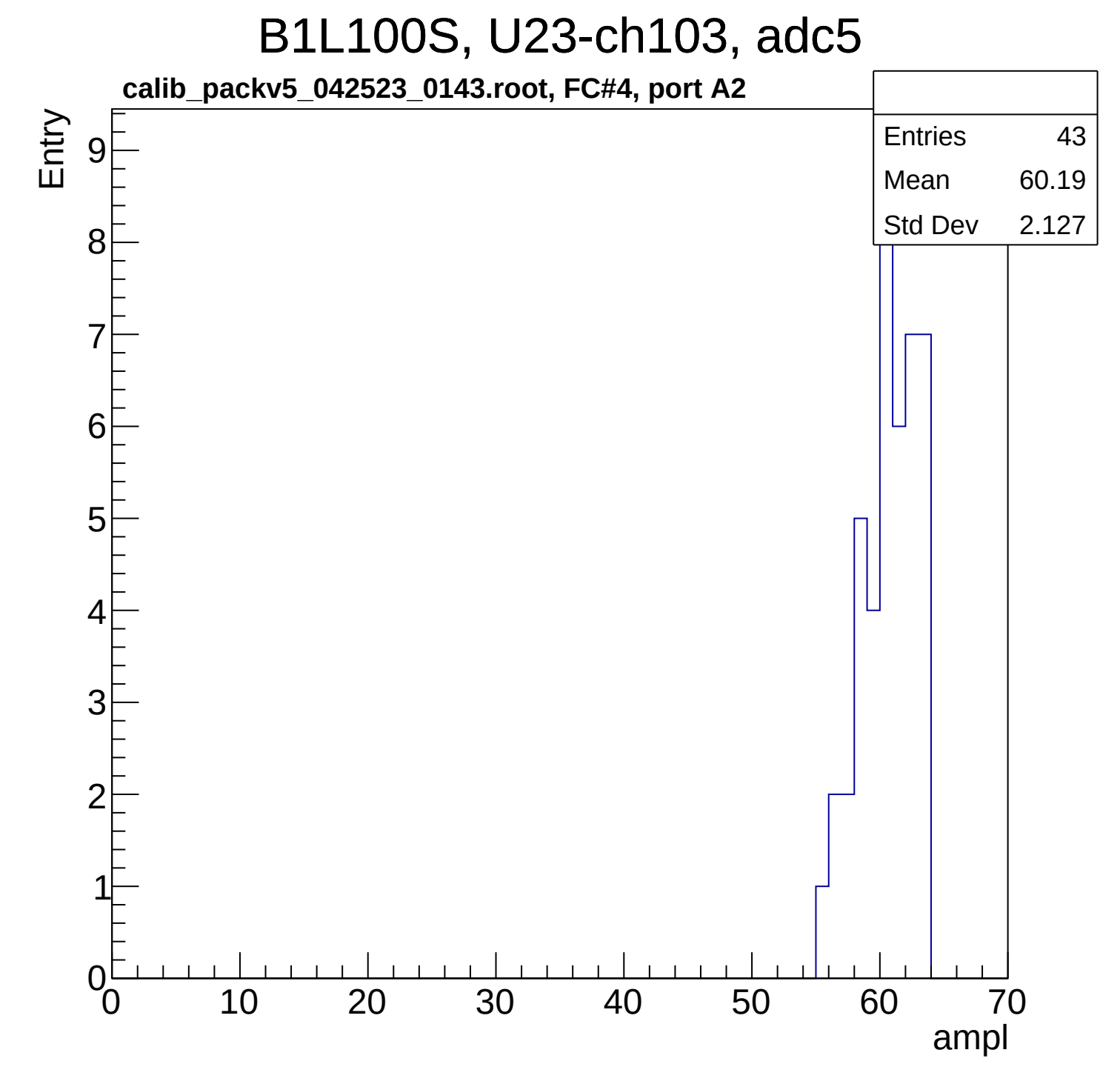
B1L100S, U23-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 60.19
Std Dev 2.127
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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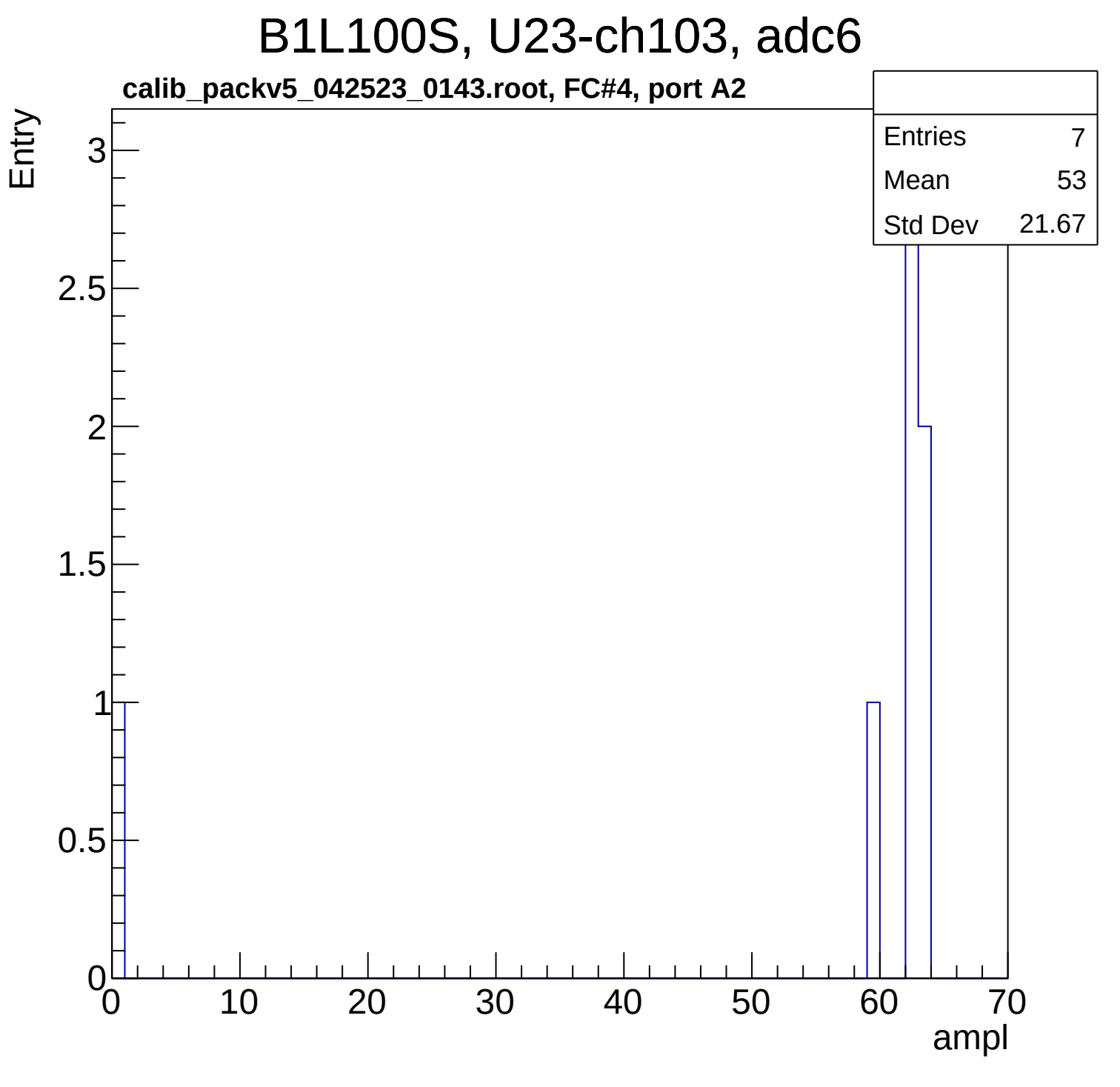
B1L100S, U23-ch103, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53
Std Dev 21.67
Entry
ampl
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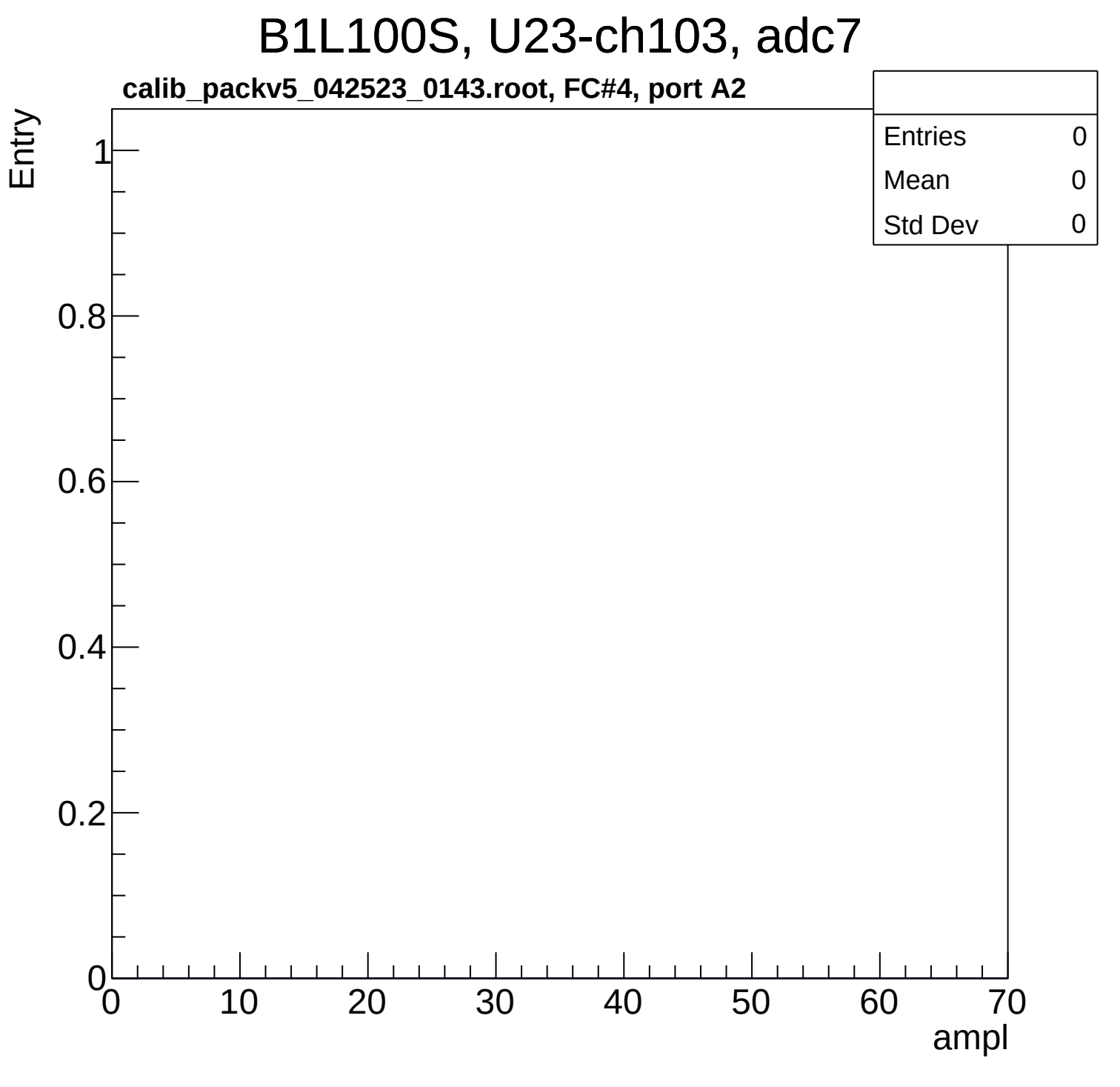
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch103, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
20
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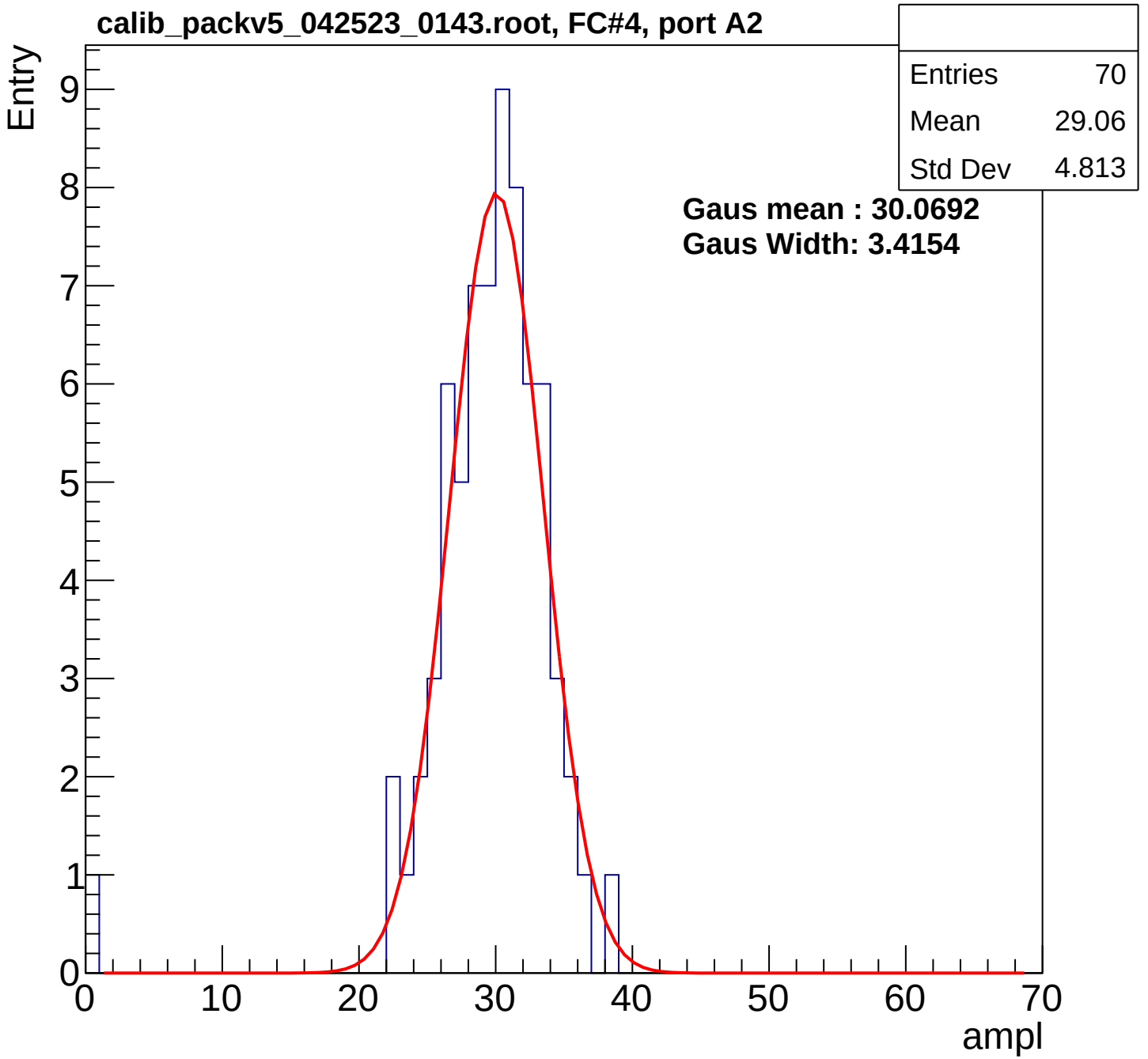
40
50
60
70
ampl


B1L100S, U23-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 29.06
Std Dev 4.813
Gaus mean : 30.0692
Gaus Width: 3.4154
Entry
ampl
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2
3
4
5
6
7
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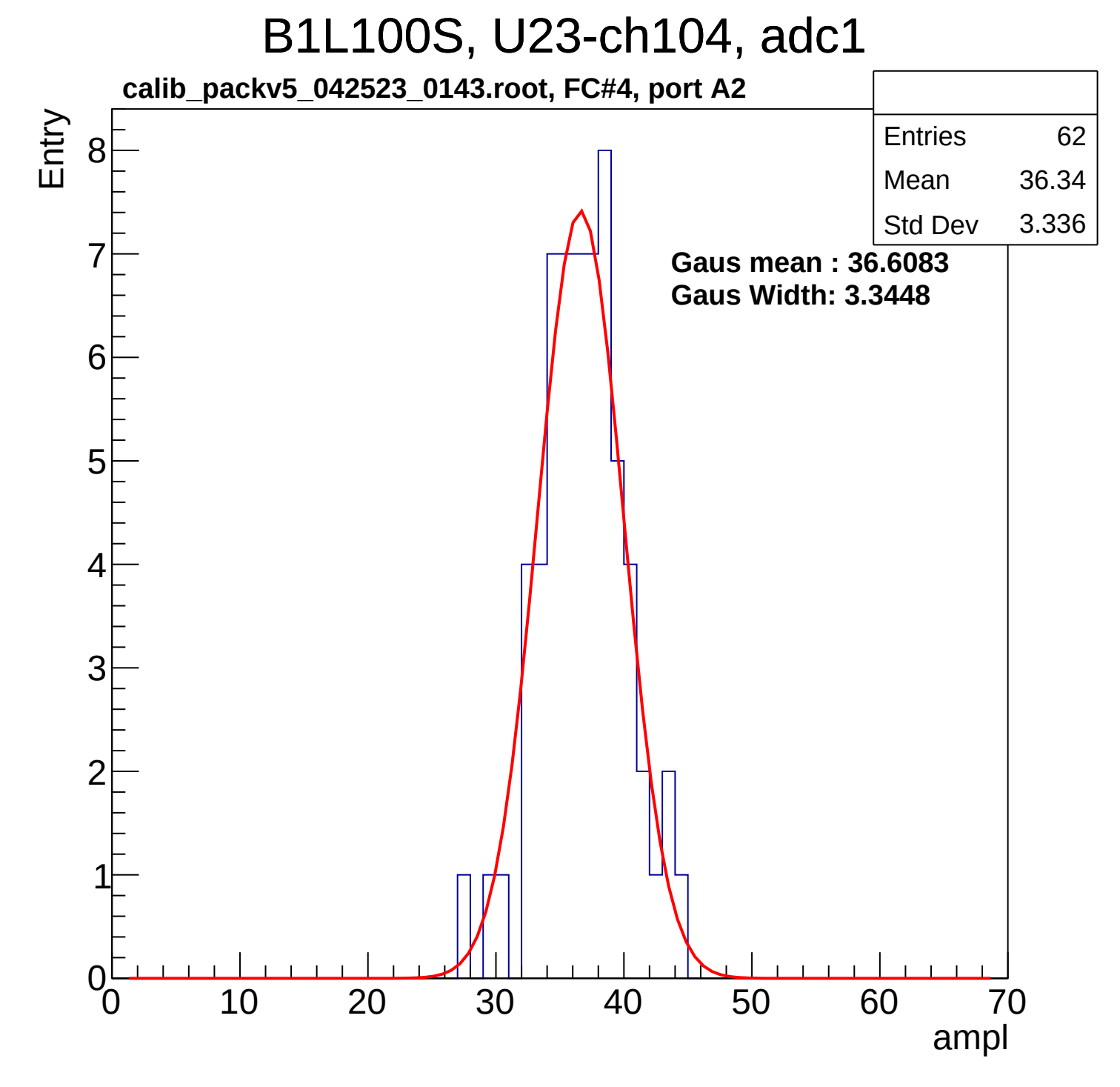
B1L100S, U23-ch104, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.34
Std Dev 3.336
Gaus mean : 36.6083
Gaus Width: 3.3448
Entry
ampl
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3
4
5
6
7
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0
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60
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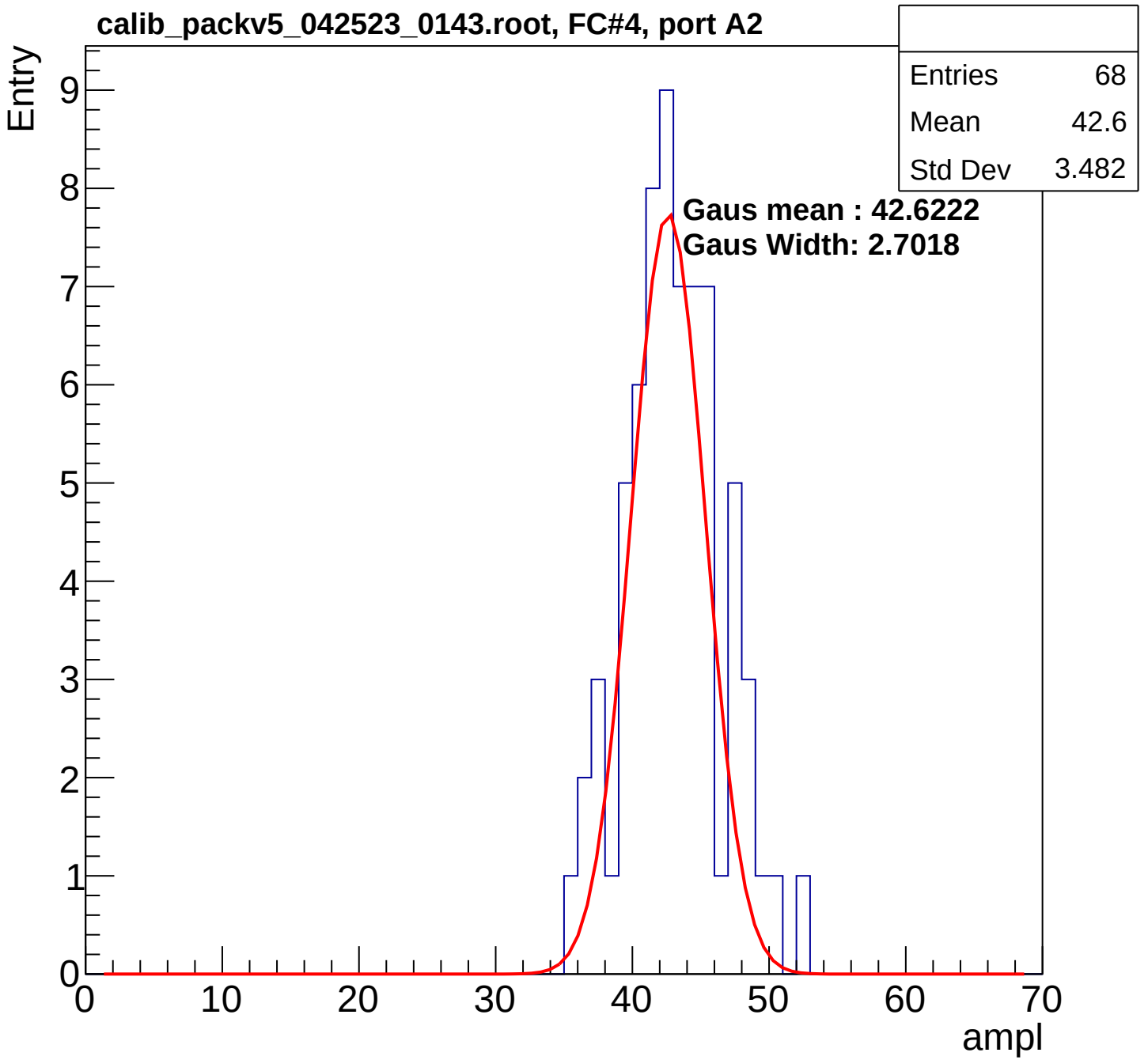
B1L100S, U23-ch104, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.6
Std Dev 3.482
Gaus mean : 42.6222
Gaus Width: 2.7018
Entry
ampl
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2
3
4
5
6
7
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0
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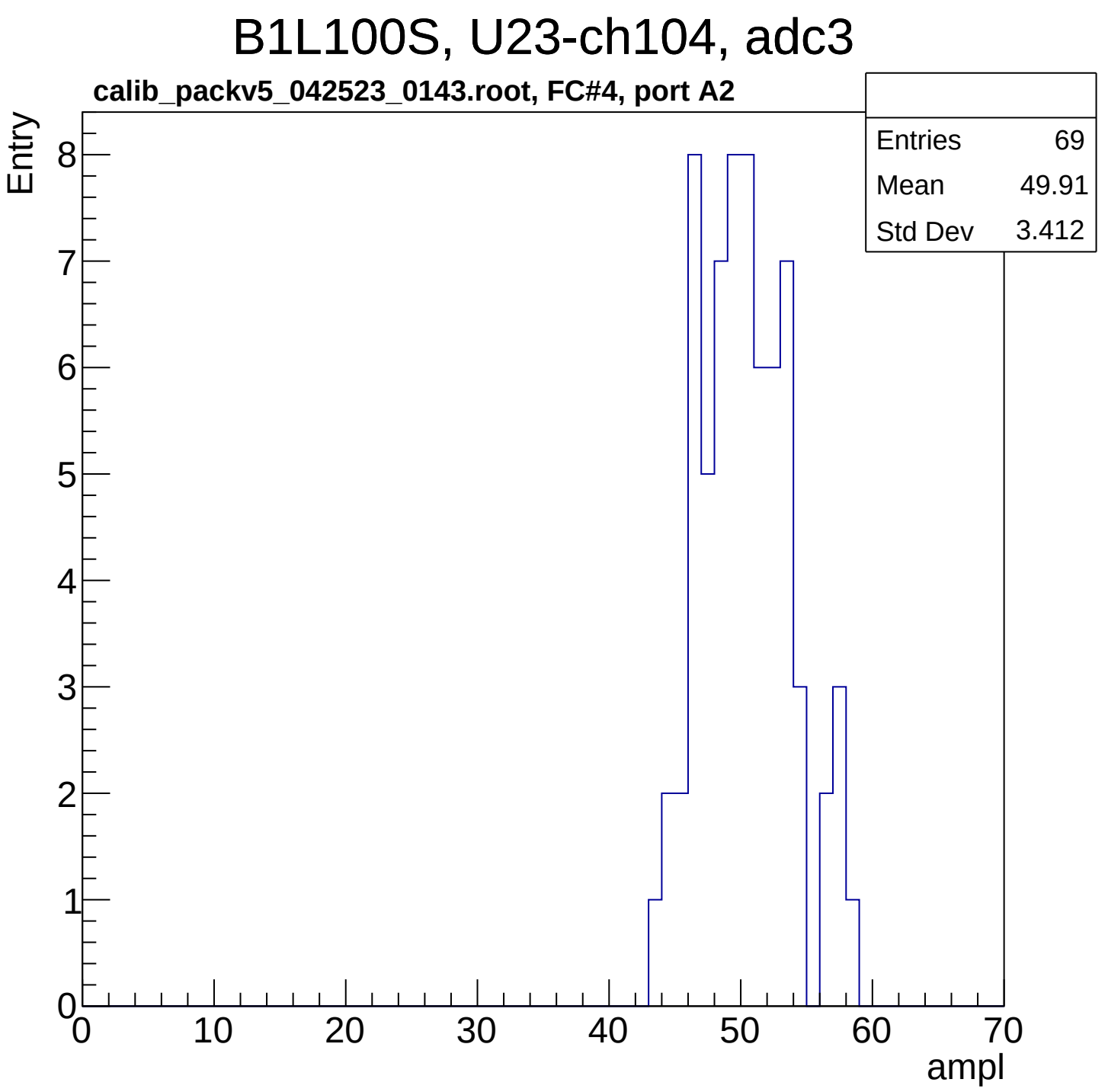
B1L100S, U23-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.91
Std Dev 3.412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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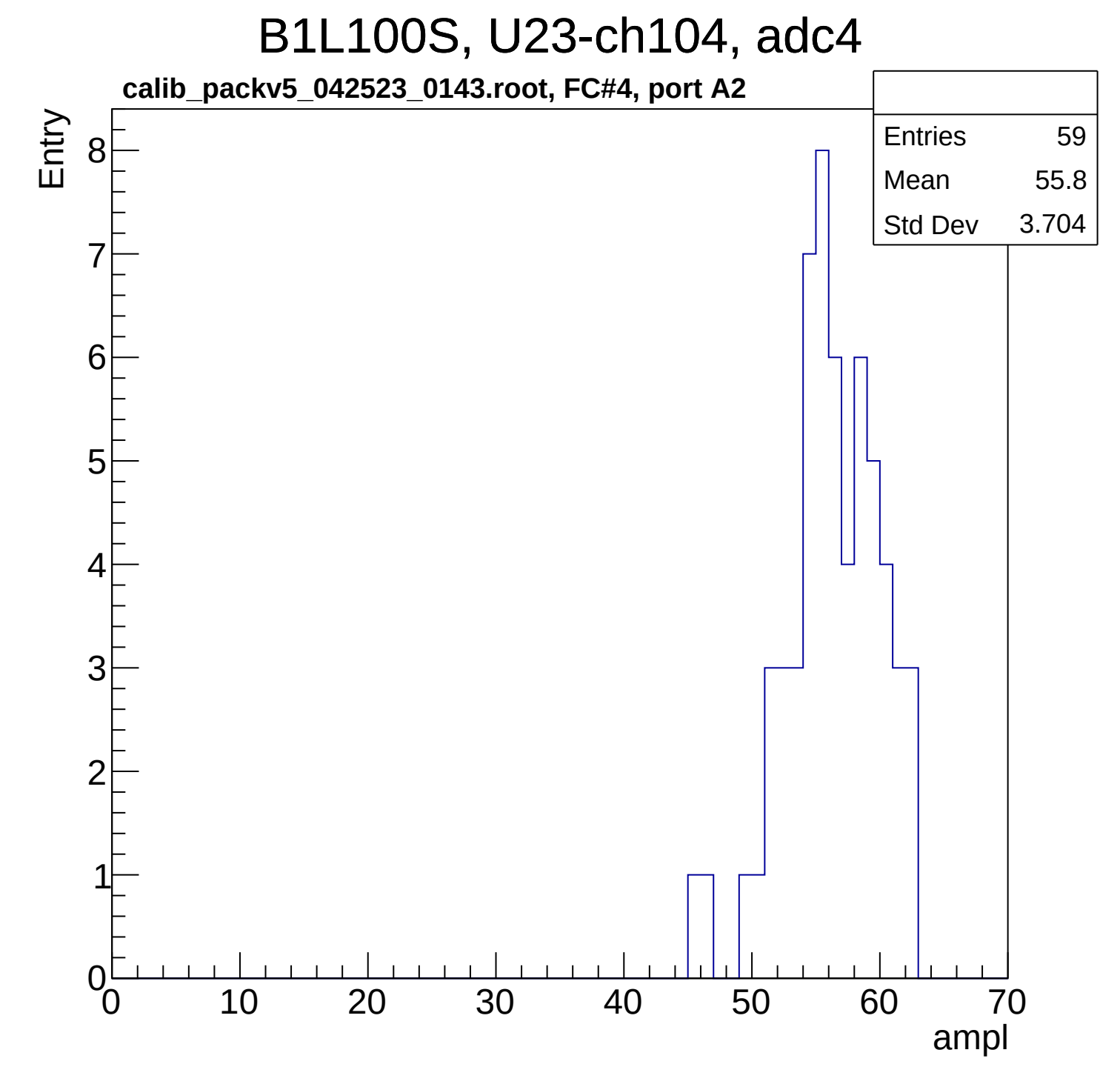
B1L100S, U23-ch104, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.8
Std Dev 3.704
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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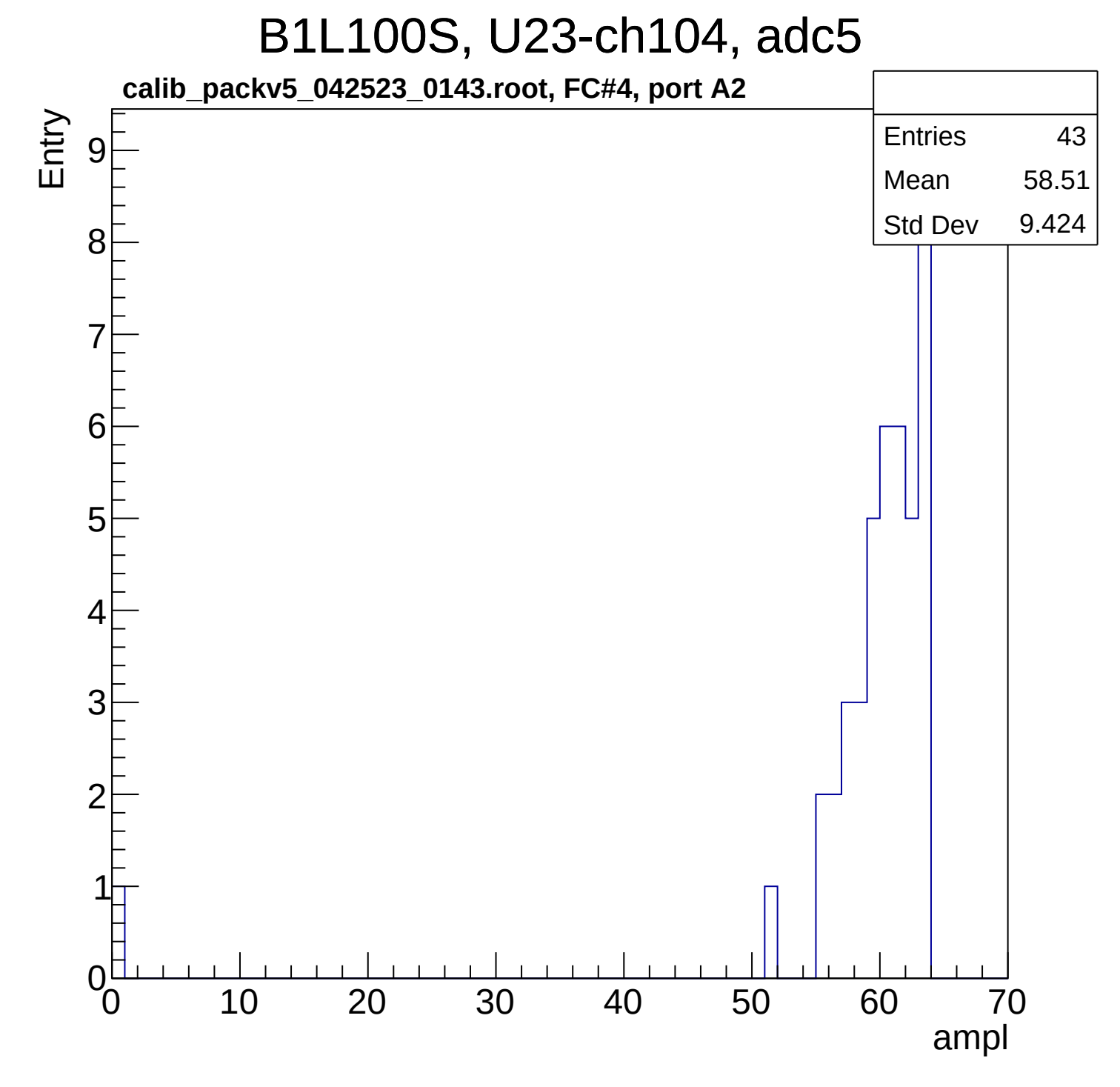
B1L100S, U23-ch104, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.51
Std Dev 9.424
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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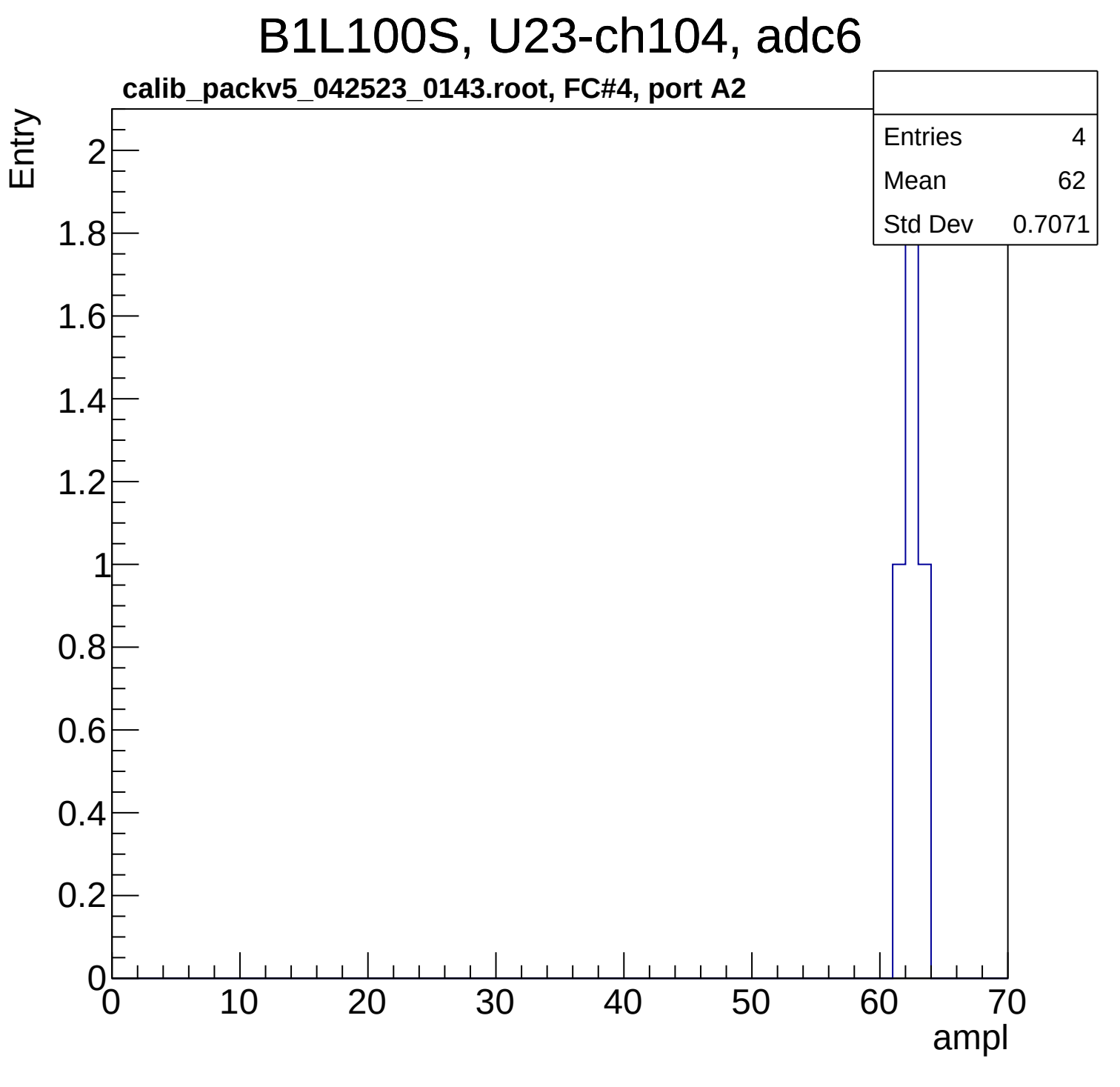
B1L100S, U23-ch104, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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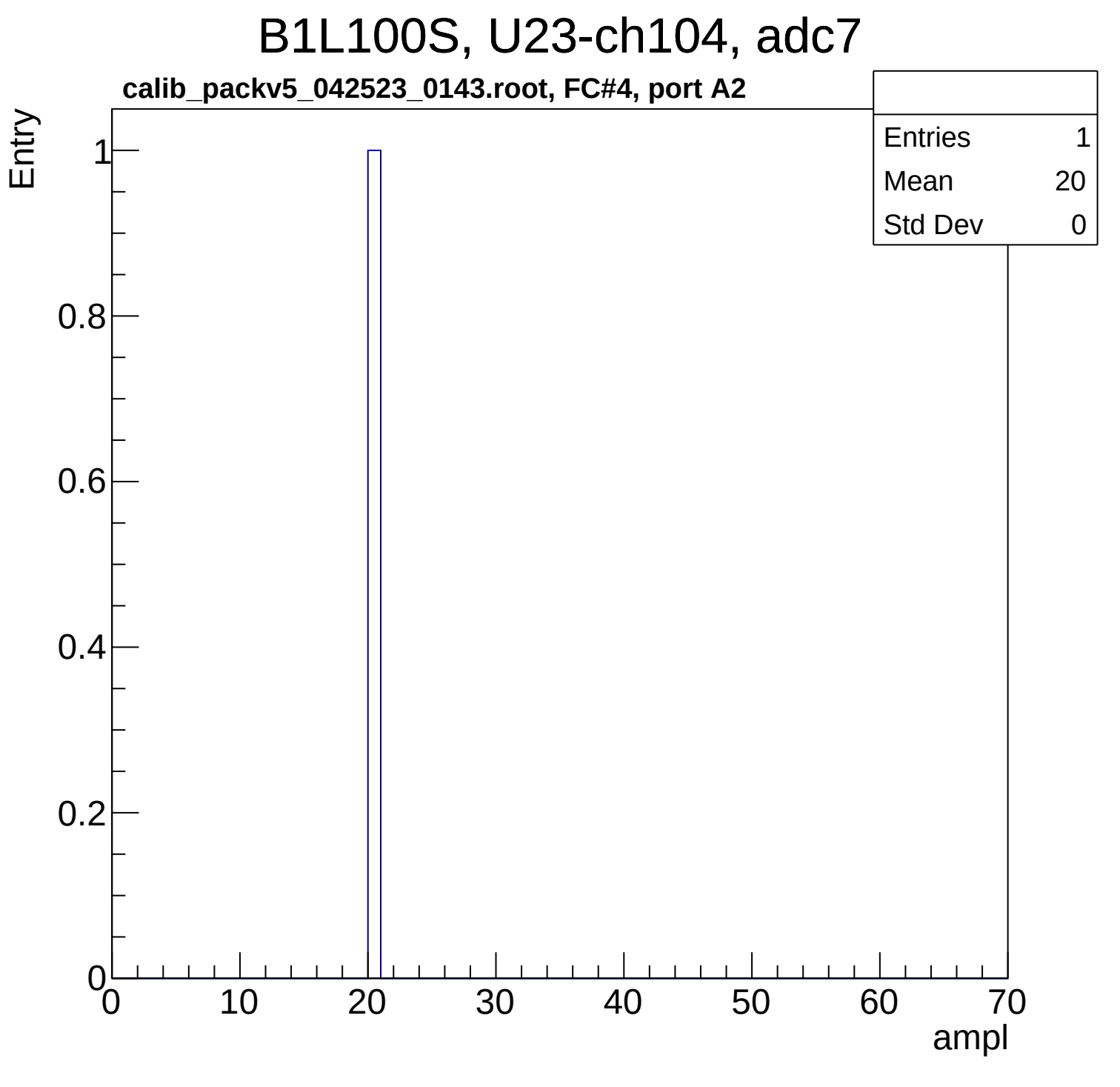
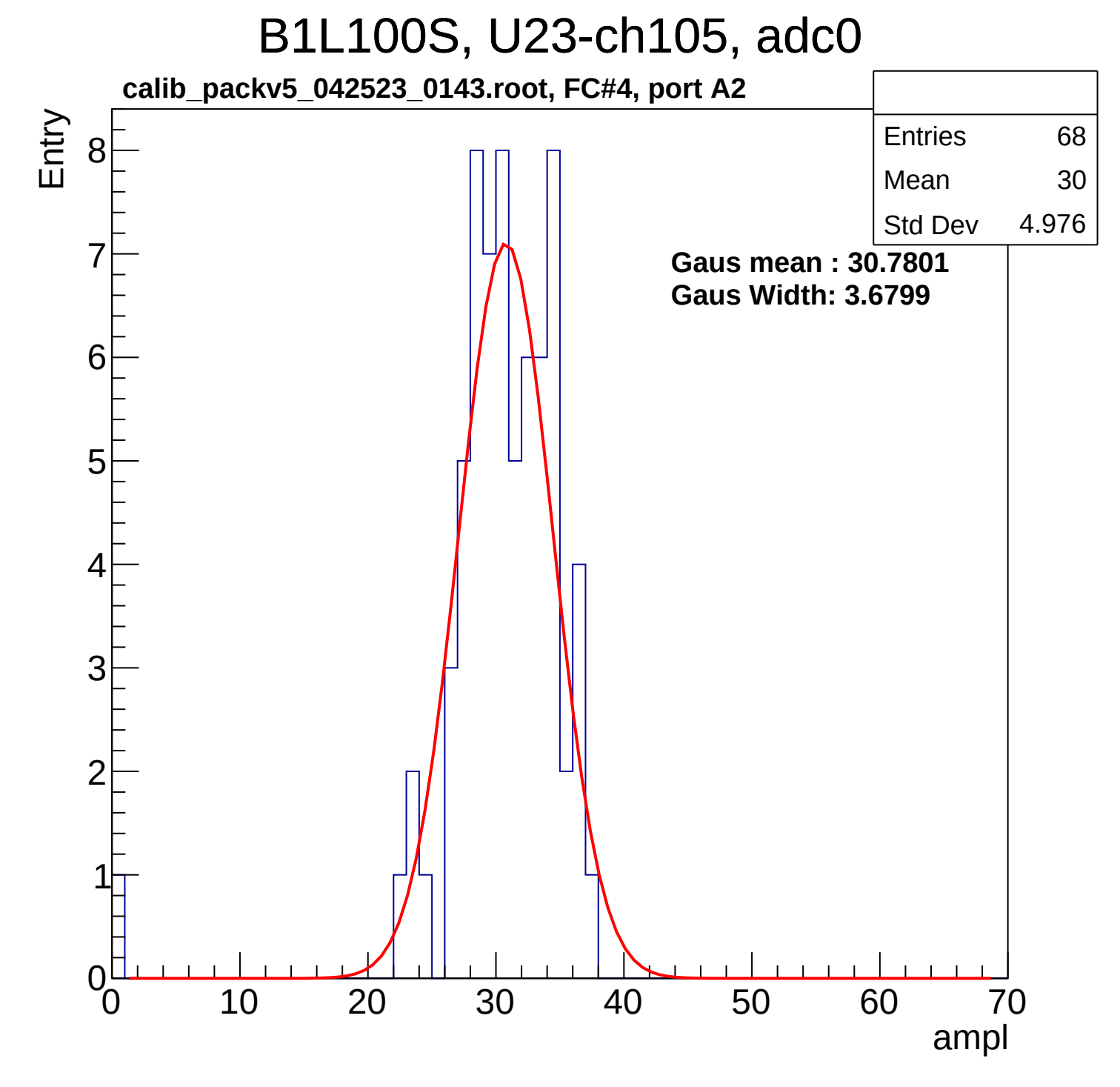

B1L100S, U23-ch104, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 30
Std Dev 4.976
Gaus mean : 30.7801
Gaus Width: 3.6799
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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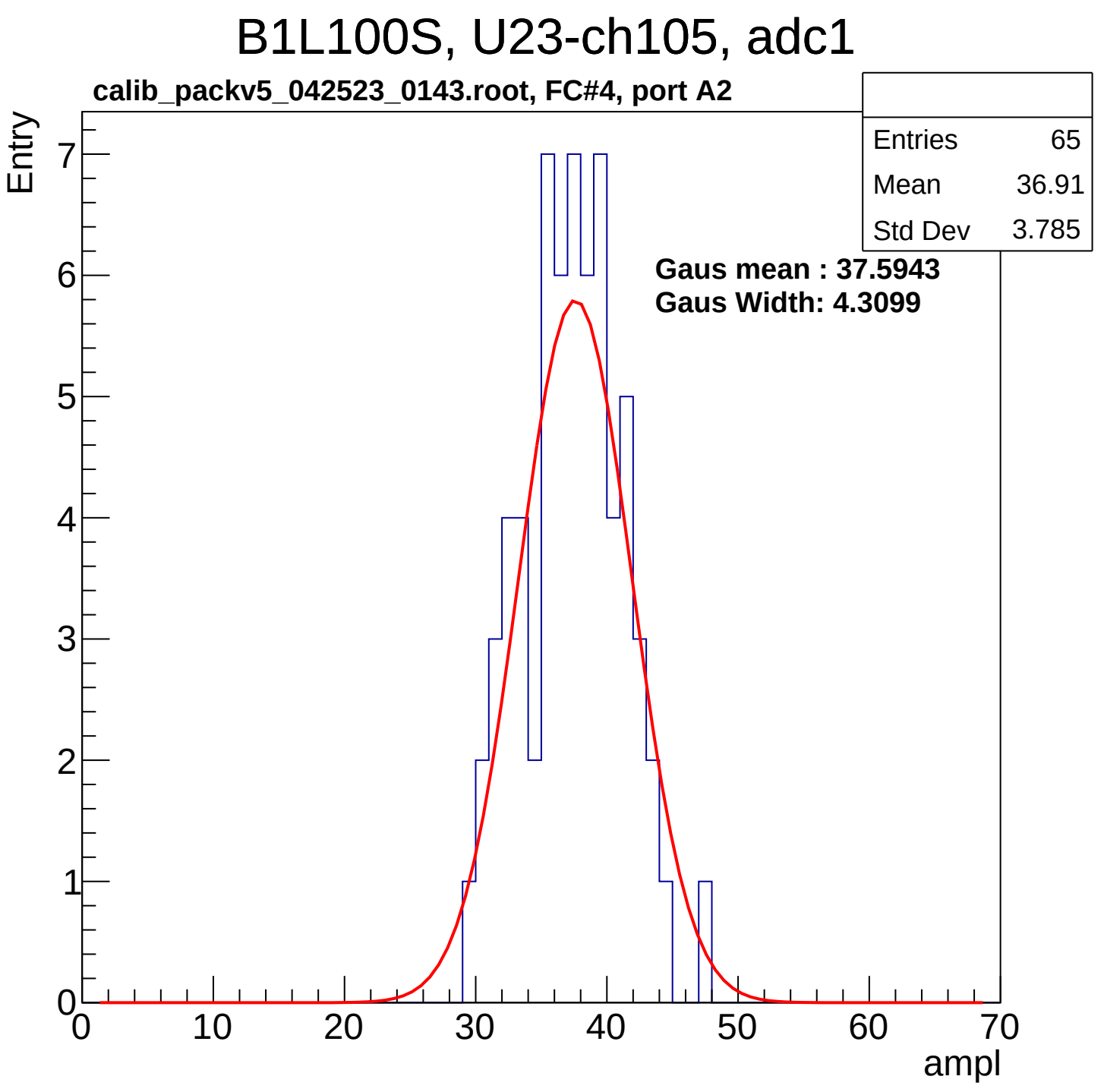
B1L100S, U23-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.91
Std Dev 3.785
Gaus mean : 37.5943
Gaus Width: 4.3099
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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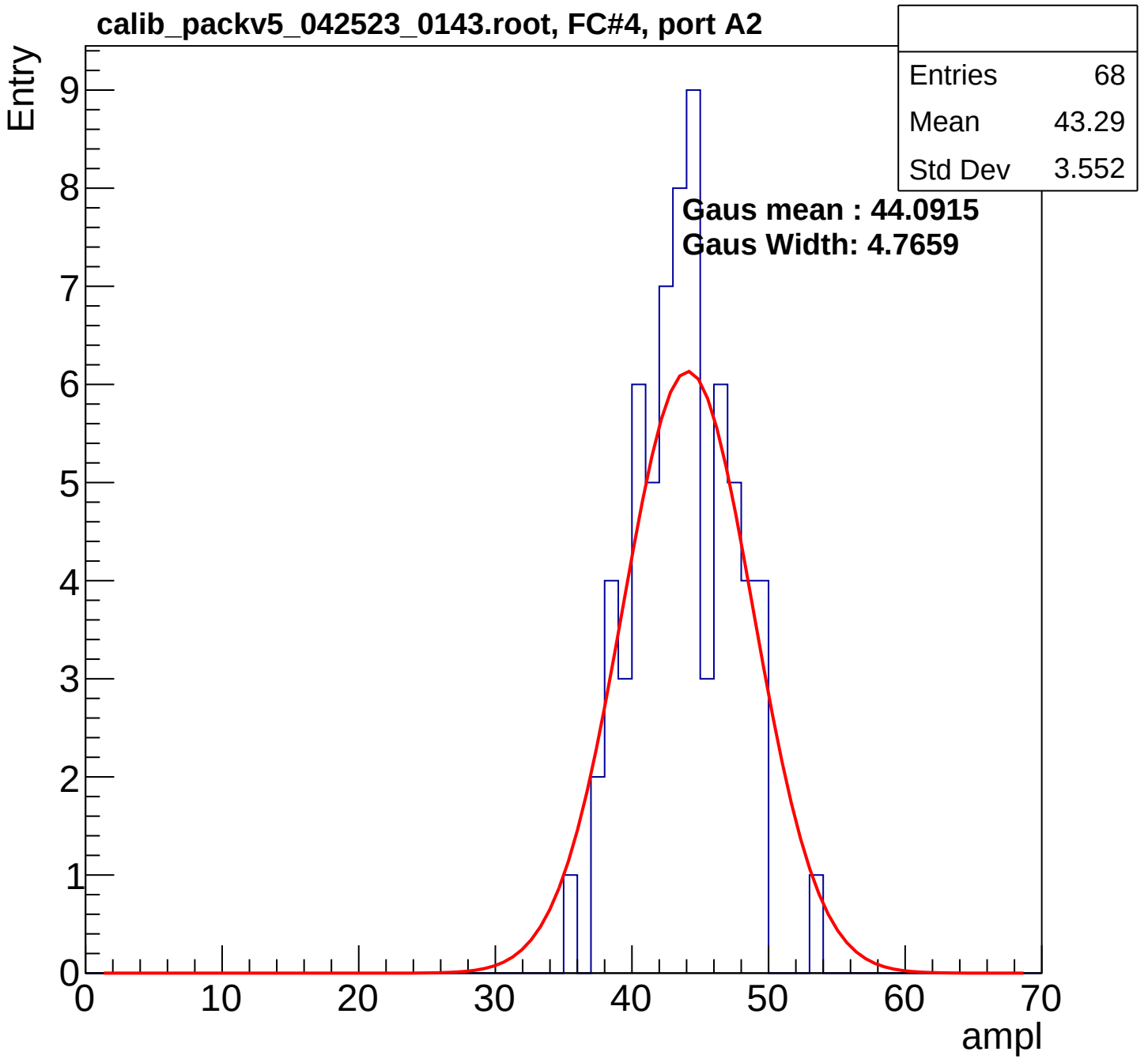
B1L100S, U23-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.29
Std Dev 3.552
Gaus mean : 44.0915
Gaus Width: 4.7659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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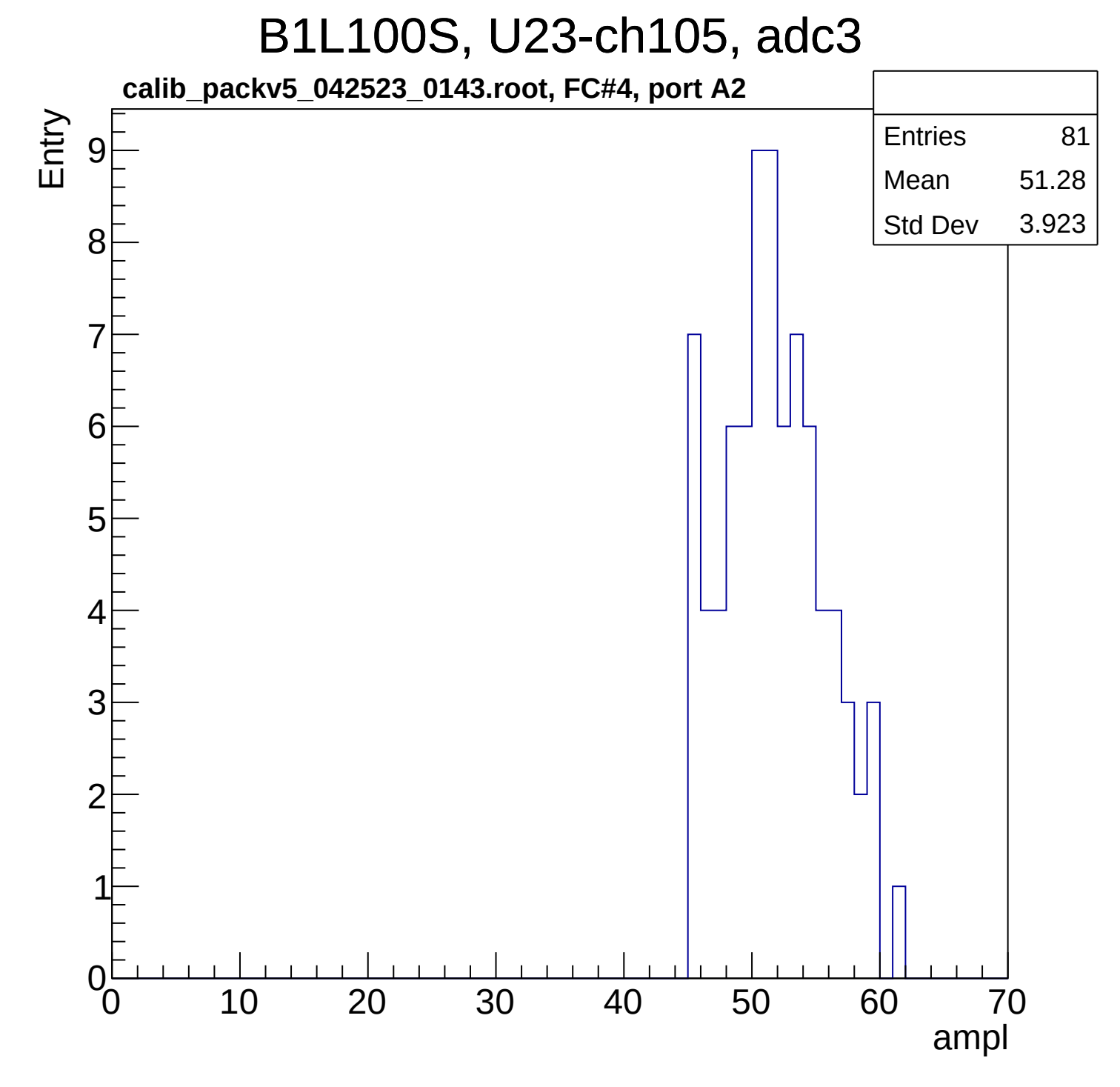
B1L100S, U23-ch105, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 51.28
Std Dev 3.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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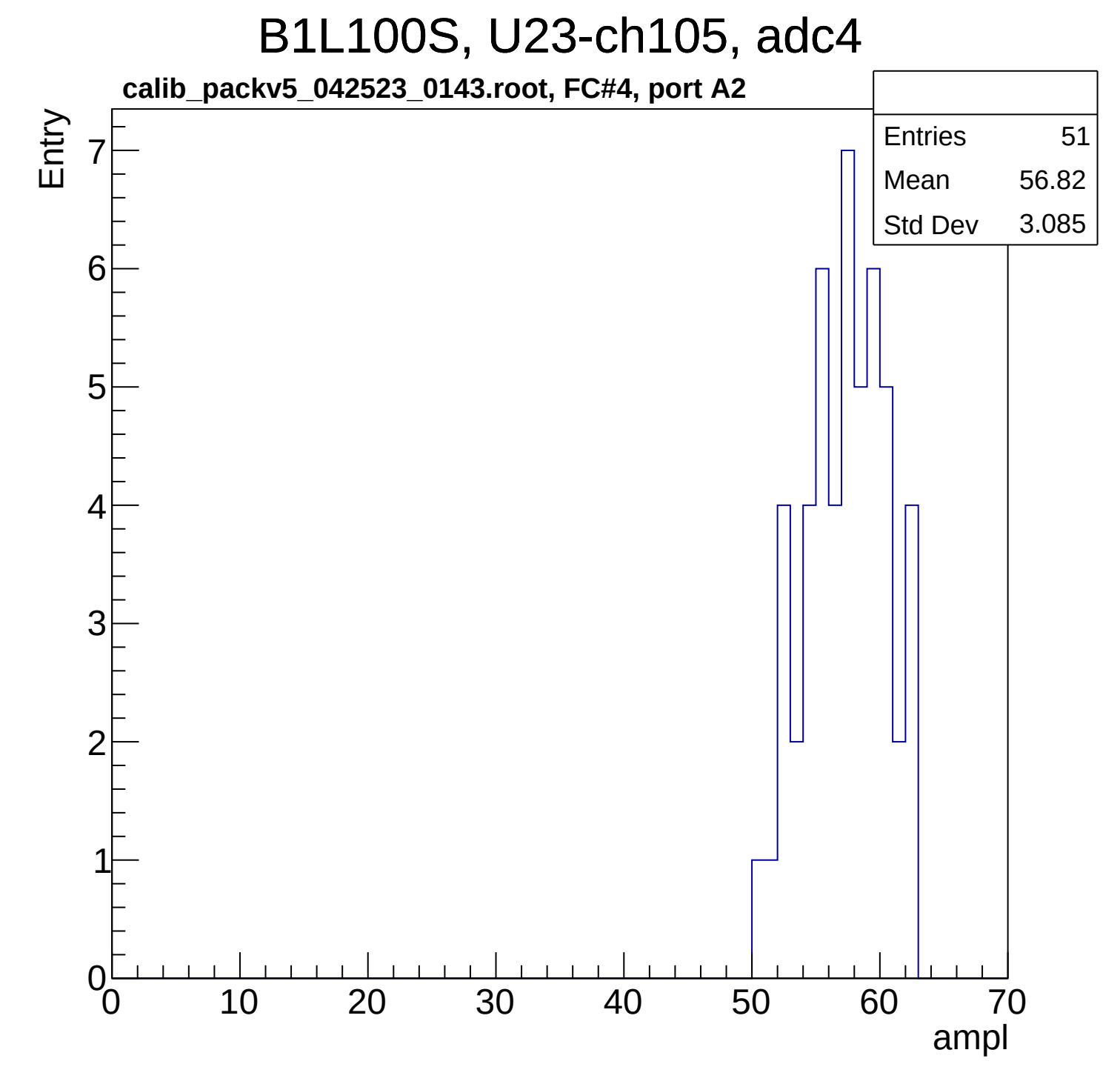
B1L100S, U23-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.82
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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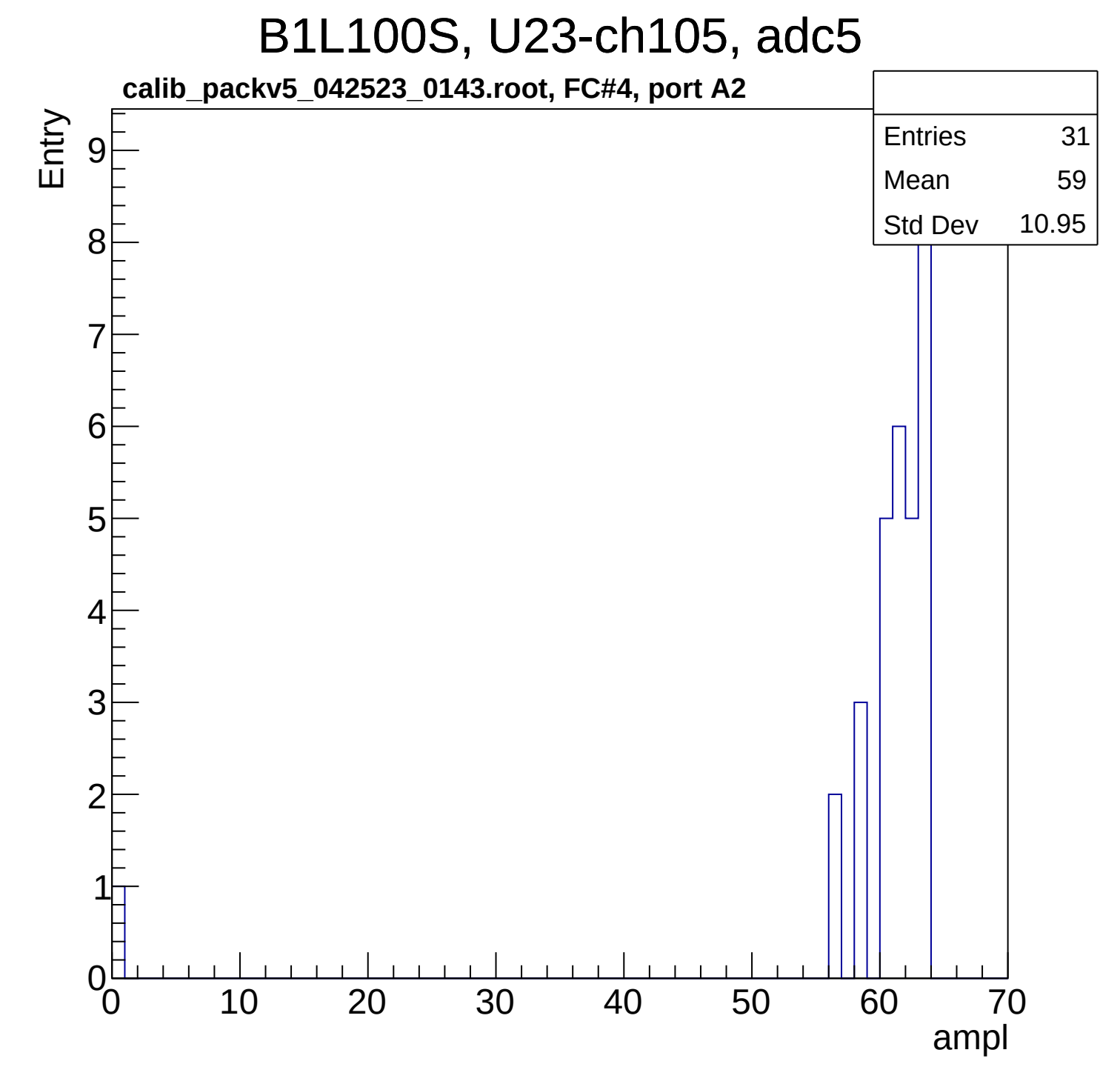
B1L100S, U23-ch105, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59
Std Dev 10.95
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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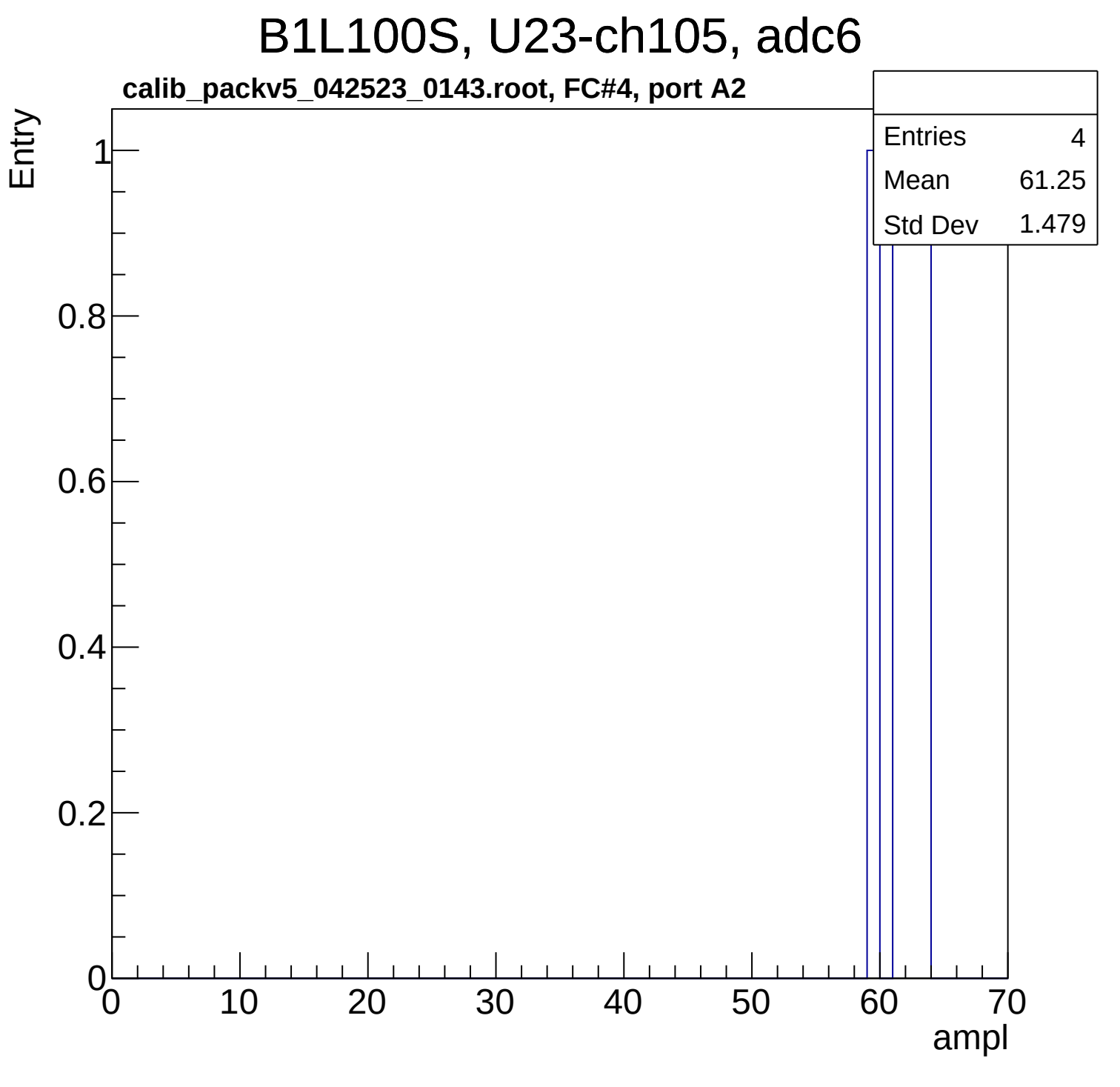
B1L100S, U23-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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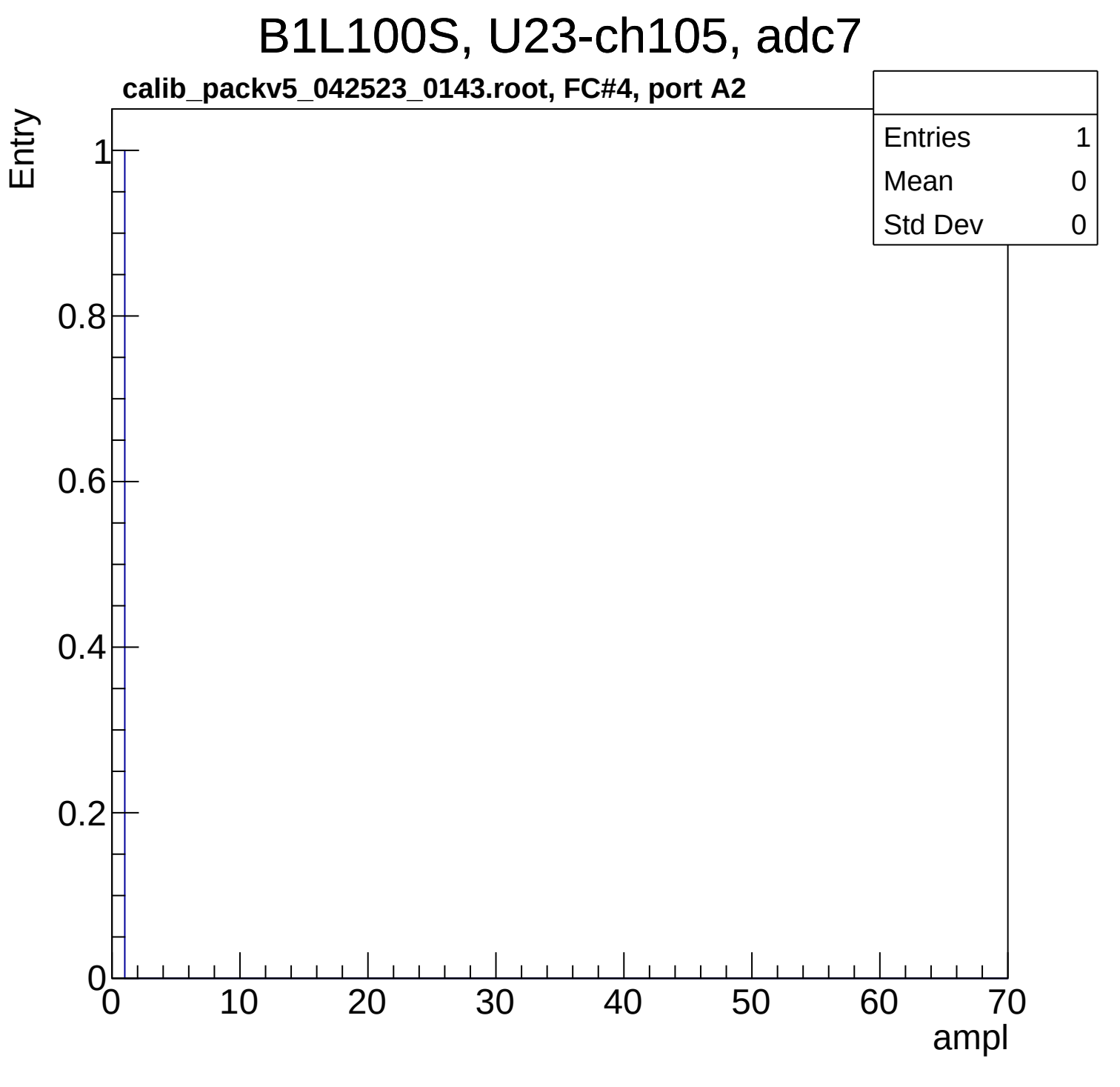
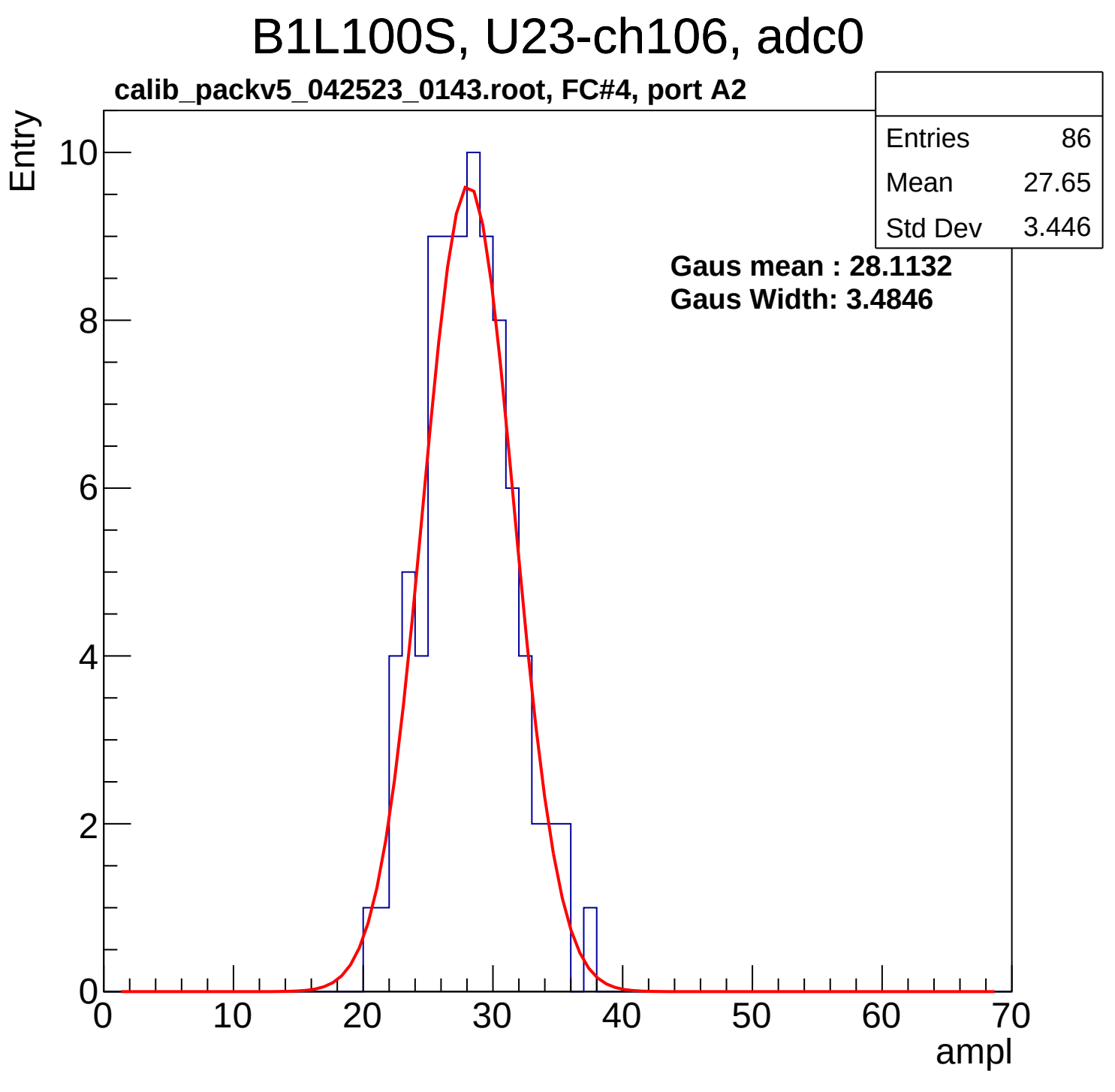
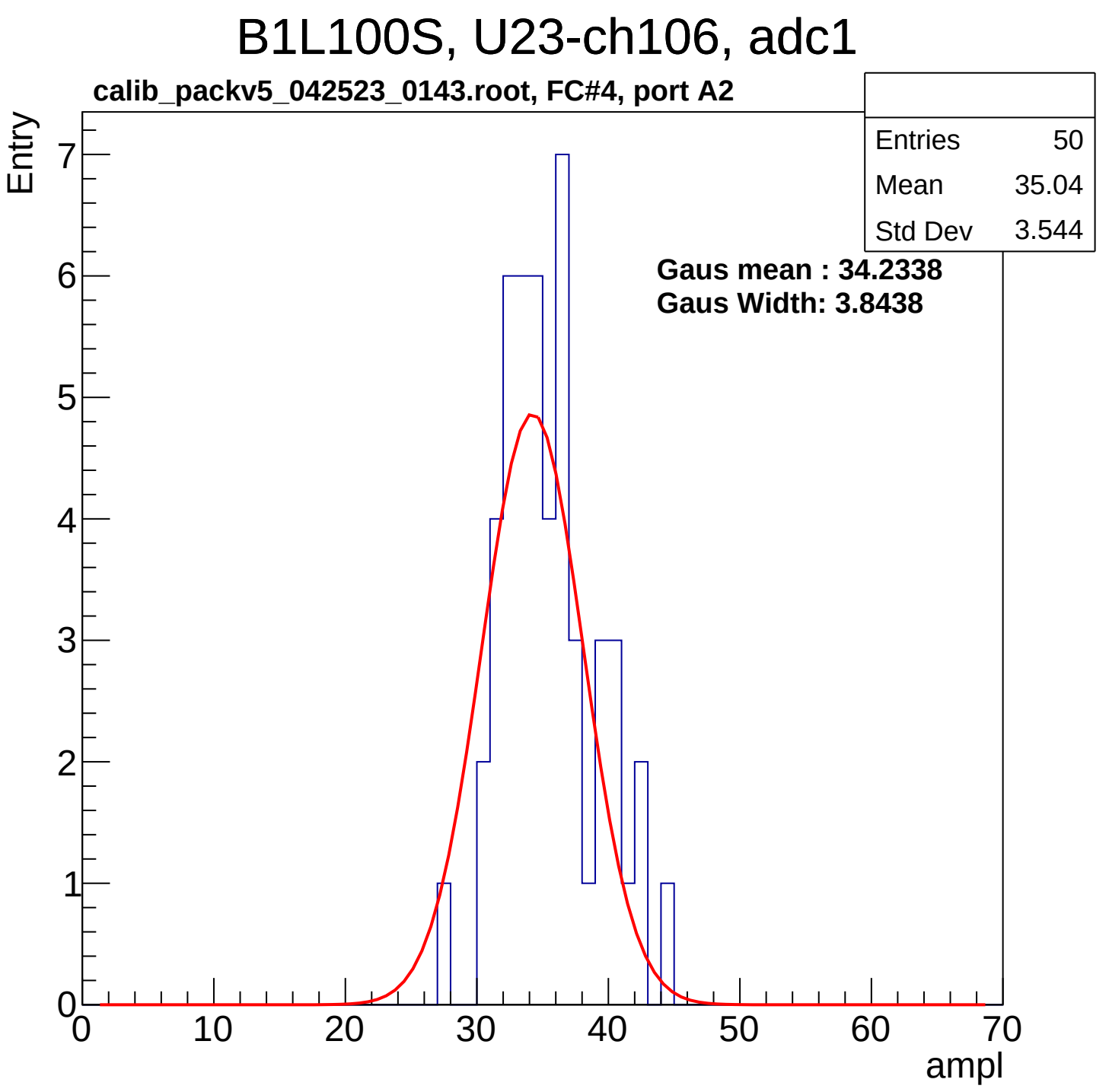
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U23-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 27.65
Std Dev 3.446
Gaus mean : 28.1132
Gaus Width: 3.4846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch106, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 35.04
Std Dev 3.544
Gaus mean : 34.2338
Gaus Width: 3.8438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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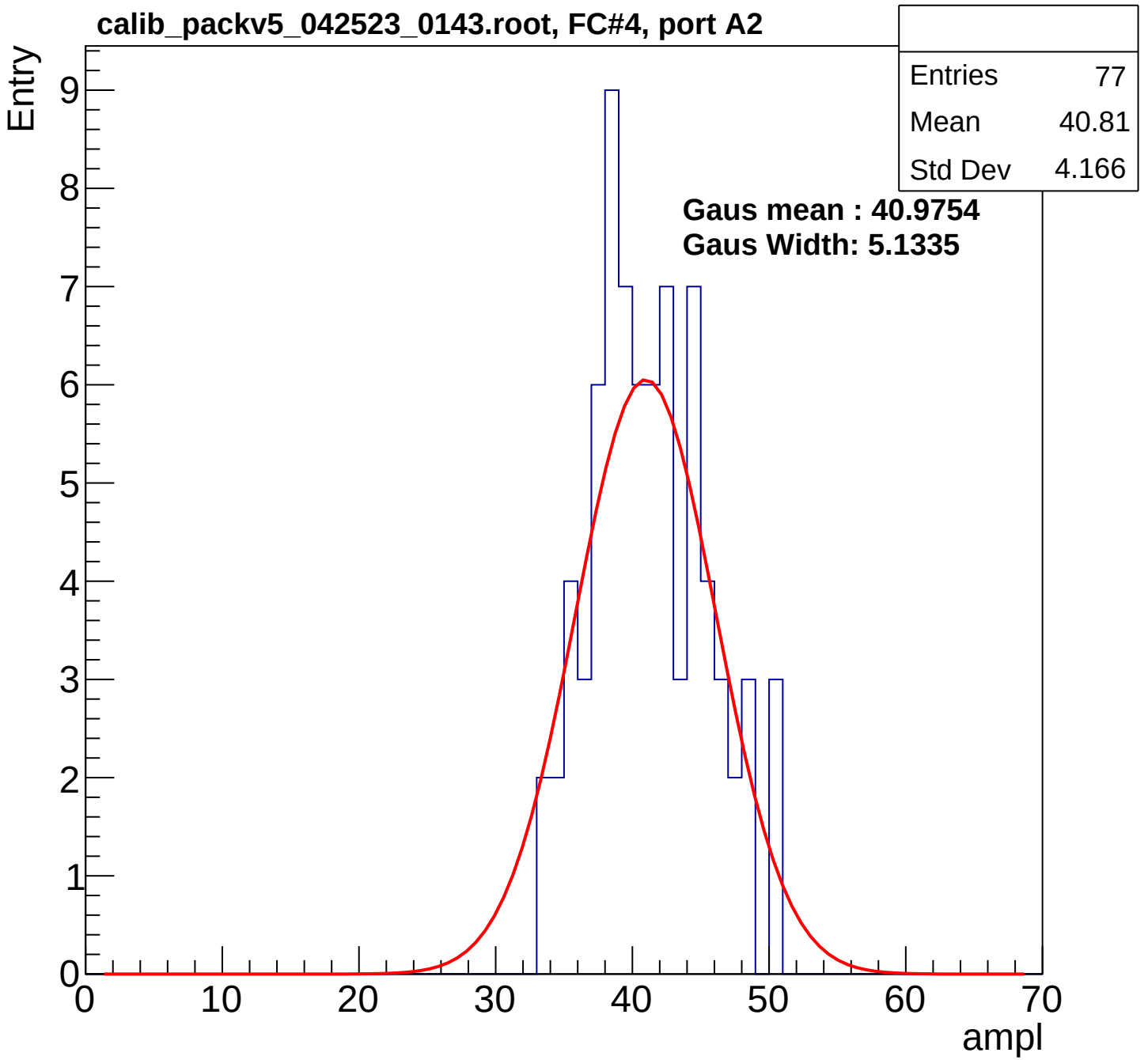
B1L100S, U23-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 40.81
Std Dev 4.166
Gaus mean : 40.9754
Gaus Width: 5.1335
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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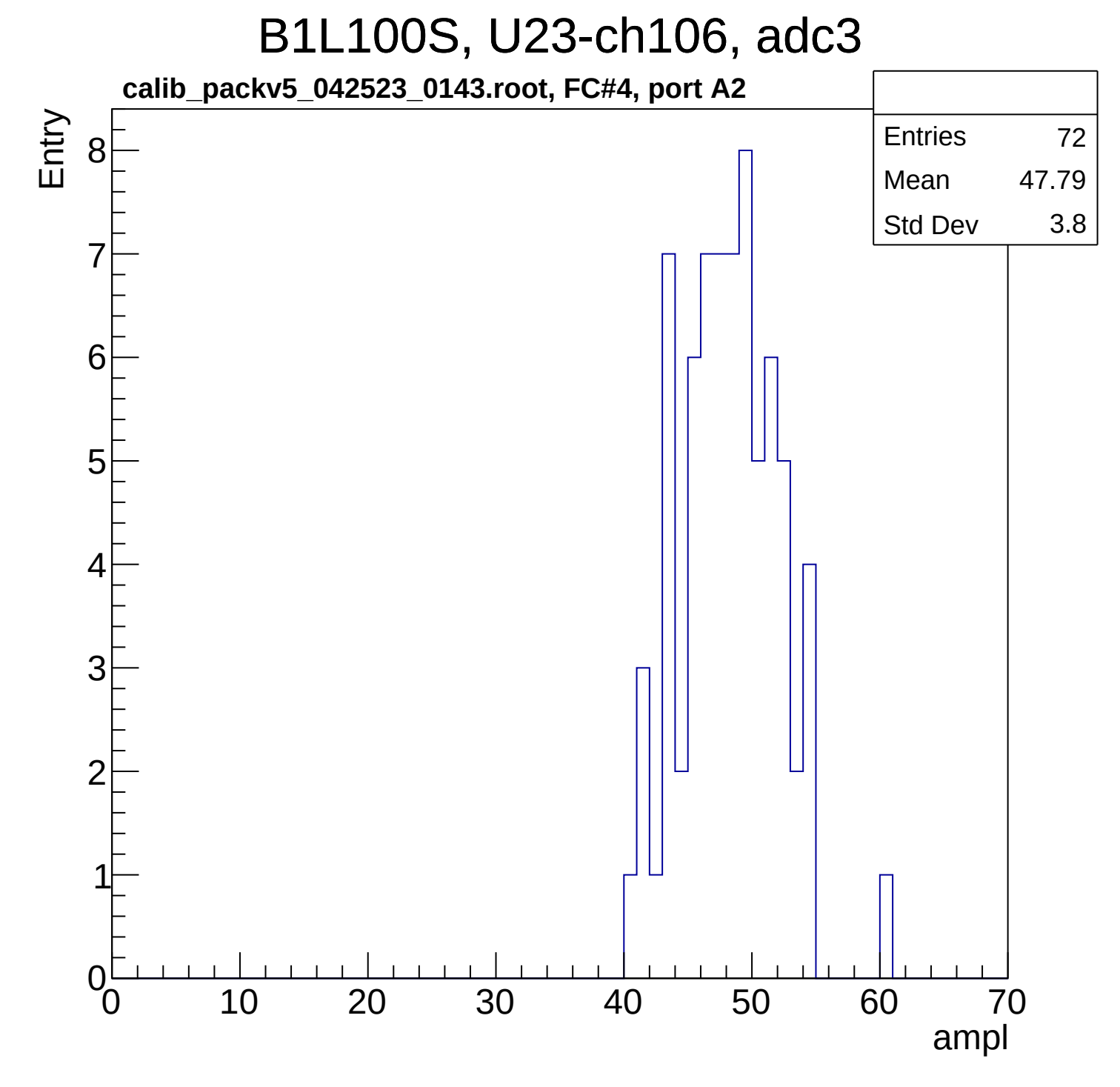
B1L100S, U23-ch106, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 47.79
Std Dev 3.8
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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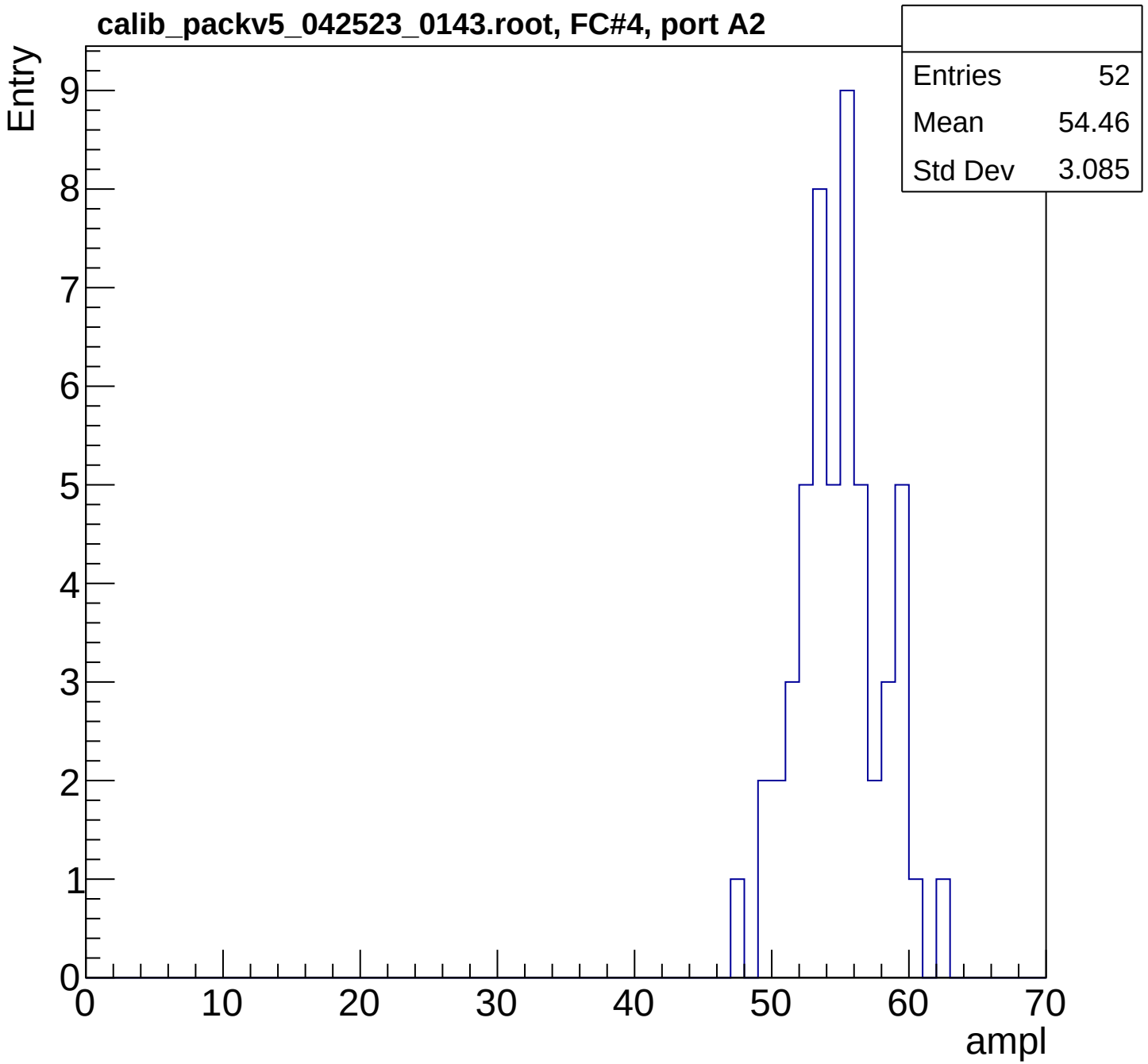
B1L100S, U23-ch106, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.46
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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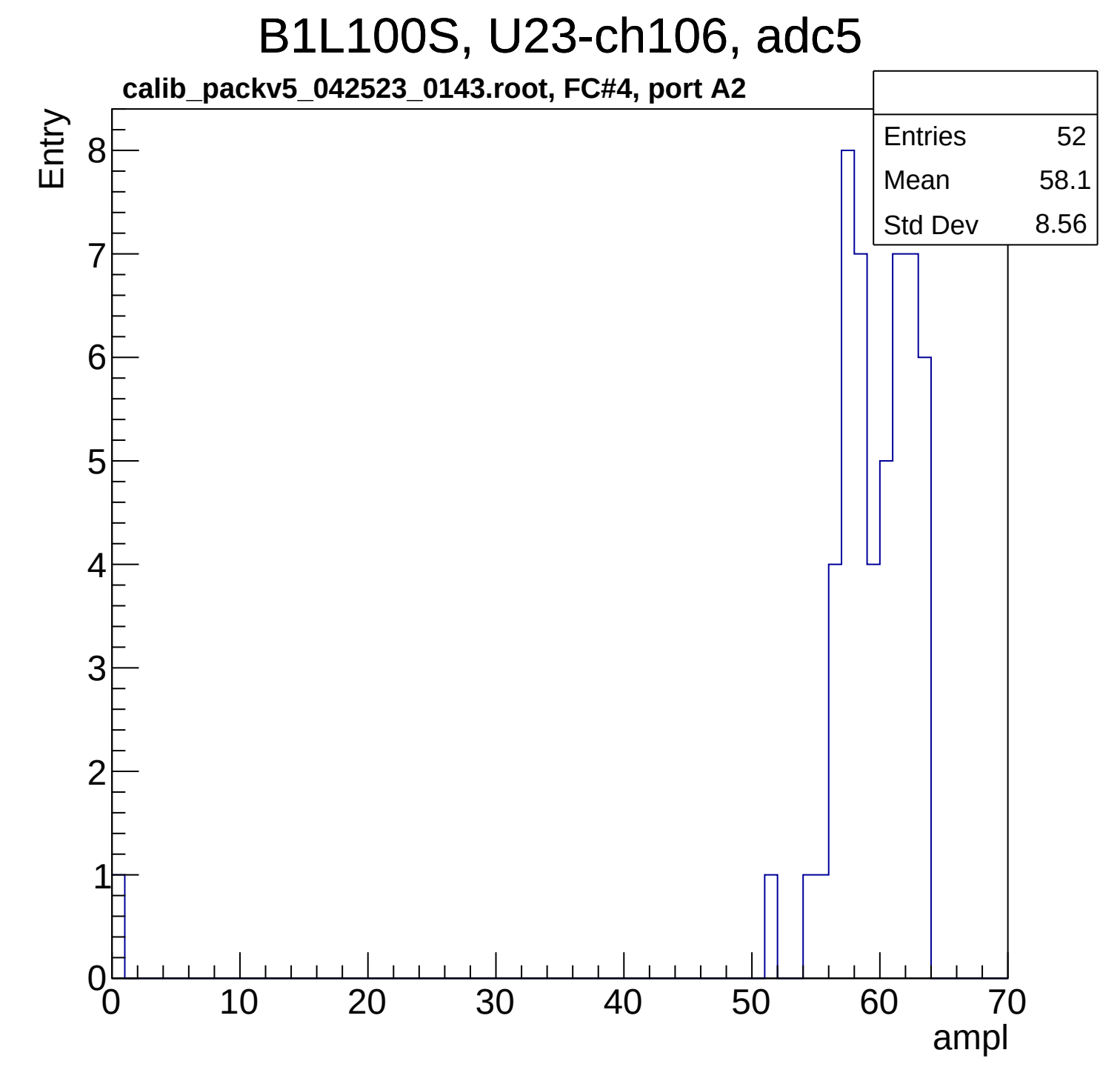
B1L100S, U23-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.1
Std Dev 8.56
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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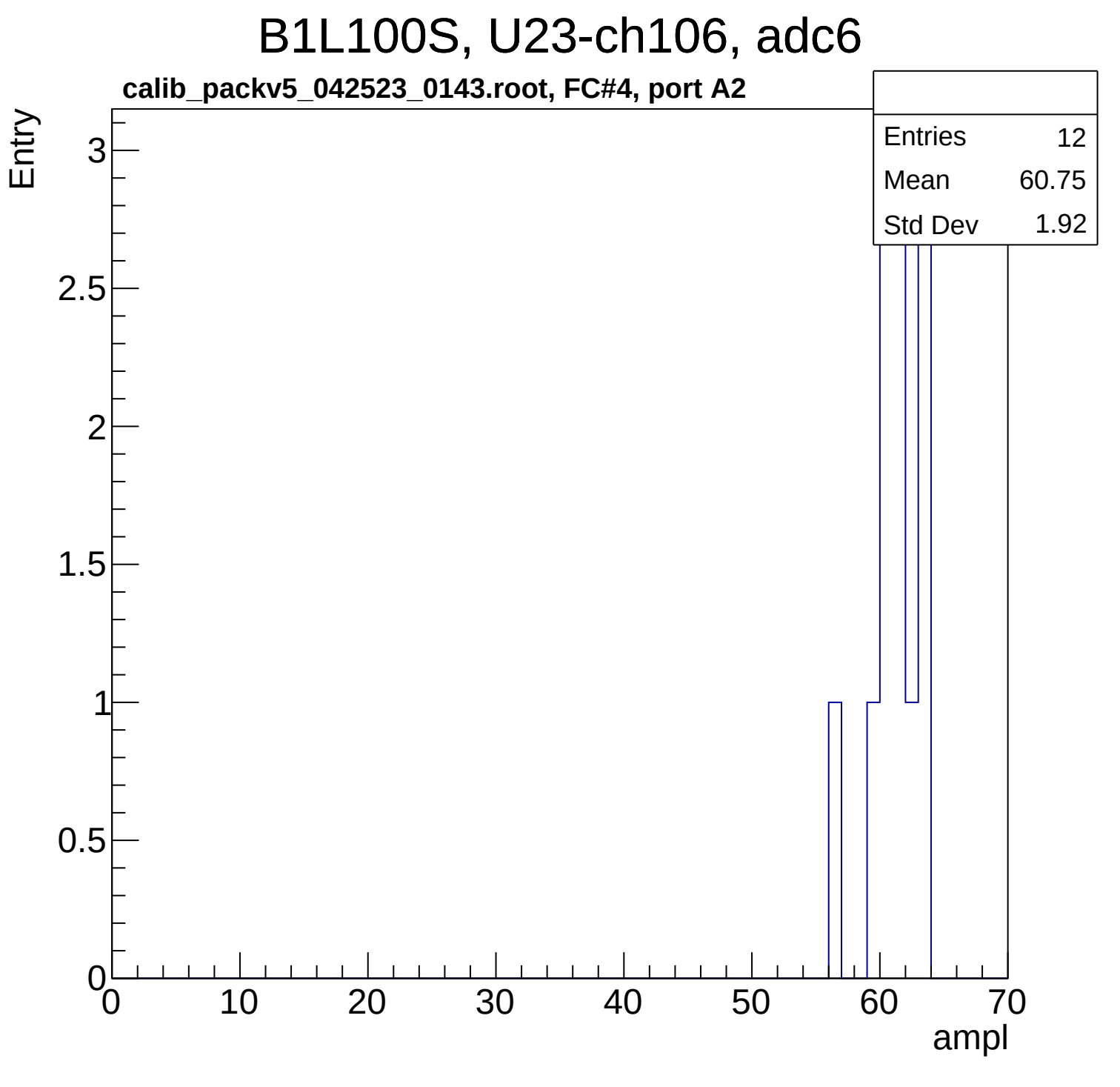
B1L100S, U23-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 60.75
Std Dev 1.92
Entry
ampl
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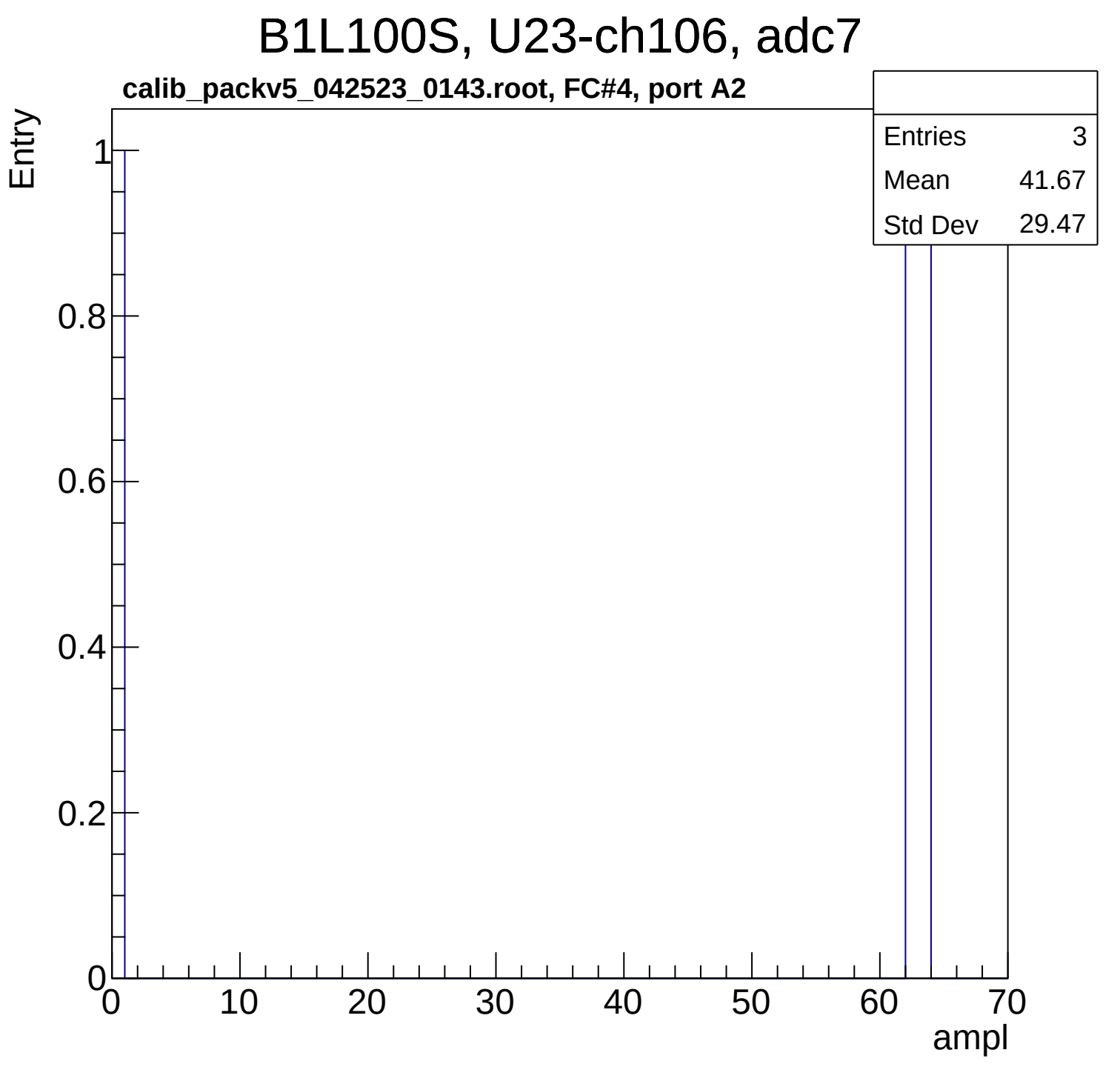
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
0.2
0
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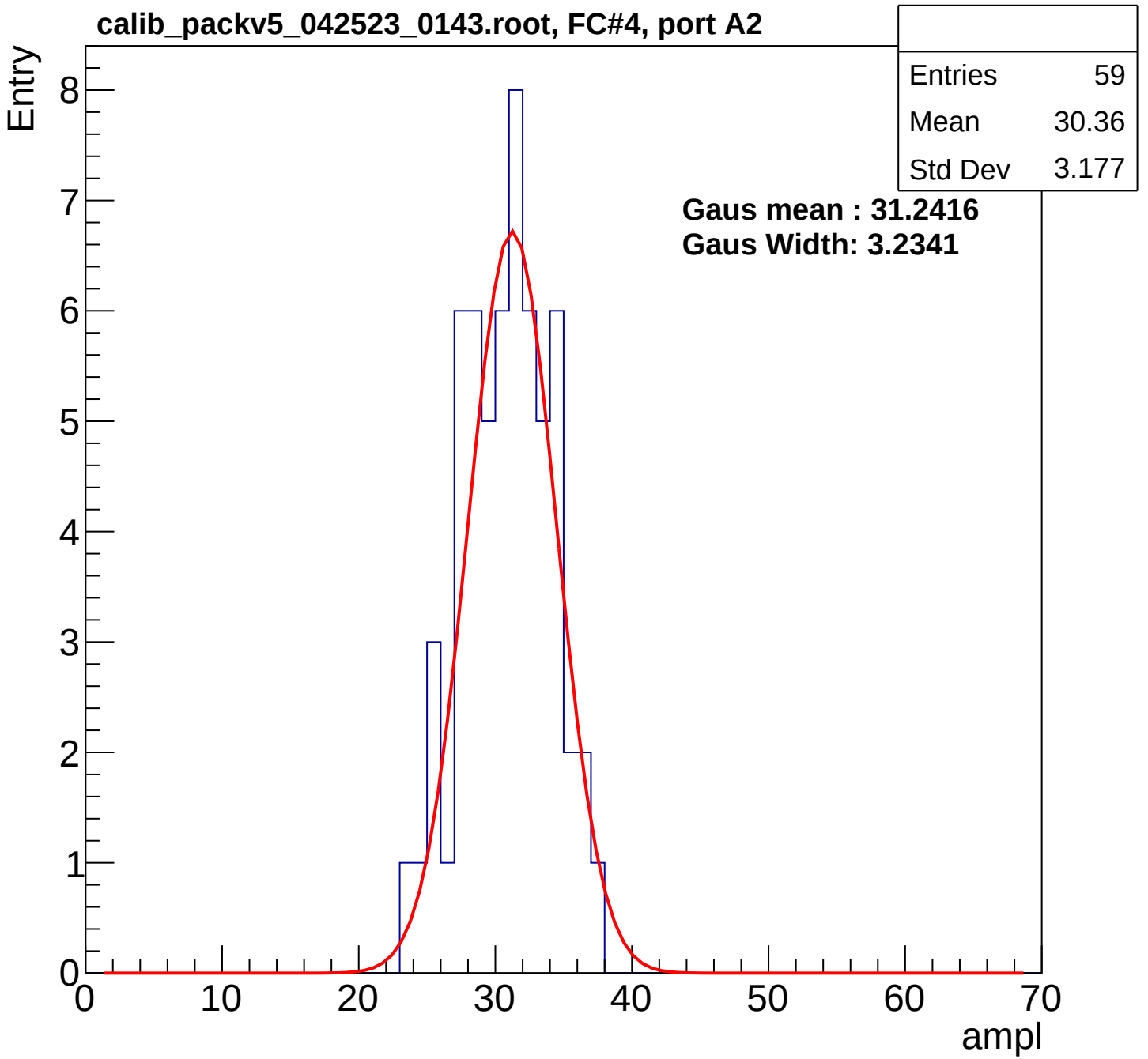
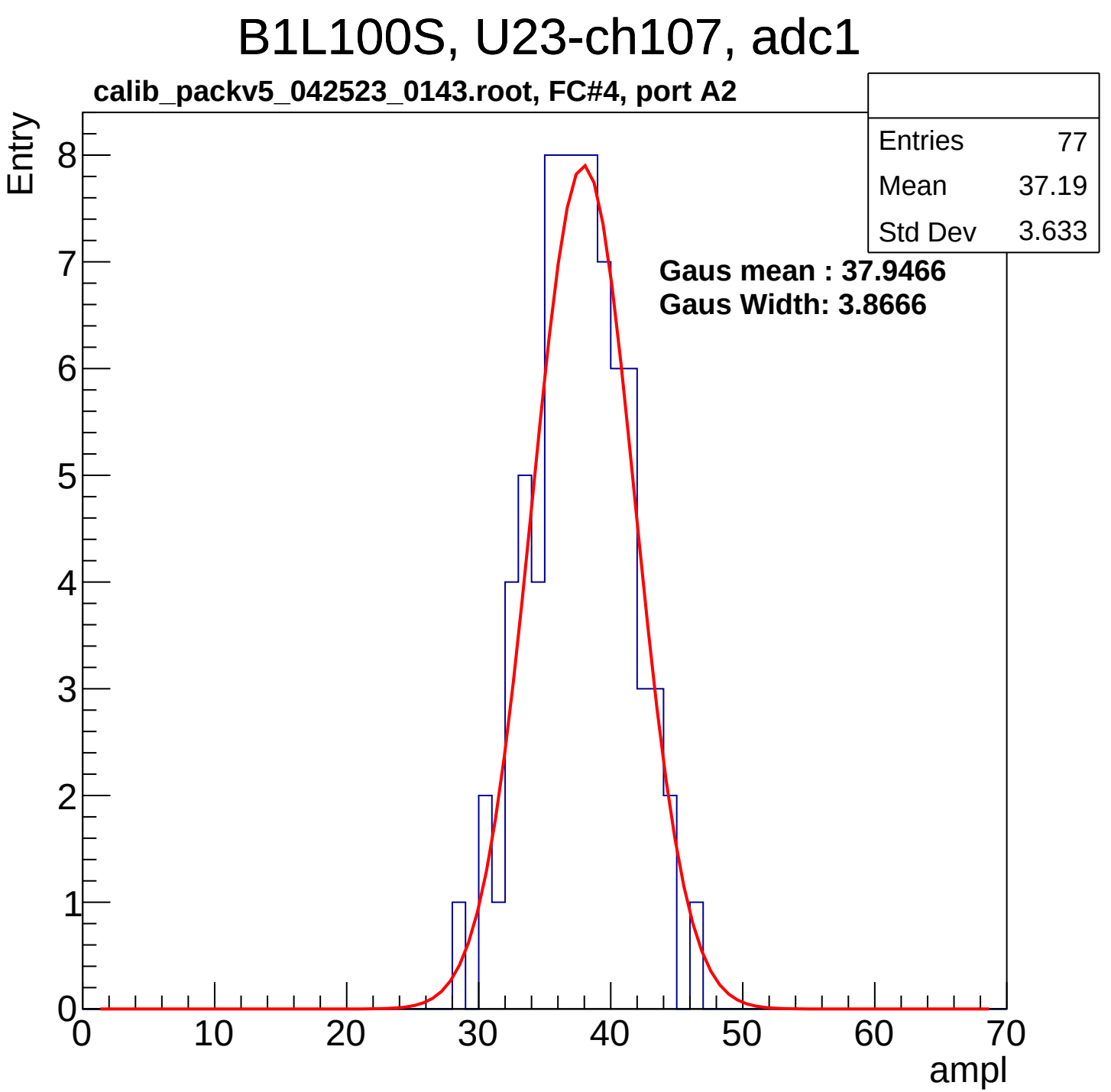
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch107, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 30.36
Std Dev 3.177
Gaus mean : 31.2416
Gaus Width: 3.2341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.19
Std Dev 3.633
Gaus mean : 37.9466
Gaus Width: 3.8666
Entry
ampl
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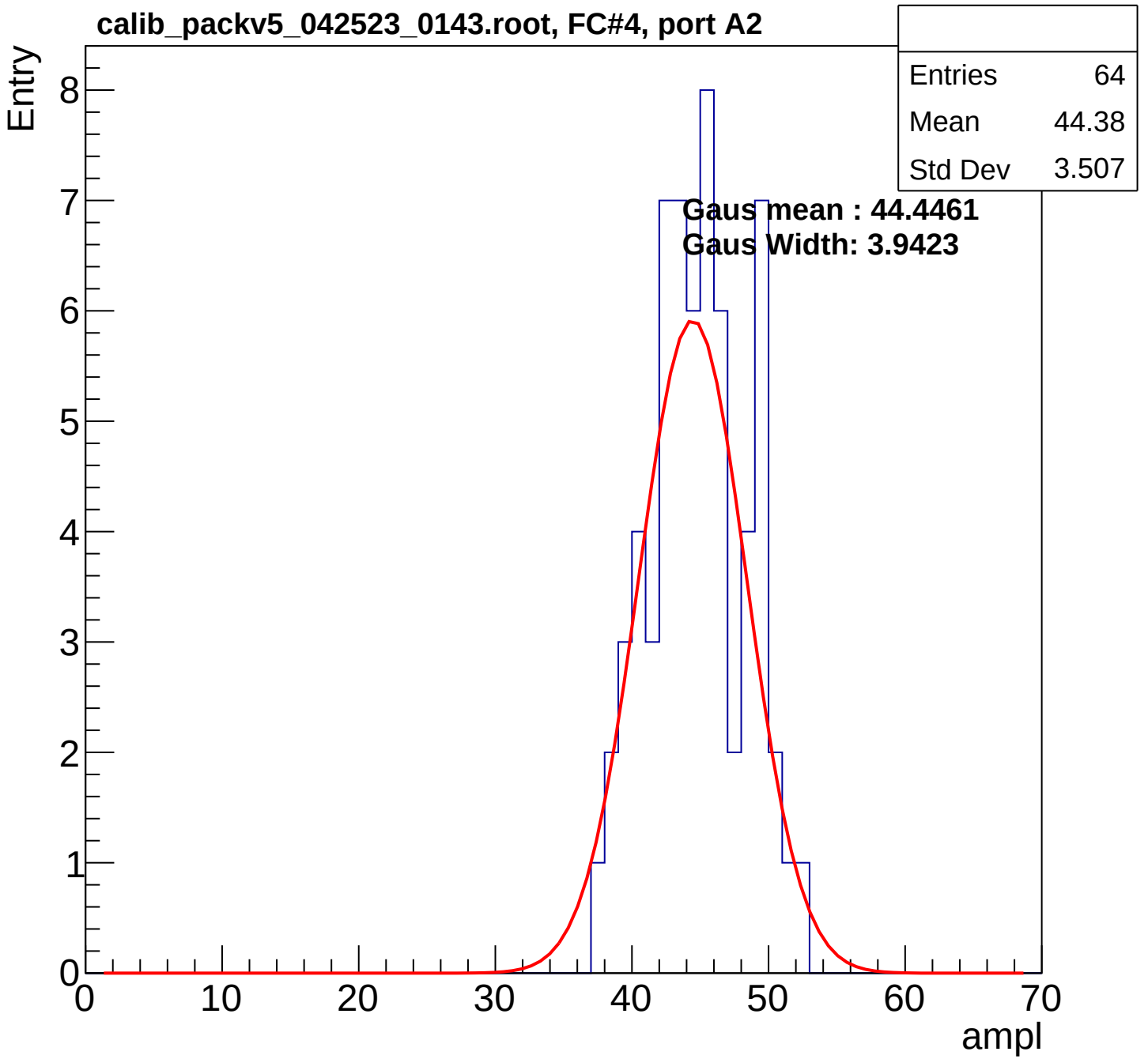
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 44.38
Std Dev 3.507
Gaus mean : 44.4461
Gaus Width: 3.9423
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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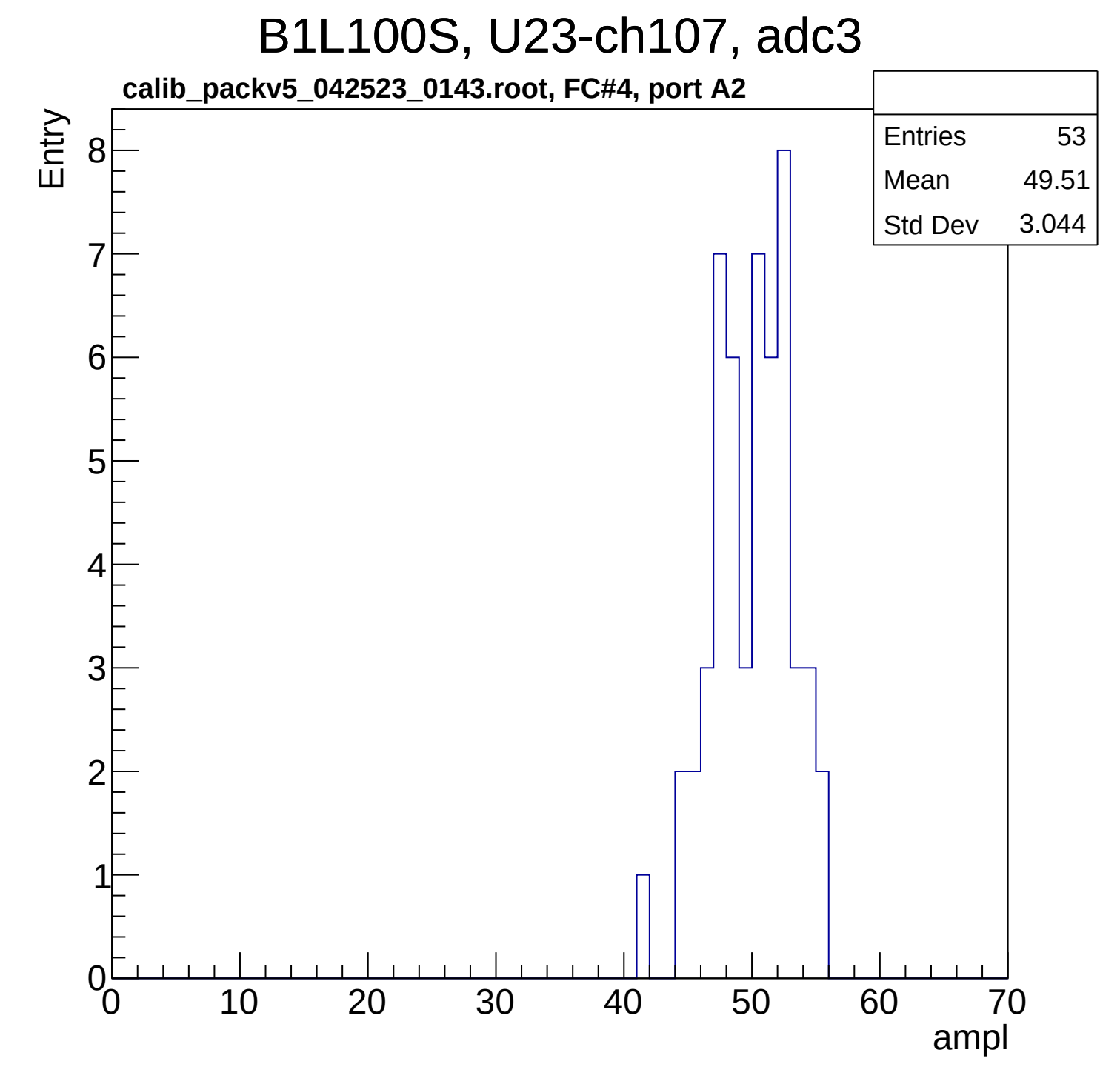
B1L100S, U23-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 49.51
Std Dev 3.044
Entry
ampl
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3
4
5
6
7
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0
10
20
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40
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60
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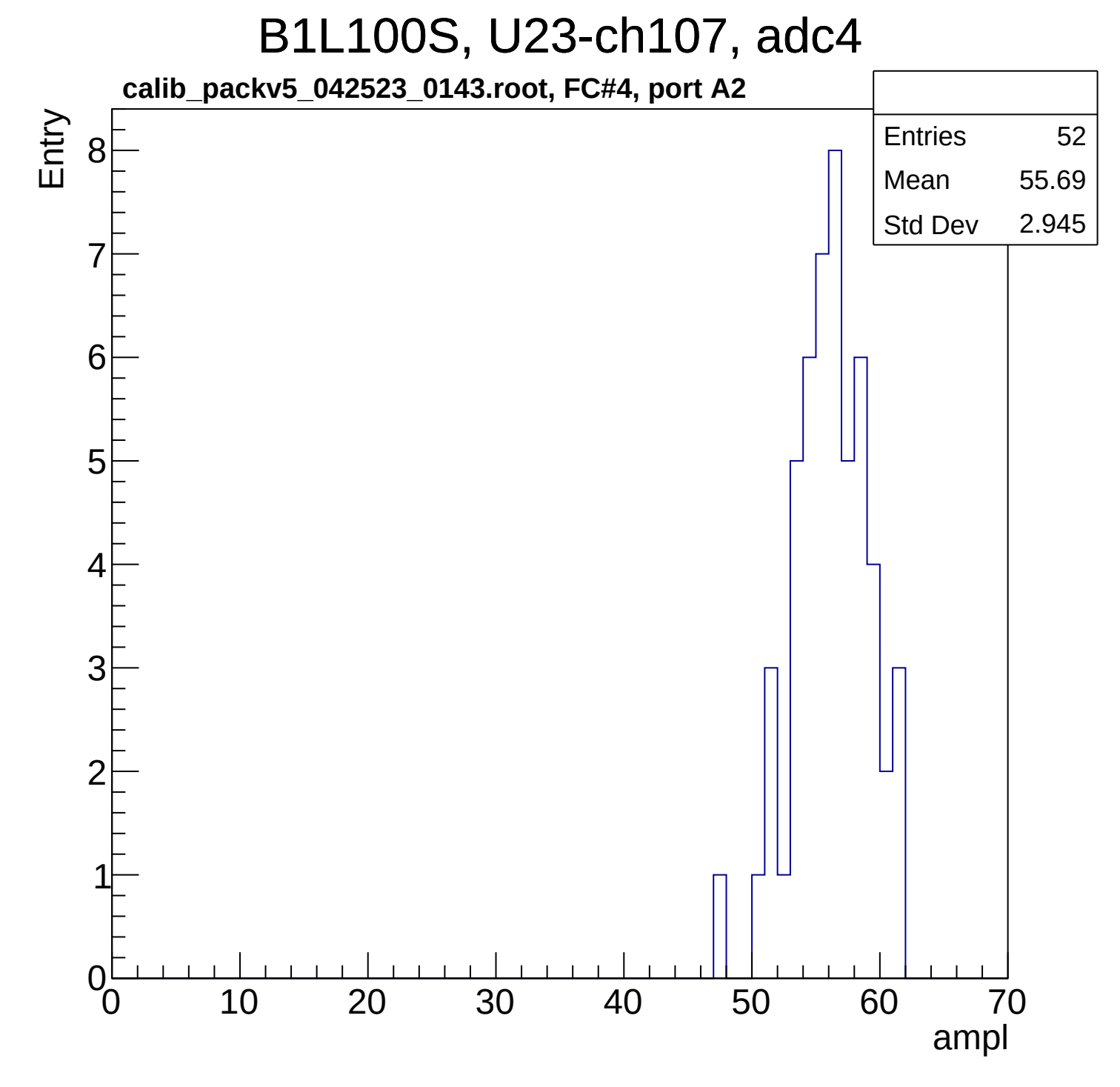
B1L100S, U23-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.69
Std Dev 2.945
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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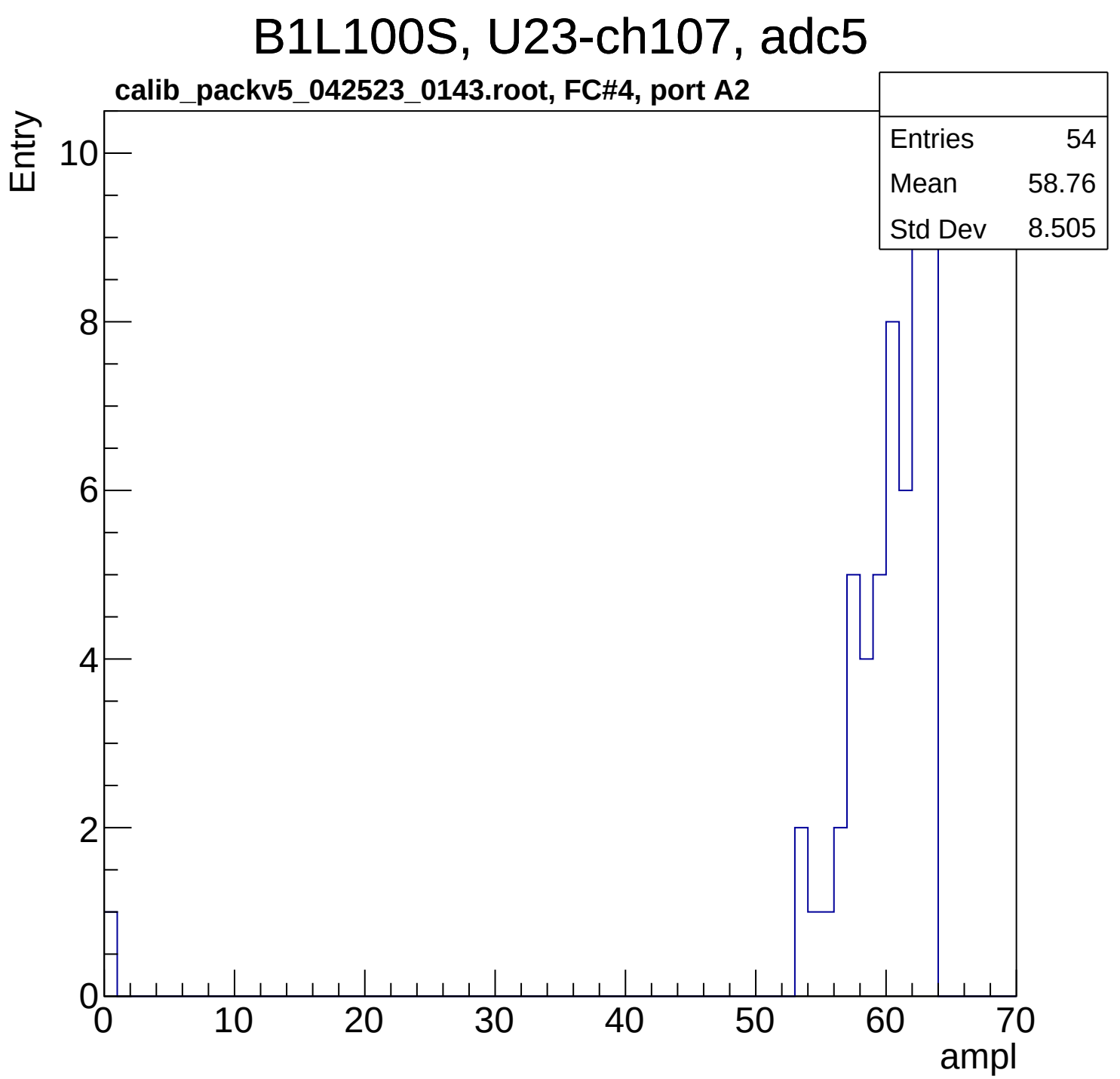
B1L100S, U23-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 58.76
Std Dev 8.505
Entry
ampl
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2
4
6
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0
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40
50
60
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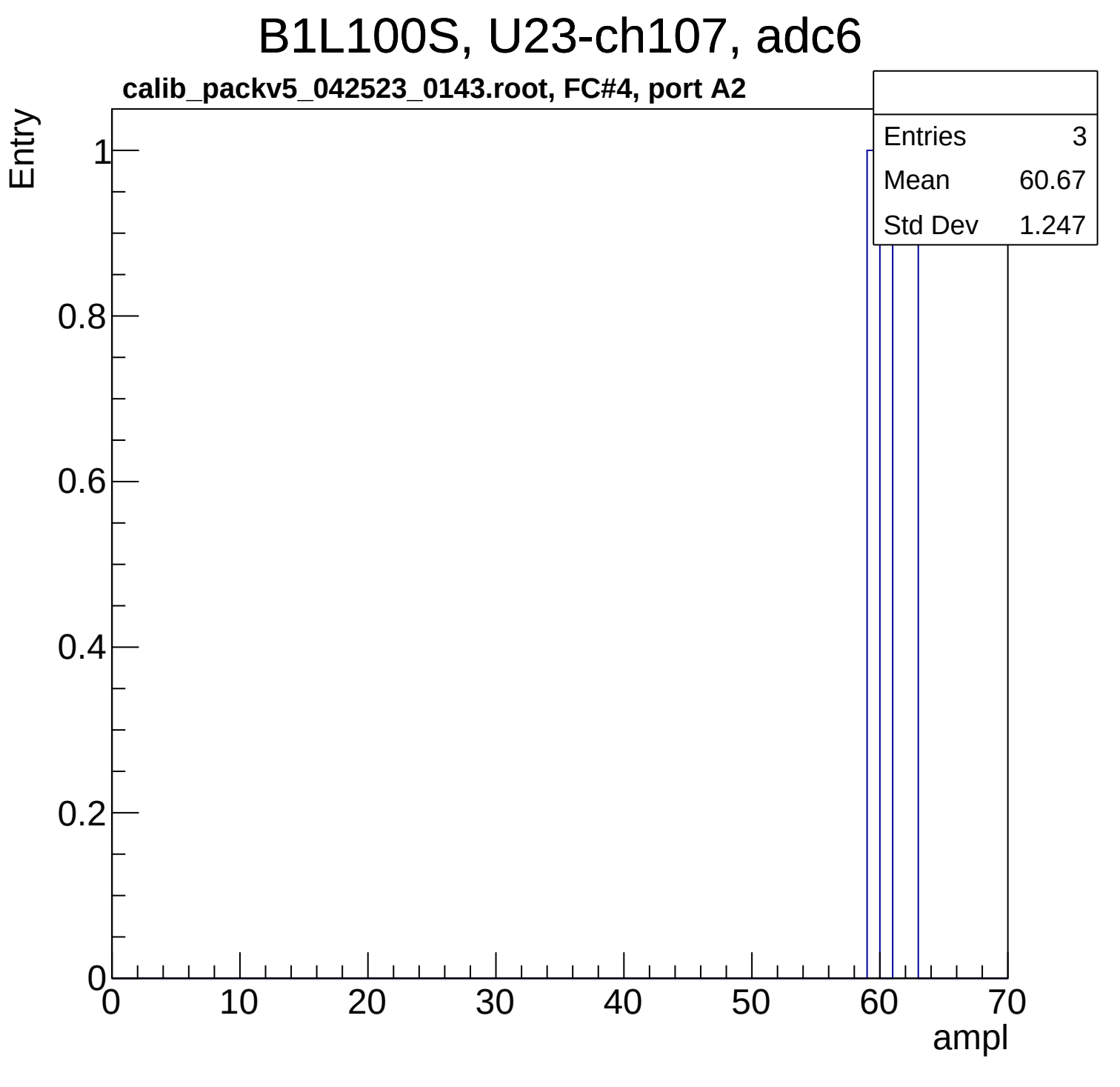
B1L100S, U23-ch107, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 1.247
Entry
ampl
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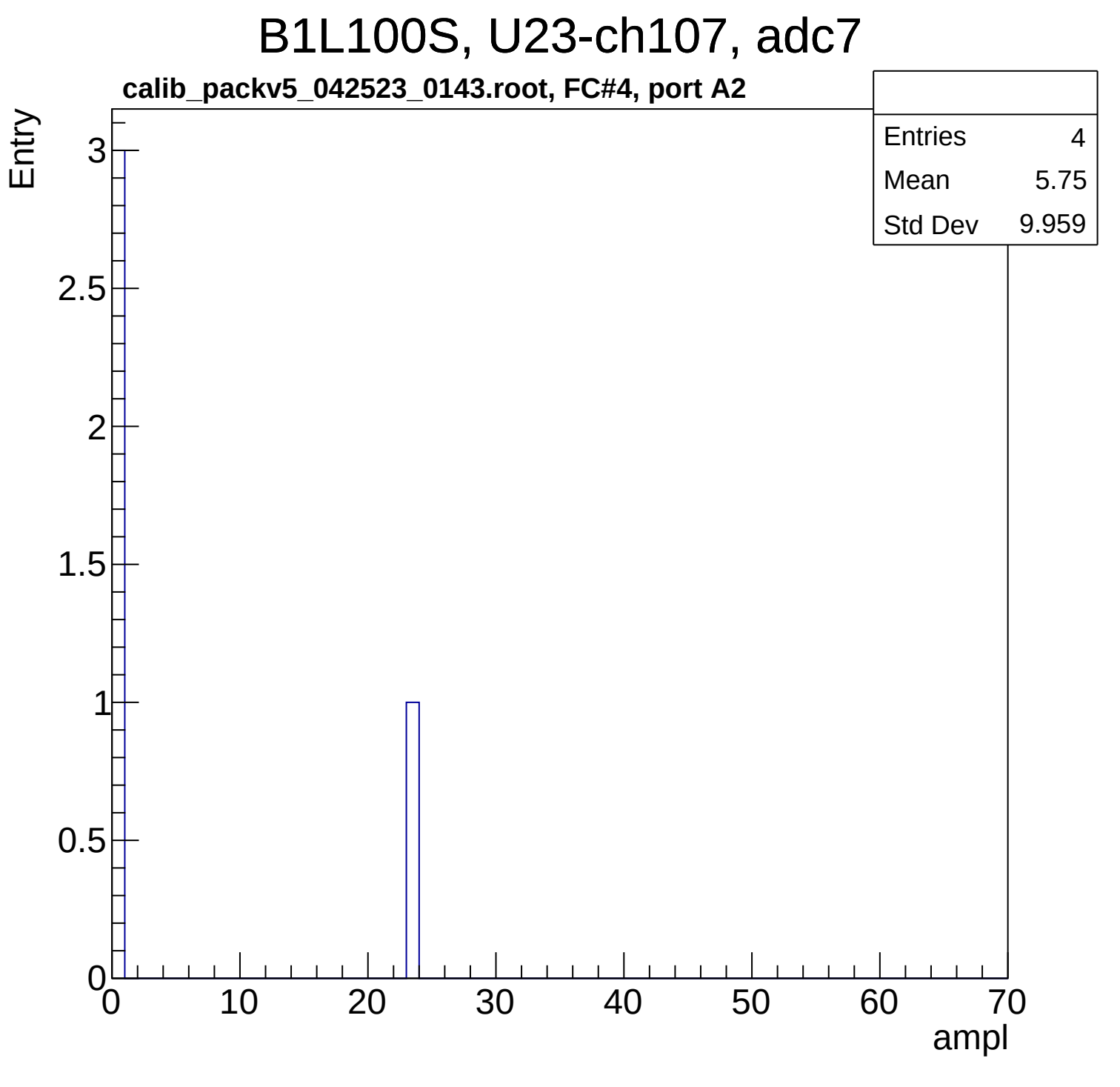
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U23-ch107, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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40
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60
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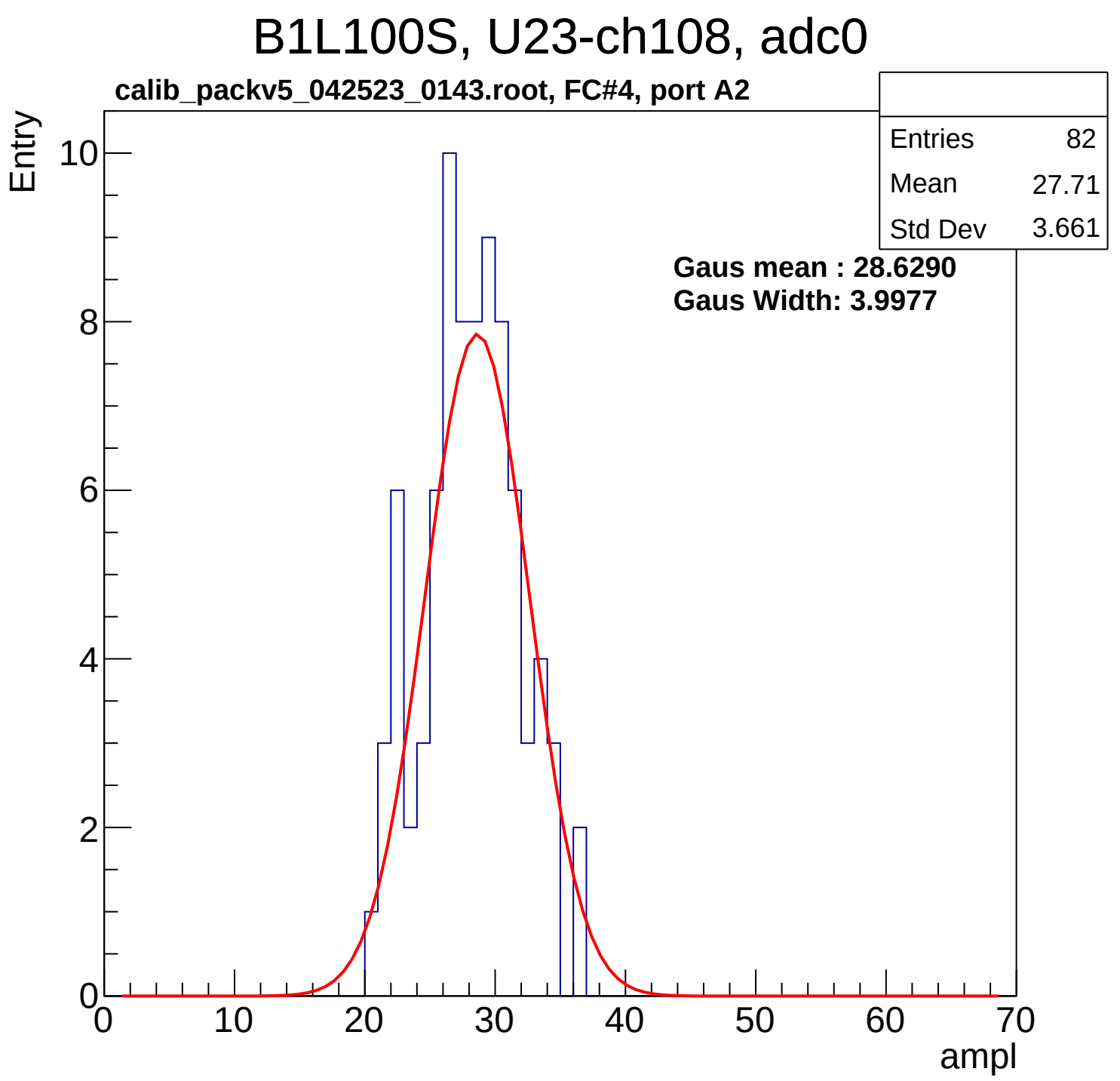
B1L100S, U23-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 27.71
Std Dev 3.661
Gaus mean : 28.6290
Gaus Width: 3.9977
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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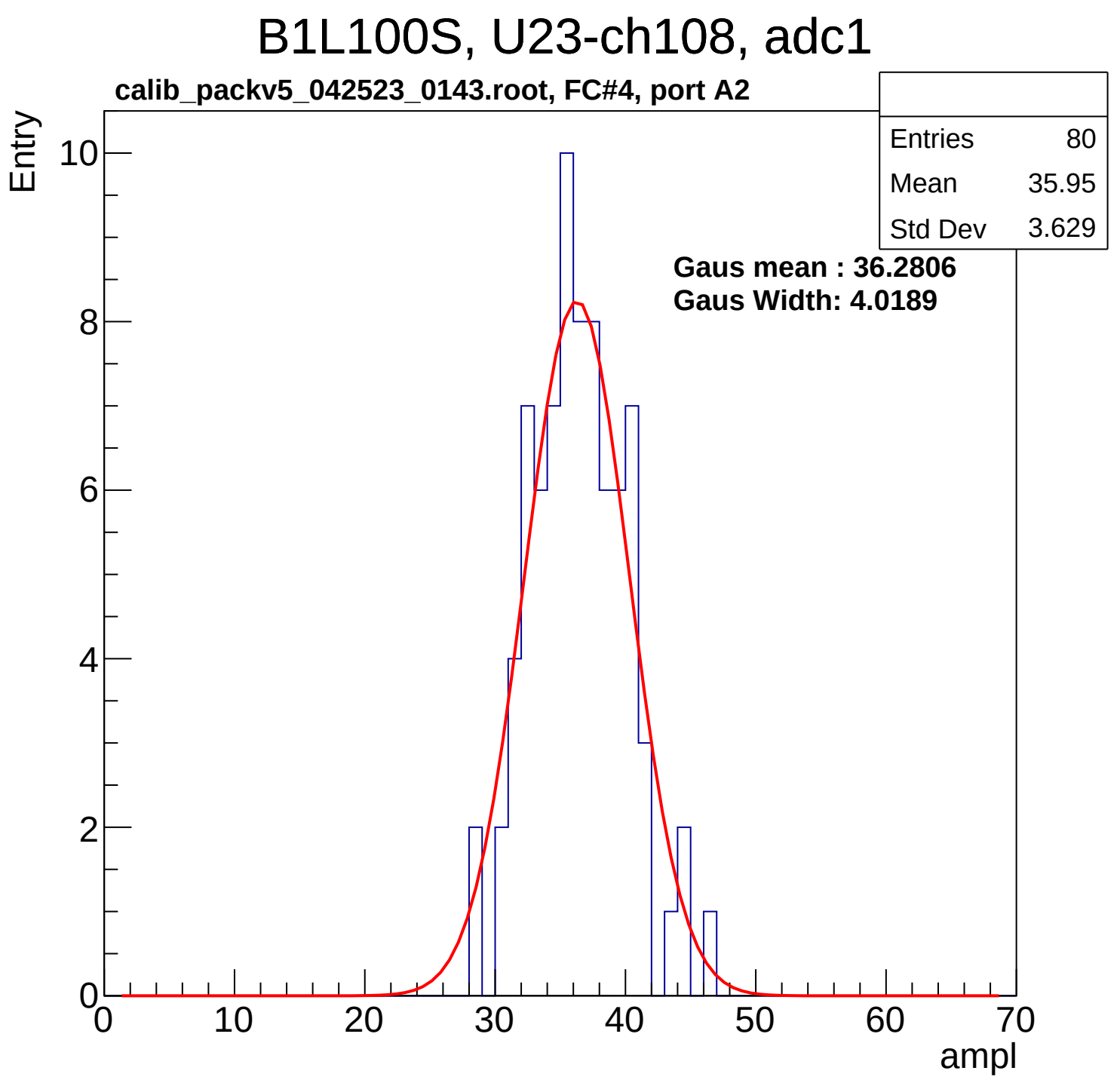
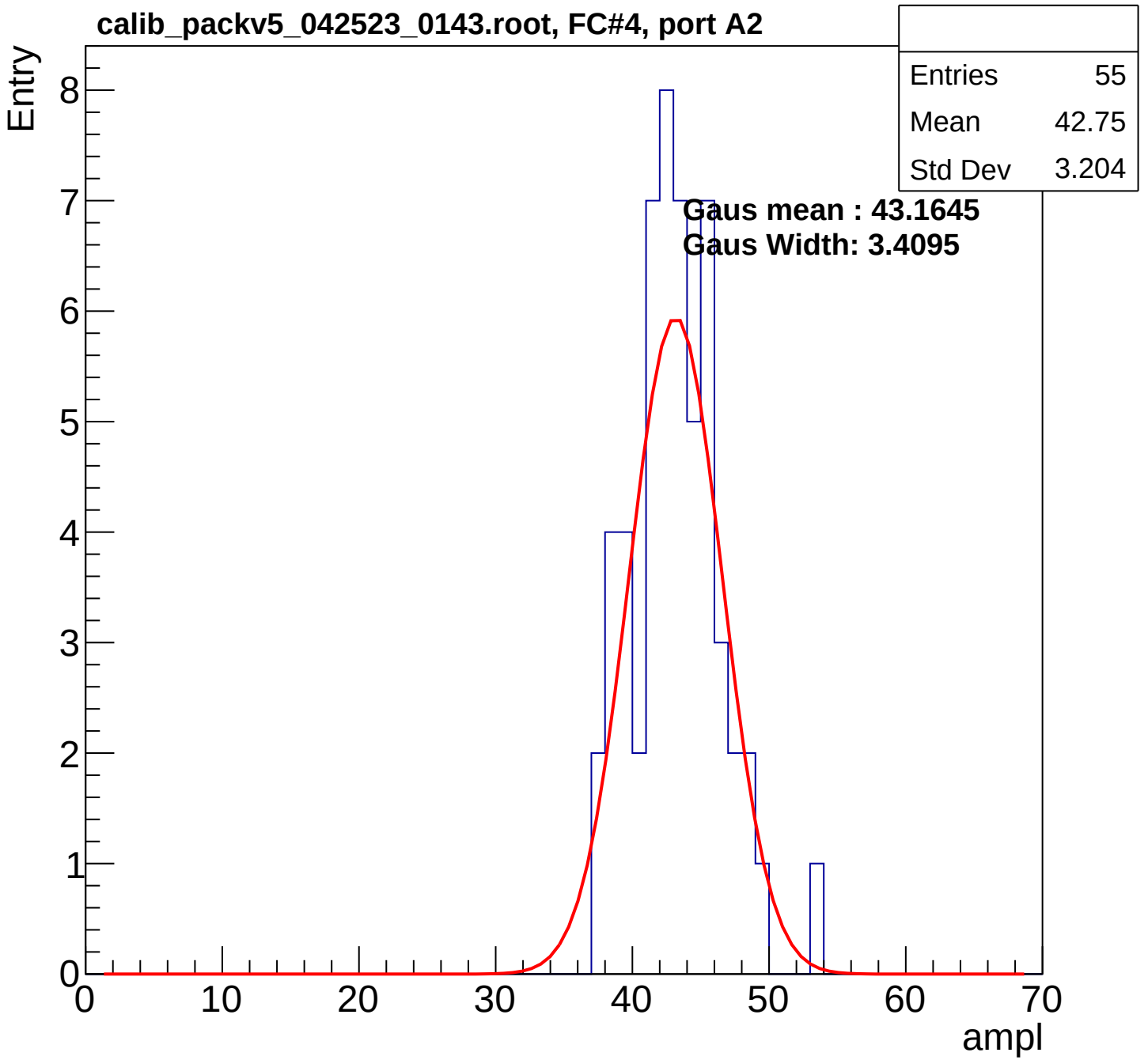

B1L100S, U23-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.95
Std Dev 3.629
Gaus mean : 36.2806
Gaus Width: 4.0189
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 42.75
Std Dev 3.204
Gaus mean : 43.1645
Gaus Width: 3.4095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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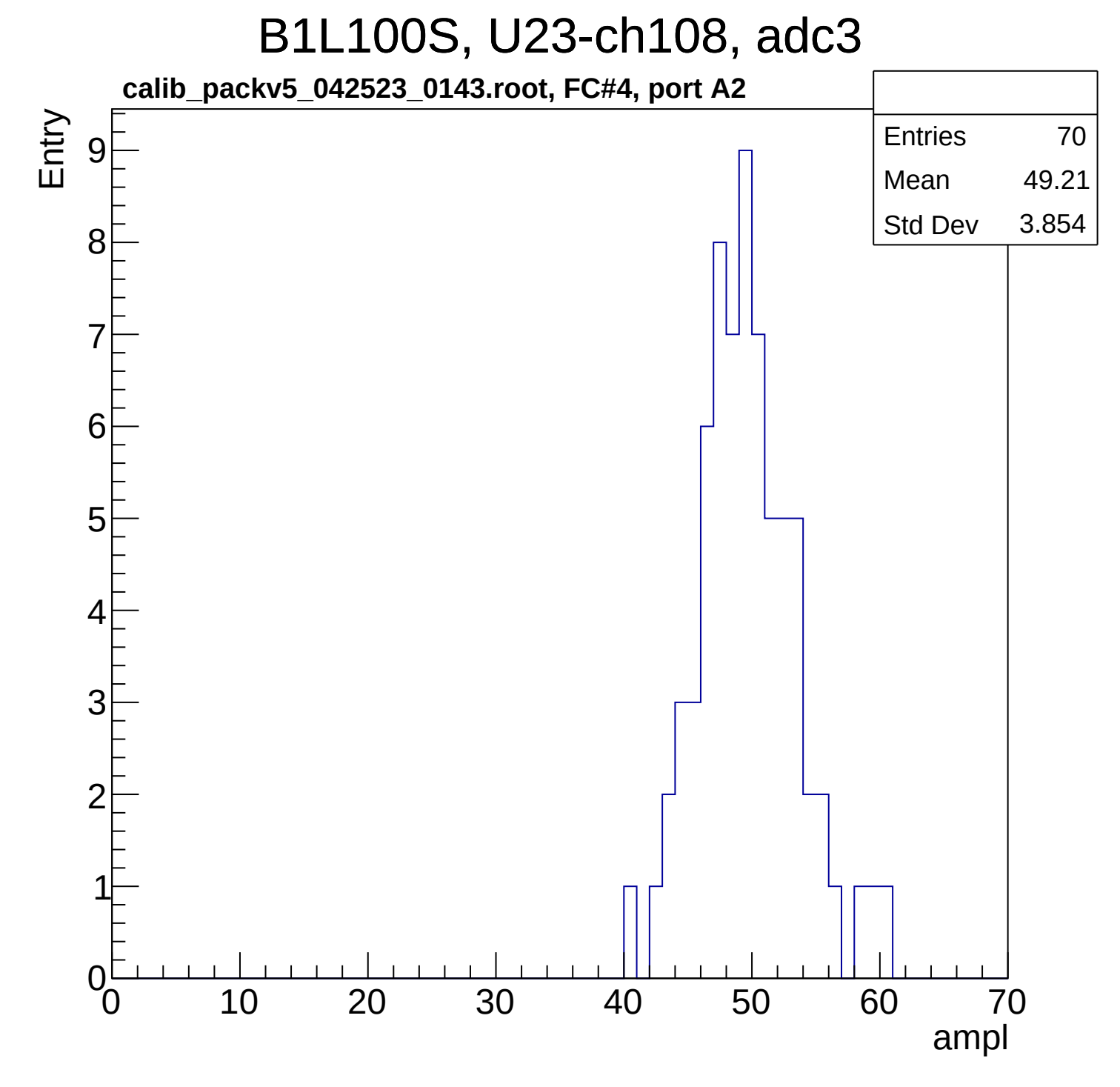
B1L100S, U23-ch108, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.21
Std Dev 3.854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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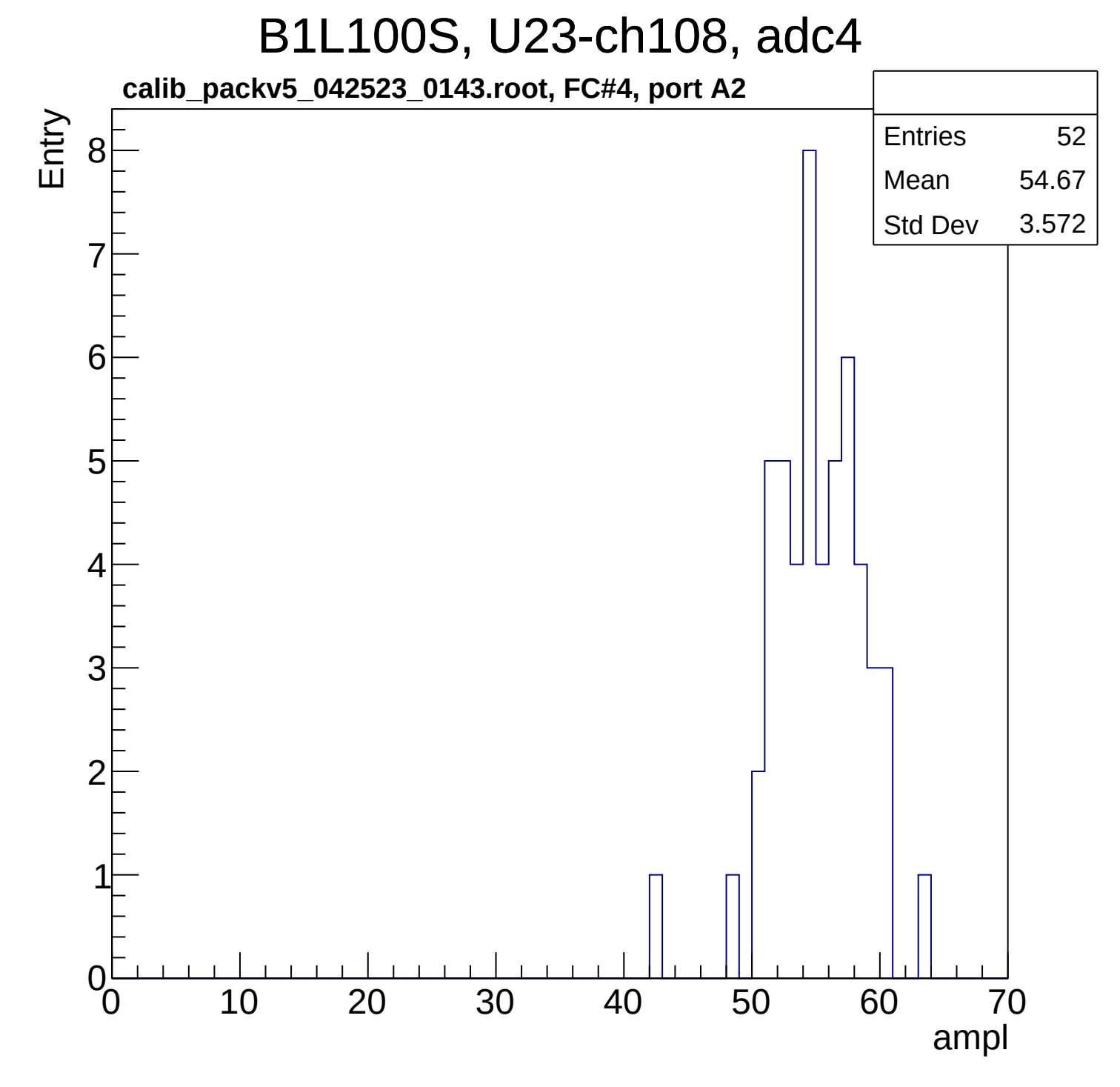
B1L100S, U23-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.67
Std Dev 3.572
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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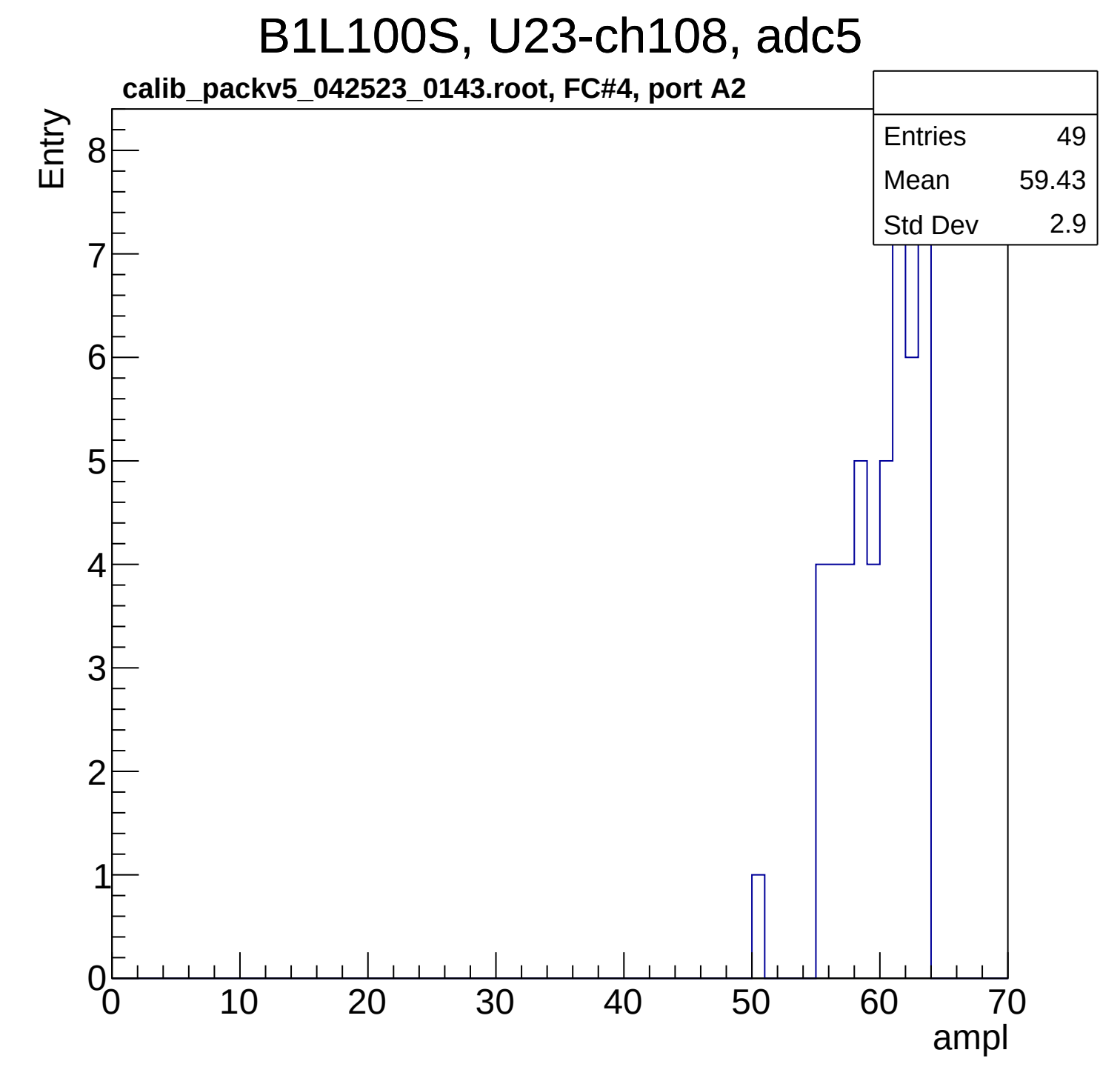
B1L100S, U23-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 59.43
Std Dev 2.9
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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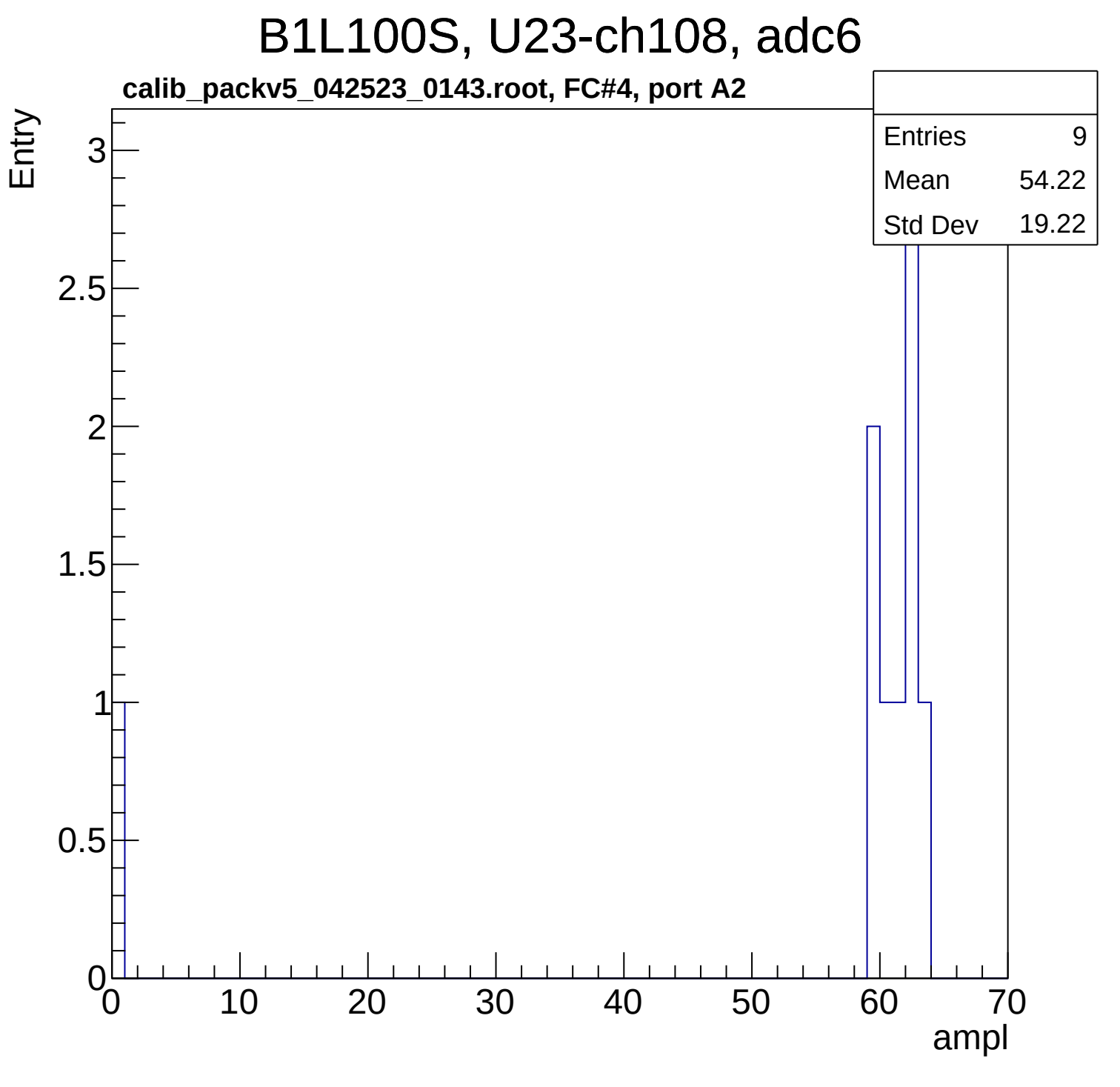
B1L100S, U23-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 54.22
Std Dev 19.22
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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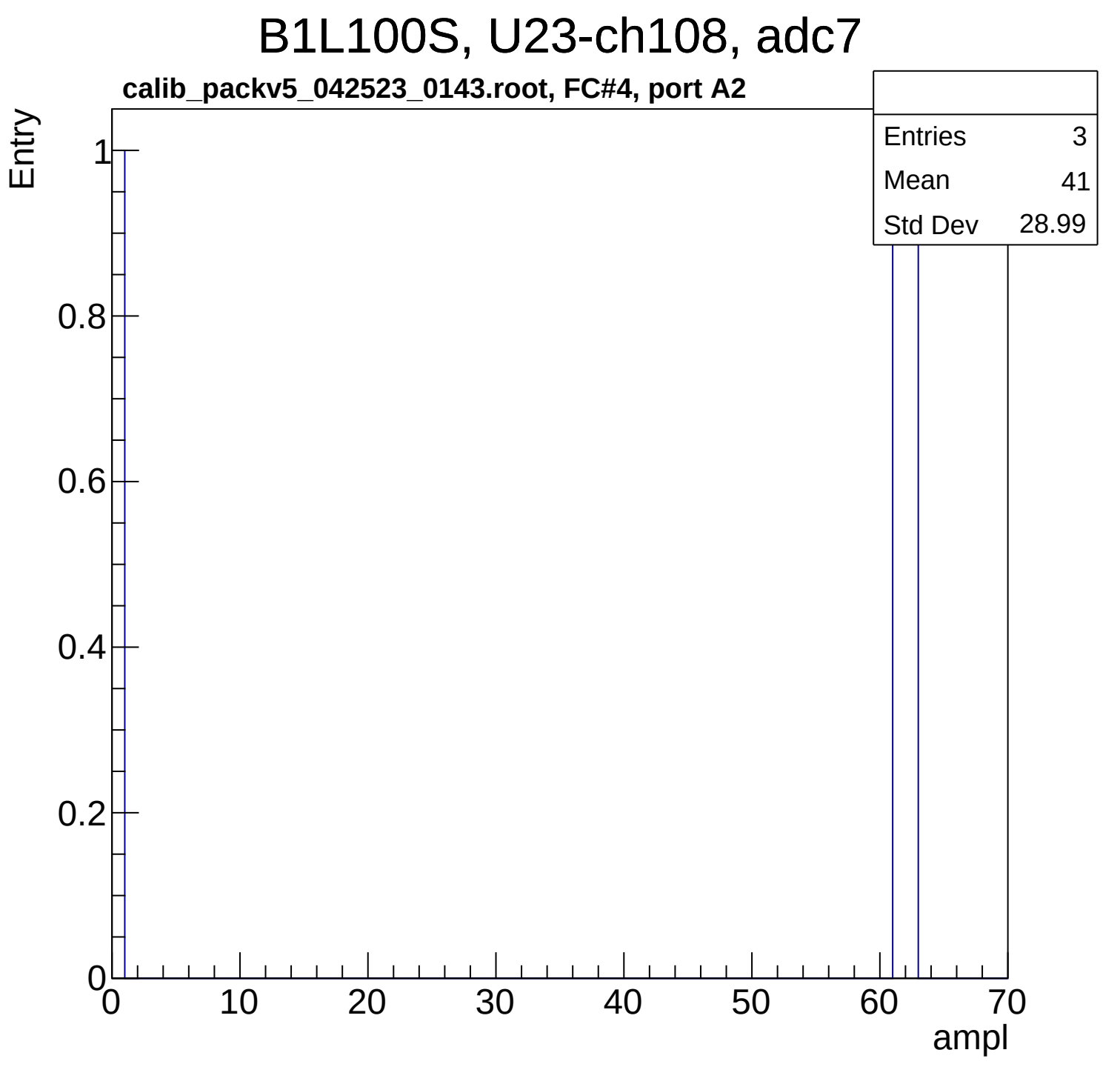
B1L100S, U23-ch108, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41
Std Dev 28.99
Entry
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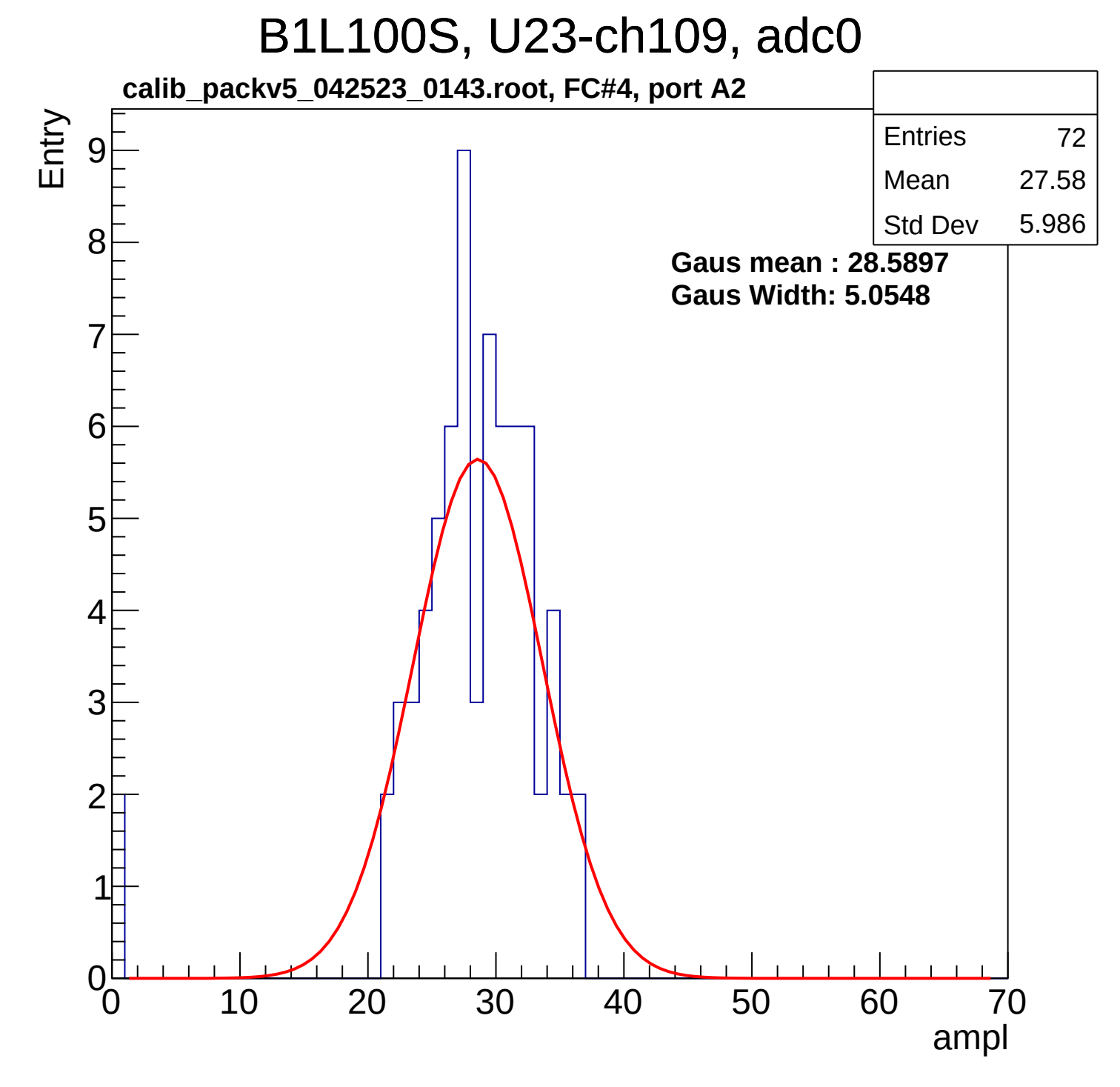
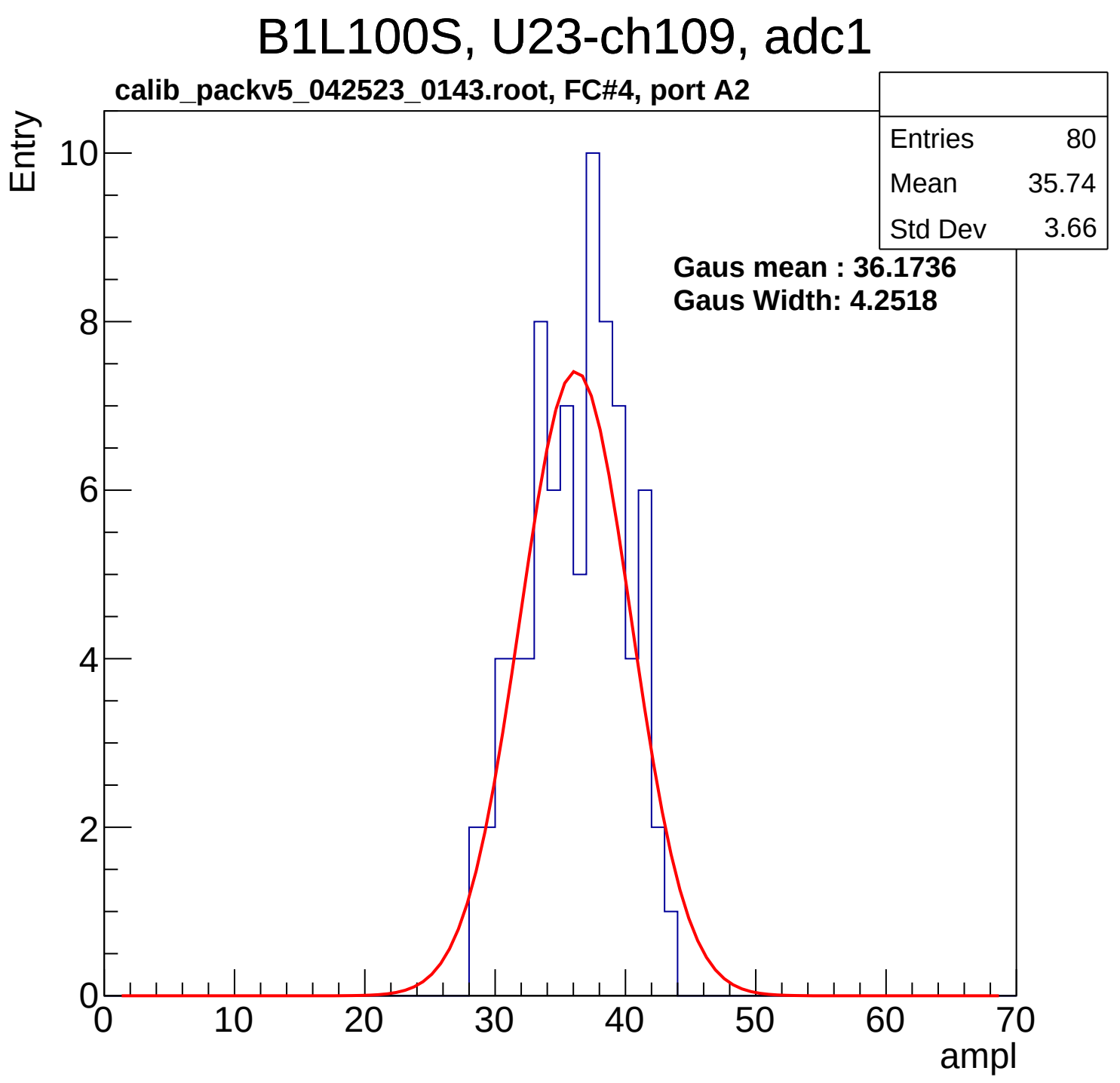
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 27.58
Std Dev 5.986
Gaus mean : 28.5897
Gaus Width: 5.0548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.74
Std Dev 3.66
Gaus mean : 36.1736
Gaus Width: 4.2518
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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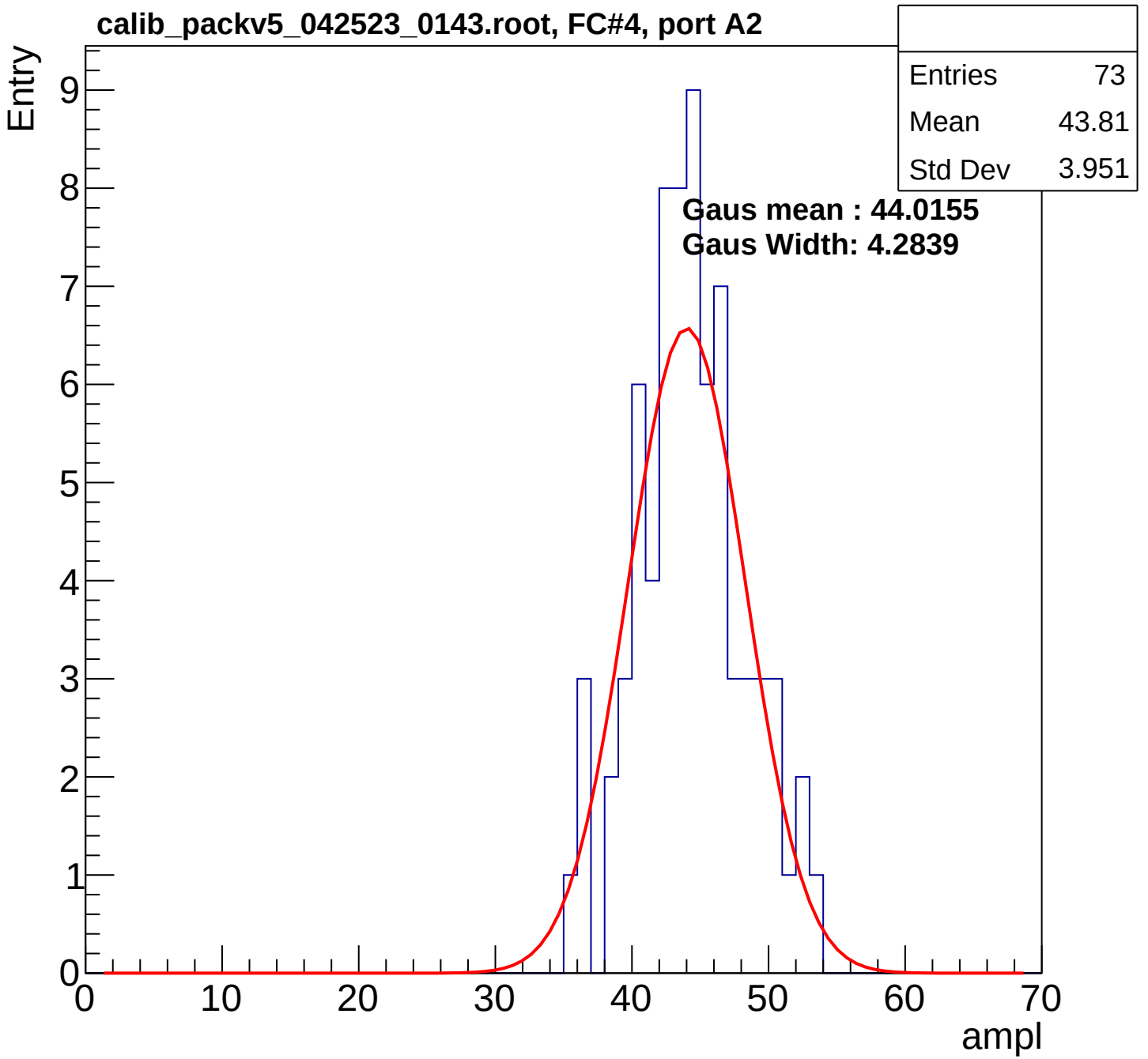
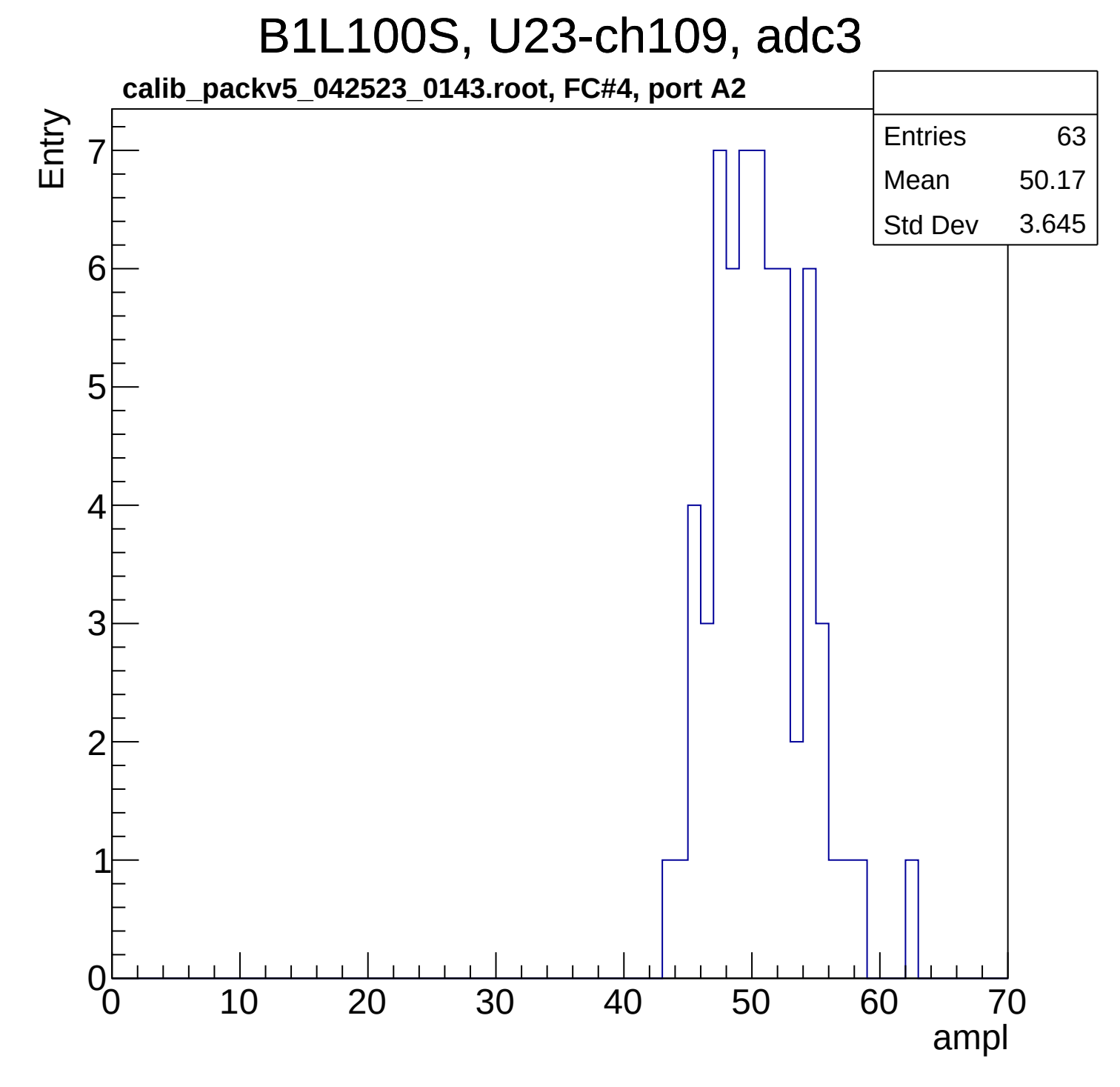
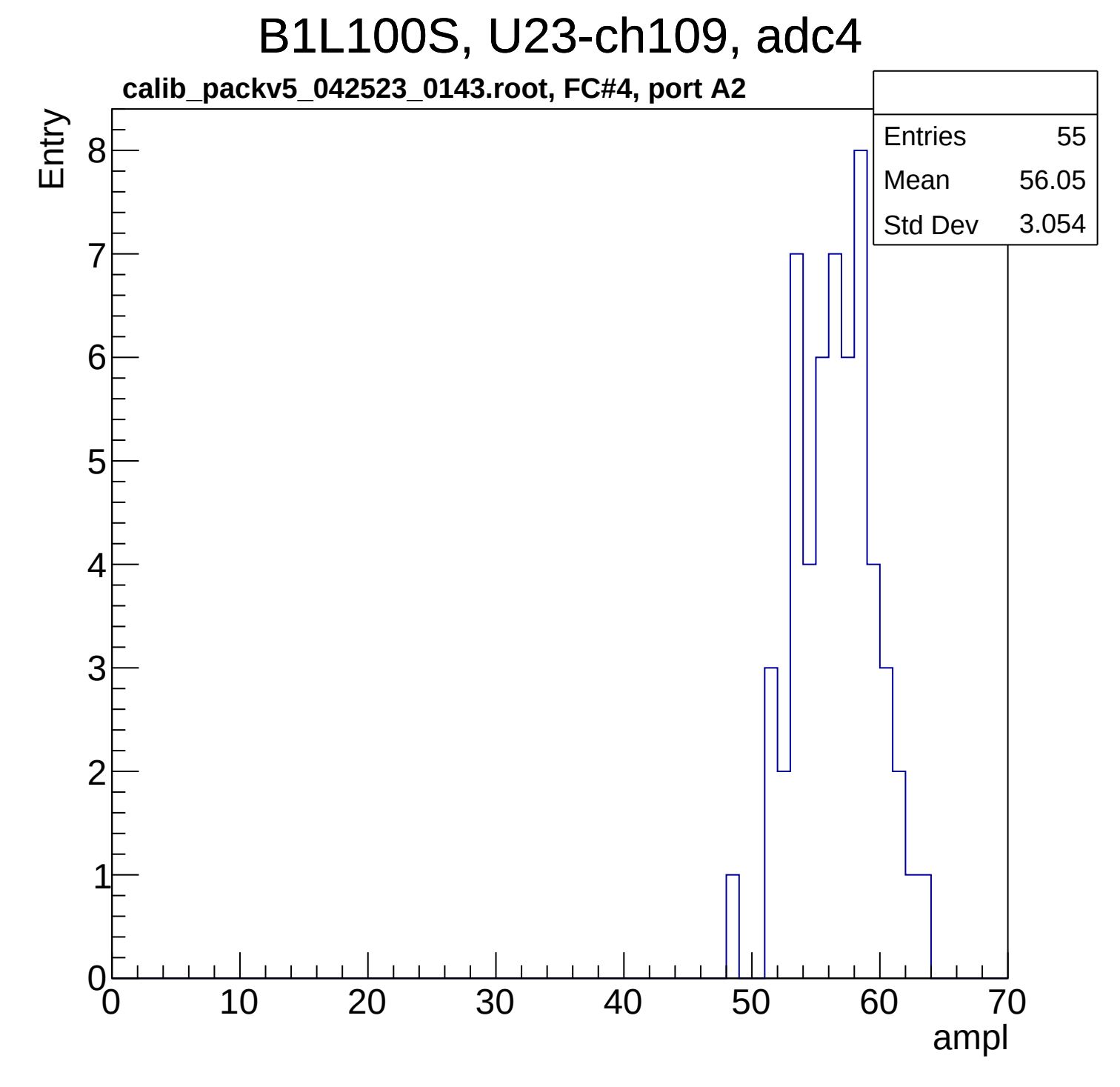

B1L100S, U23-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.81
Std Dev 3.951
Gaus mean : 44.0155
Gaus Width: 4.2839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch109, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.17
Std Dev 3.645
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch109, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.05
Std Dev 3.054
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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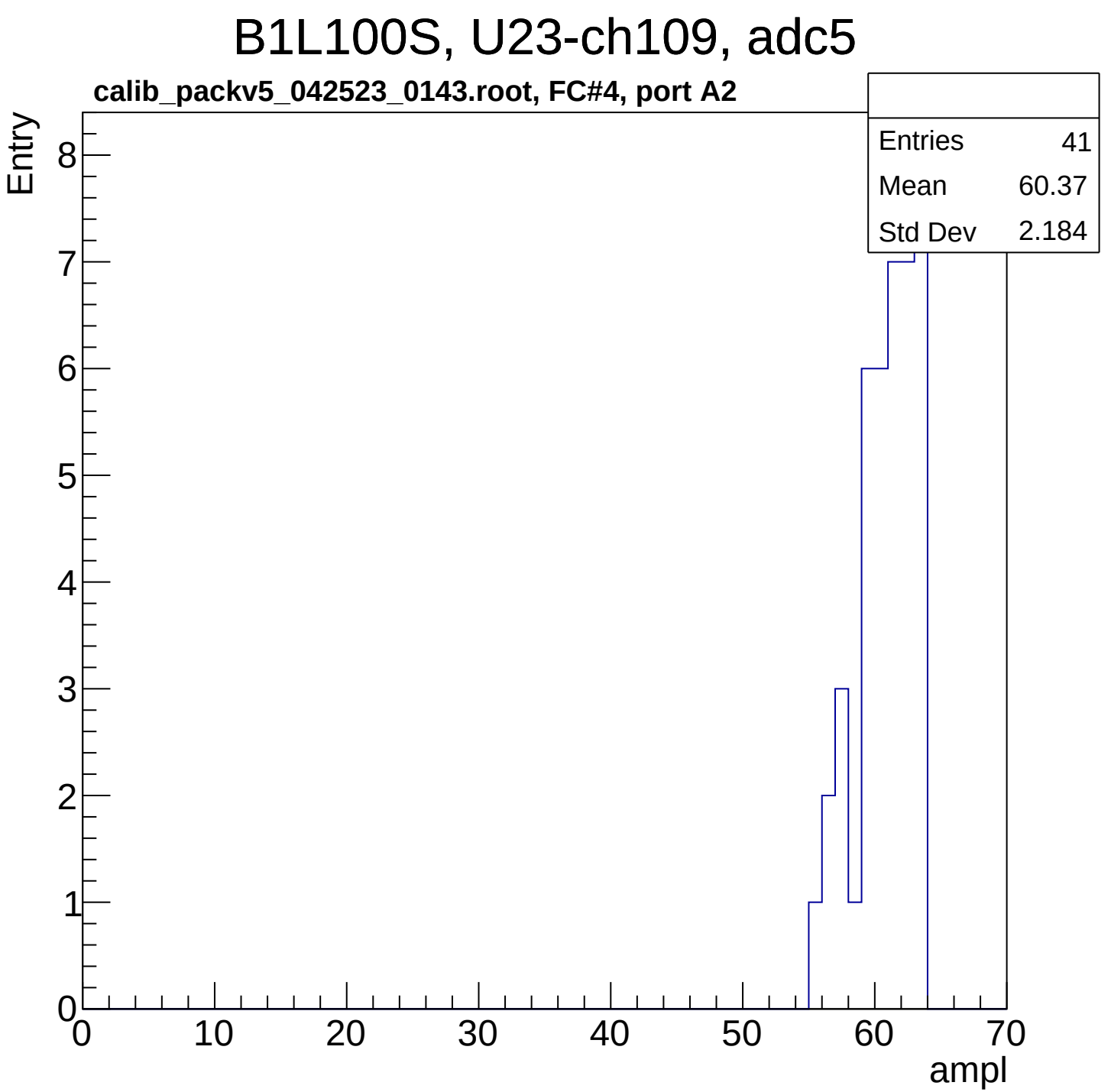
B1L100S, U23-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 60.37
Std Dev 2.184
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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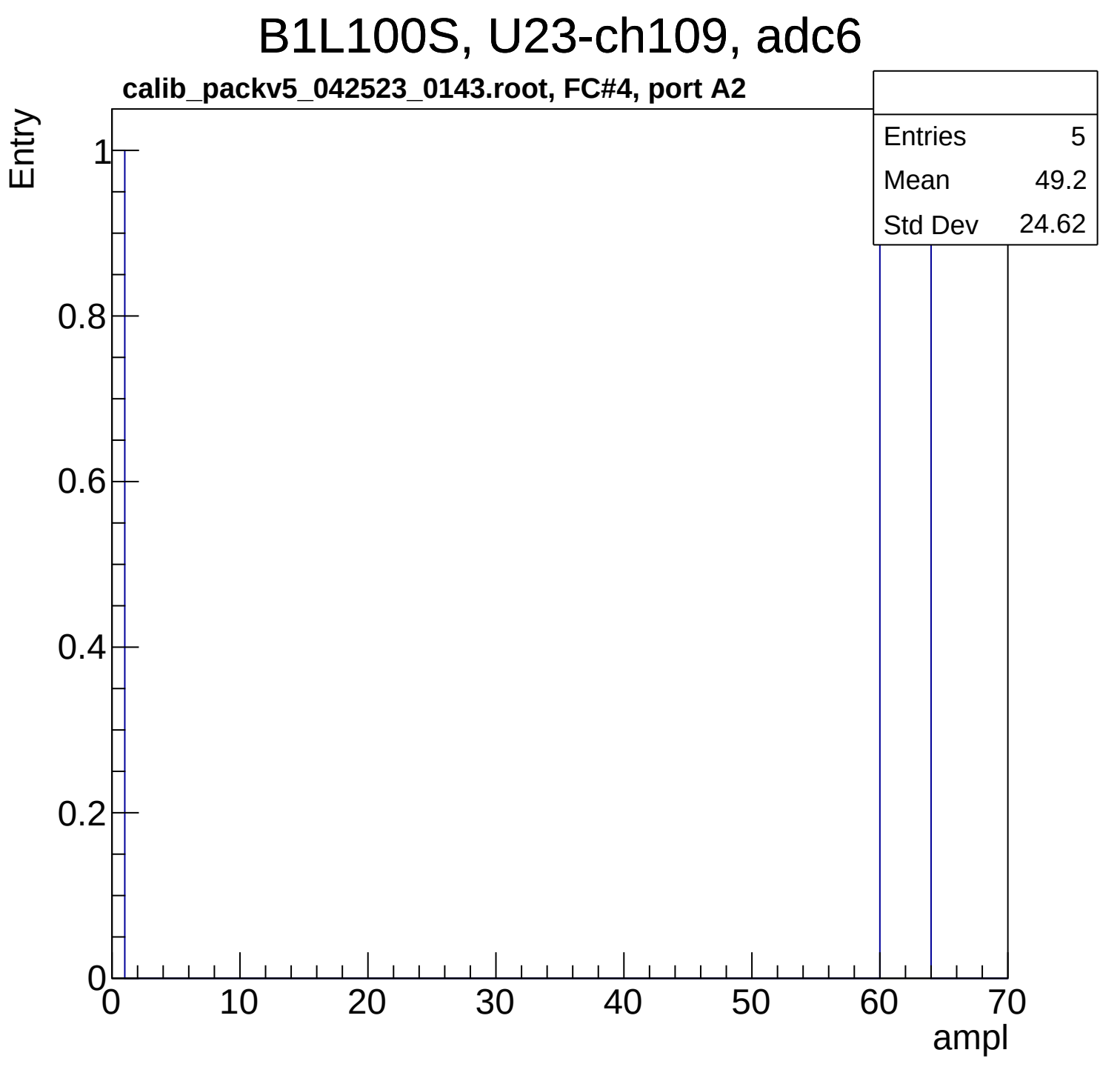
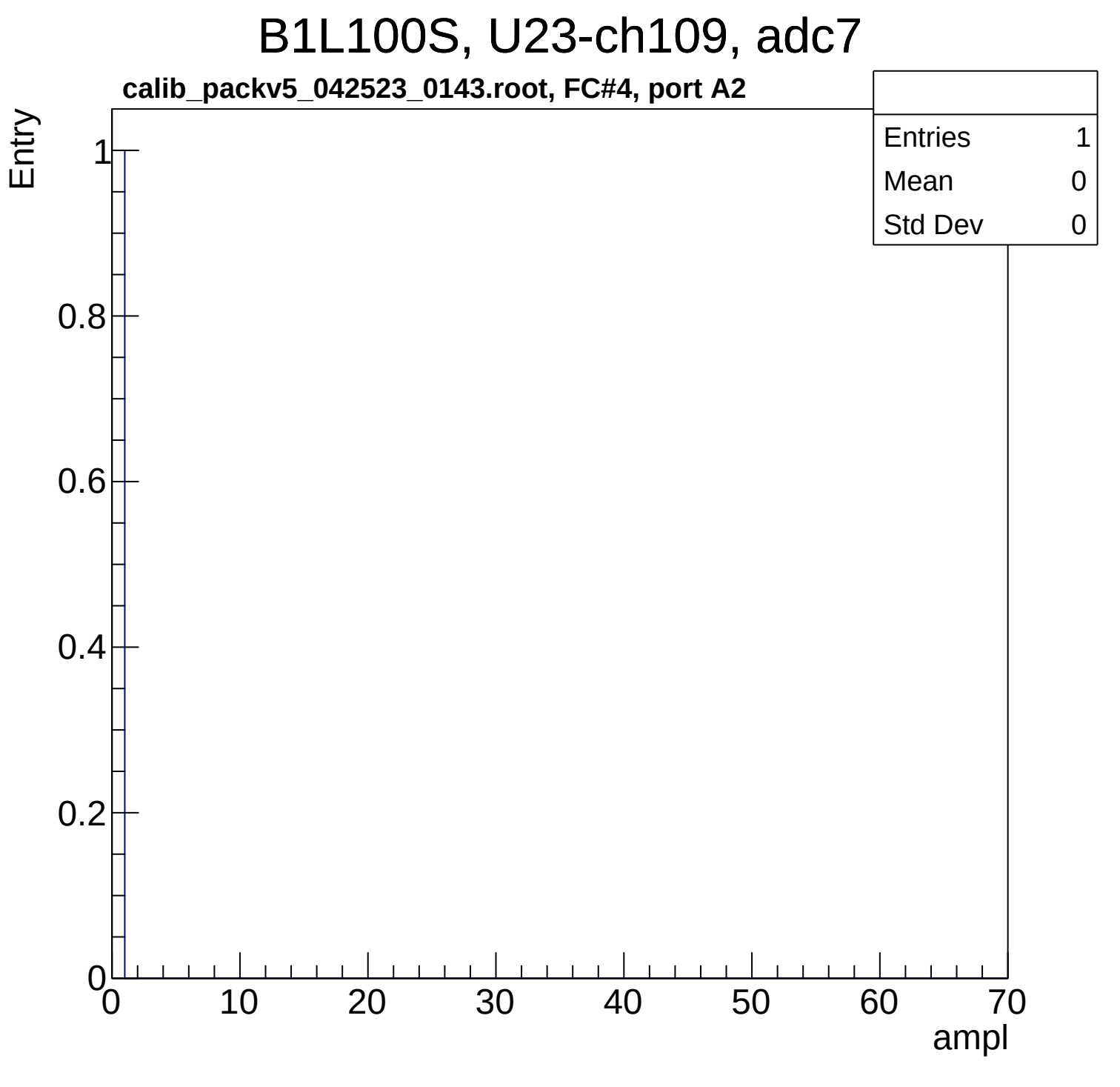
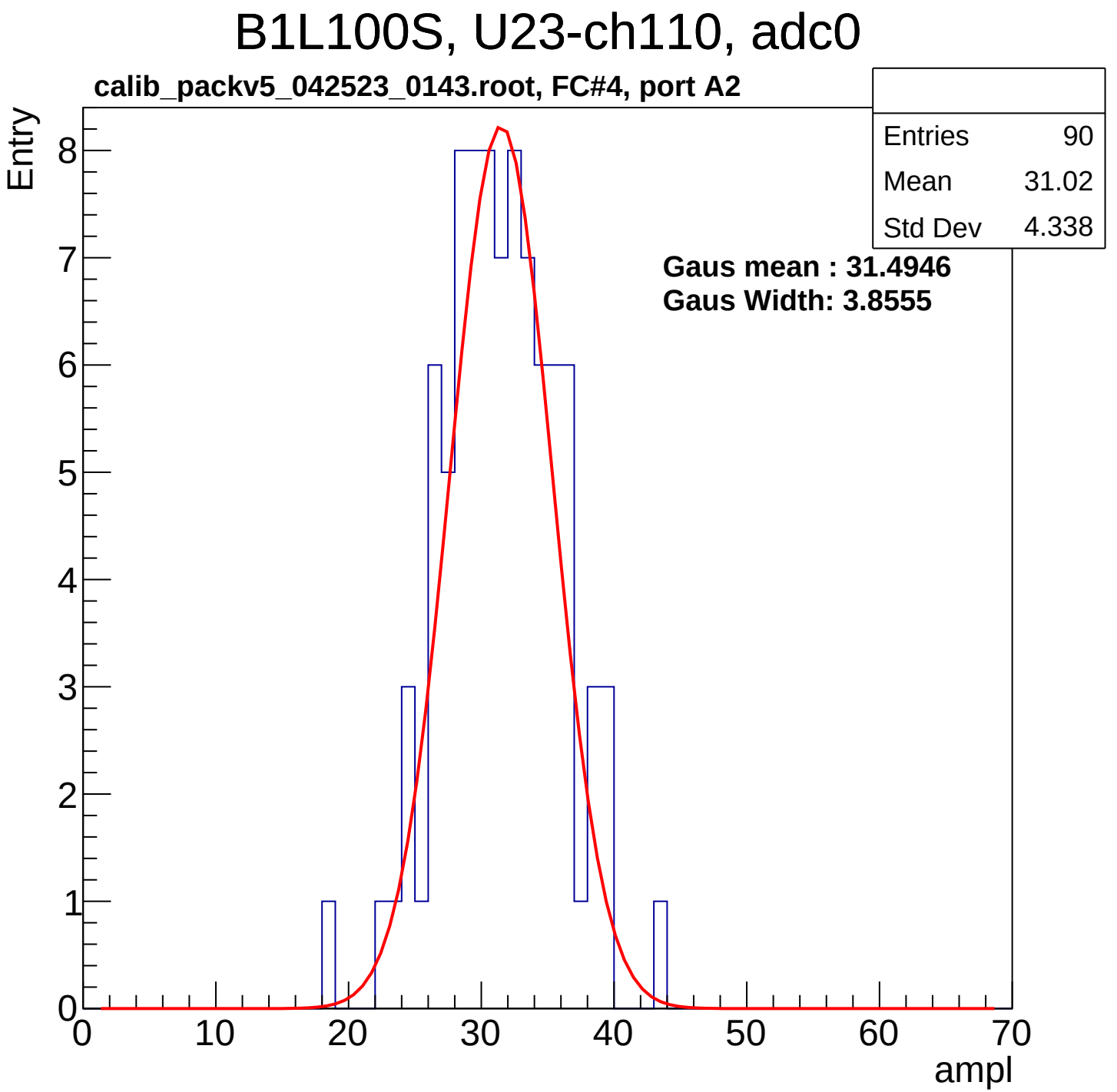

B1L100S, U23-ch109, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.2
Std Dev 24.62
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch109, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 31.02
Std Dev 4.338
Gaus mean : 31.4946
Gaus Width: 3.8555
Entry
ampl
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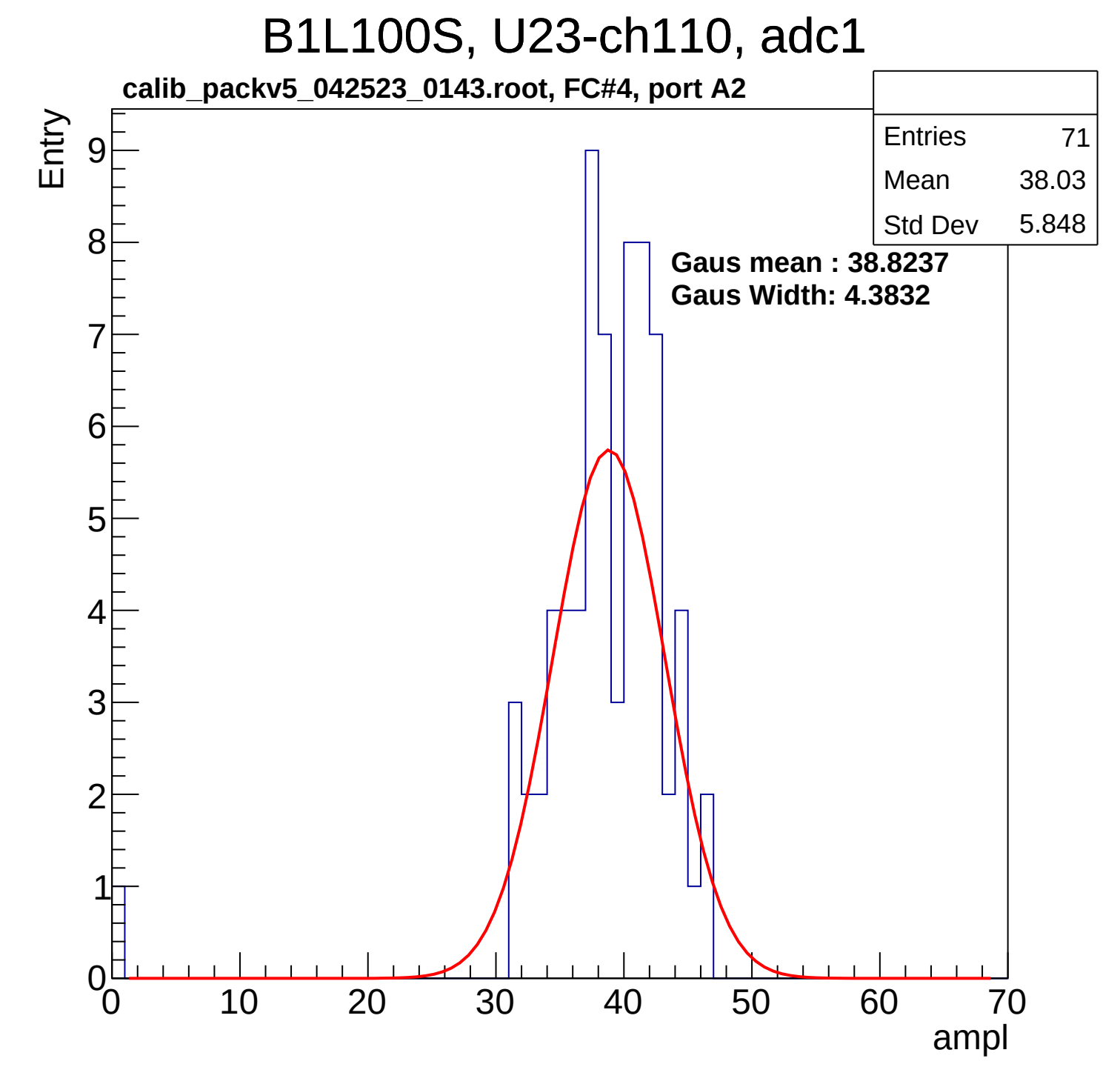
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 38.03
Std Dev 5.848
Gaus mean : 38.8237
Gaus Width: 4.3832
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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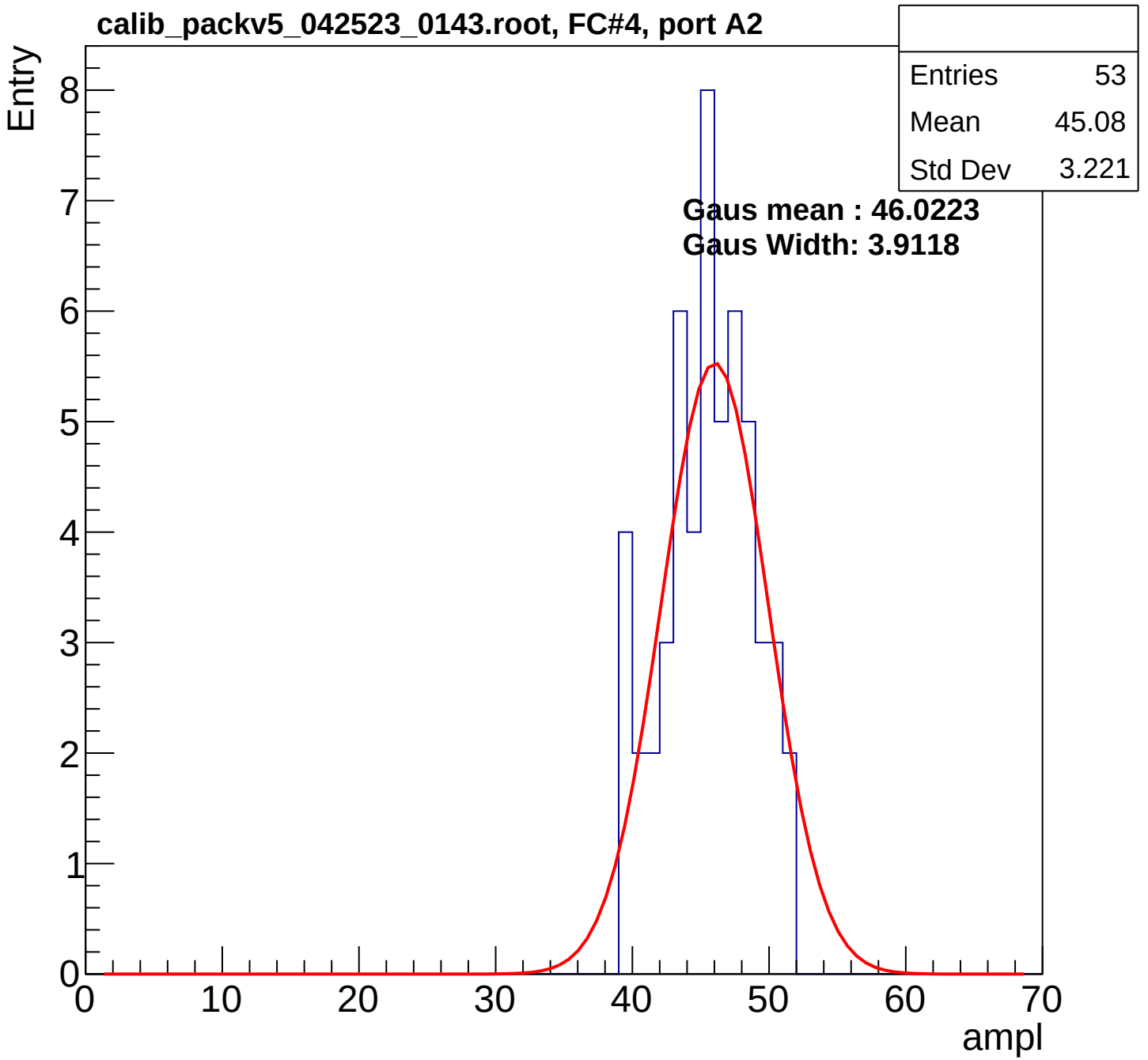
B1L100S, U23-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 45.08
Std Dev 3.221
Gaus mean : 46.0223
Gaus Width: 3.9118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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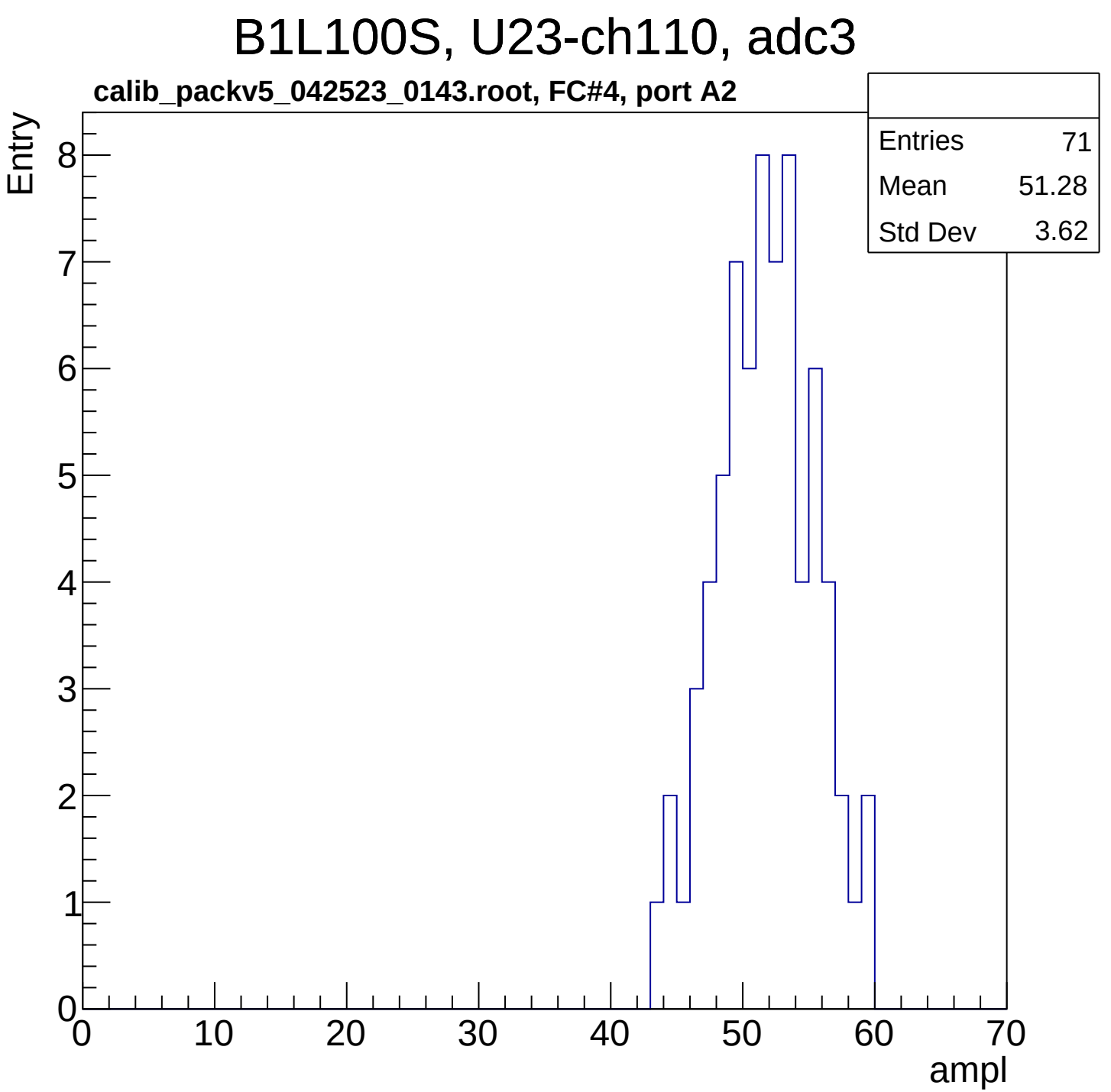
40
50
60
70


B1L100S, U23-ch110, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 51.28
Std Dev 3.62
Entry
ampl
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1
2
3
4
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6
7
8
0
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20
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40
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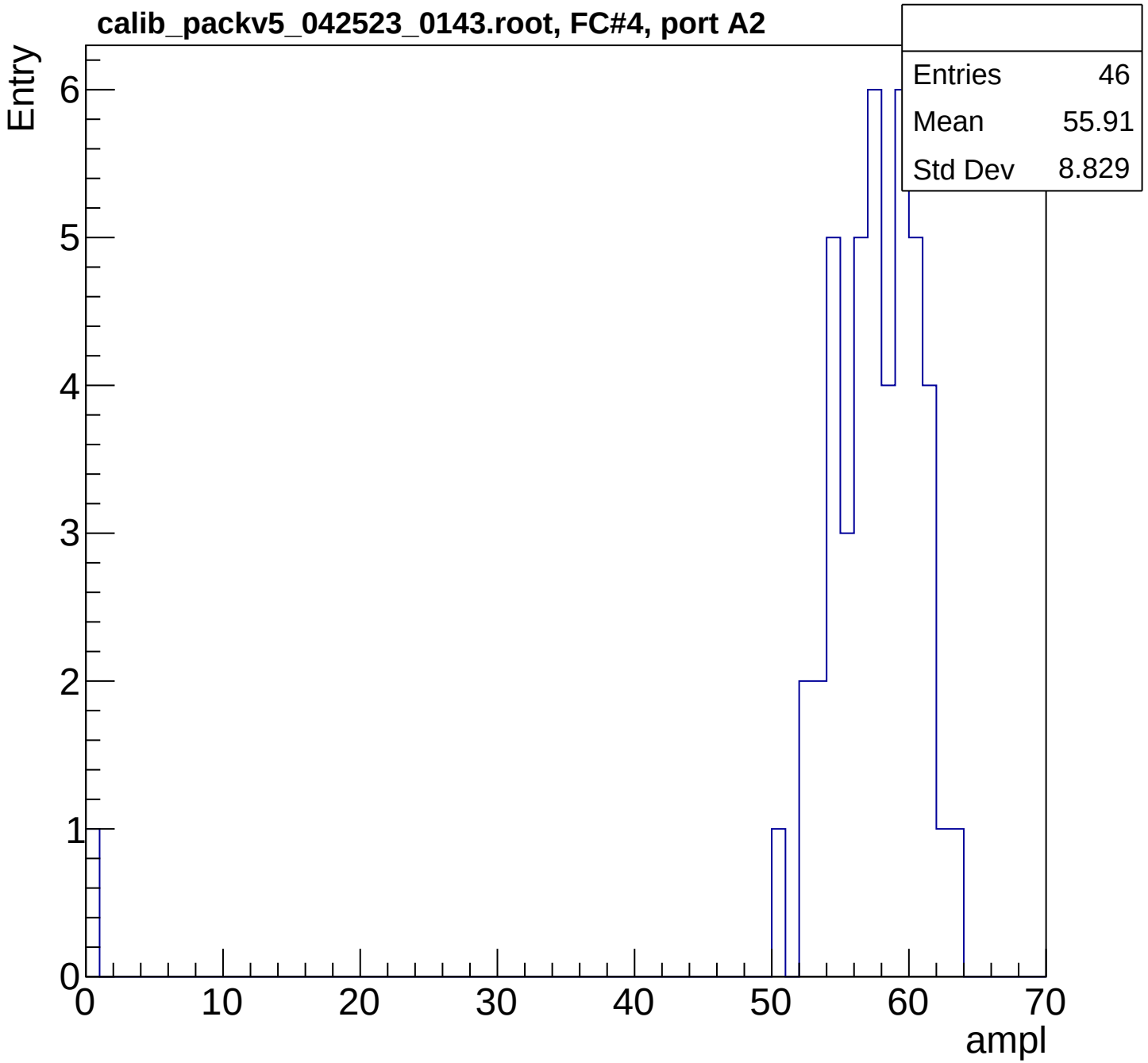
60
70


B1L100S, U23-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 55.91
Std Dev 8.829
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
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60
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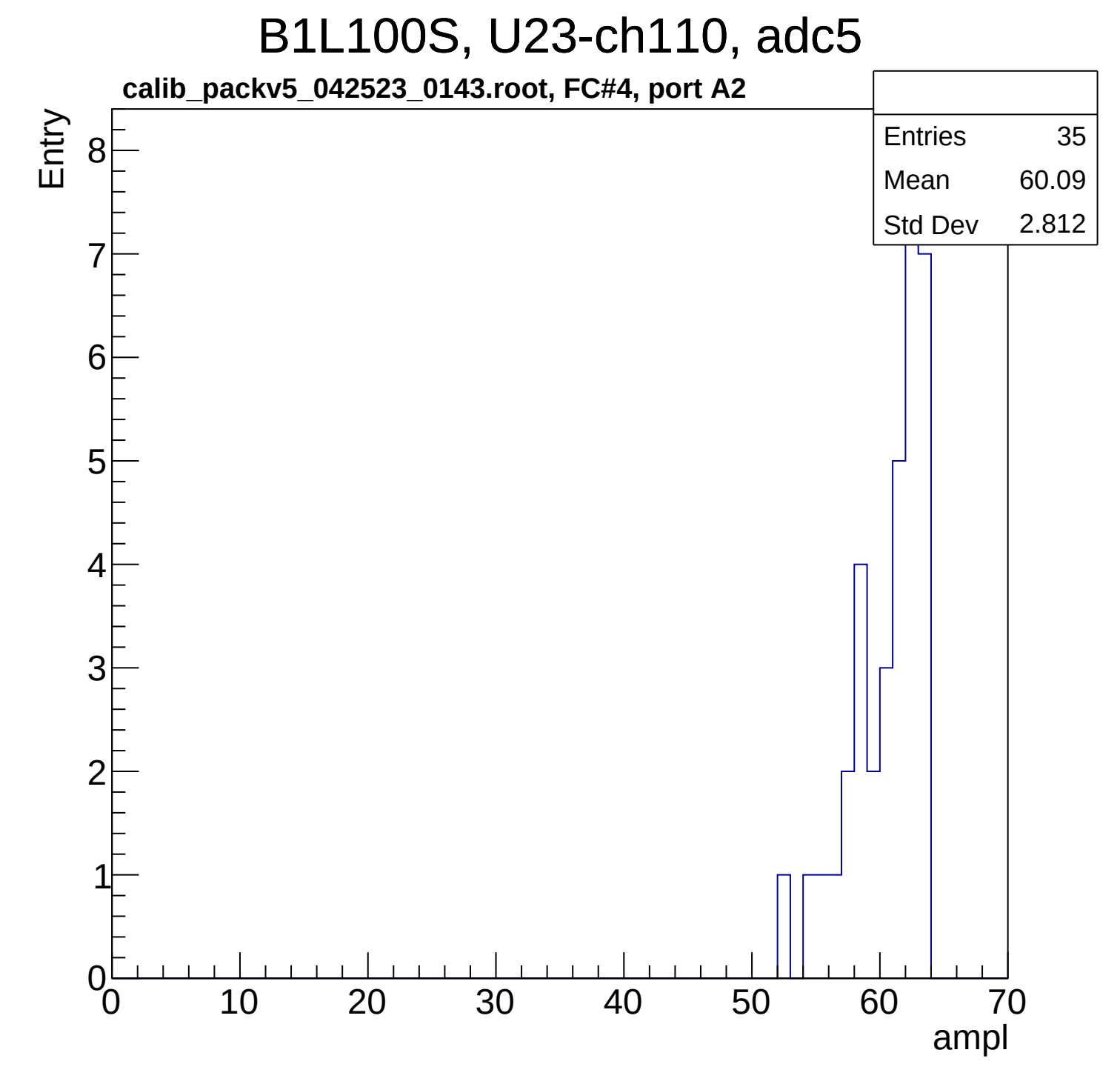
B1L100S, U23-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 60.09
Std Dev 2.812
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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60
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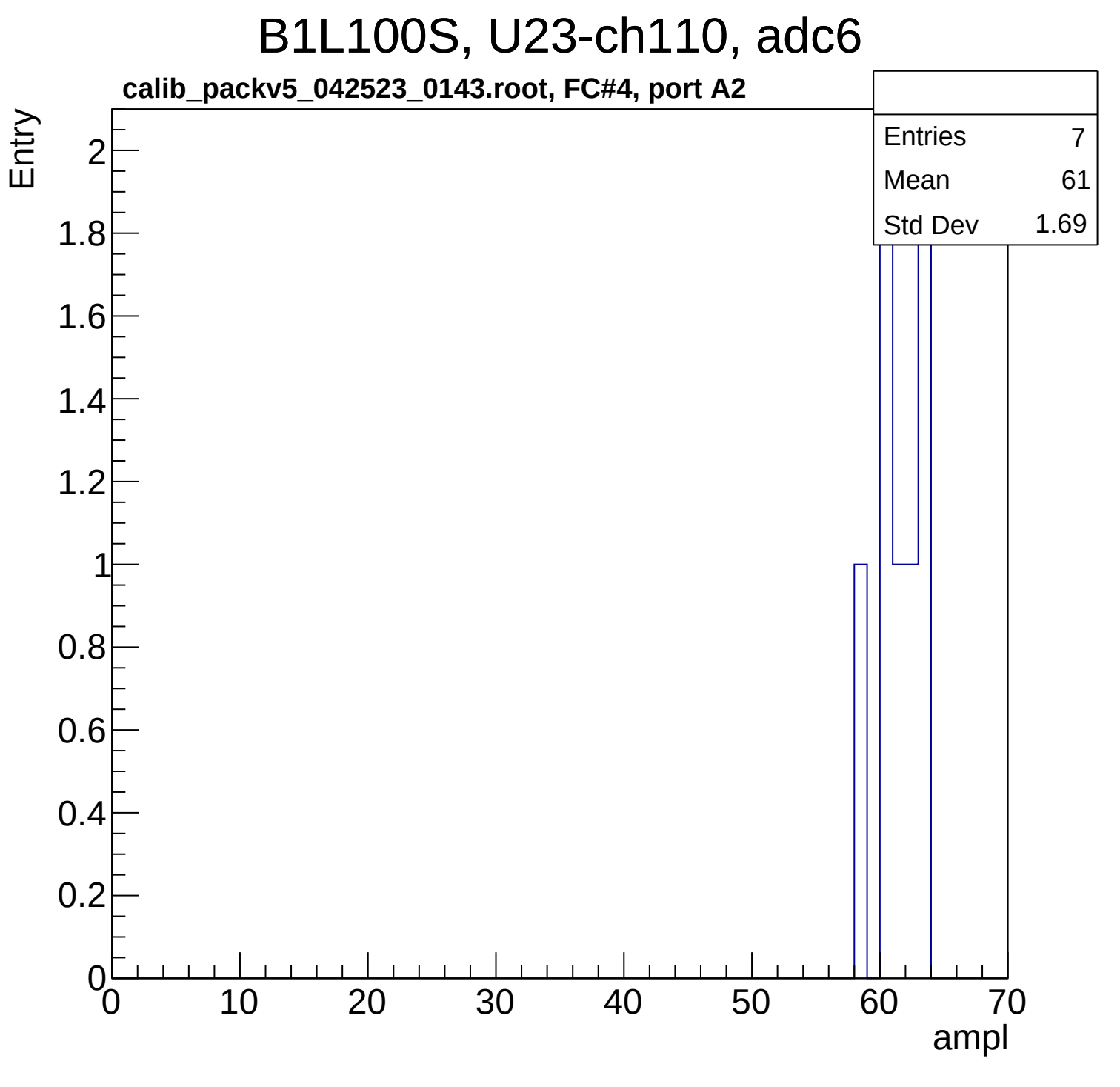
B1L100S, U23-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61
Std Dev 1.69
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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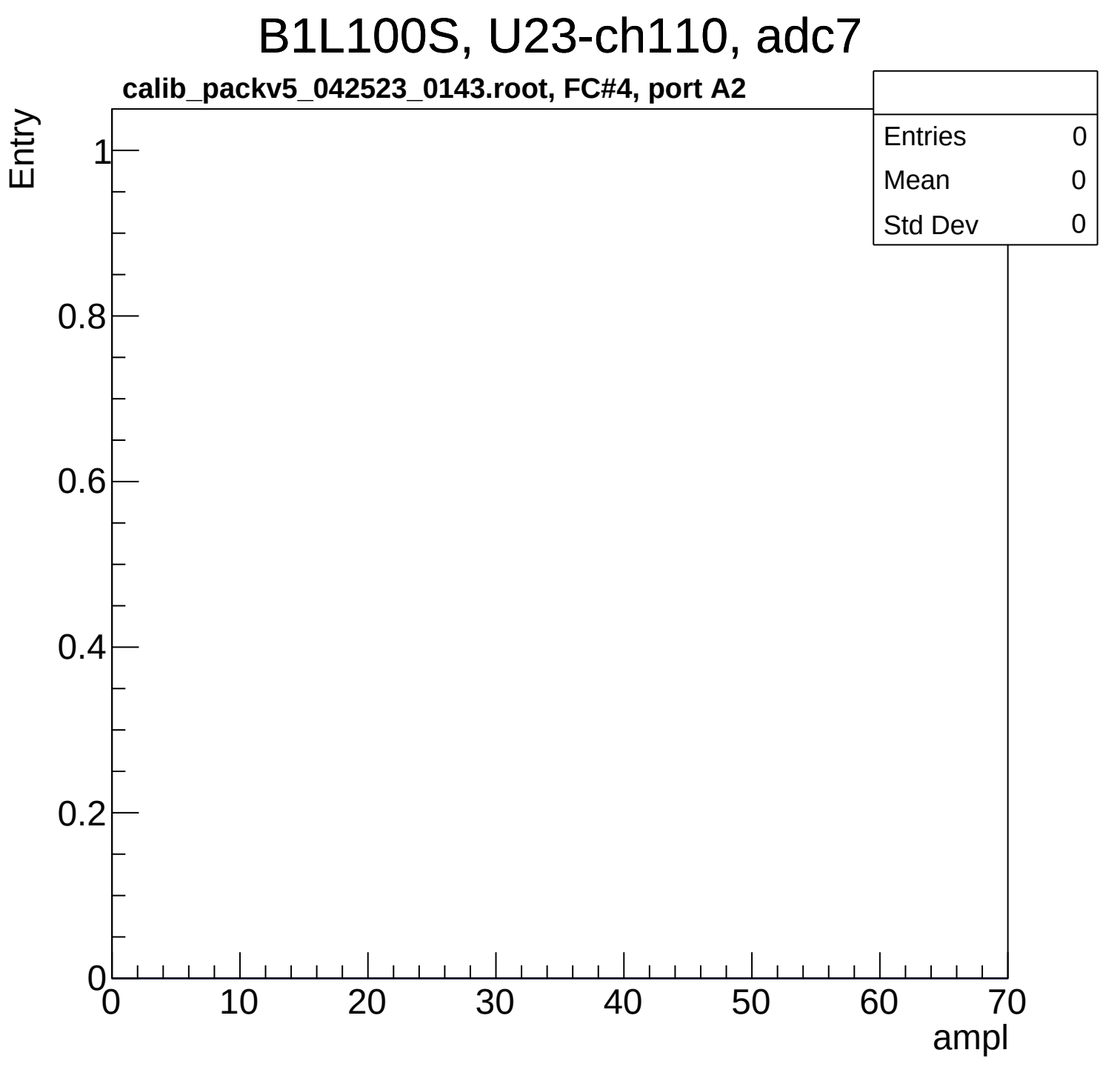
40
50
60
70


B1L100S, U23-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
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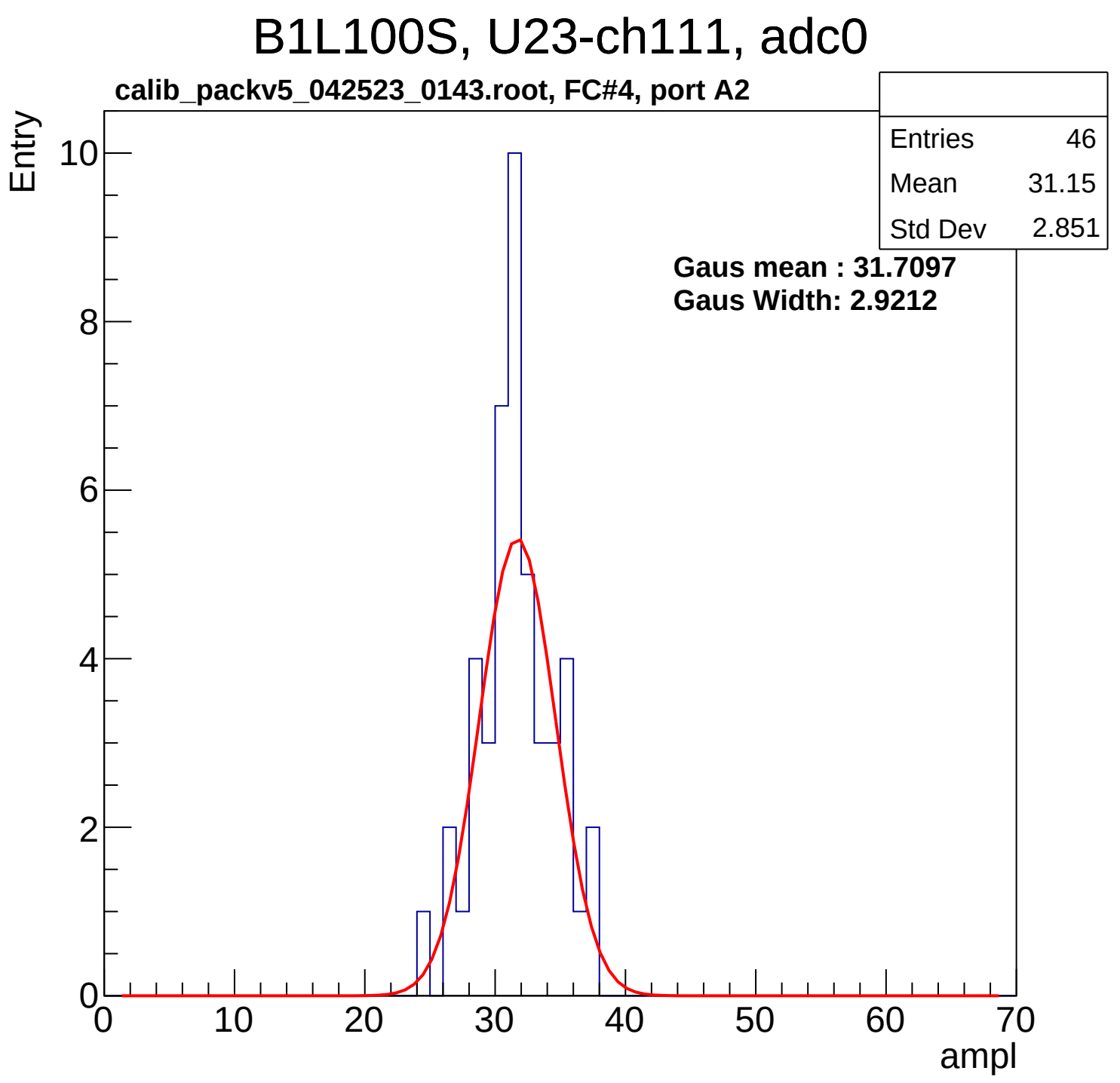
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch111, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 31.15
Std Dev 2.851
Gaus mean : 31.7097
Gaus Width: 2.9212
Entry
ampl
0
2
4
6
8
10
0
10
20
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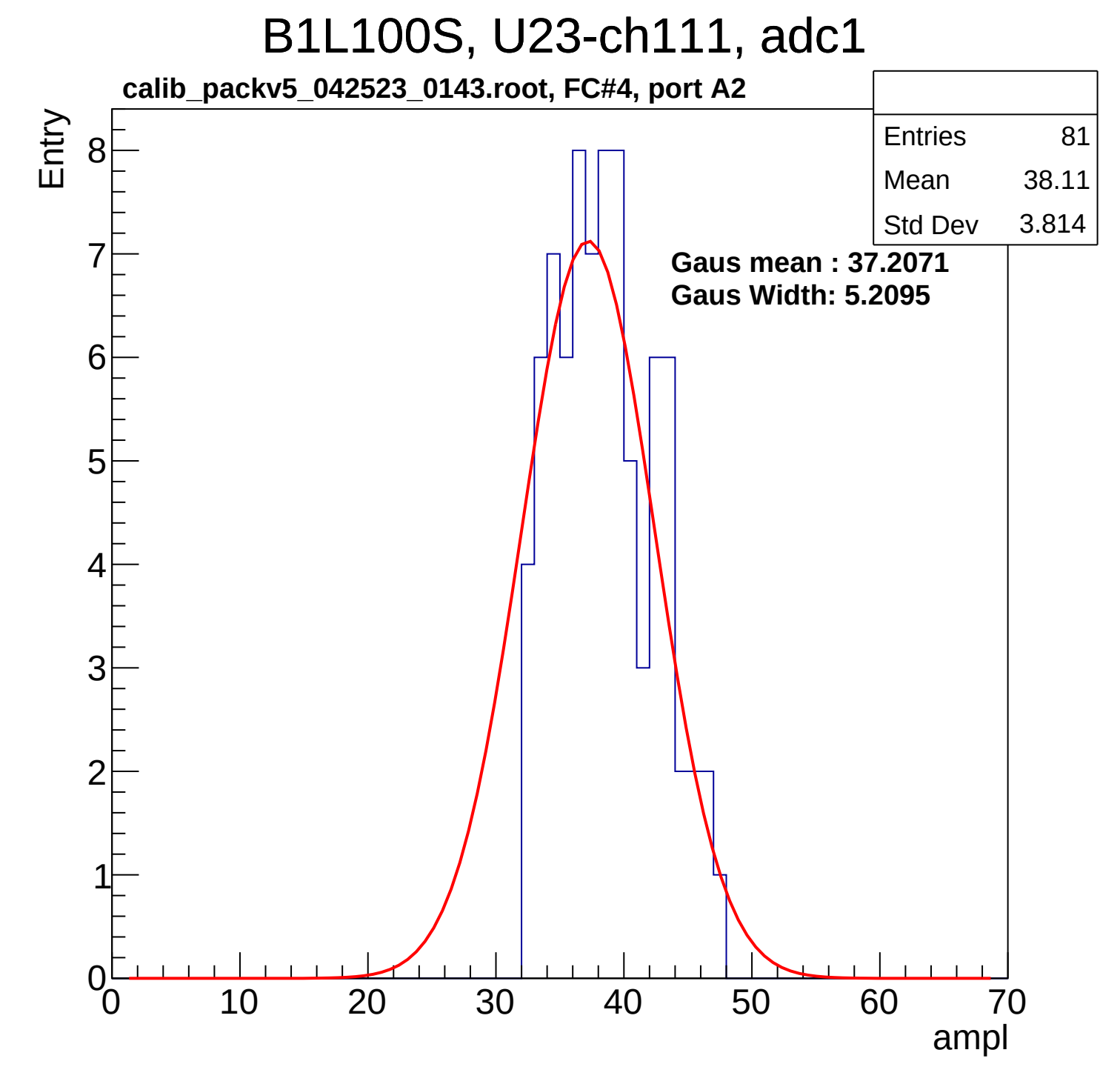
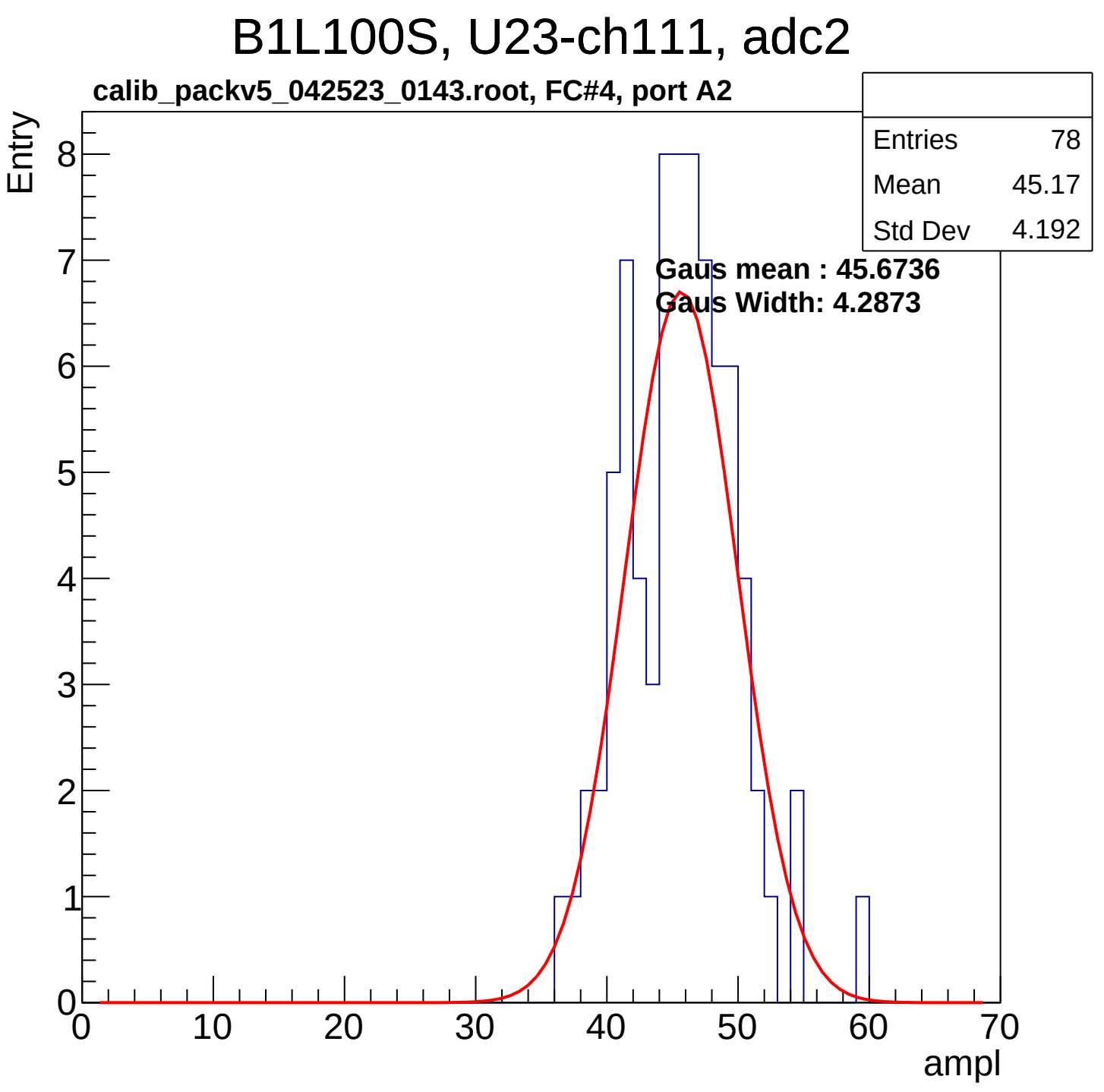
40
50
60
70


B1L100S, U23-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 38.11
Std Dev 3.814
Gaus mean : 37.2071
Gaus Width: 5.2095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 45.17
Std Dev 4.192
Gaus mean : 45.6736
Gaus Width: 4.2873
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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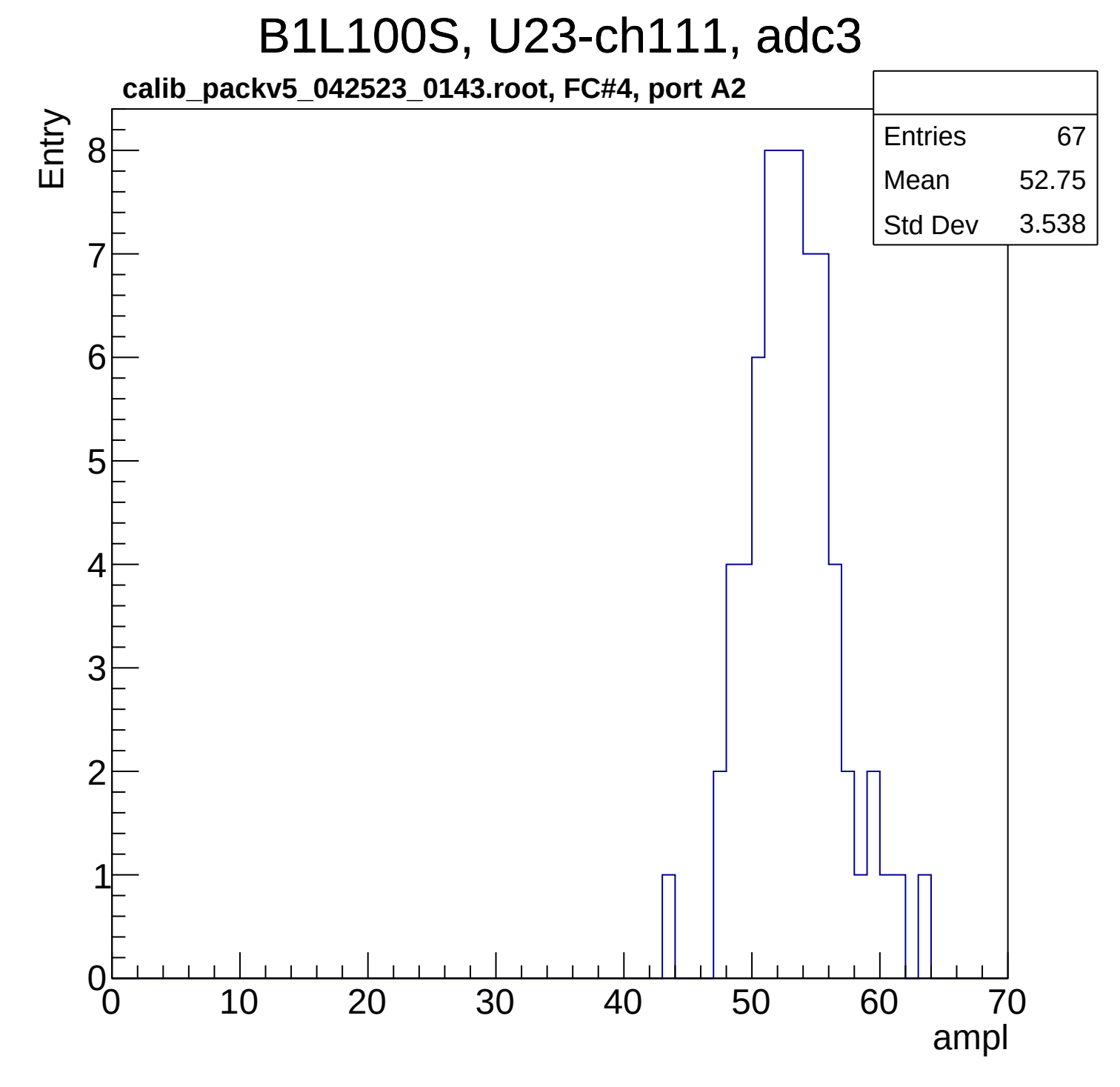
B1L100S, U23-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 52.75
Std Dev 3.538
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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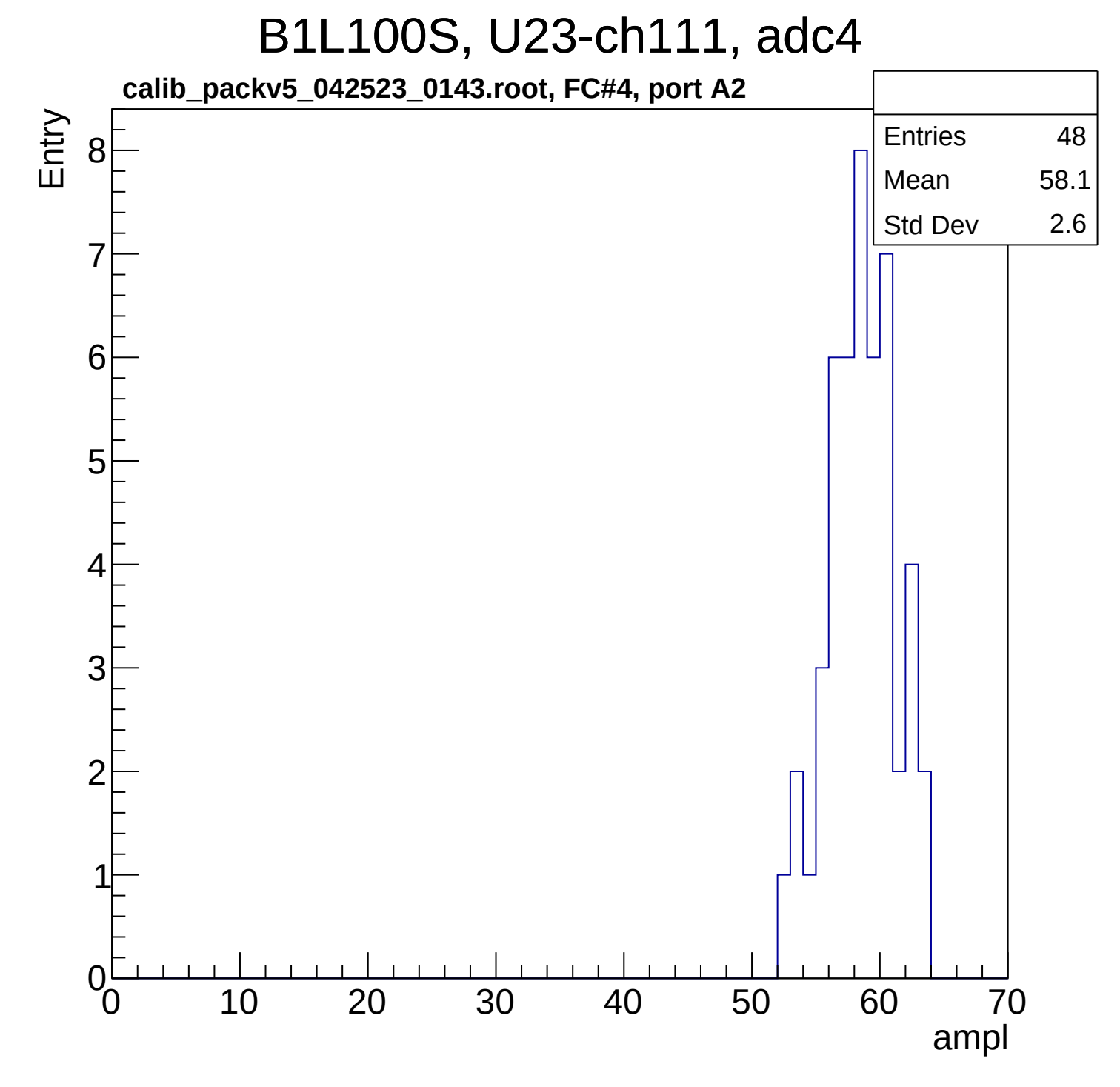
B1L100S, U23-ch111, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.1
Std Dev 2.6
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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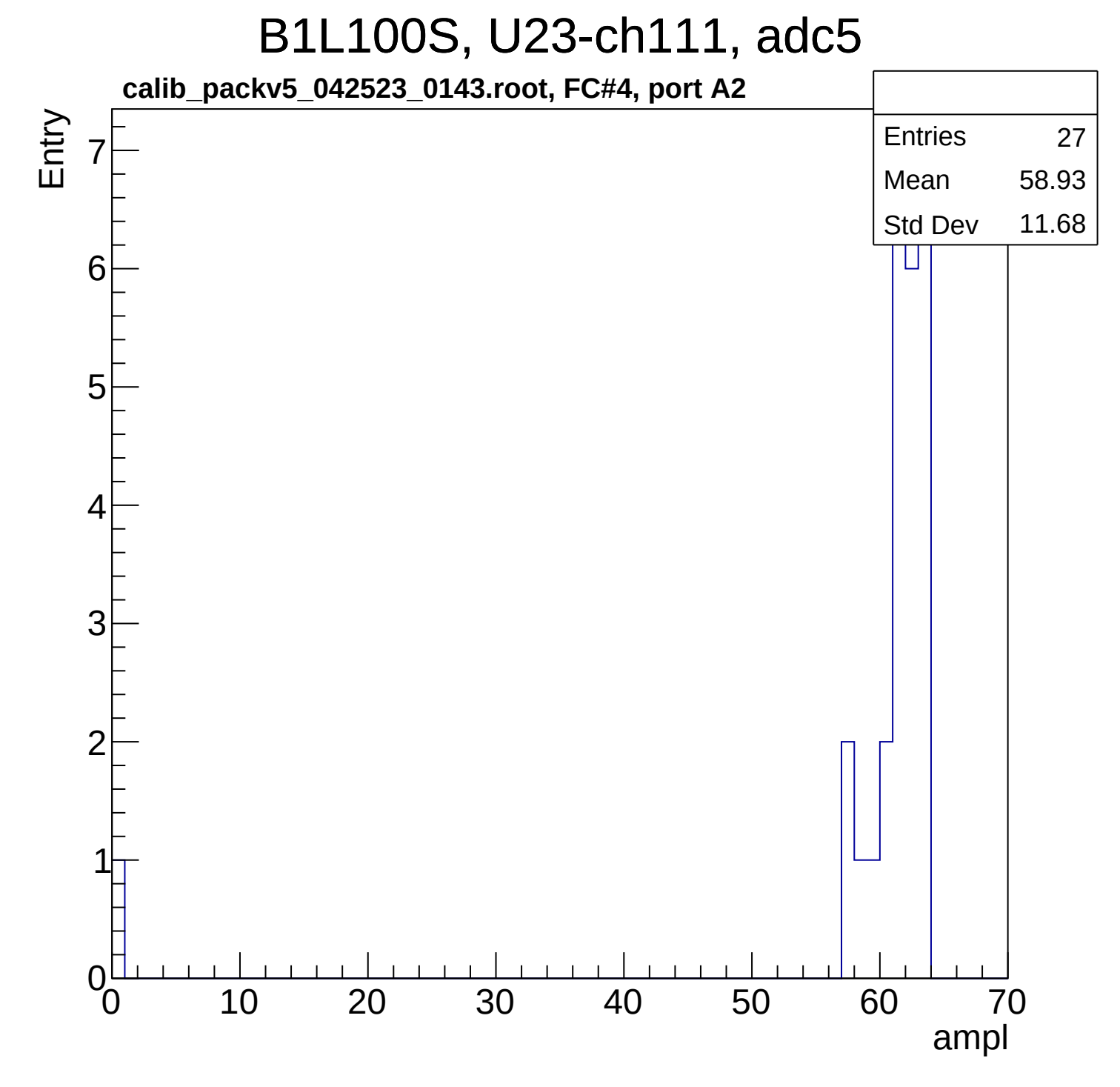
B1L100S, U23-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.93
Std Dev 11.68
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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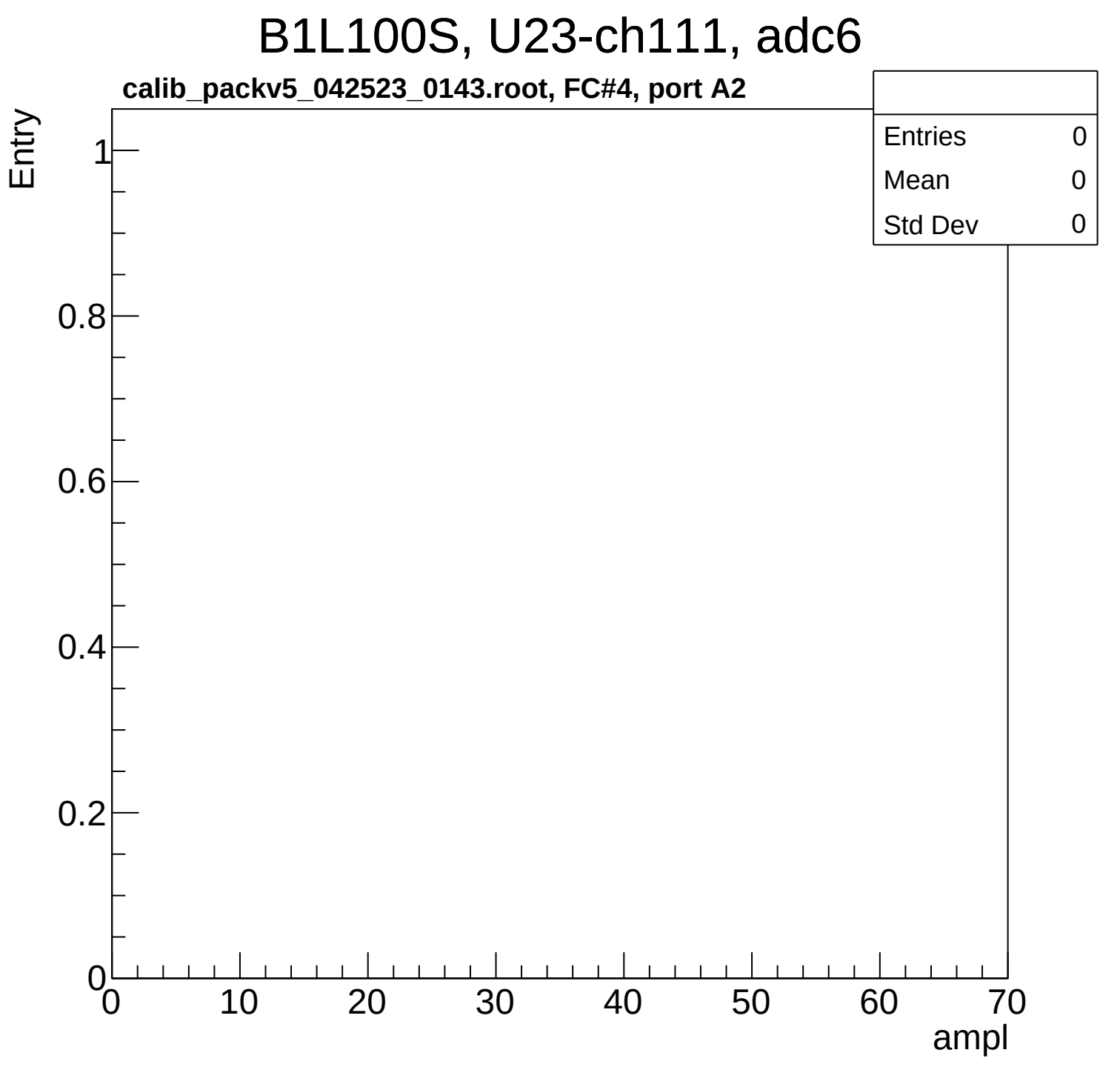
B1L100S, U23-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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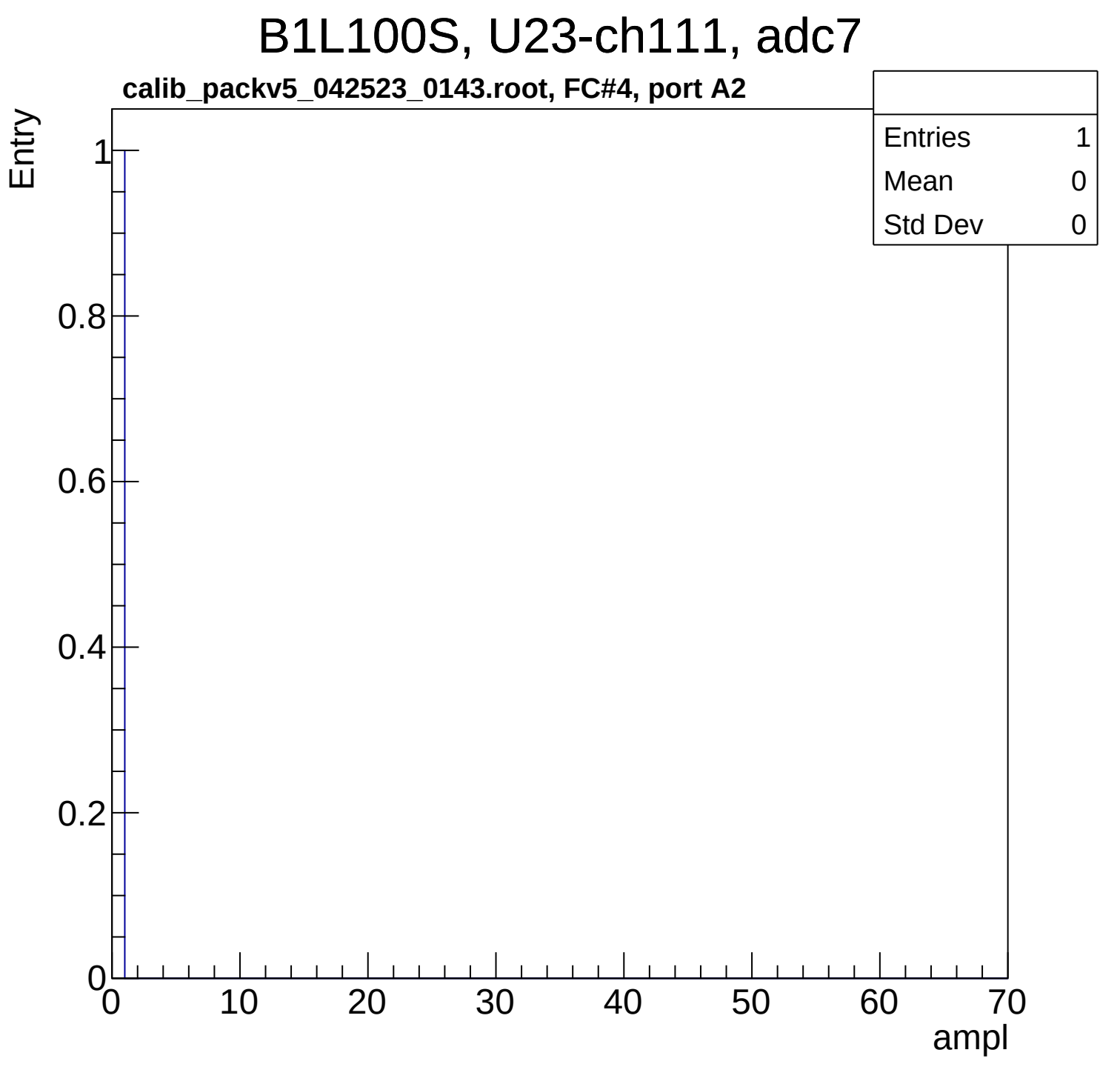
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch111, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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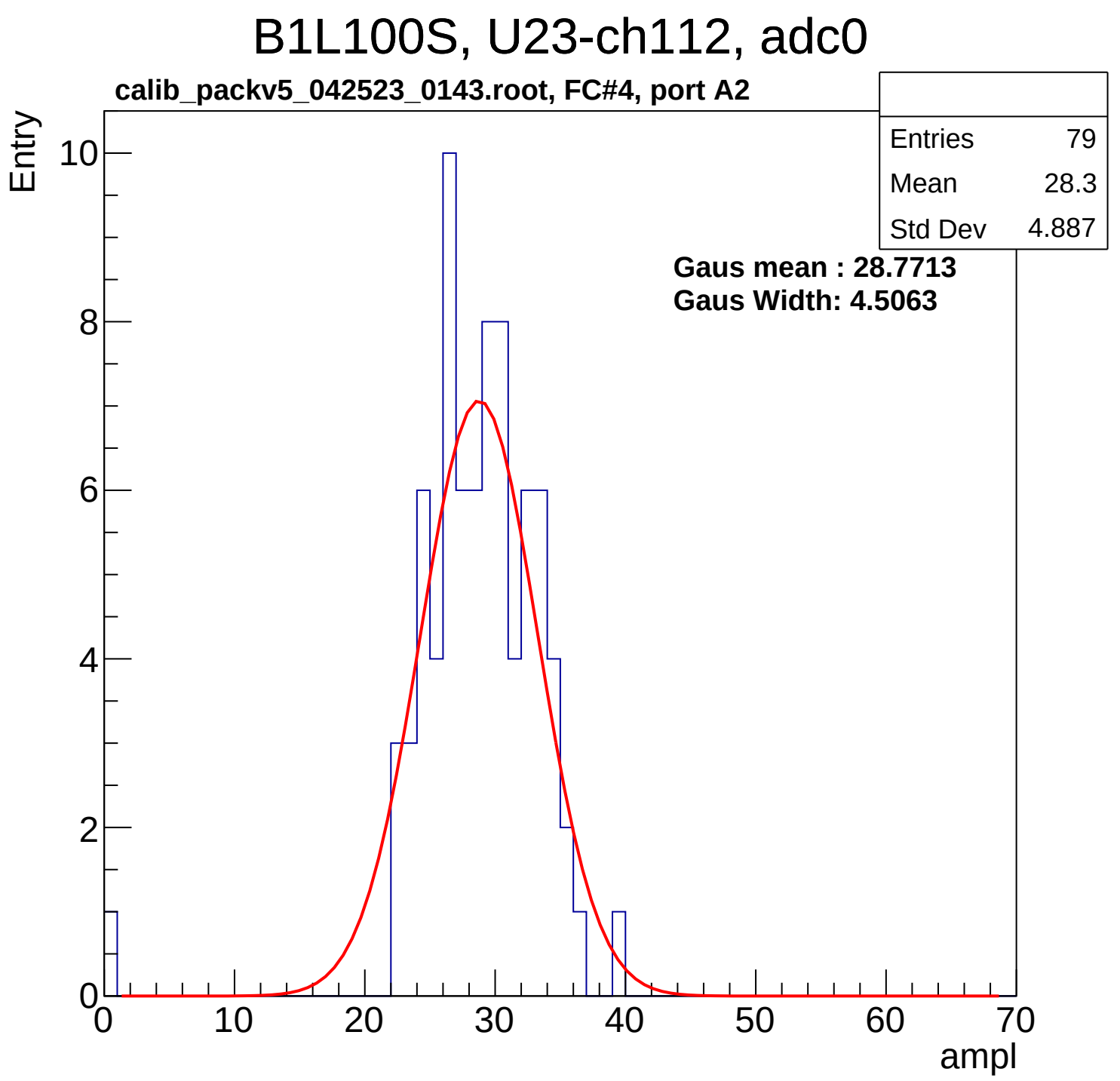
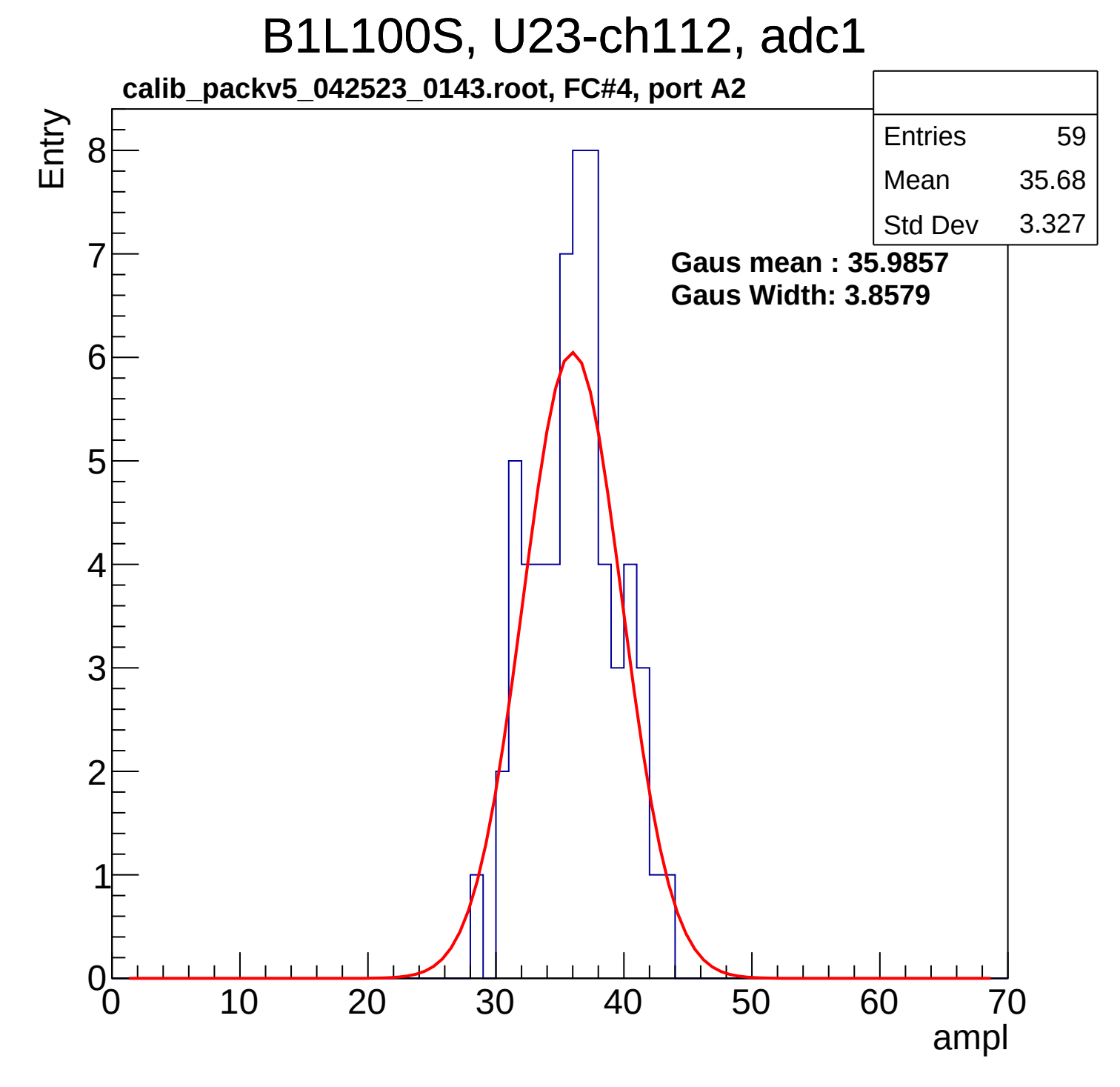
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.3
Std Dev 4.887
Gaus mean : 28.7713
Gaus Width: 4.5063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.68
Std Dev 3.327
Gaus mean : 35.9857
Gaus Width: 3.8579
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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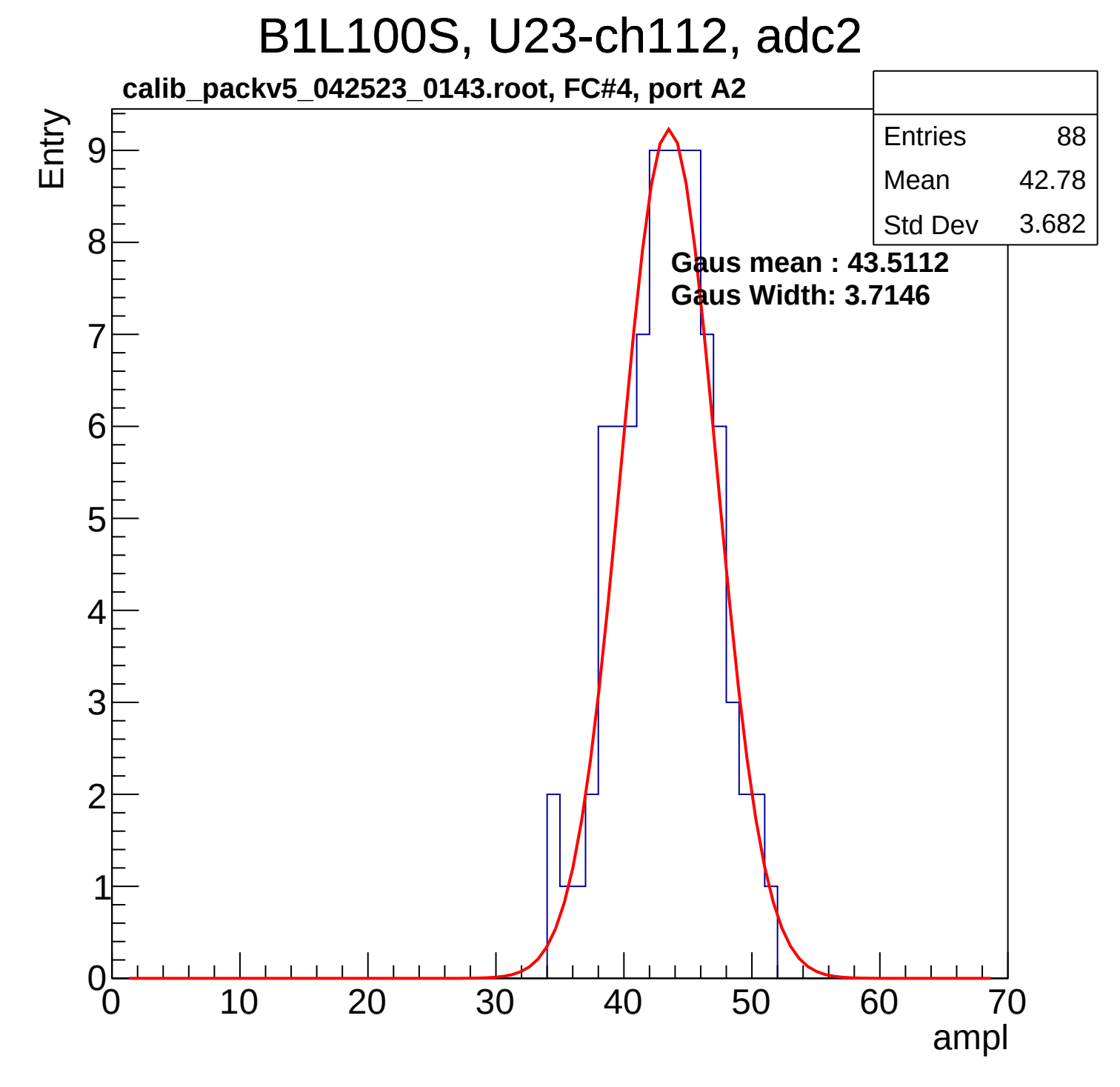
B1L100S, U23-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 42.78
Std Dev 3.682
Gaus mean : 43.5112
Gaus Width: 3.7146
Entry
ampl
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2
3
4
5
6
7
8
9
0
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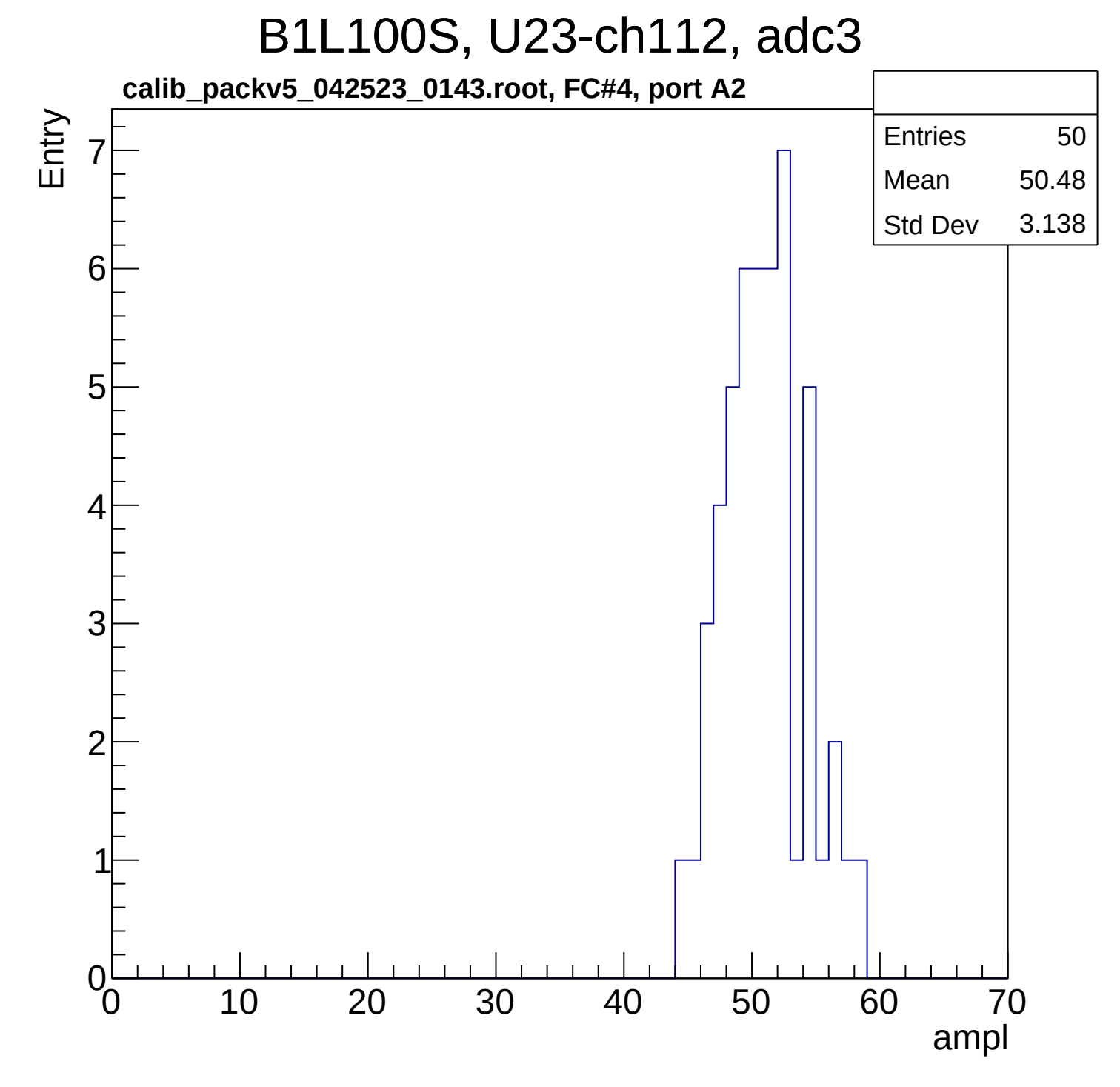
B1L100S, U23-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 50.48
Std Dev 3.138
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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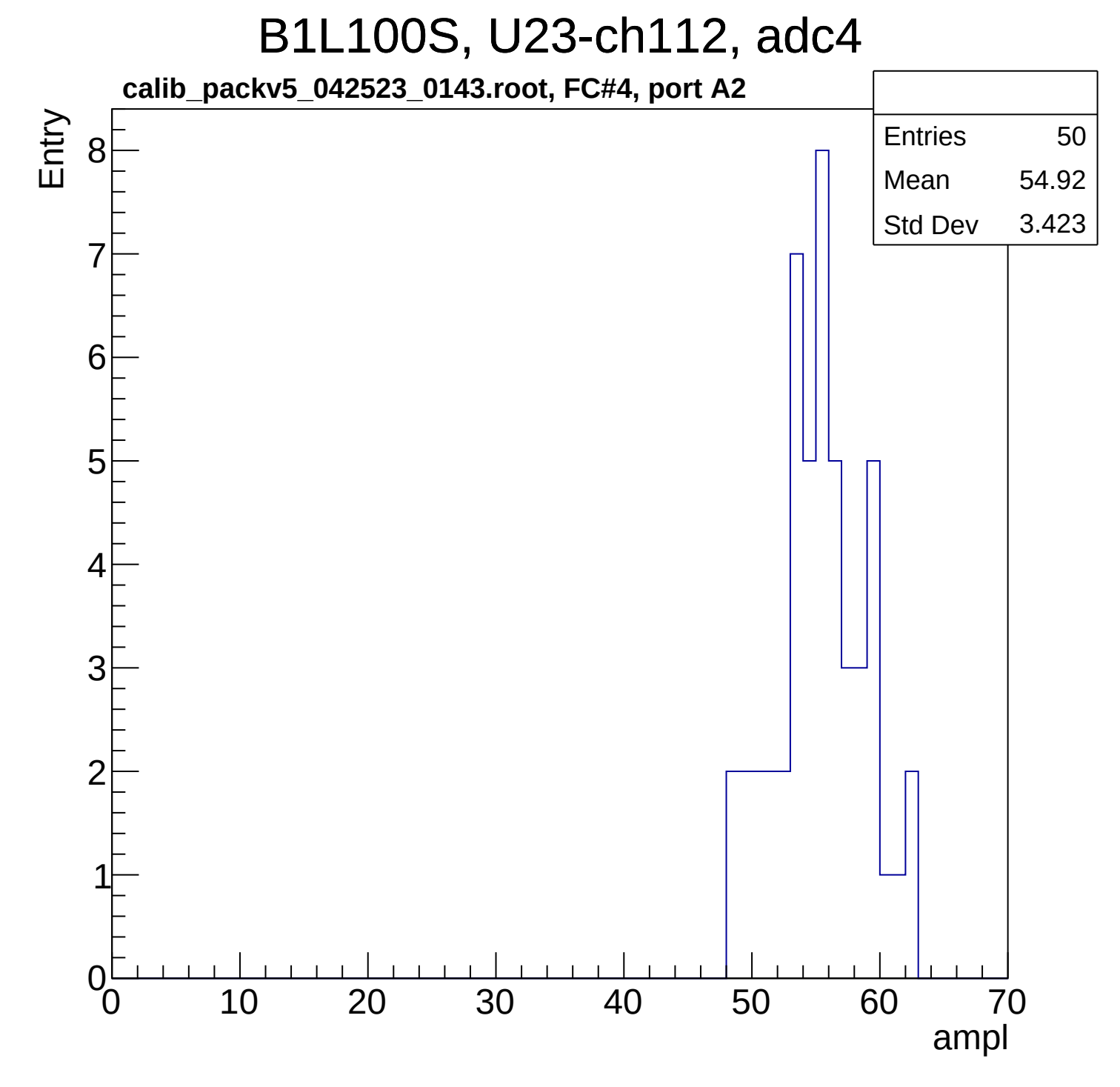
B1L100S, U23-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.92
Std Dev 3.423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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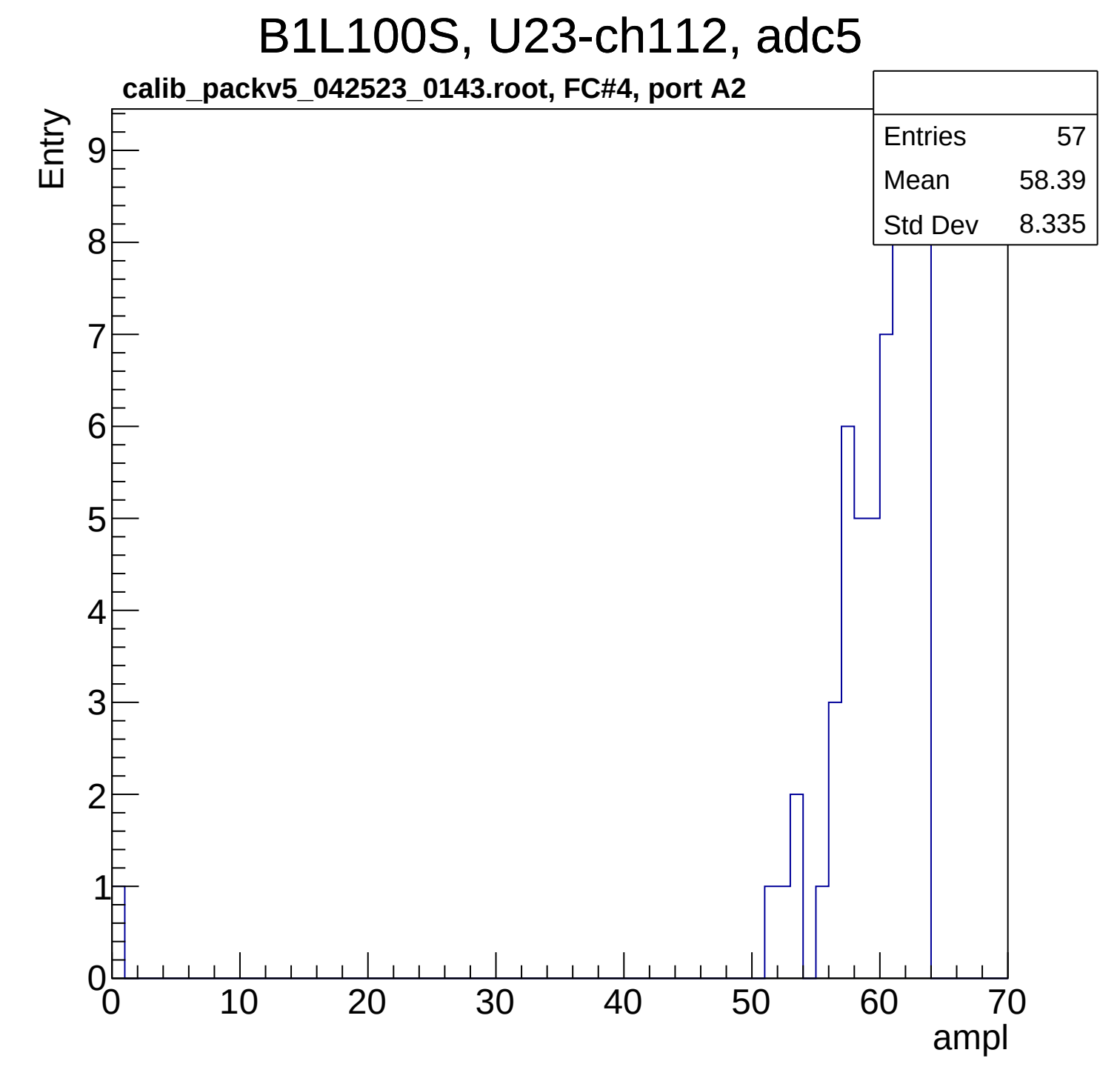
B1L100S, U23-ch112, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 58.39
Std Dev 8.335
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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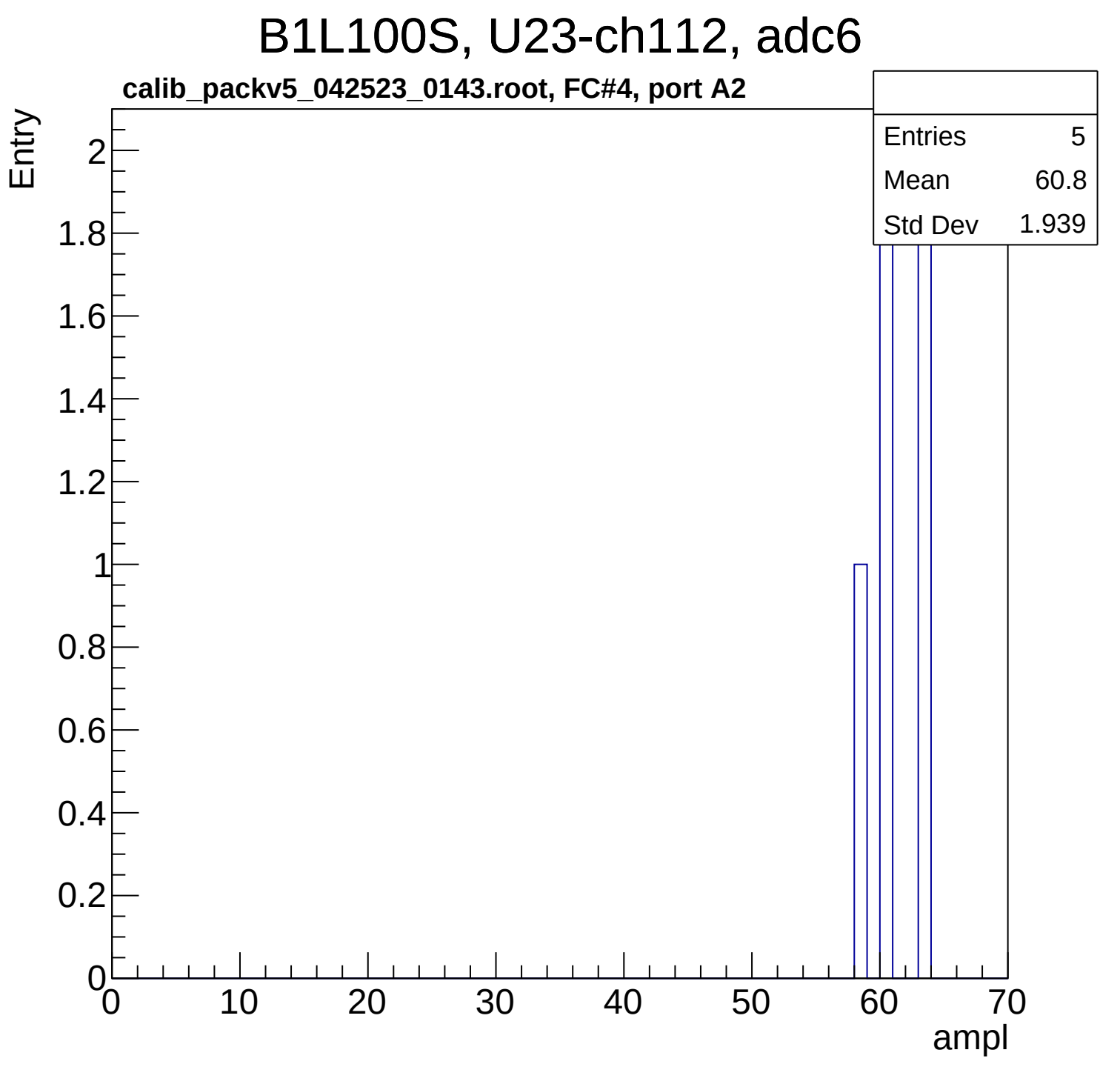
B1L100S, U23-ch112, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 60.8
Std Dev 1.939
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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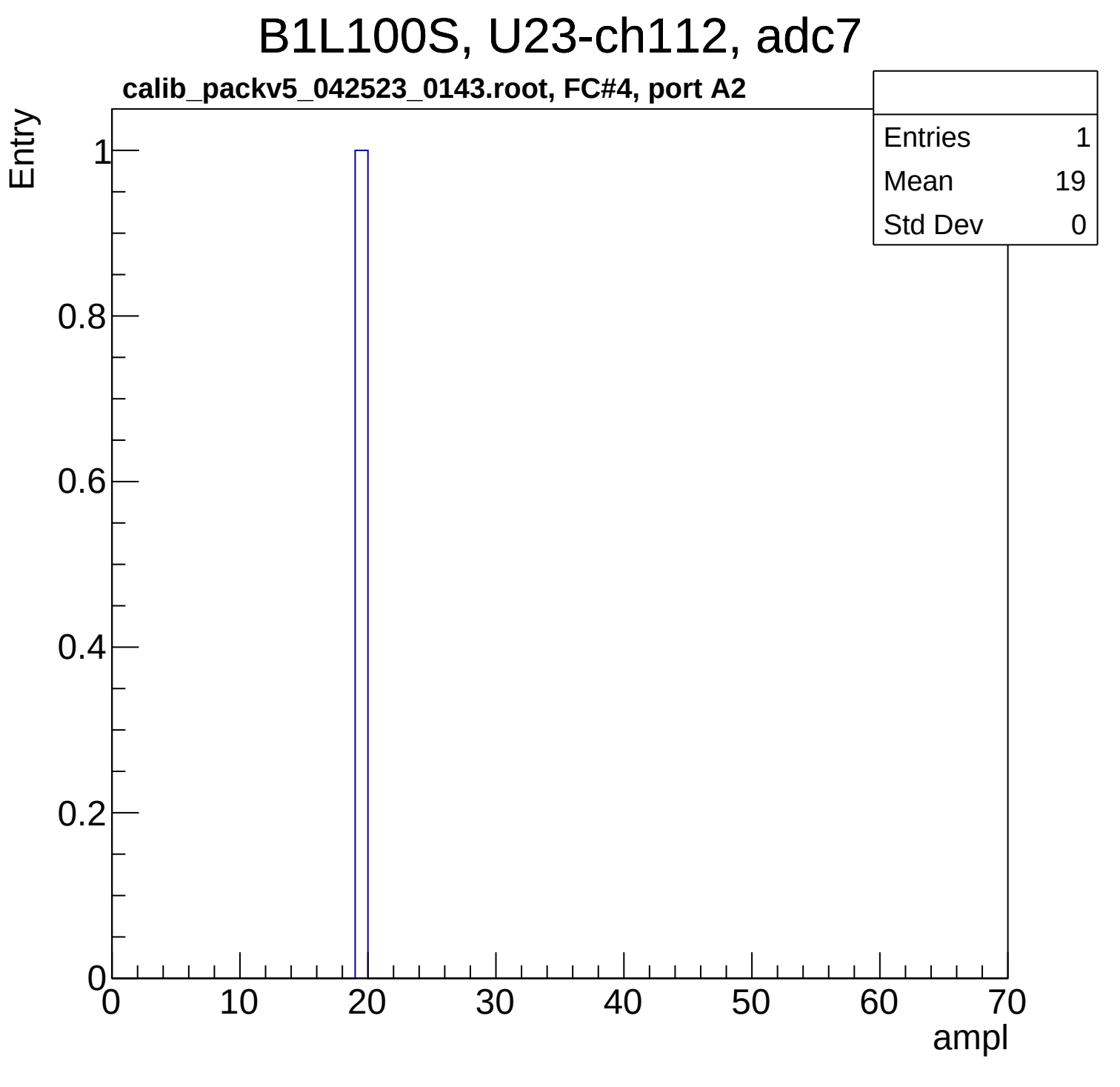
B1L100S, U23-ch112, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 19
Std Dev 0
Entry
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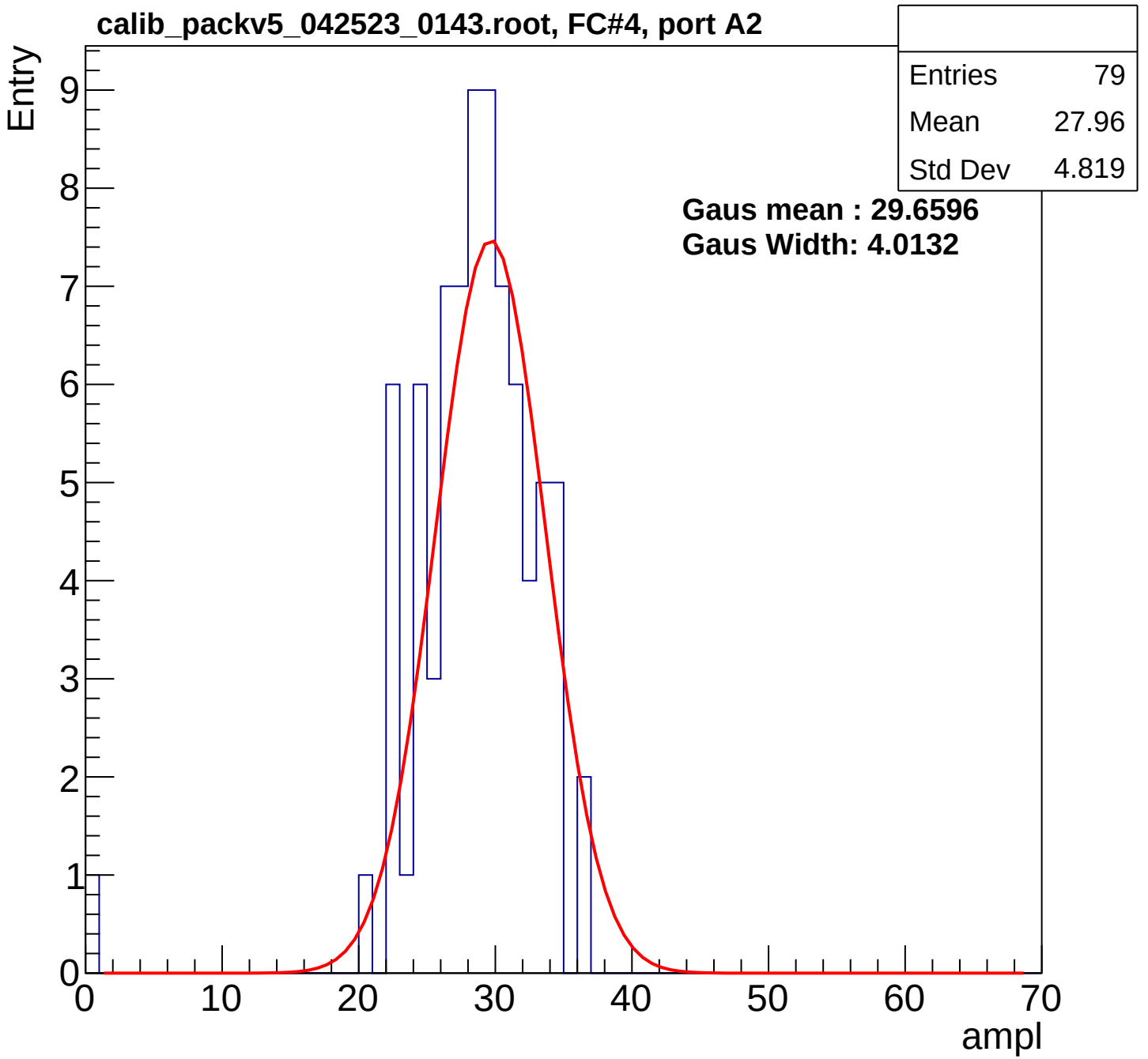
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 27.96
Std Dev 4.819
Gaus mean : 29.6596
Gaus Width: 4.0132
Entry
ampl
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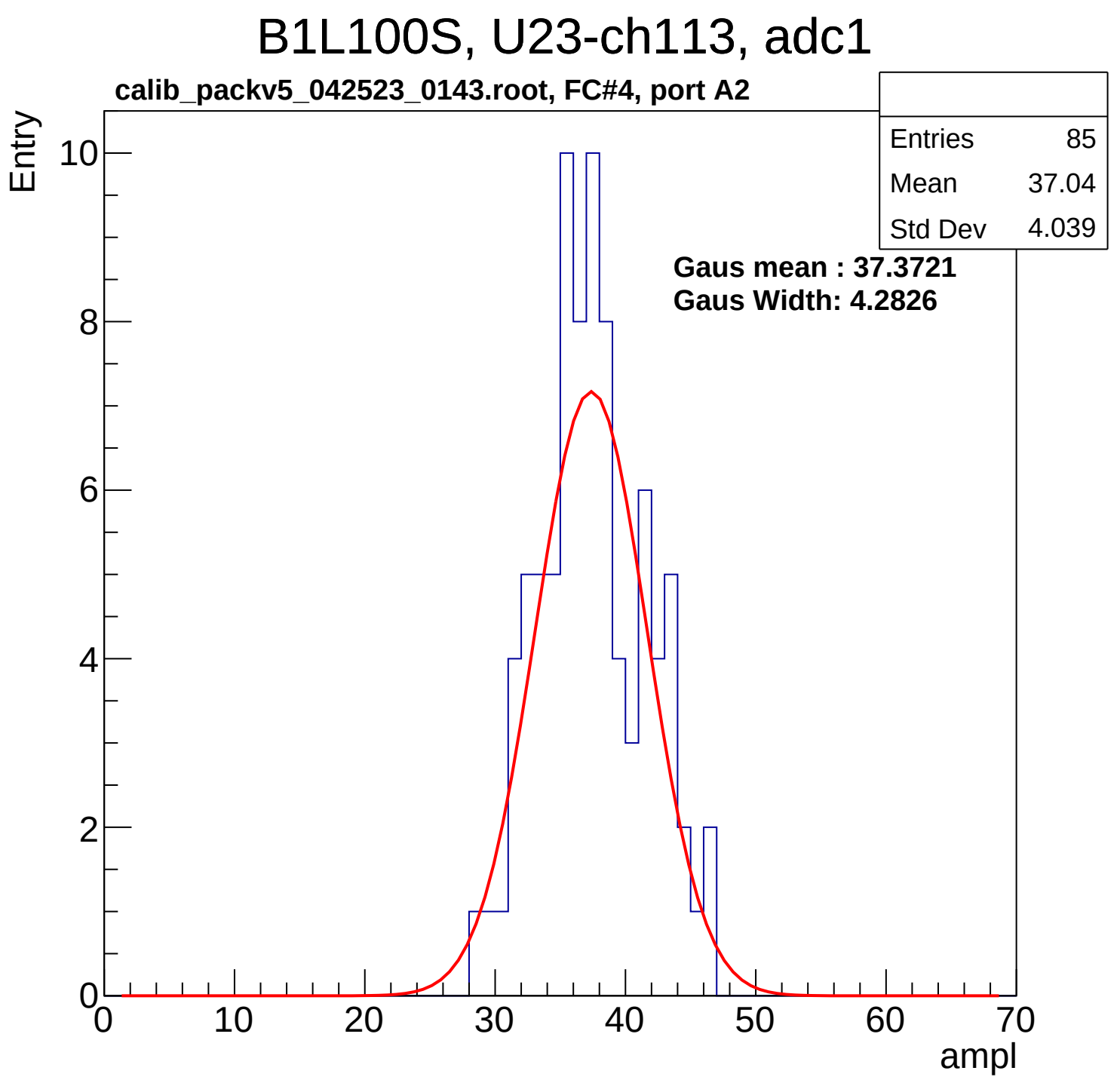
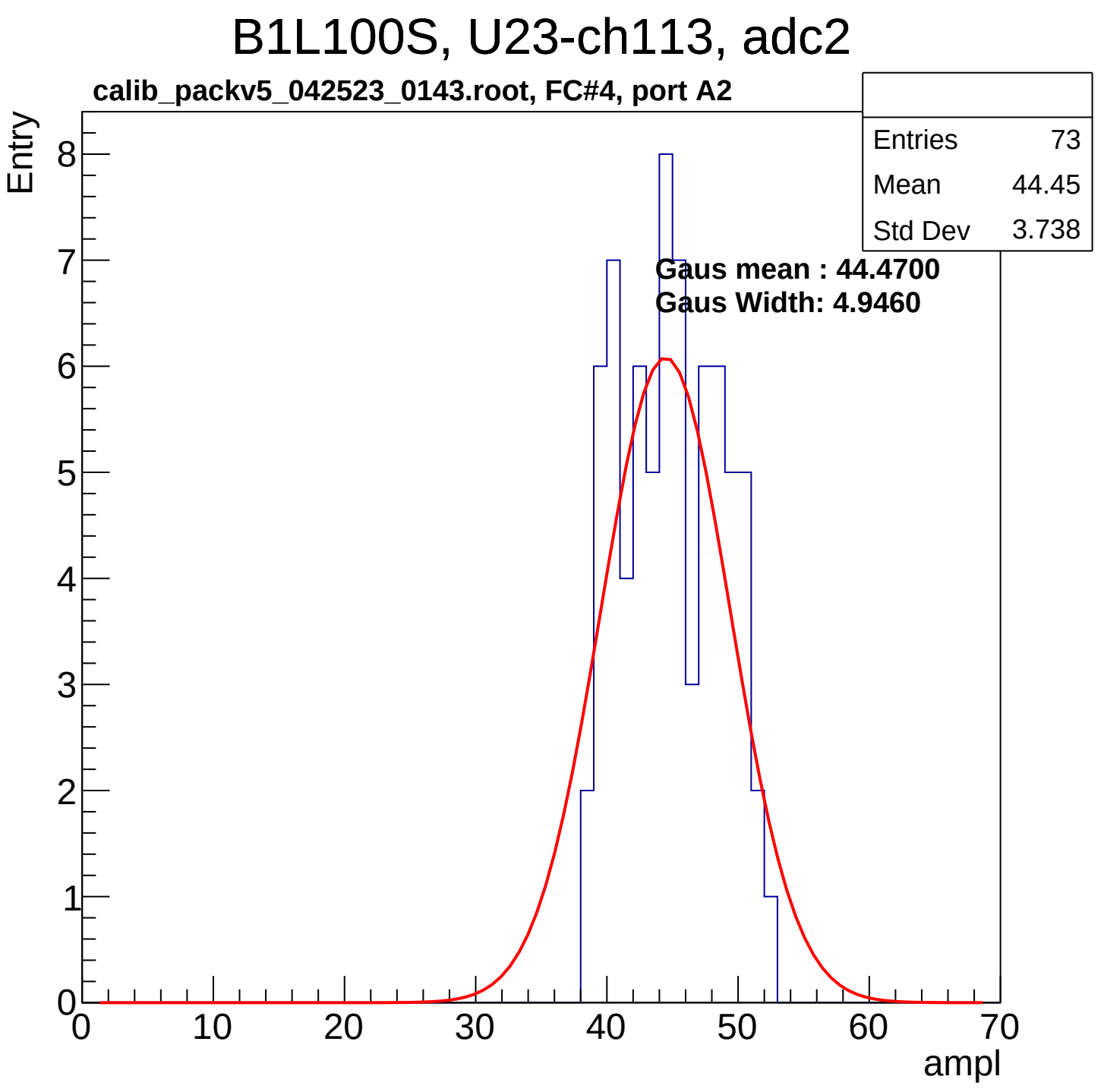
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 37.04
Std Dev 4.039
Gaus mean : 37.3721
Gaus Width: 4.2826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 44.45
Std Dev 3.738
Gaus mean : 44.4700
Gaus Width: 4.9460
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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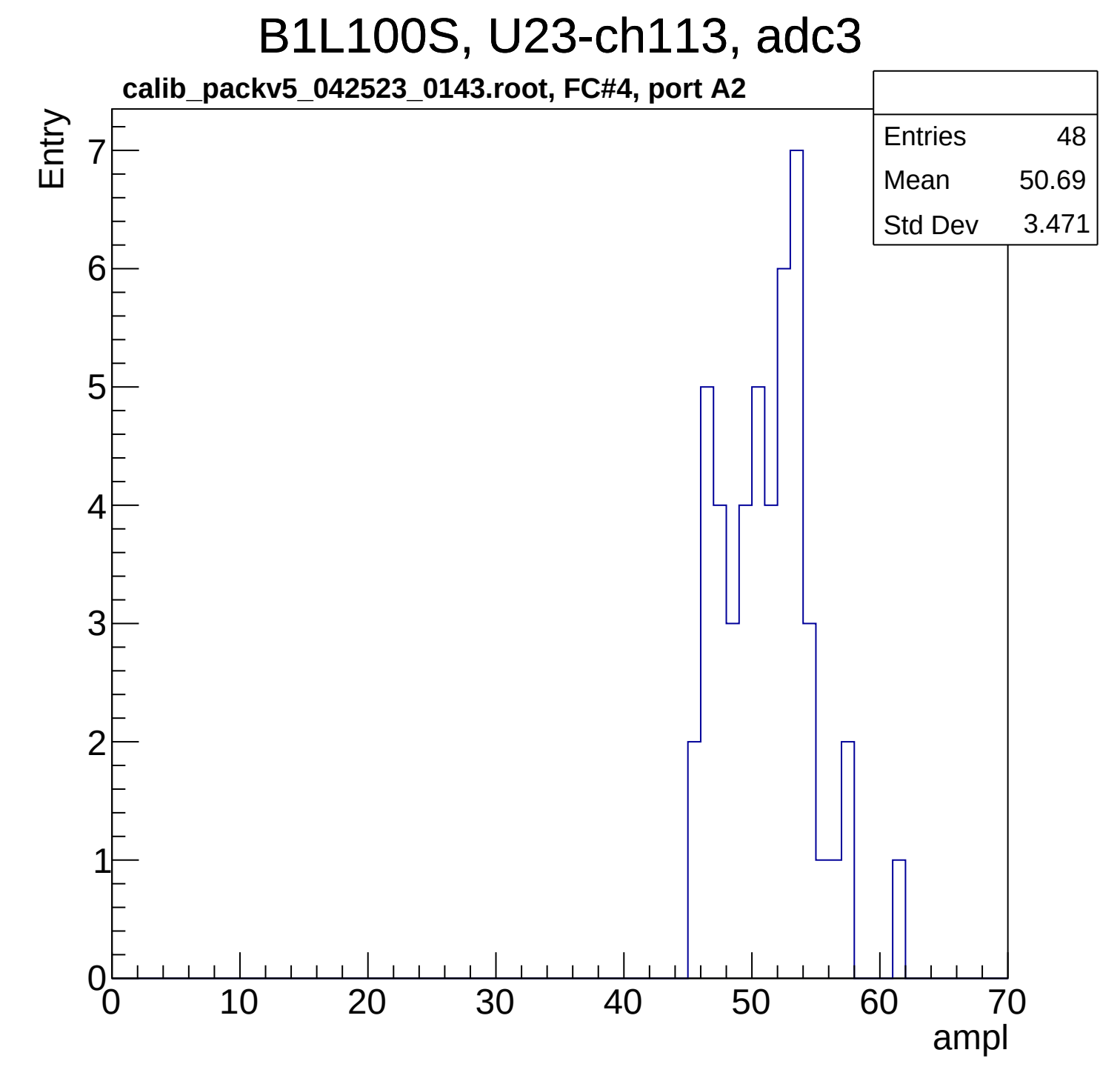
B1L100S, U23-ch113, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 50.69
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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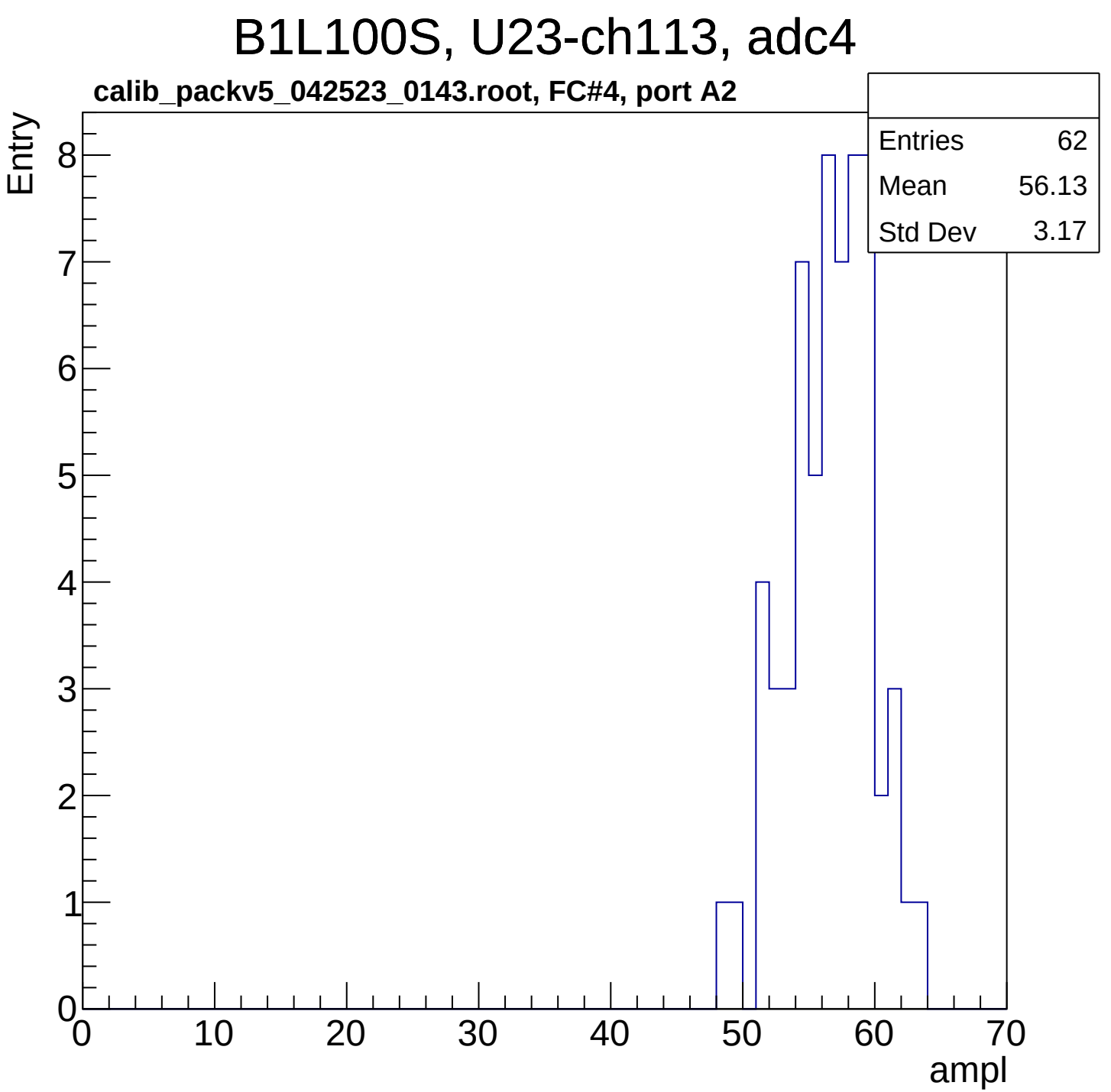
B1L100S, U23-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 56.13
Std Dev 3.17
Entry
ampl
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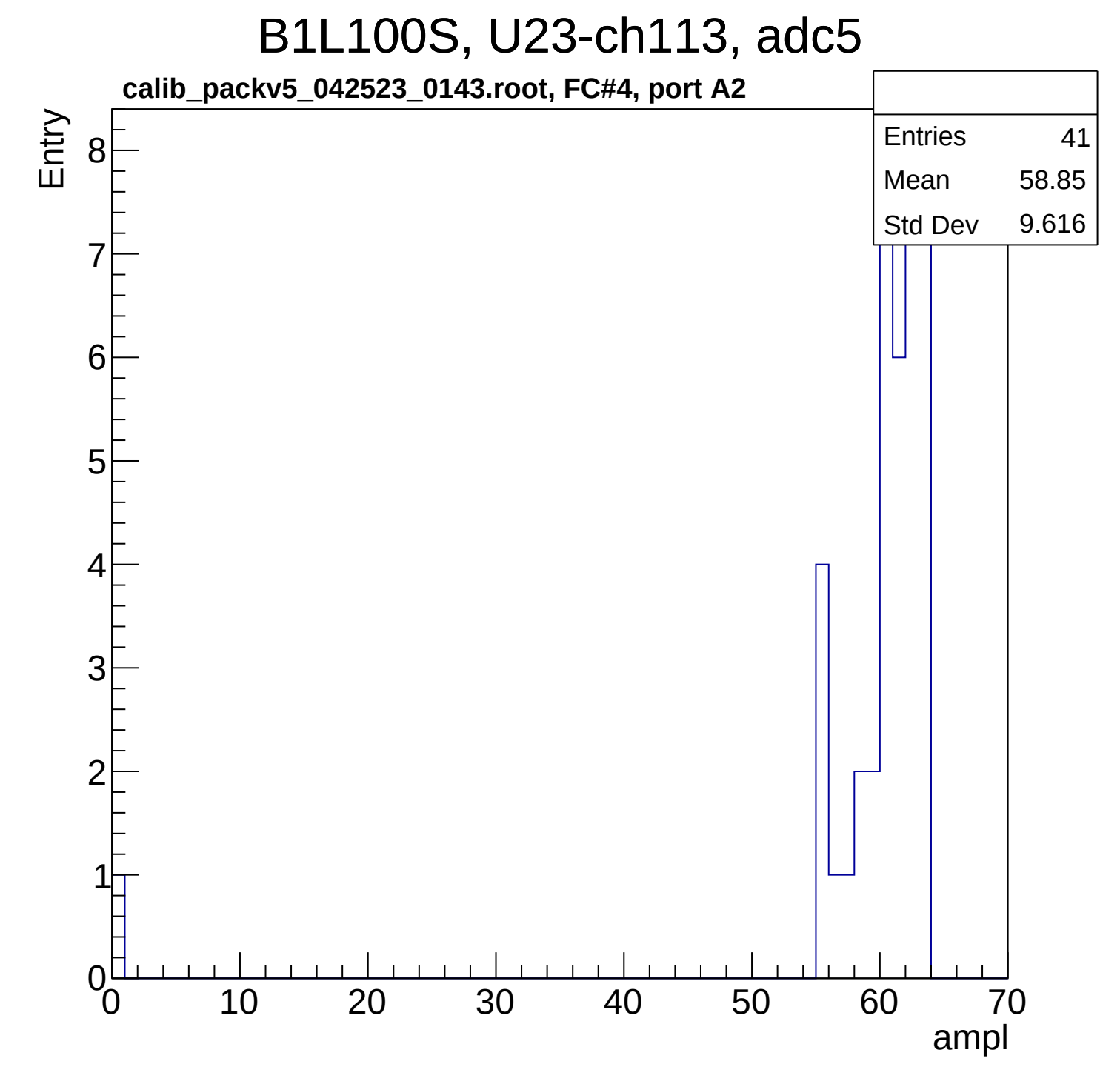
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.85
Std Dev 9.616
Entry
ampl
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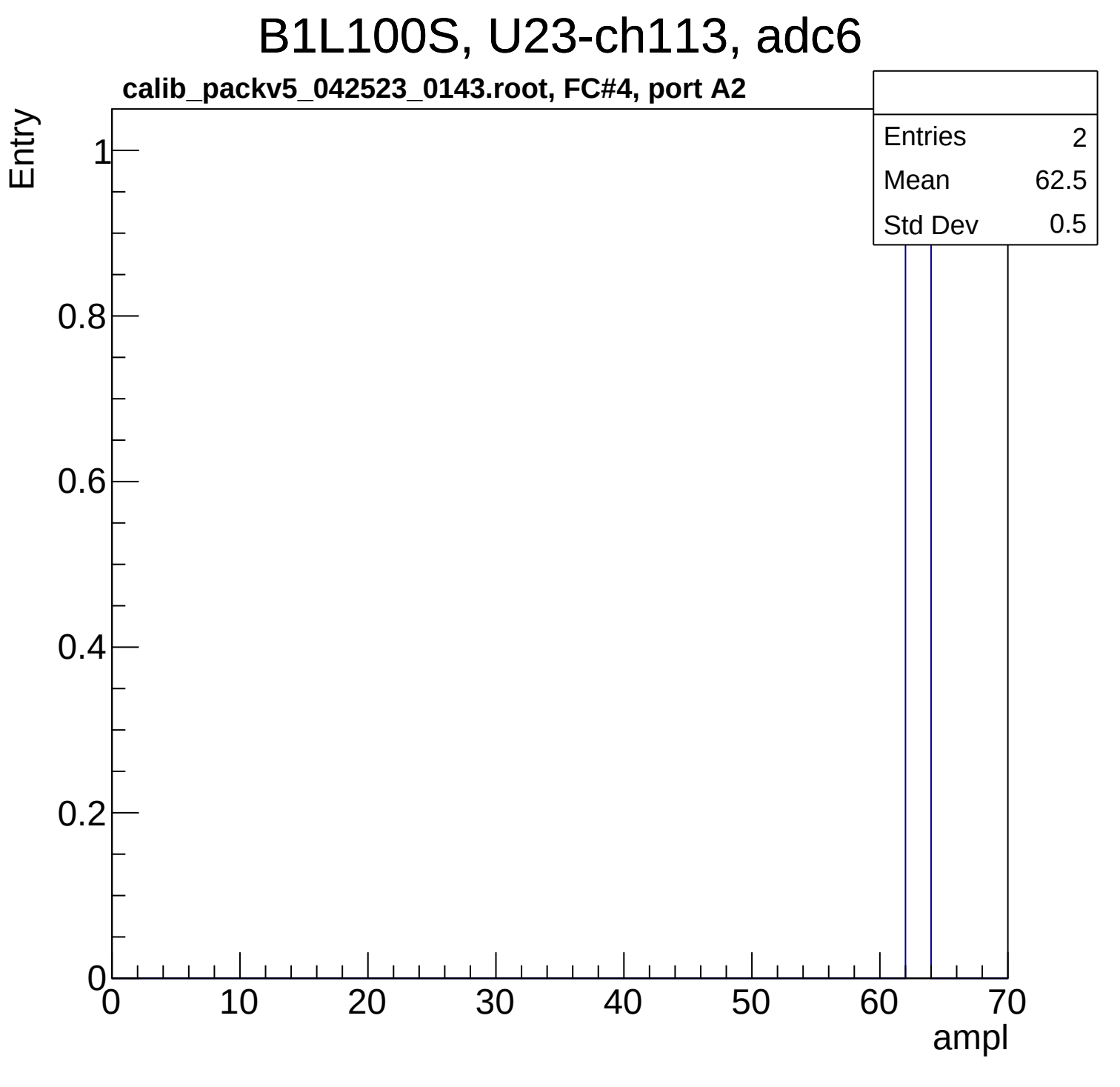
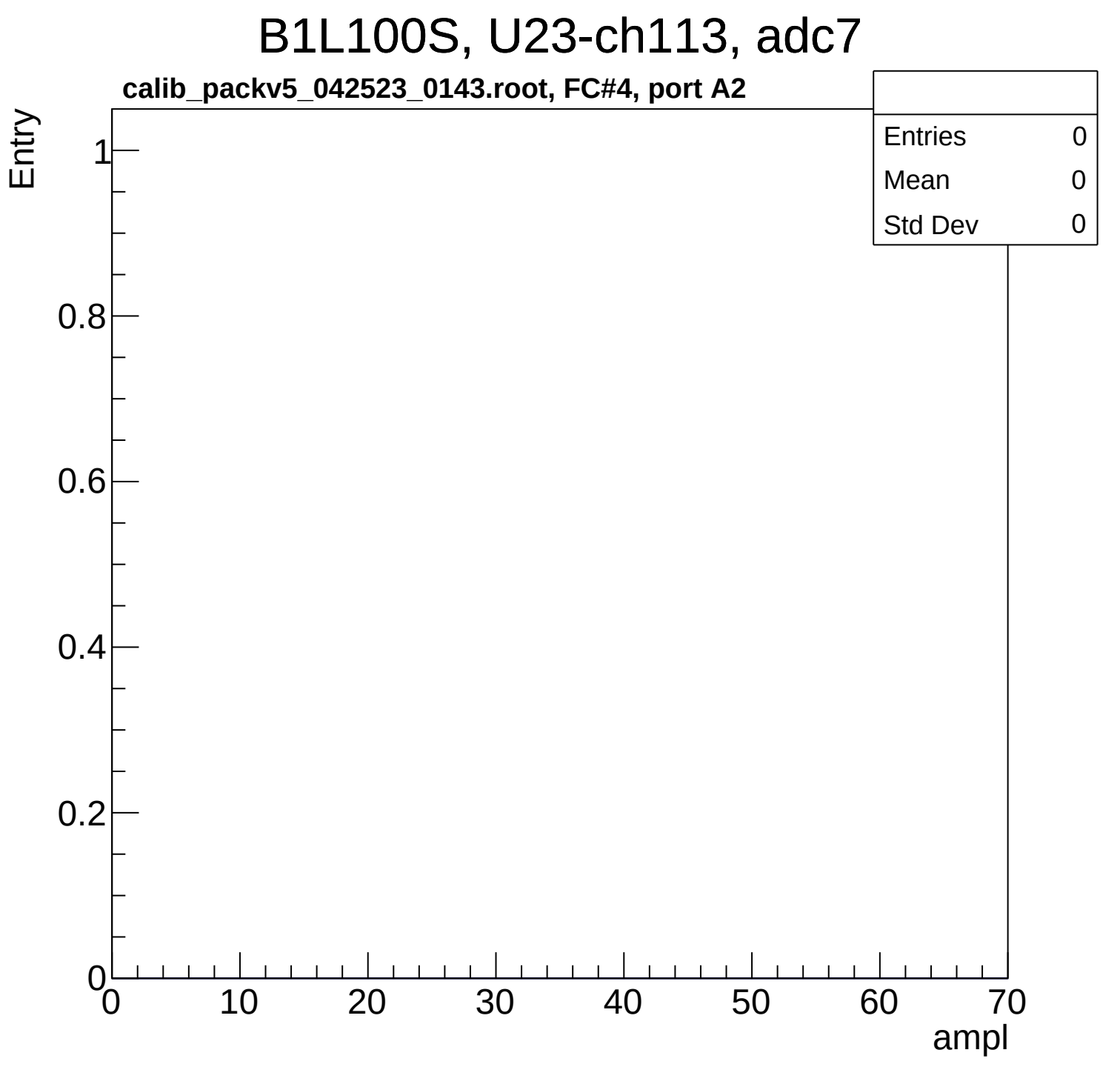
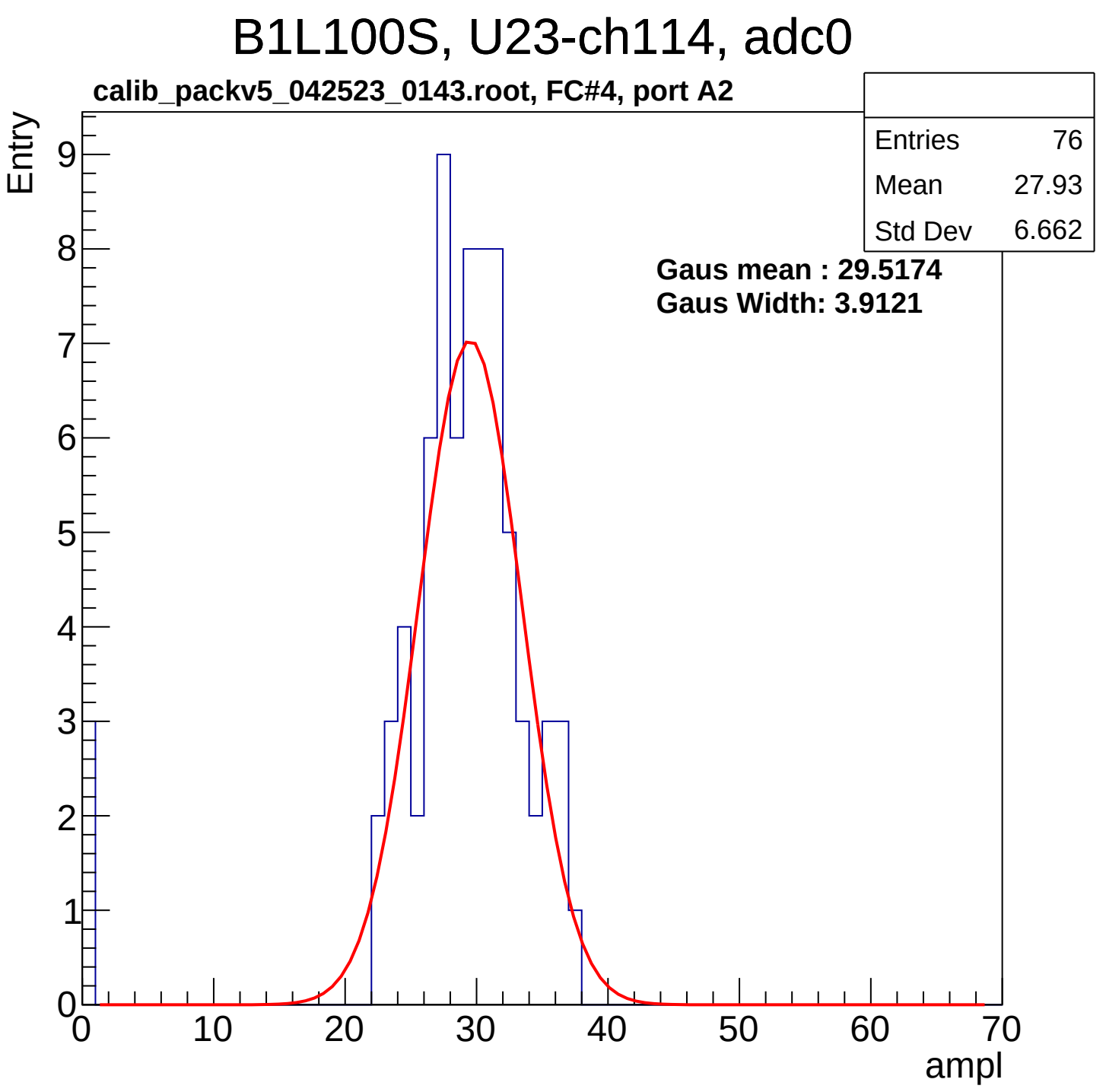
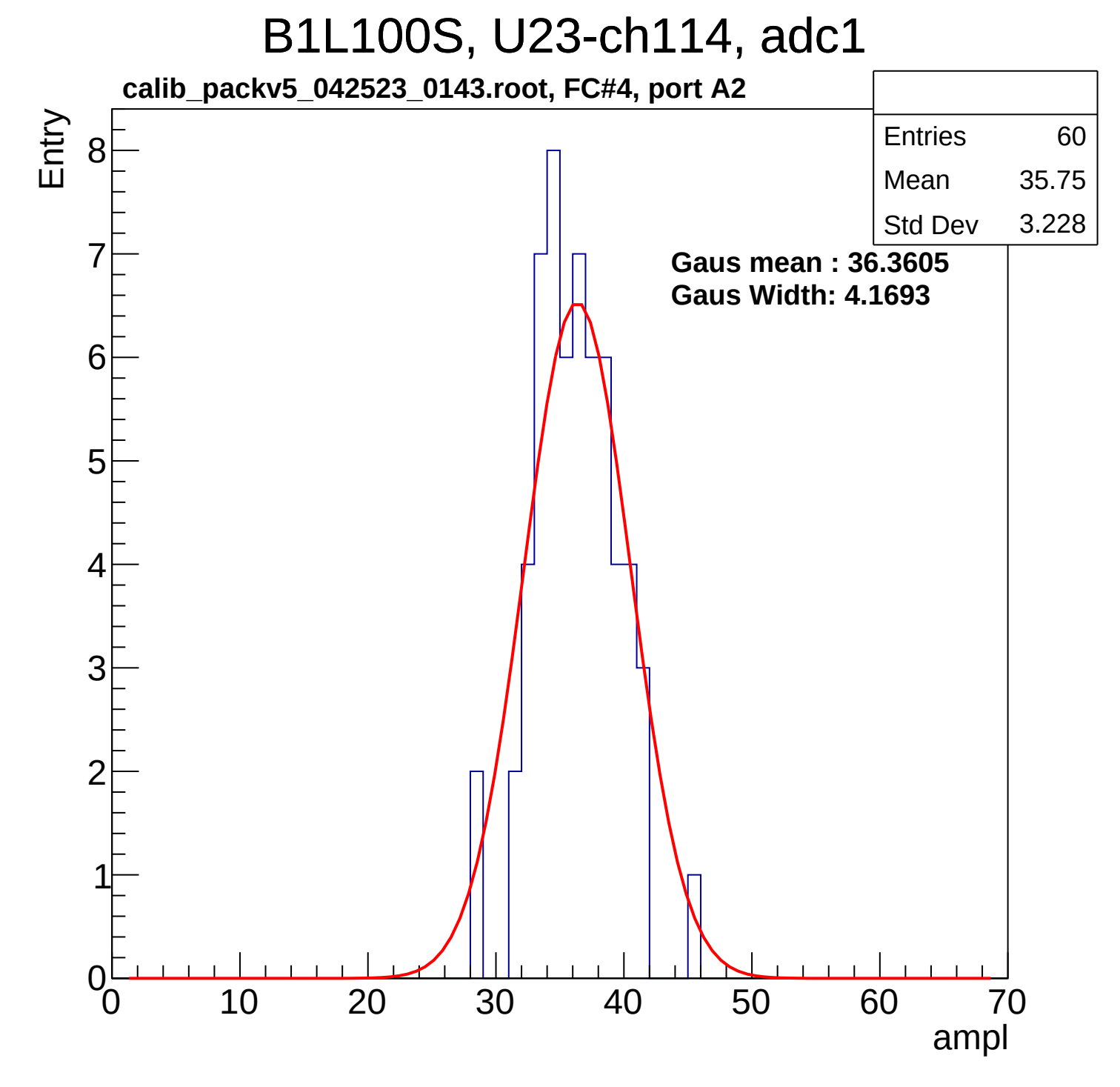
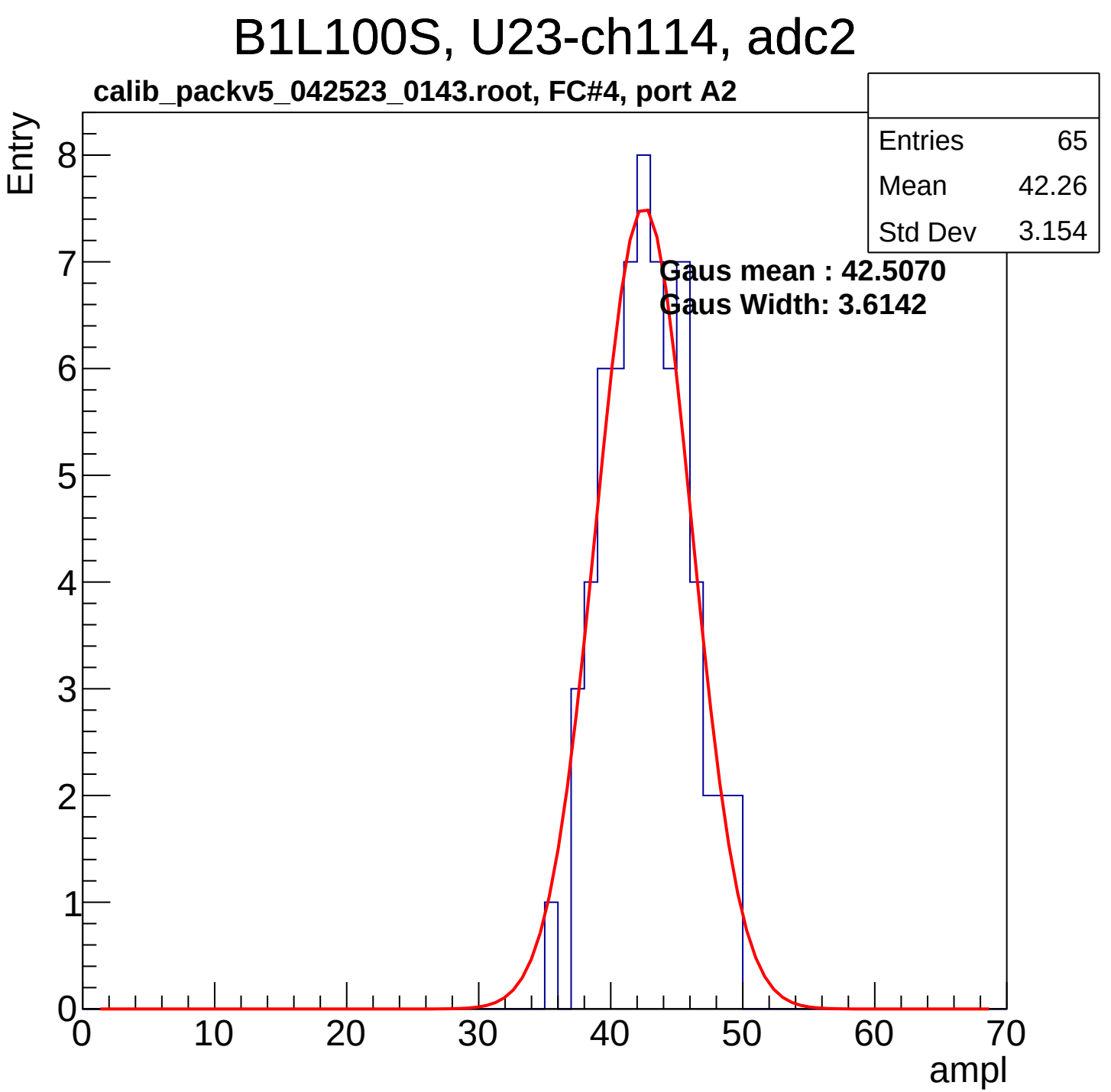
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch113, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch114, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 27.93
Std Dev 6.662
Gaus mean : 29.5174
Gaus Width: 3.9121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.75
Std Dev 3.228
Gaus mean : 36.3605
Gaus Width: 4.1693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.26
Std Dev 3.154
Gaus mean : 42.5070
Gaus Width: 3.6142
Entry
ampl
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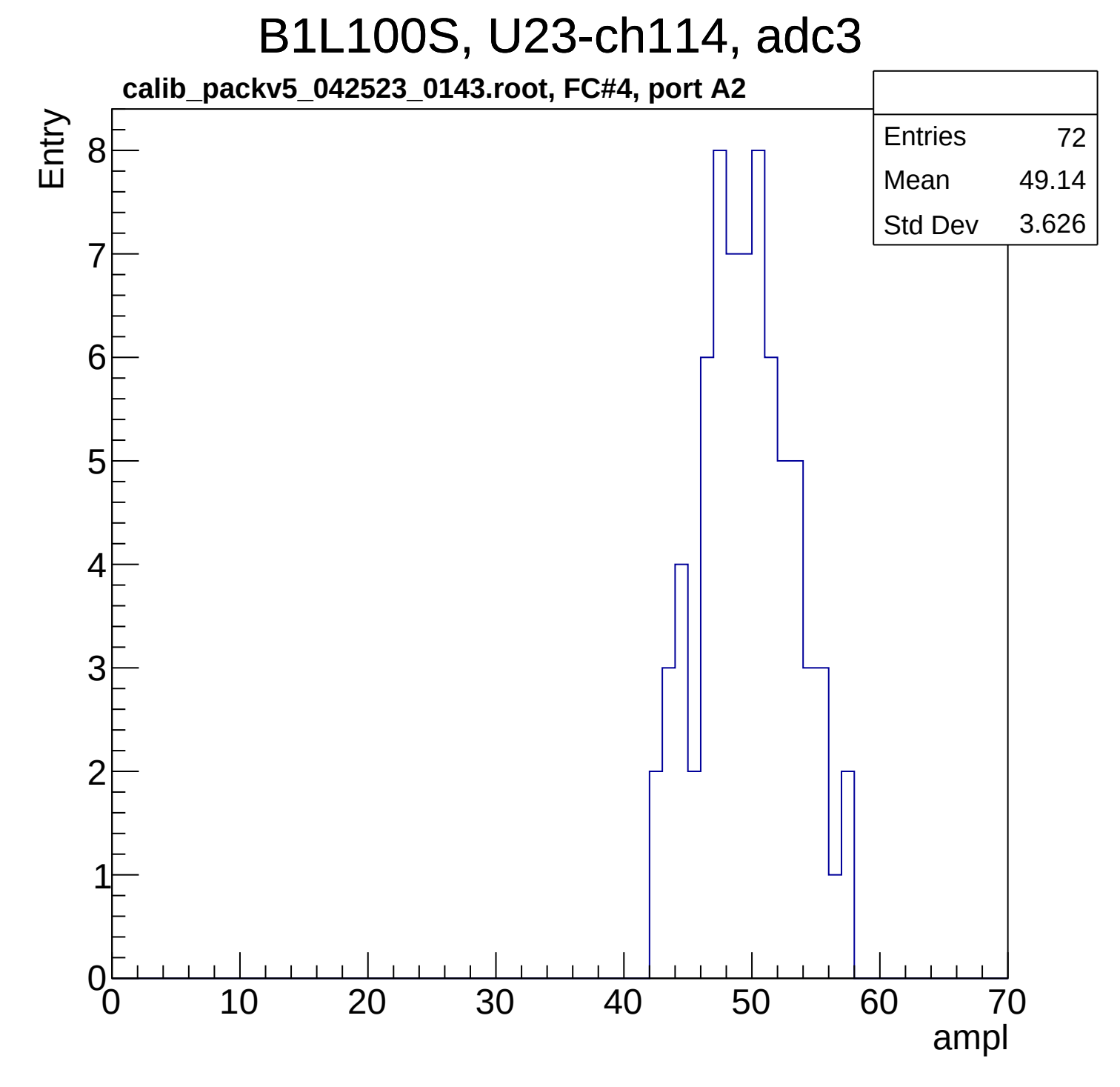
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 49.14
Std Dev 3.626
Entry
ampl
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2
3
4
5
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8
0
10
20
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40
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60
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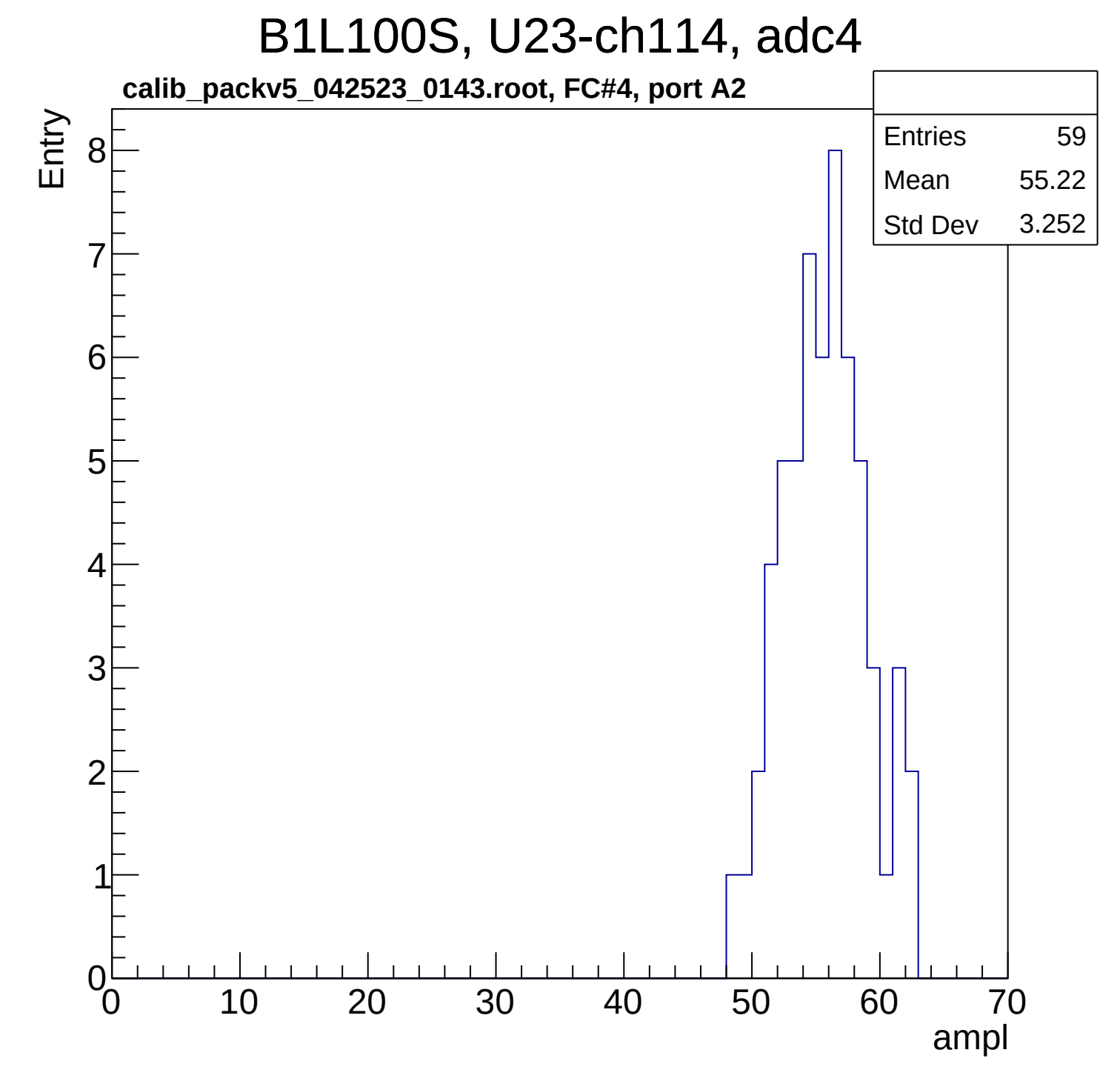
B1L100S, U23-ch114, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.22
Std Dev 3.252
Entry
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2
3
4
5
6
7
8
0
10
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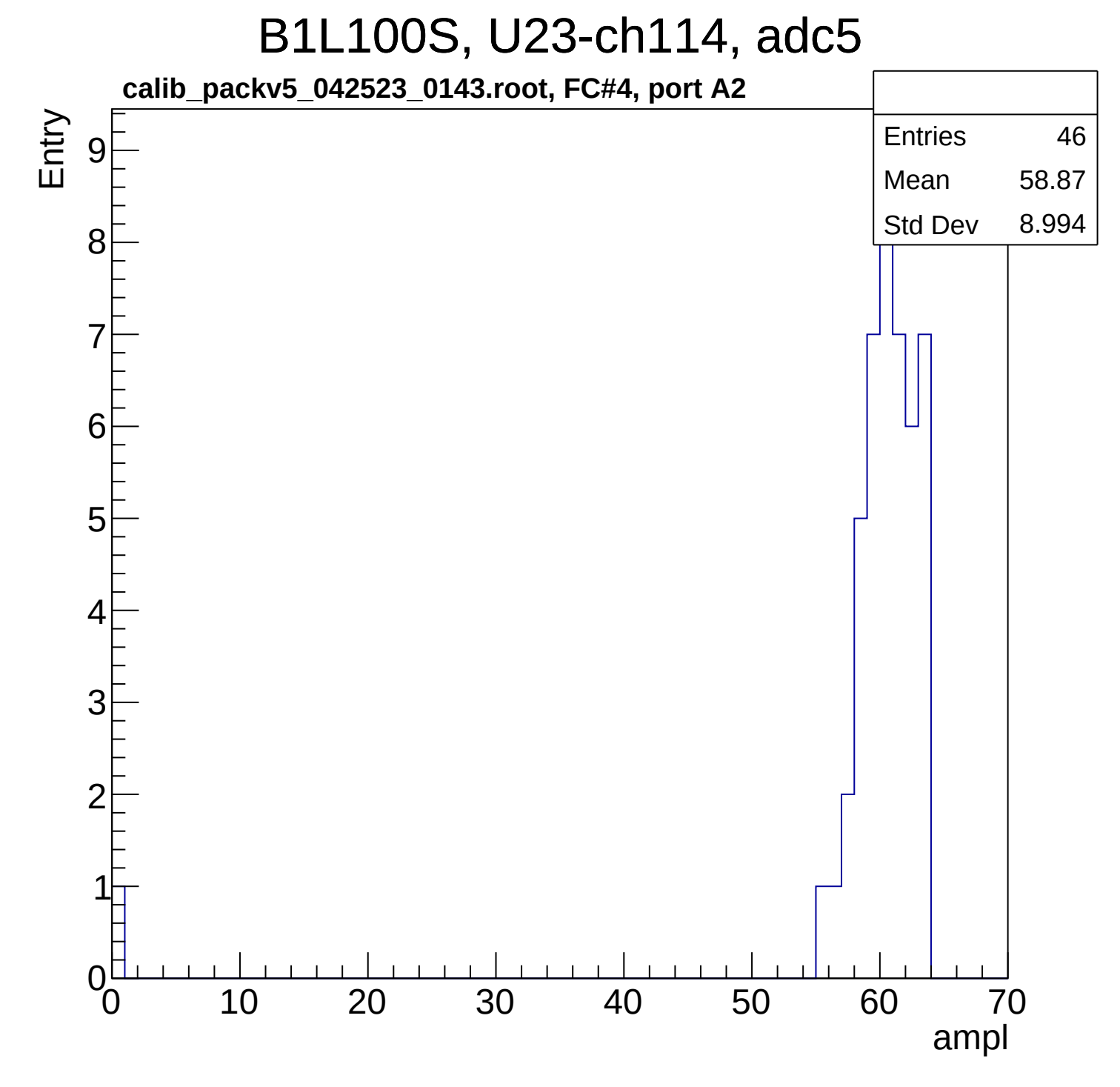
30
40
50
60
70
ampl


B1L100S, U23-ch114, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.87
Std Dev 8.994
Entry
ampl
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3
4
5
6
7
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9
0
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60
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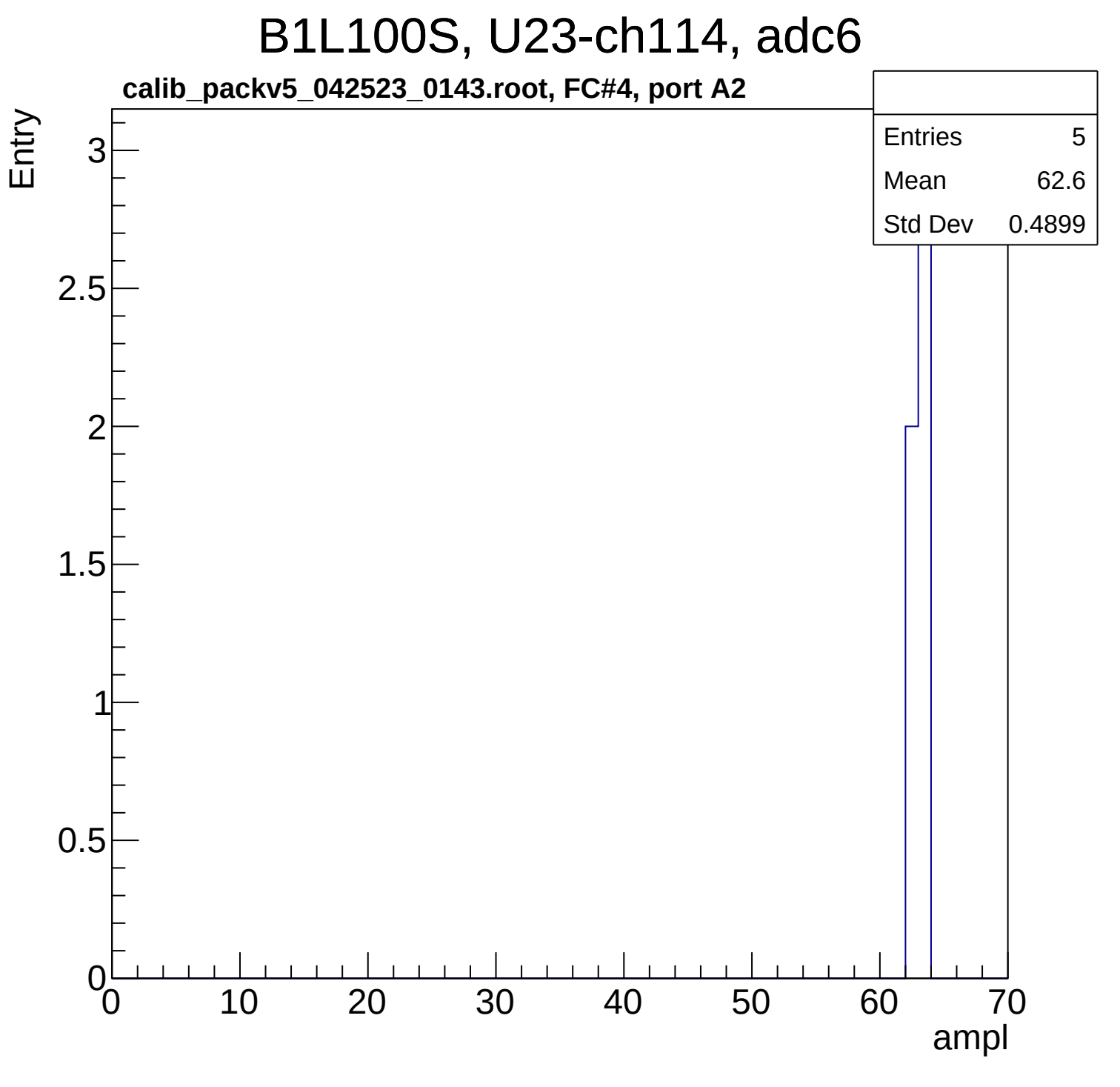
B1L100S, U23-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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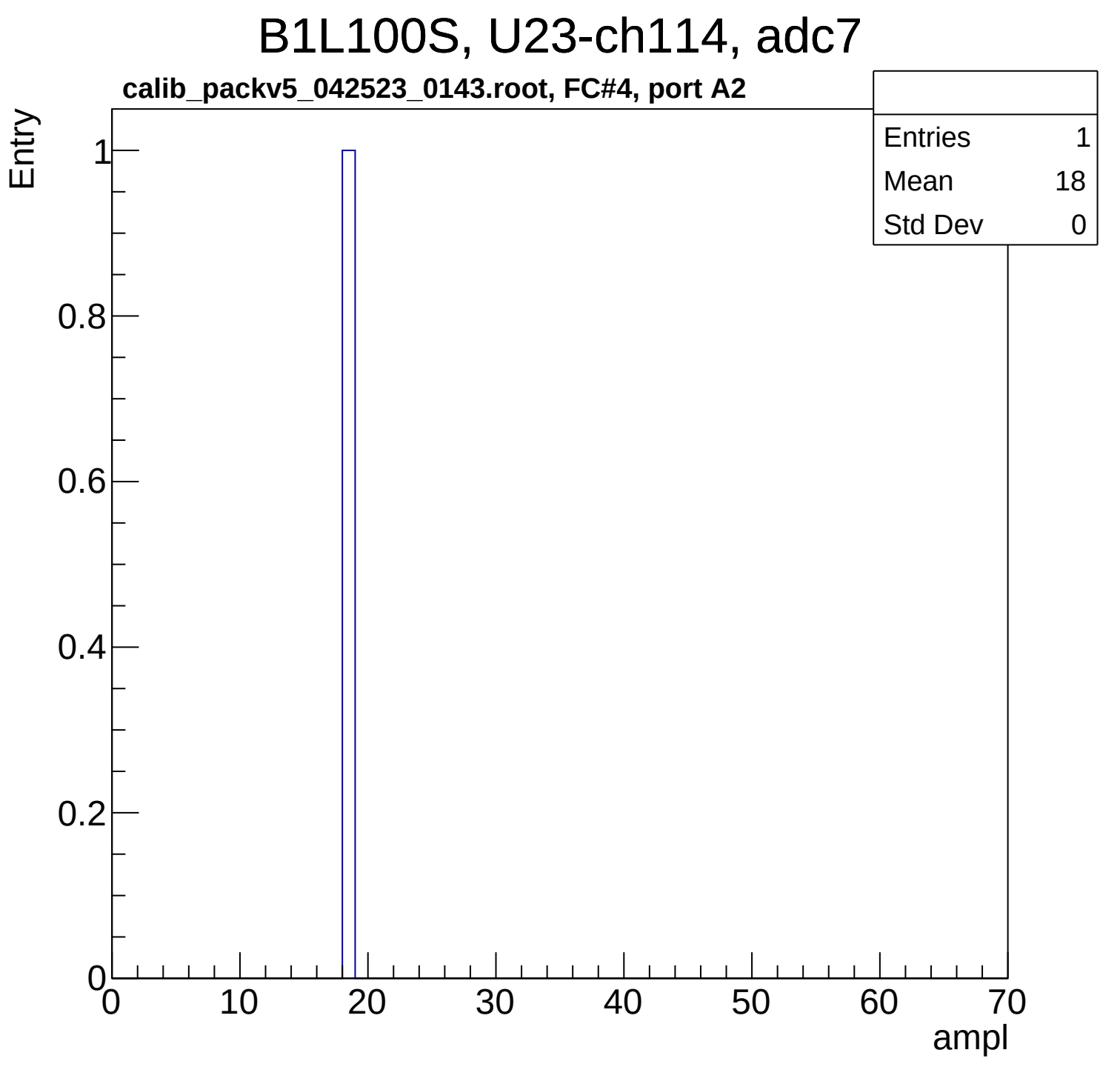
50
60
70


B1L100S, U23-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 18
Std Dev 0
Entry
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0.6
0.4
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10
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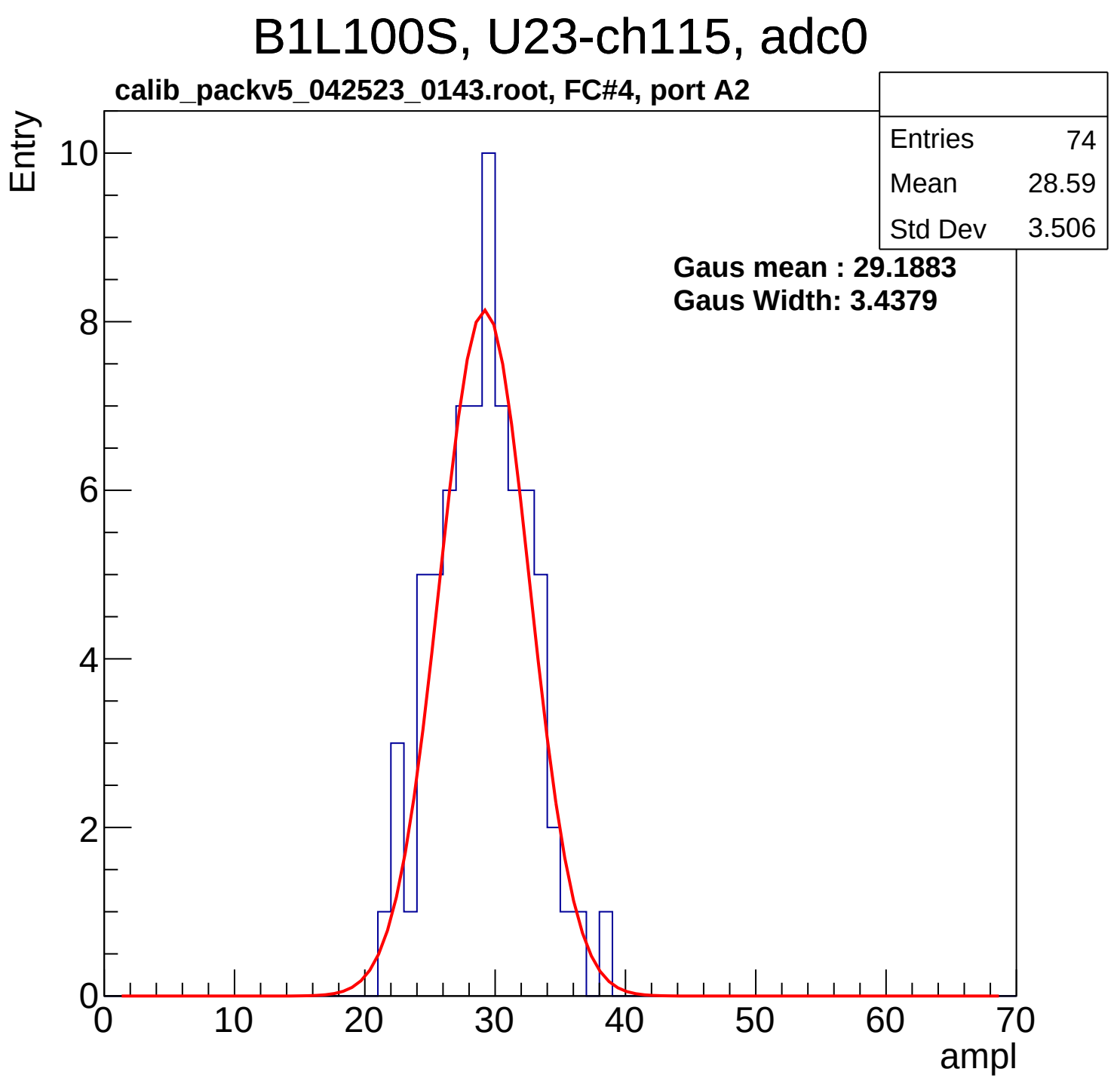
50
60
70
ampl


B1L100S, U23-ch115, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.59
Std Dev 3.506
Gaus mean : 29.1883
Gaus Width: 3.4379
Entry
ampl
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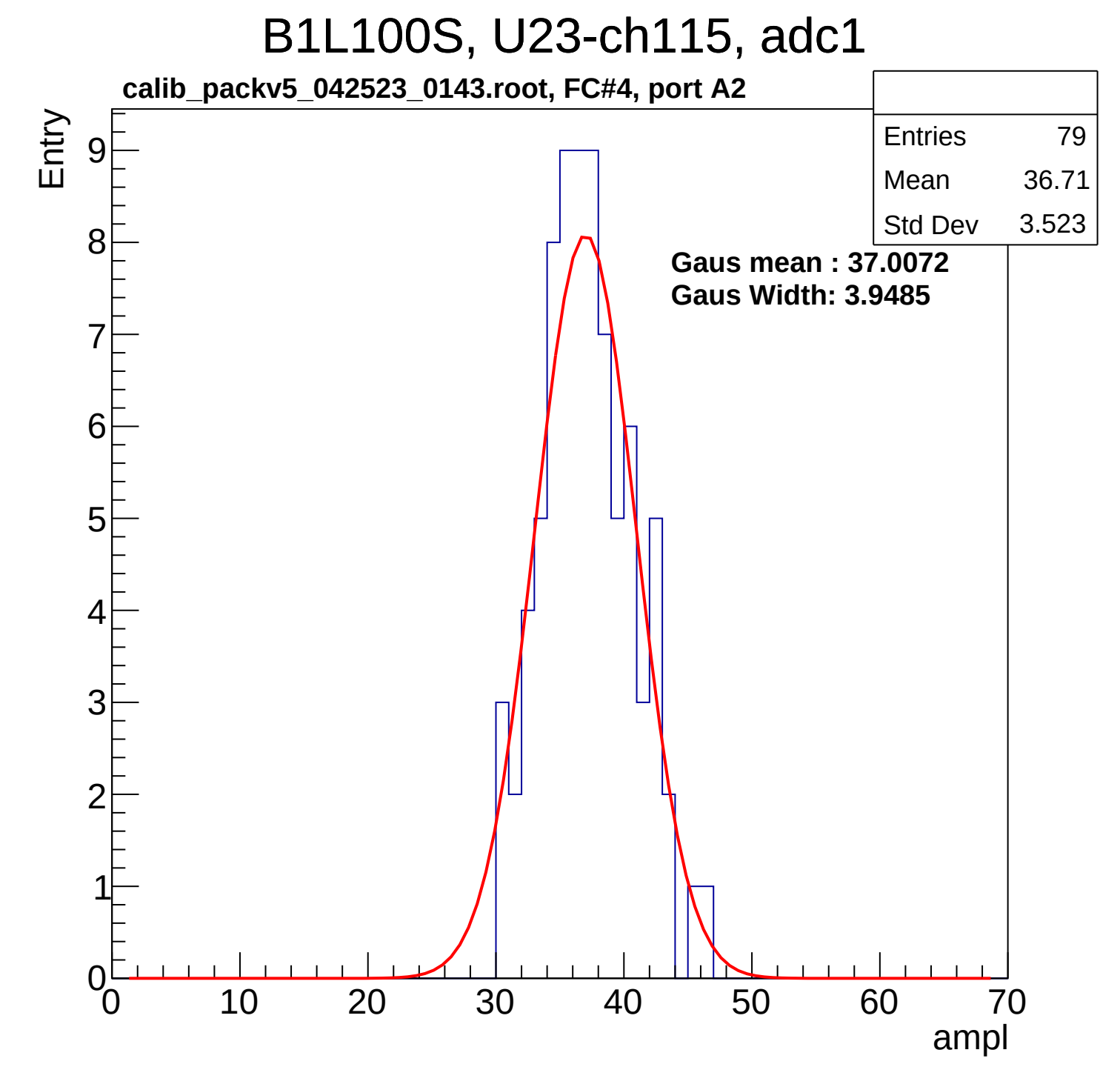
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 36.71
Std Dev 3.523
Gaus mean : 37.0072
Gaus Width: 3.9485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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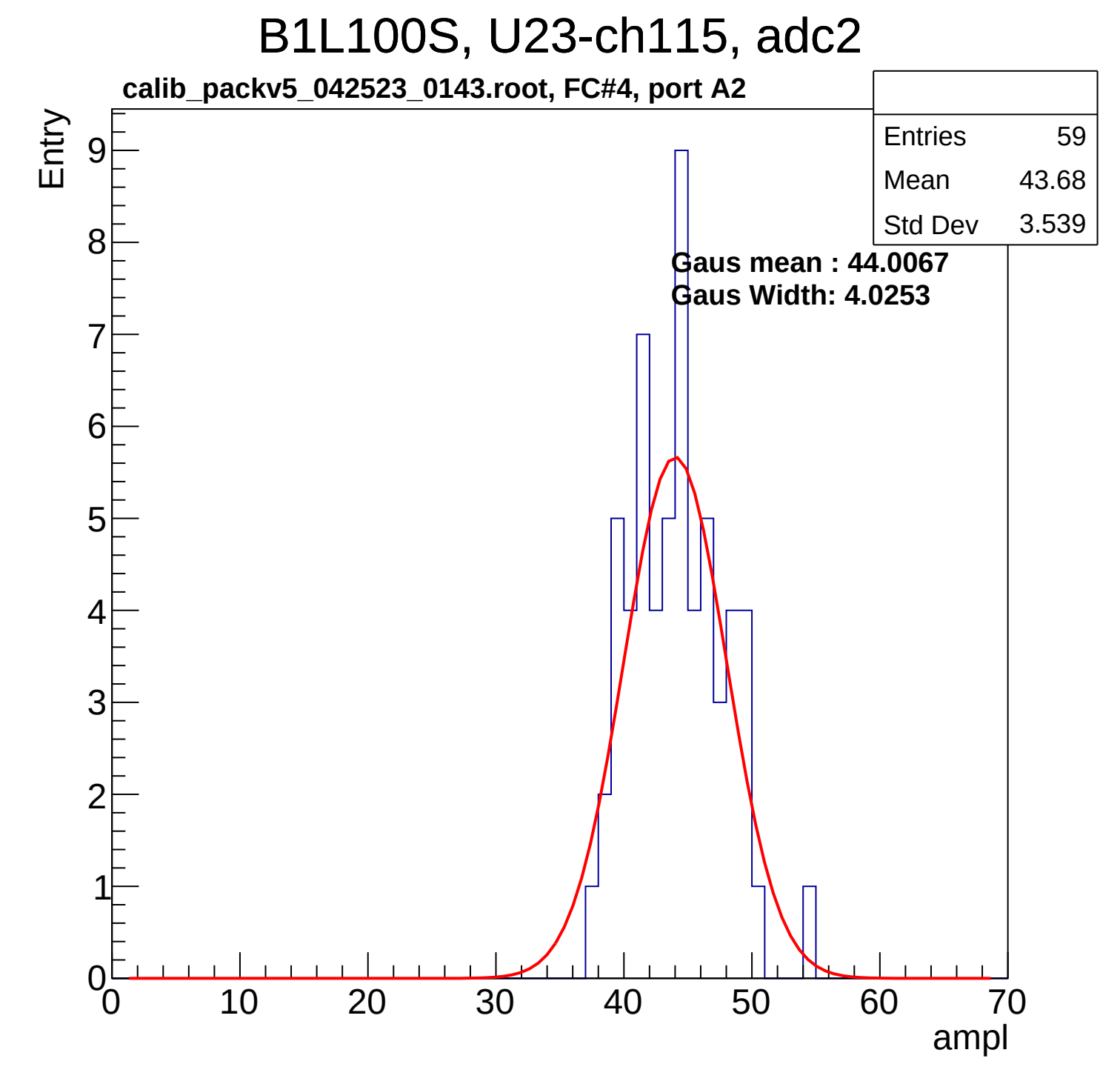
B1L100S, U23-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.68
Std Dev 3.539
Gaus mean : 44.0067
Gaus Width: 4.0253
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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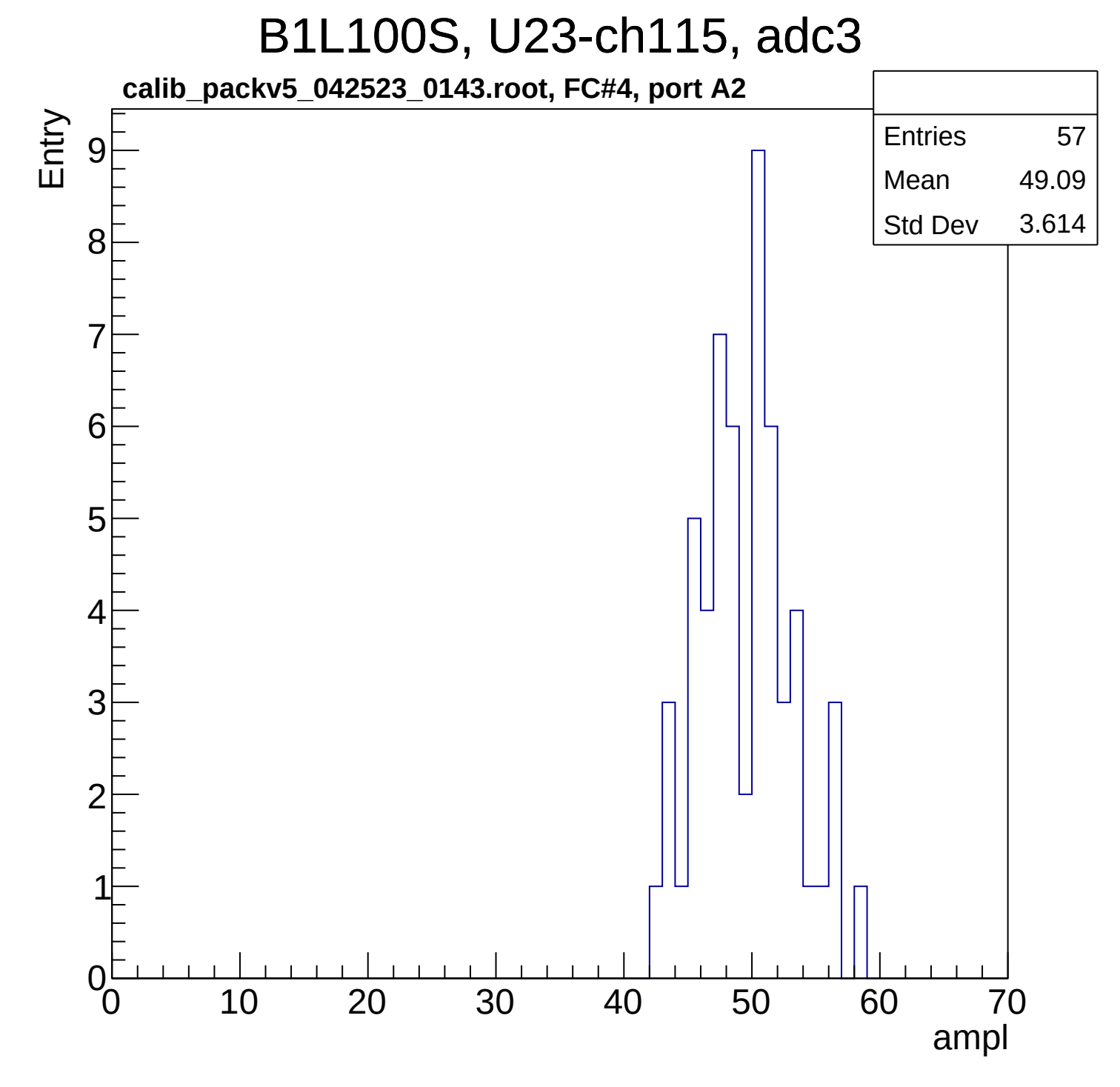
B1L100S, U23-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.09
Std Dev 3.614
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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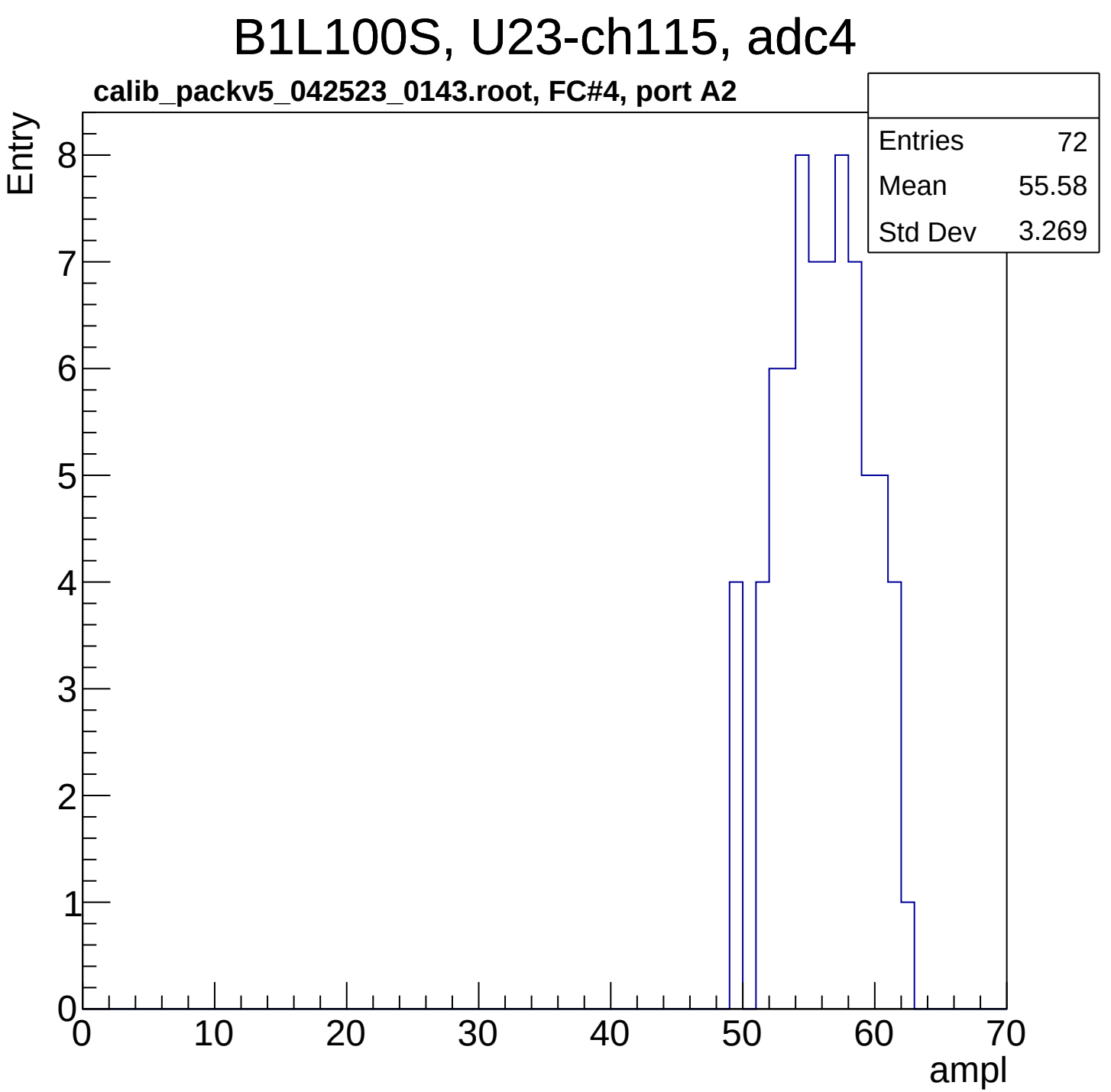
B1L100S, U23-ch115, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 55.58
Std Dev 3.269
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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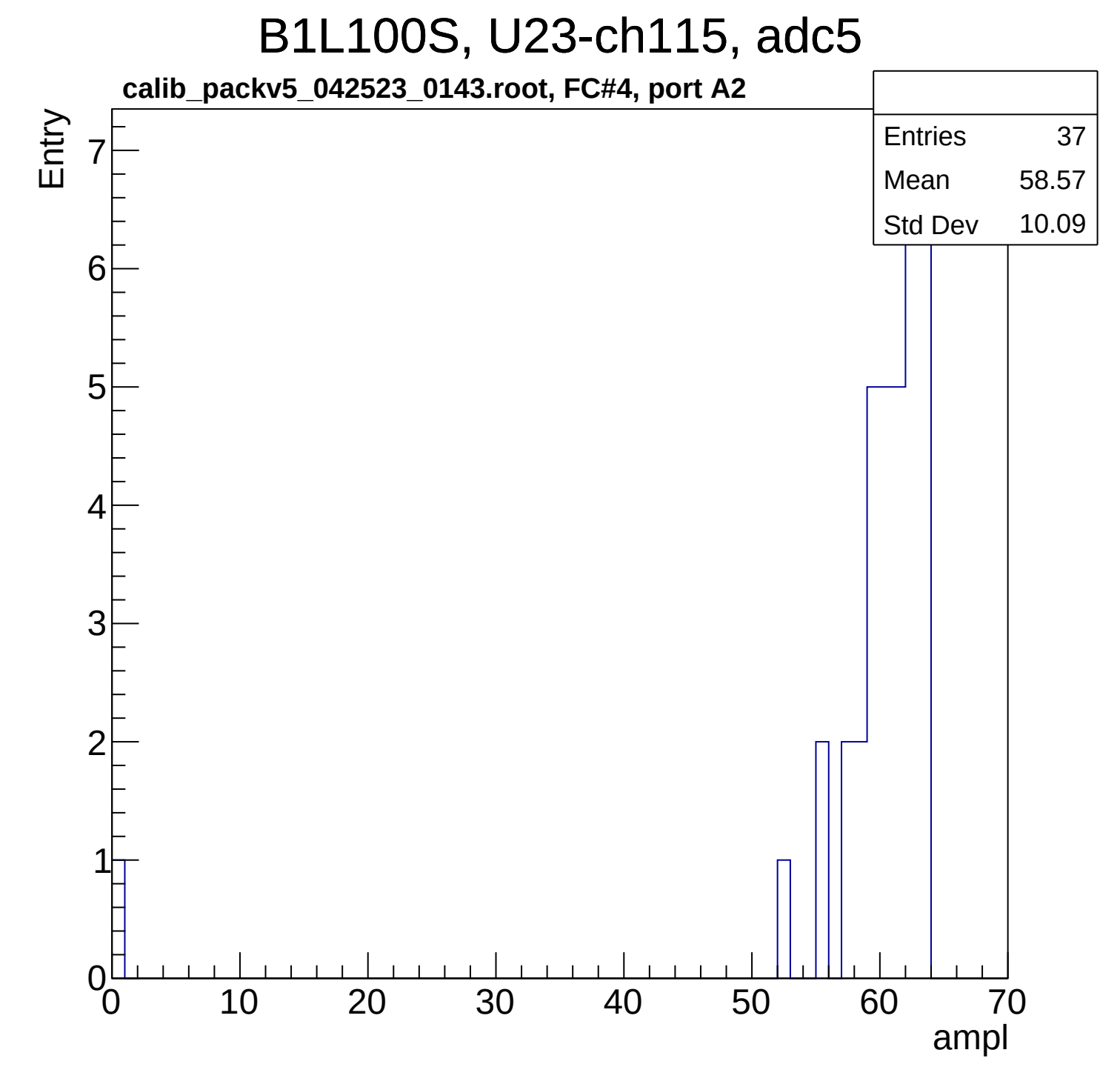
B1L100S, U23-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.57
Std Dev 10.09
Entry
ampl
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4
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7
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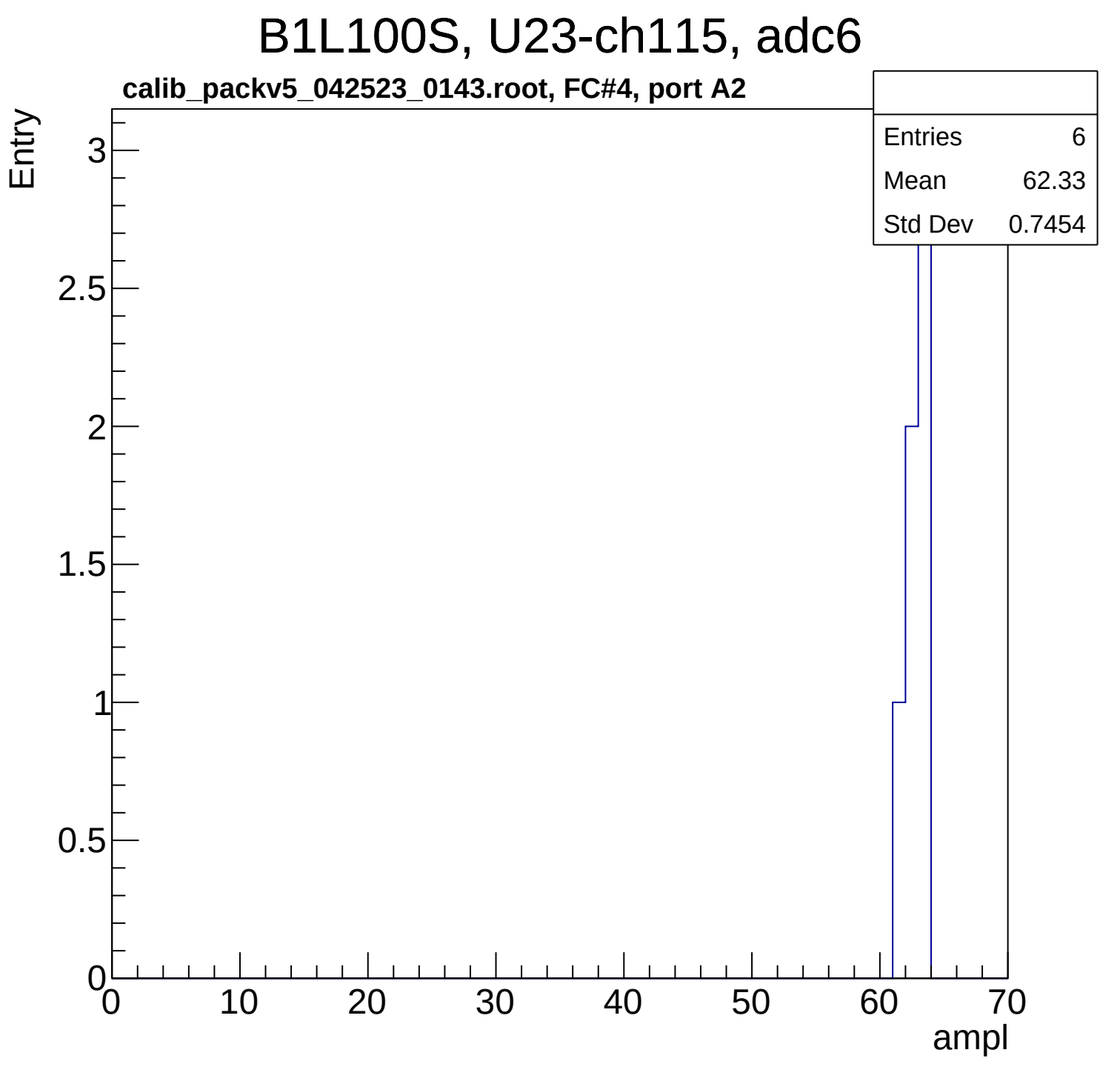
B1L100S, U23-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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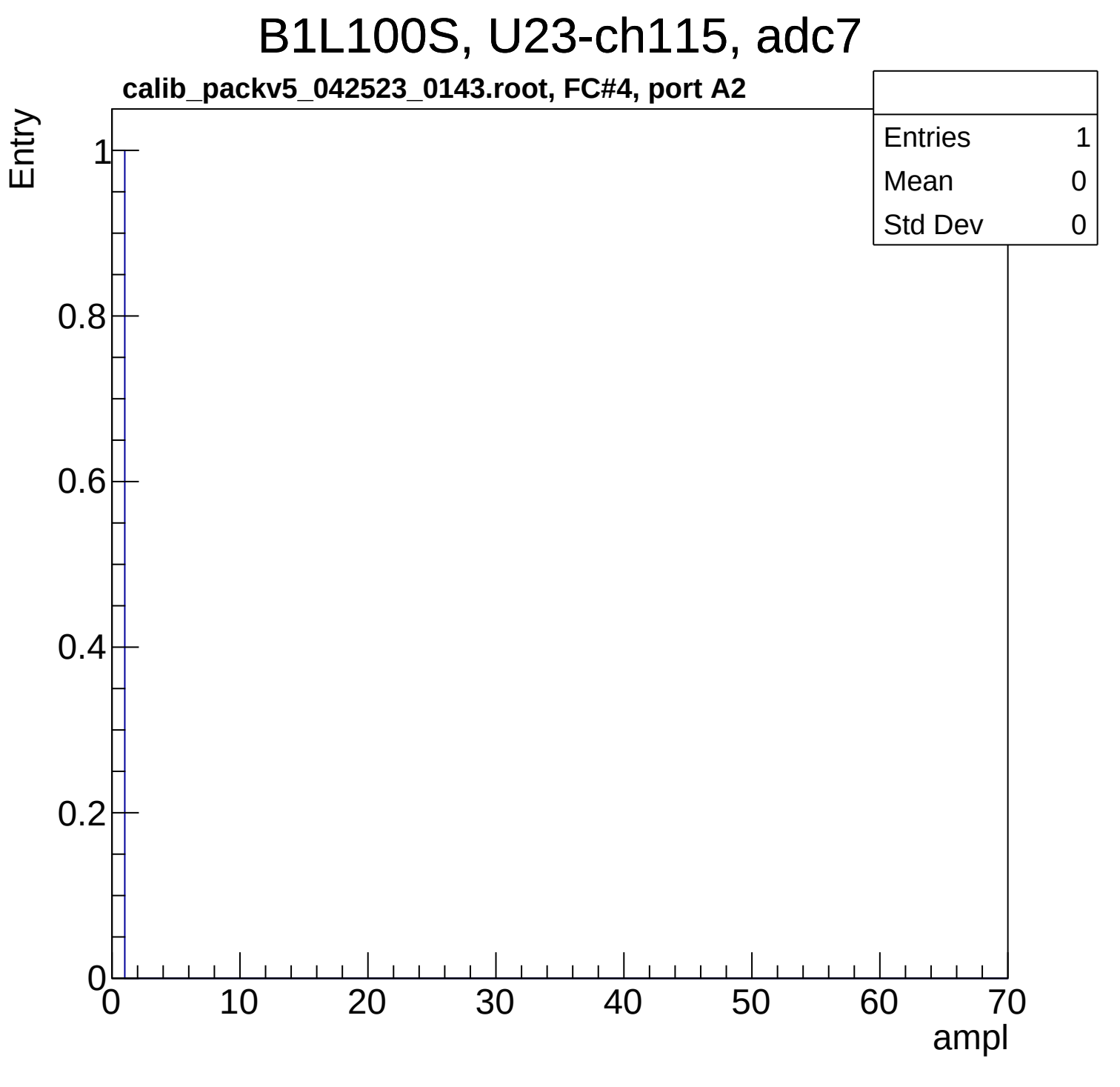
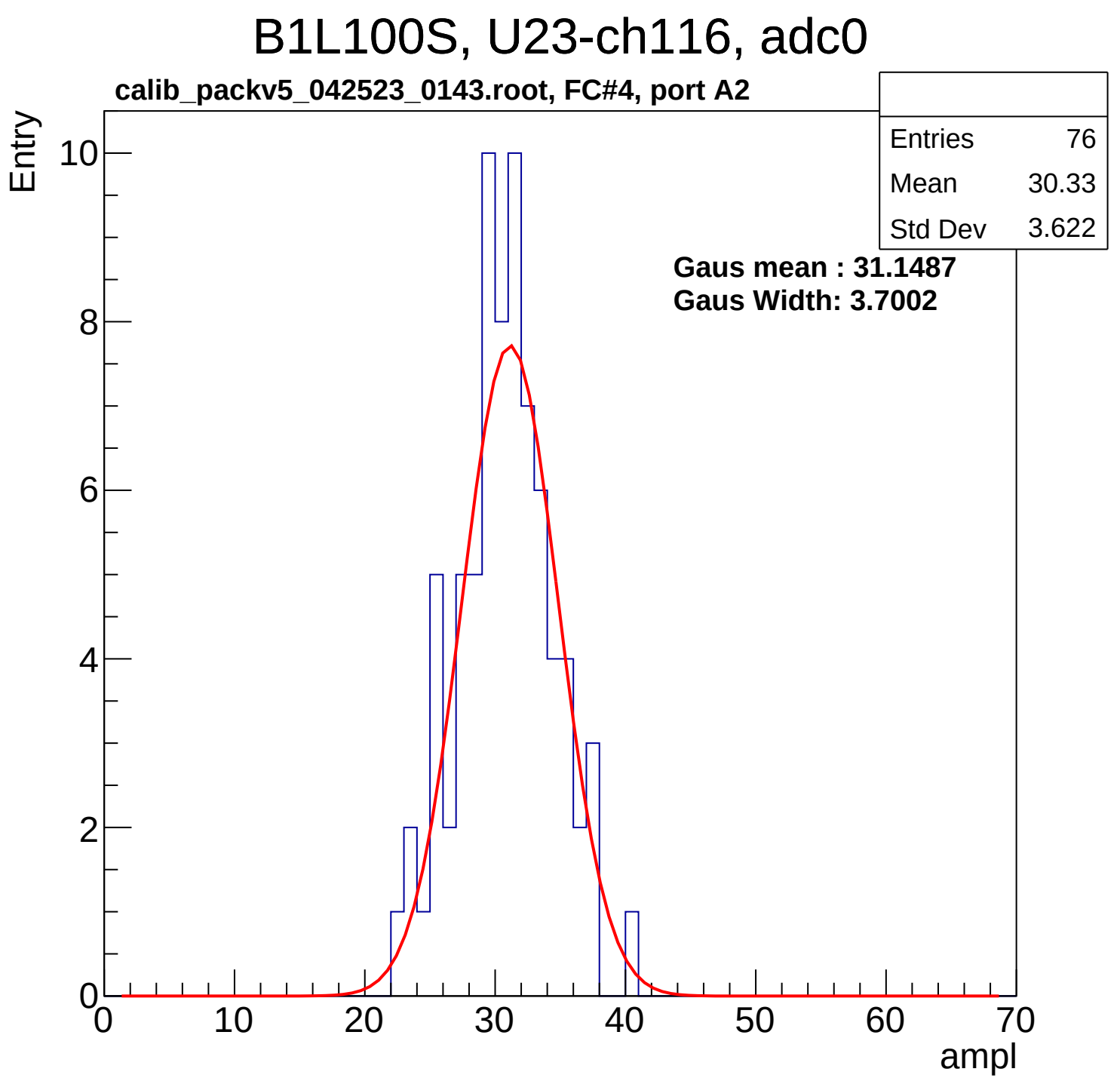

B1L100S, U23-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.33
Std Dev 3.622
Gaus mean : 31.1487
Gaus Width: 3.7002
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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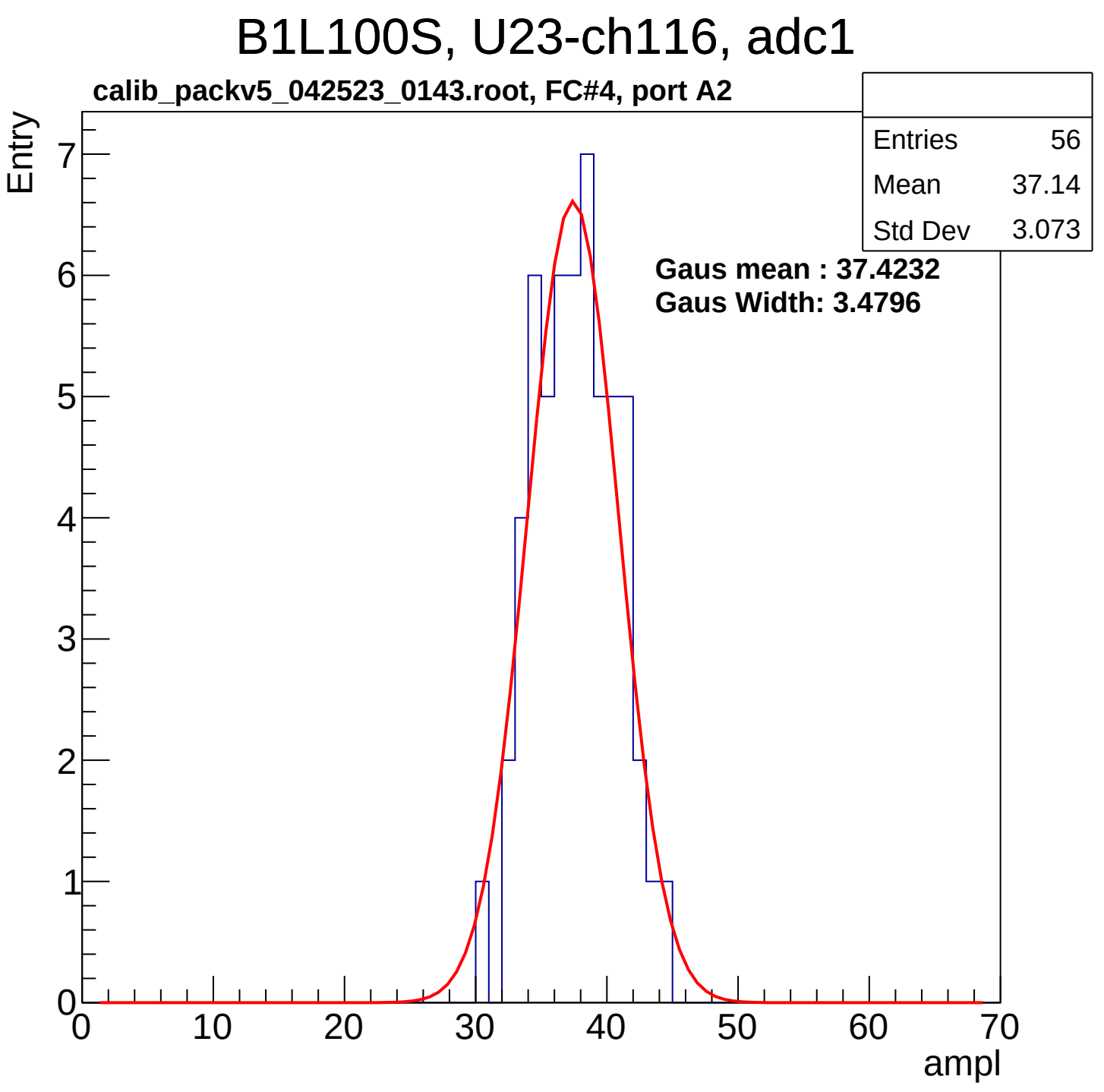
B1L100S, U23-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 37.14
Std Dev 3.073
Gaus mean : 37.4232
Gaus Width: 3.4796
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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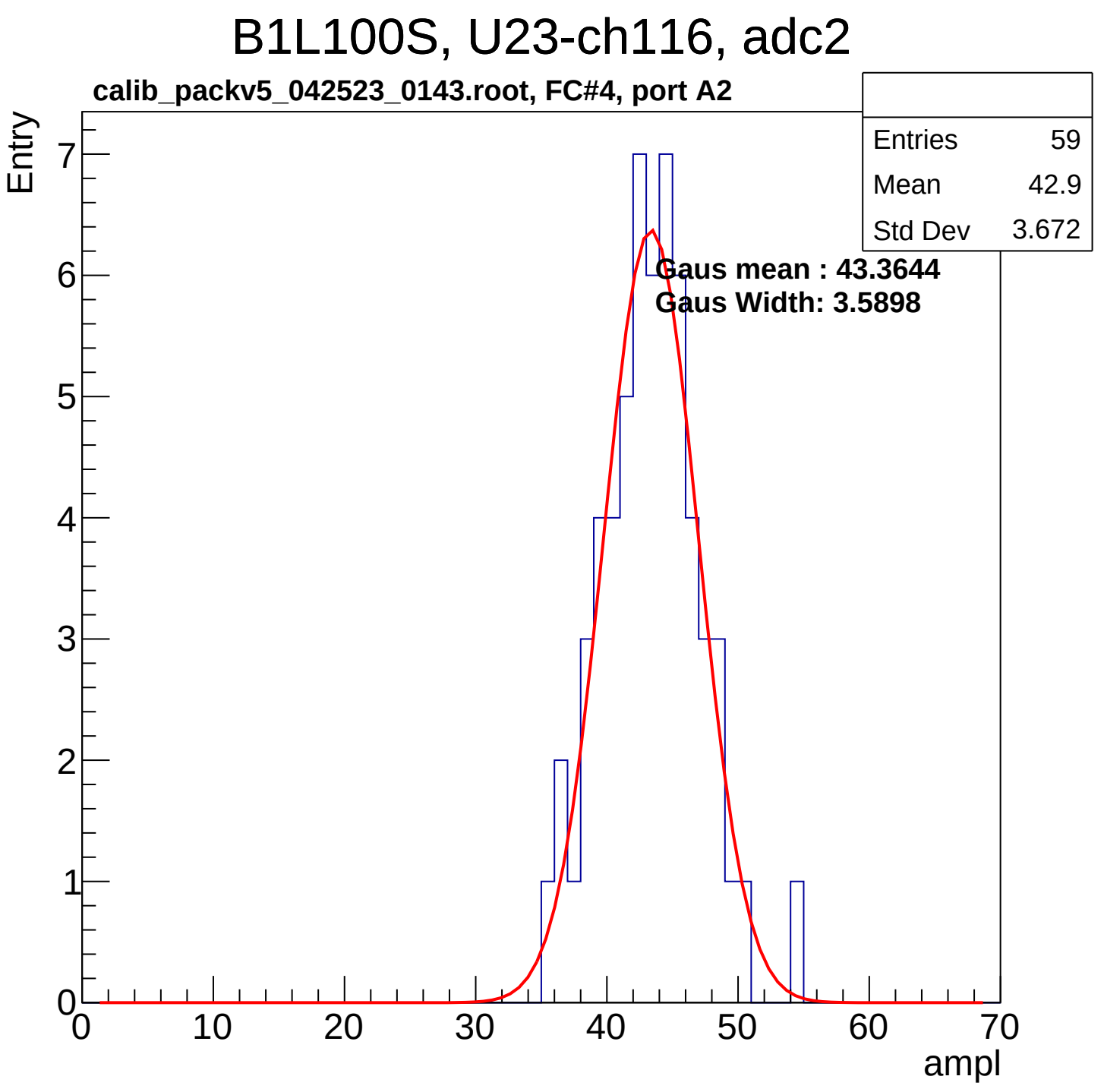
B1L100S, U23-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.9
Std Dev 3.672
Gaus mean : 43.3644
Gaus Width: 3.5898
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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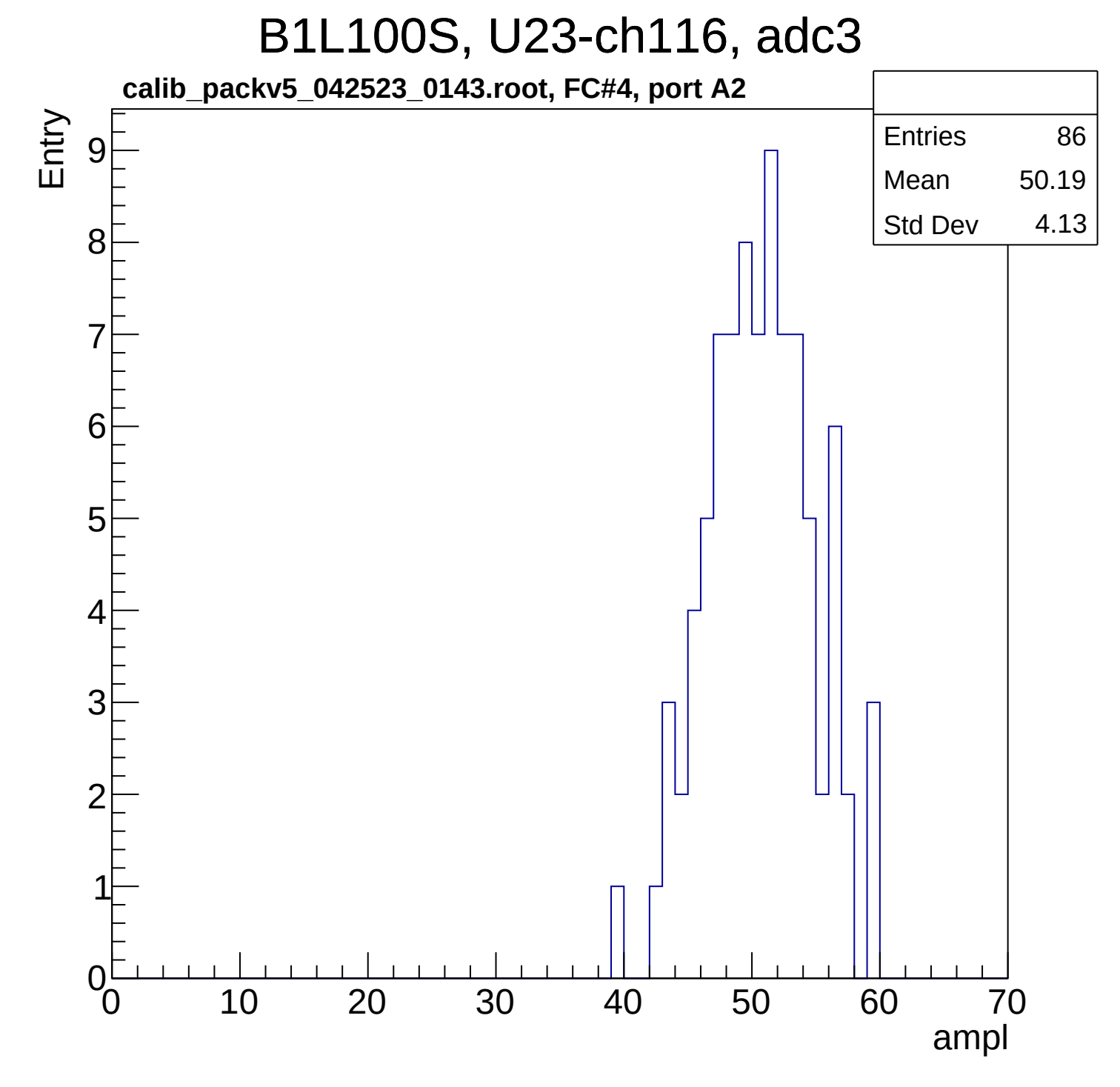
B1L100S, U23-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 50.19
Std Dev 4.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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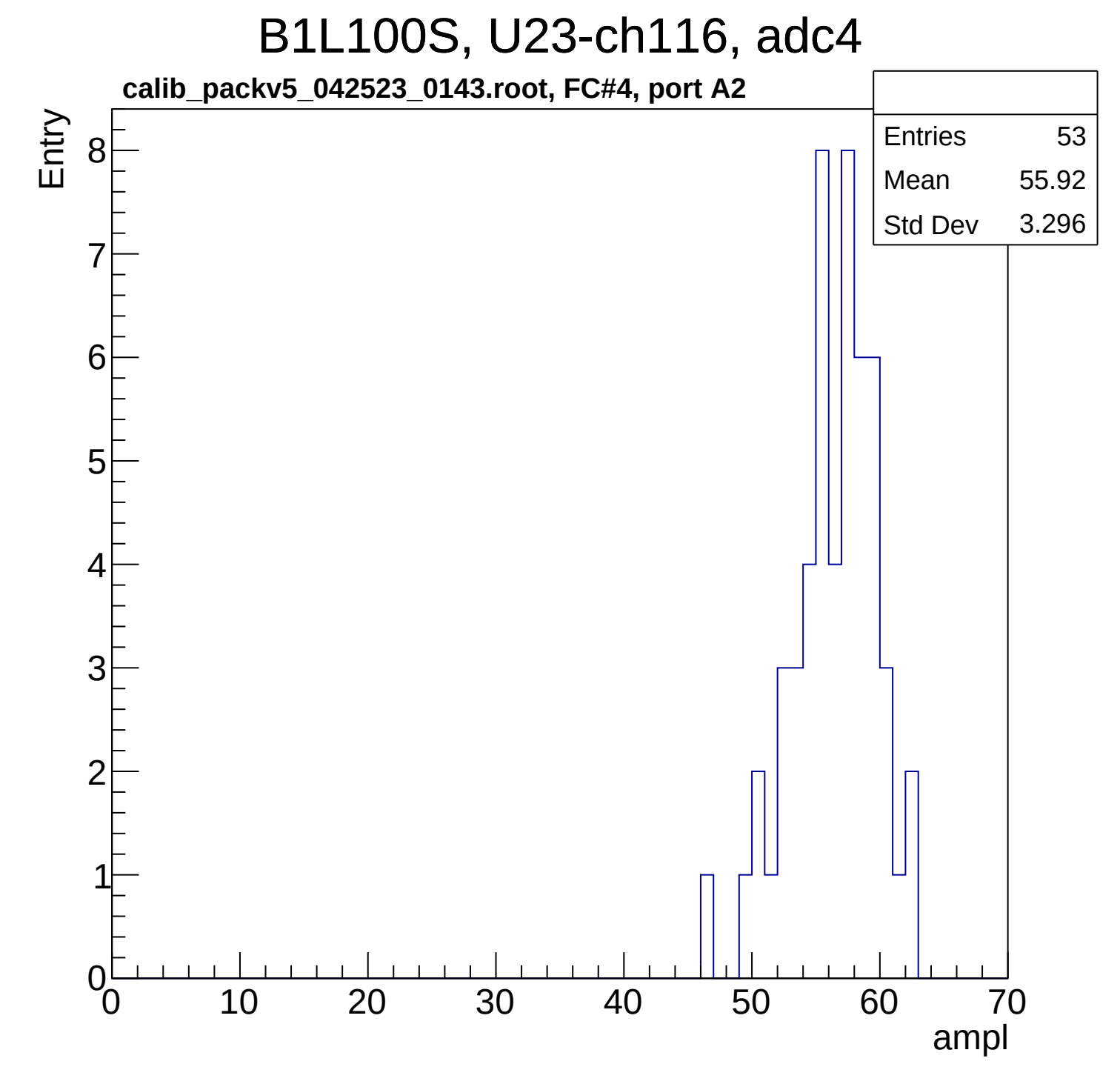
B1L100S, U23-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.92
Std Dev 3.296
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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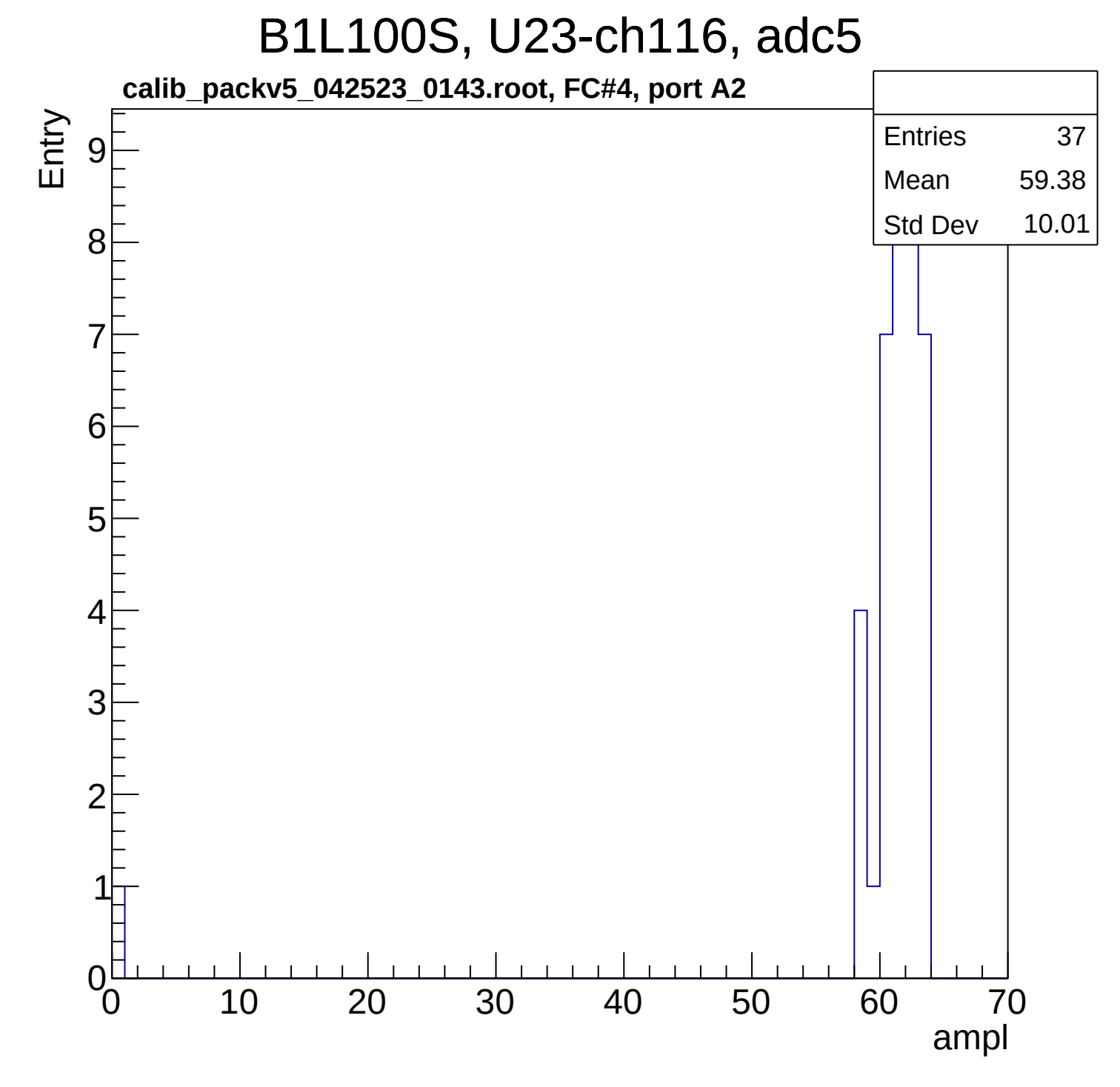
B1L100S, U23-ch116, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.38
Std Dev 10.01
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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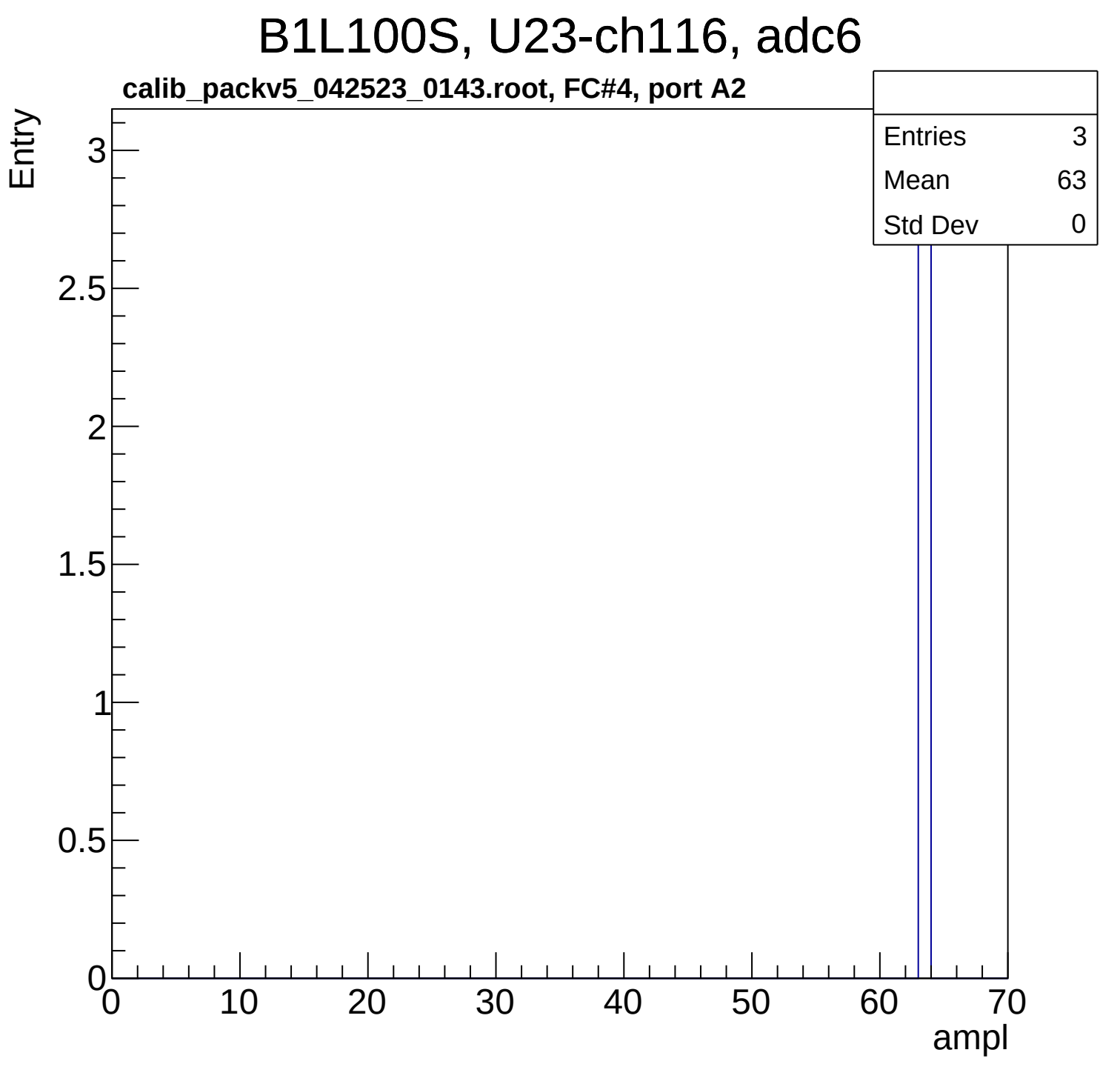
B1L100S, U23-ch116, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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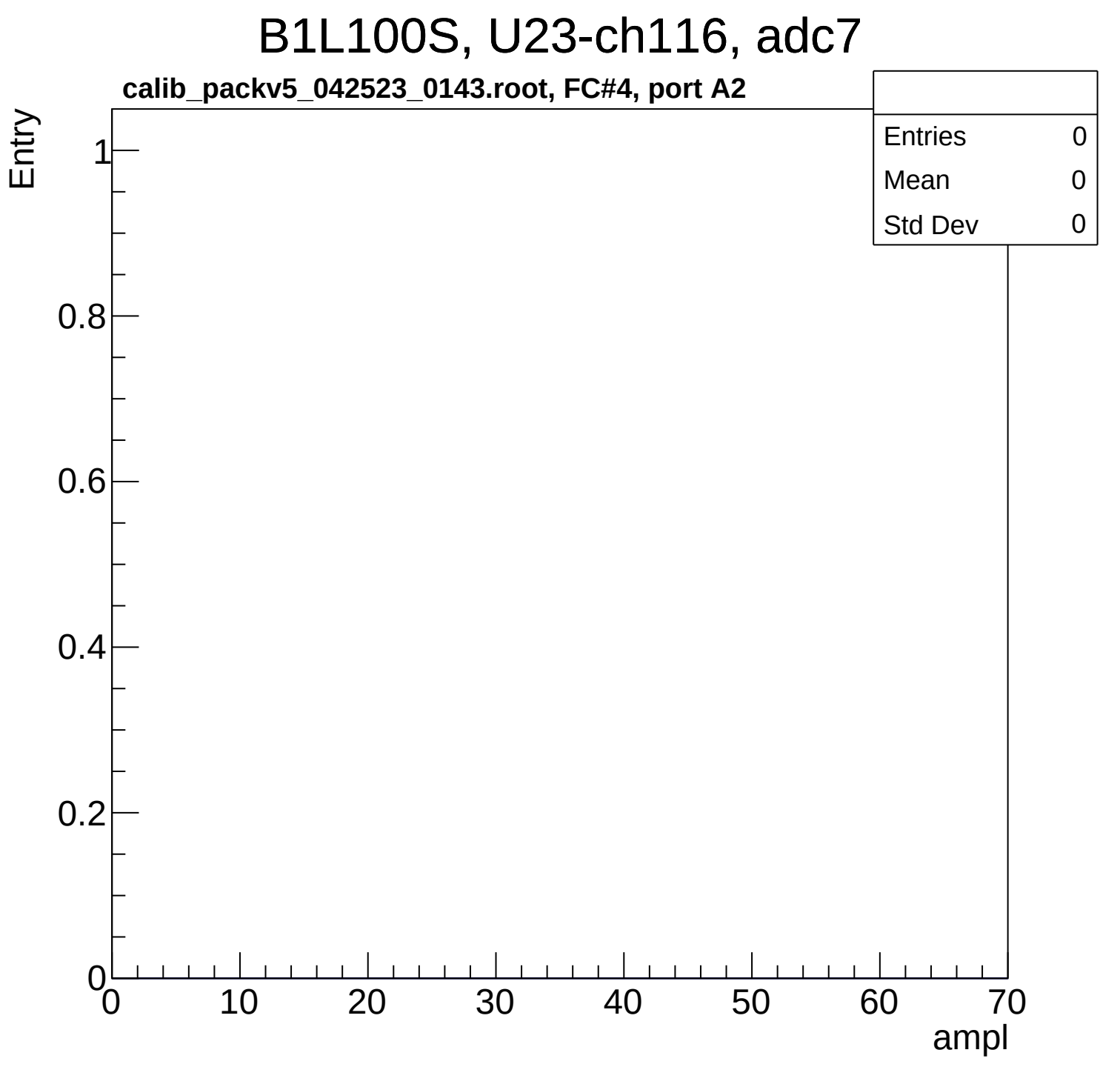
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U23-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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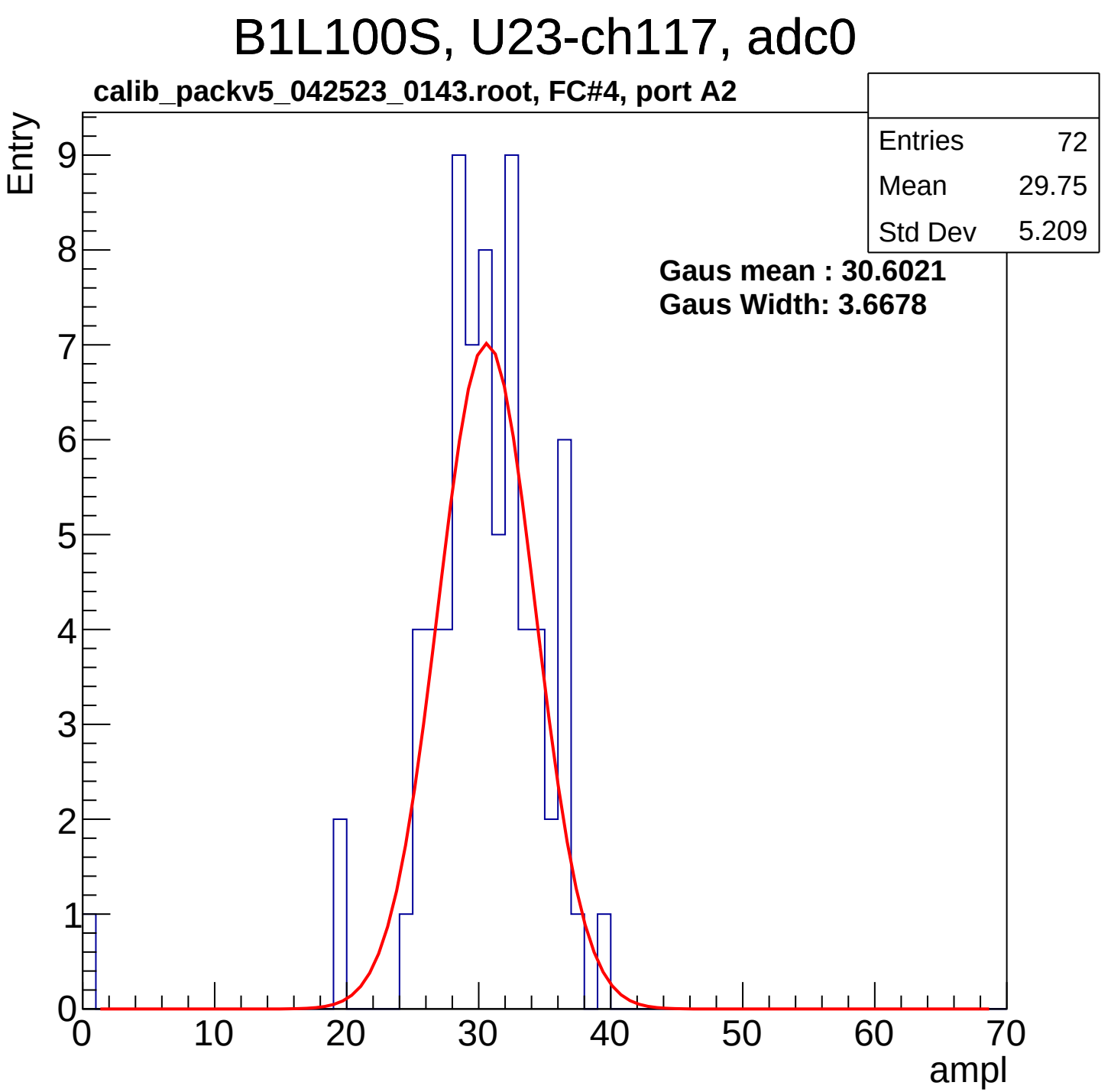
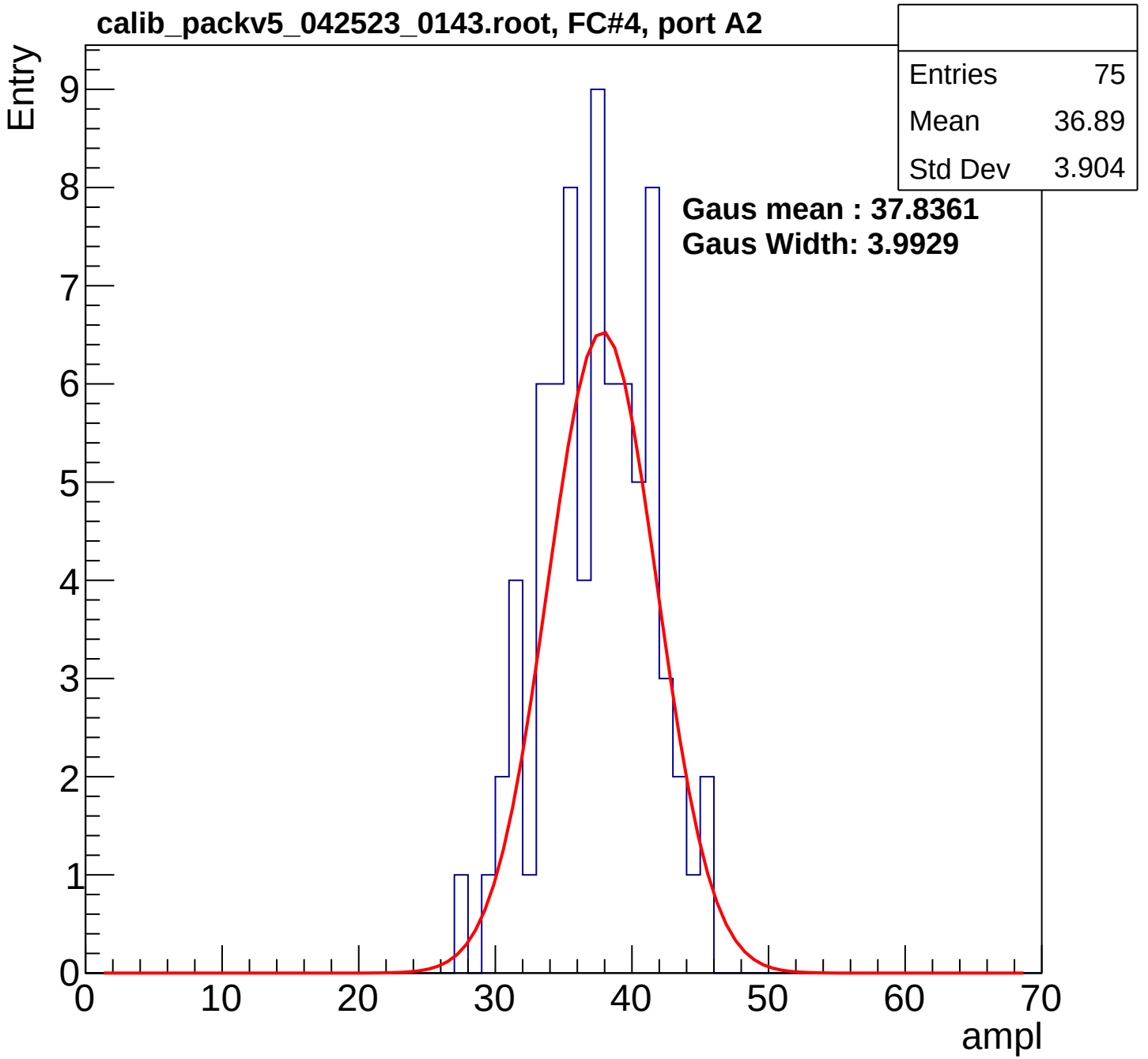
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.75
Std Dev 5.209
Gaus mean : 30.6021
Gaus Width: 3.6678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.89
Std Dev 3.904
Gaus mean : 37.8361
Gaus Width: 3.9929
Entry
ampl
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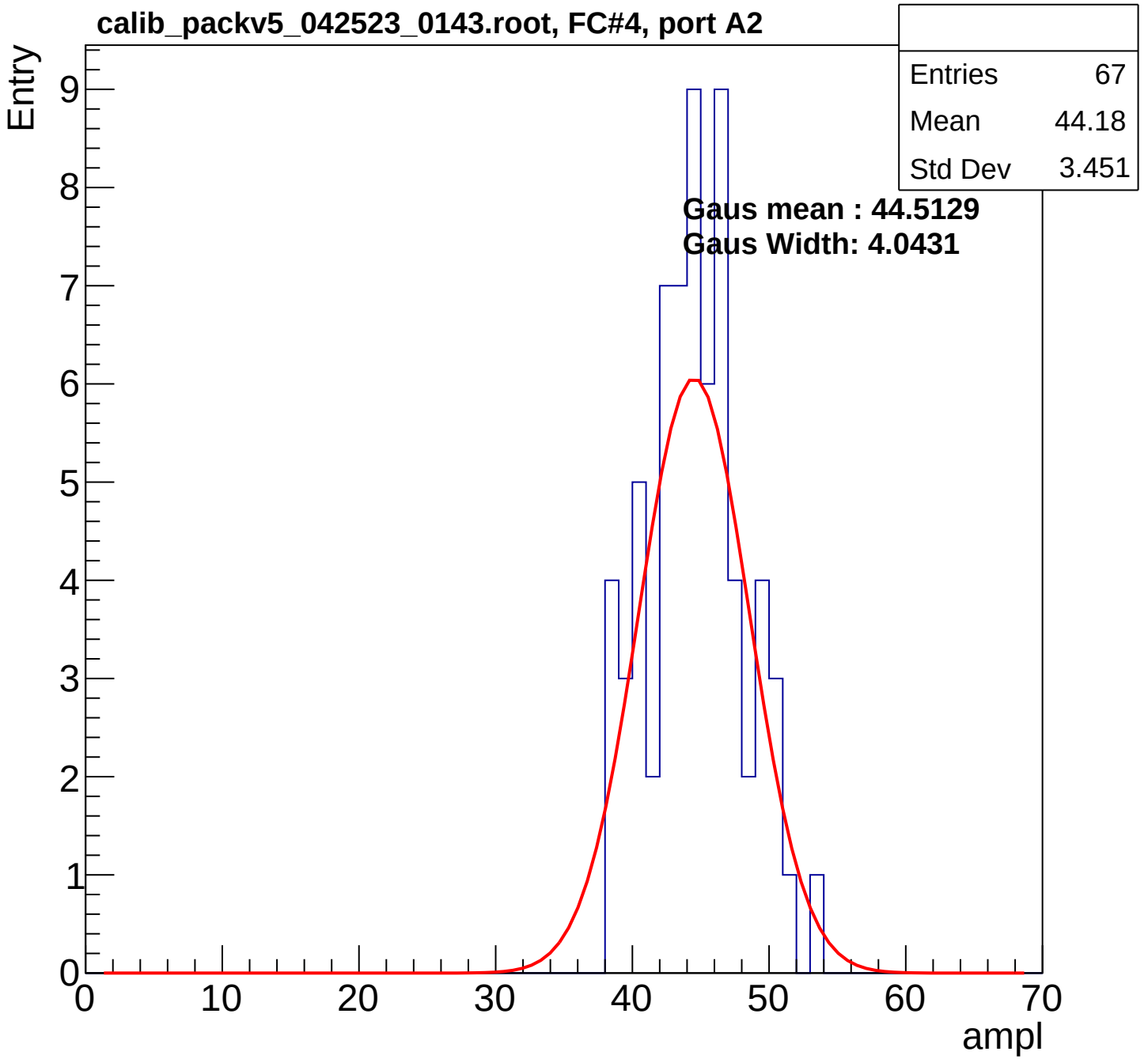
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U23-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.18
Std Dev 3.451
Gaus mean : 44.5129
Gaus Width: 4.0431
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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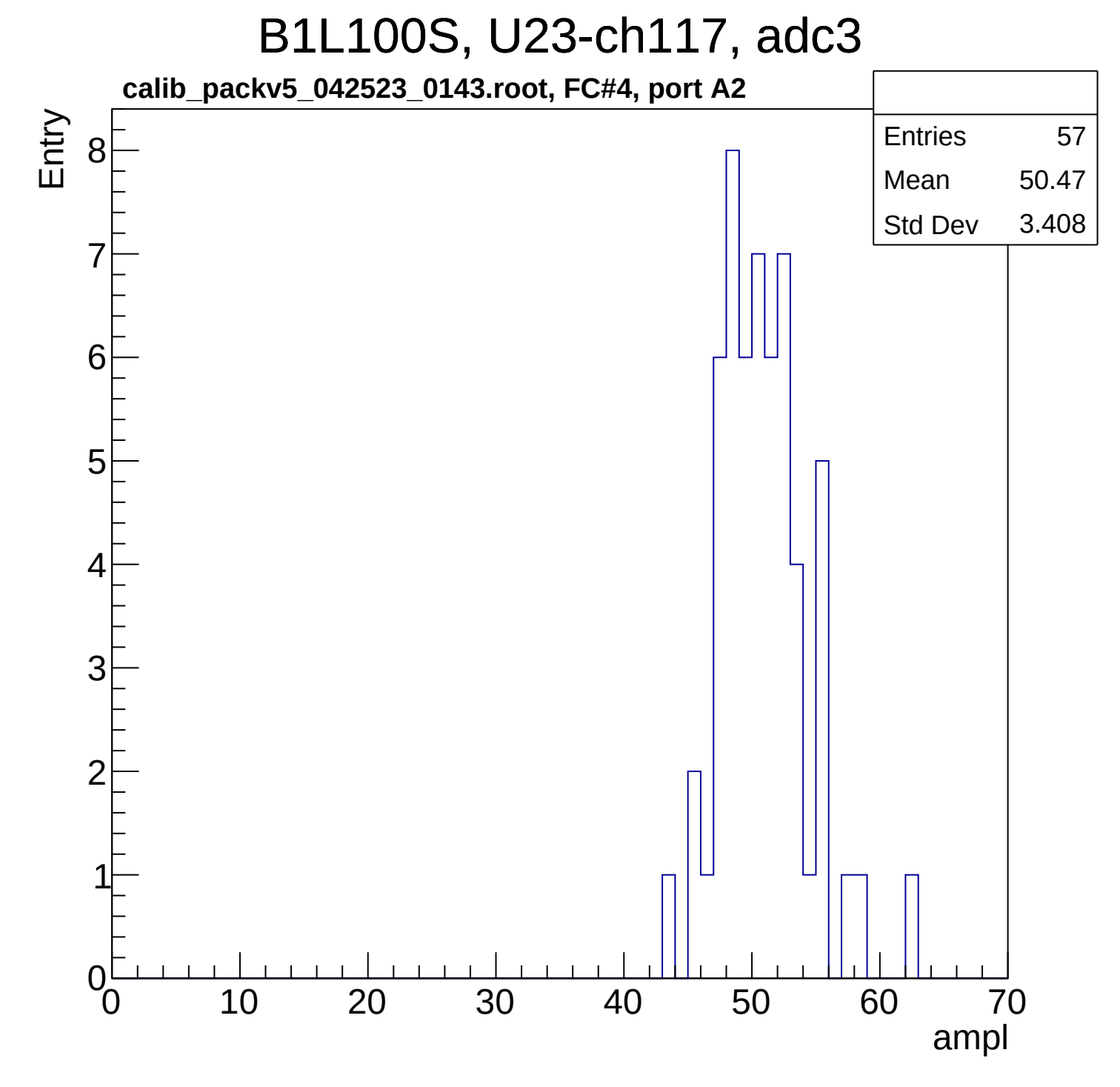
B1L100S, U23-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50.47
Std Dev 3.408
Entry
ampl
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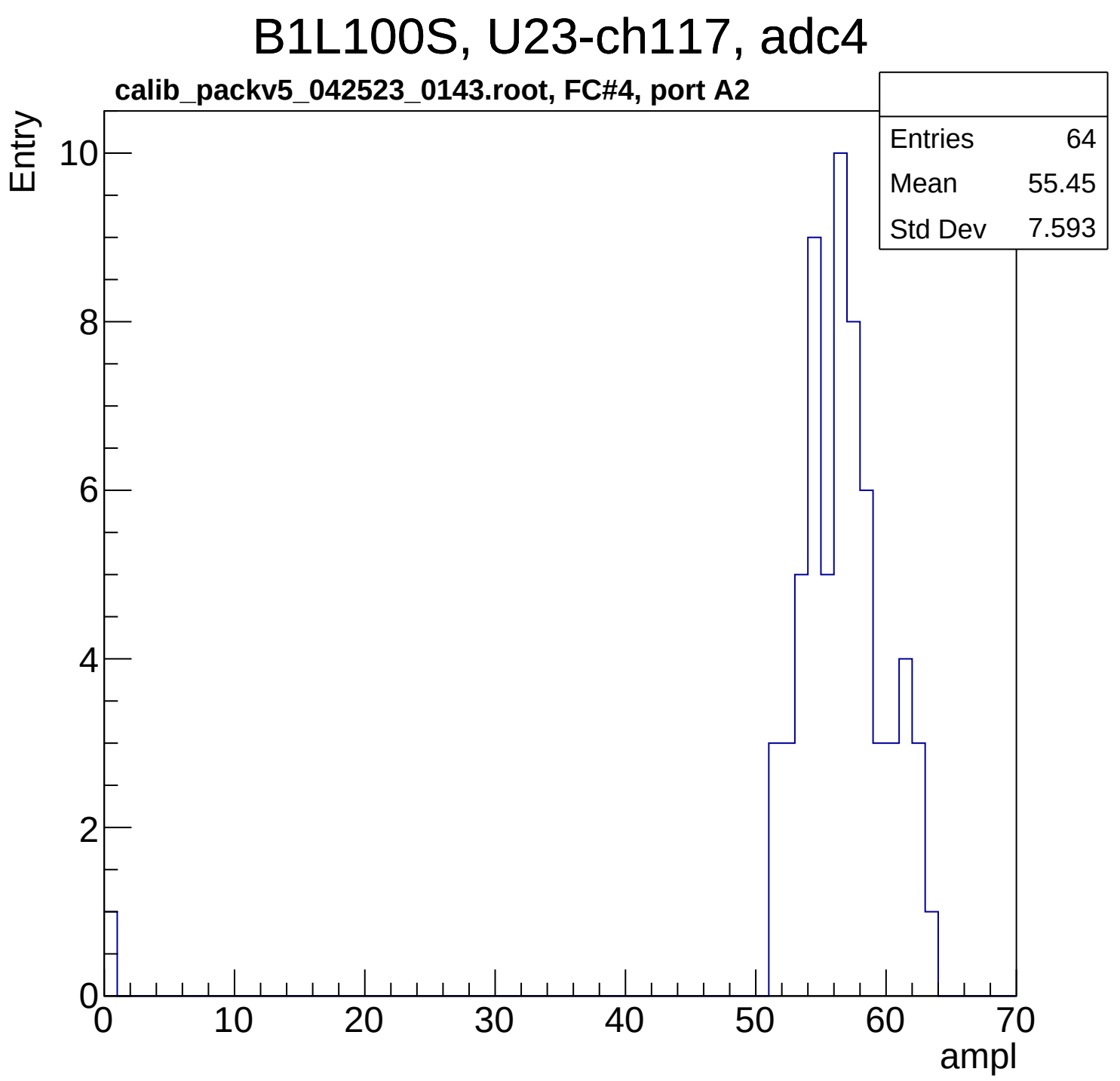
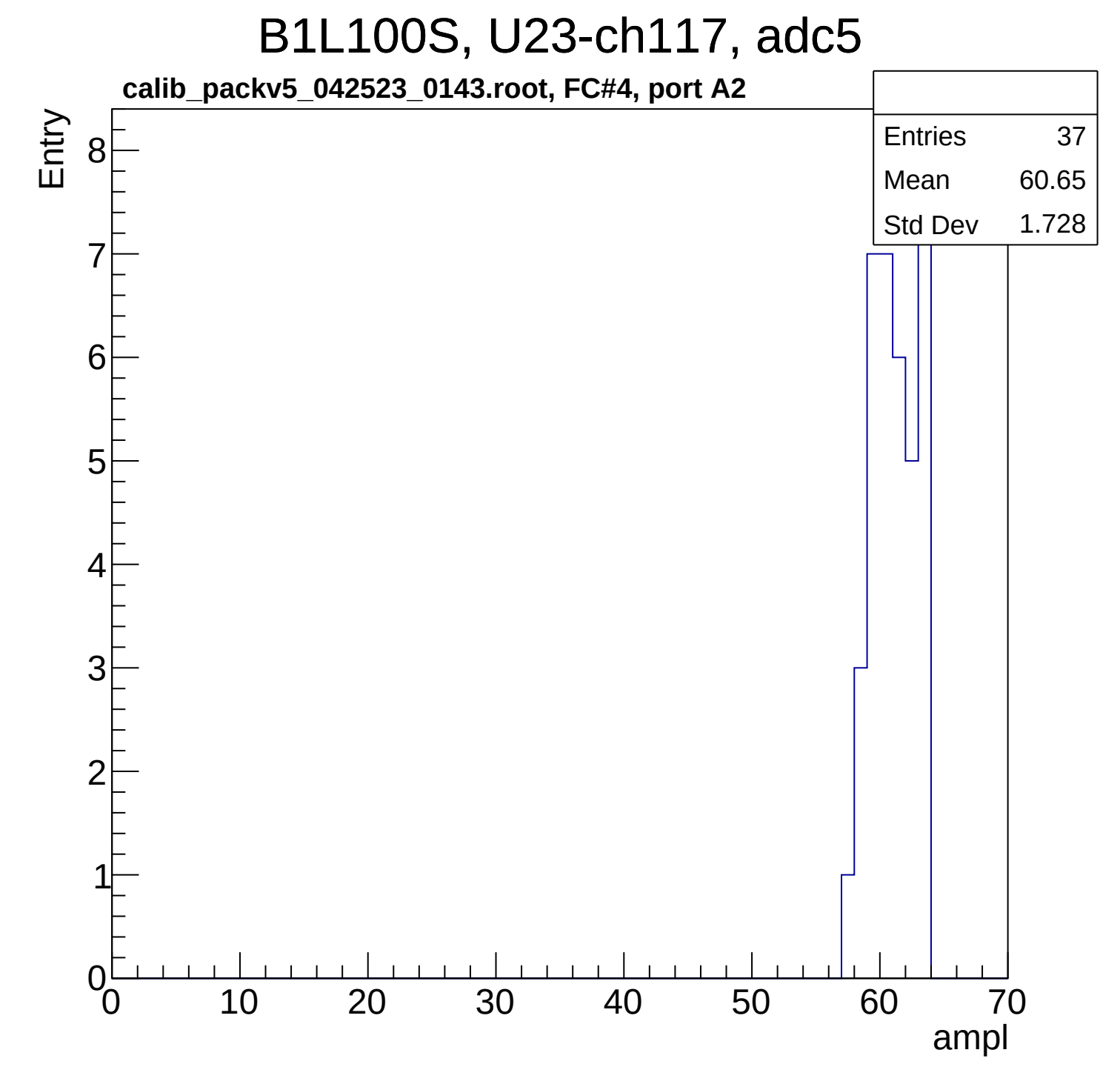
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.45
Std Dev 7.593
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch117, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.65
Std Dev 1.728
Entry
ampl
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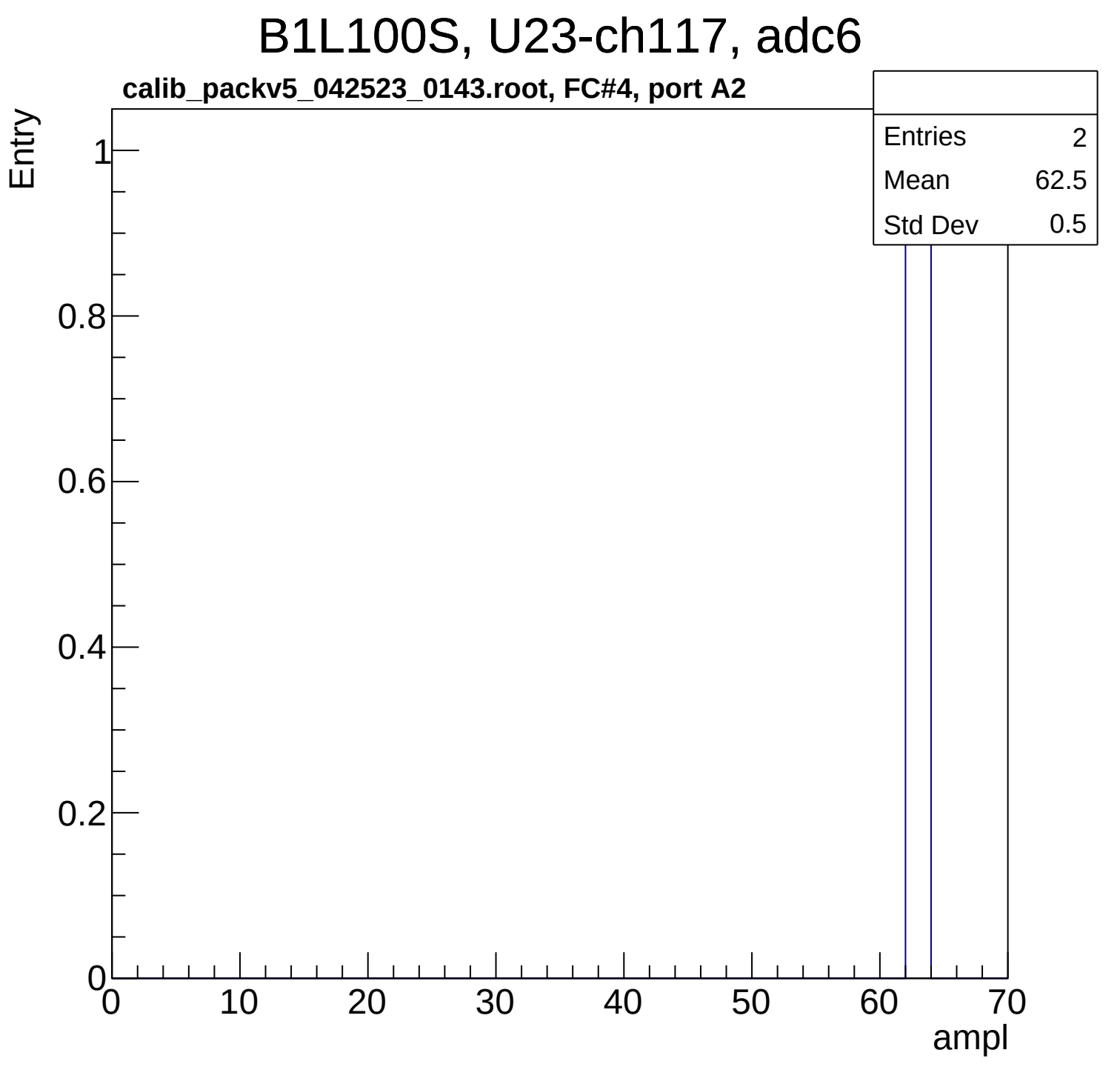
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch117, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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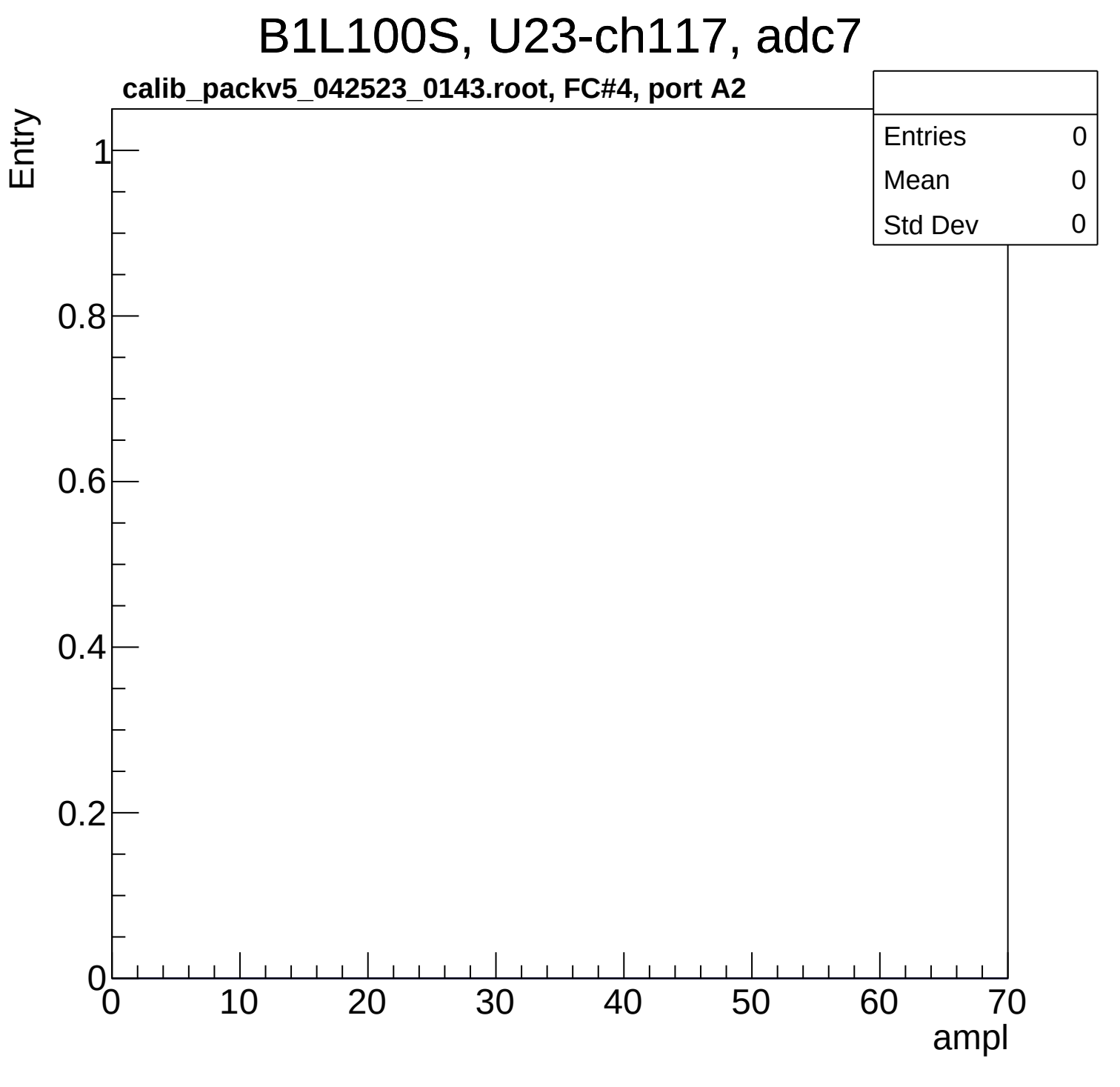
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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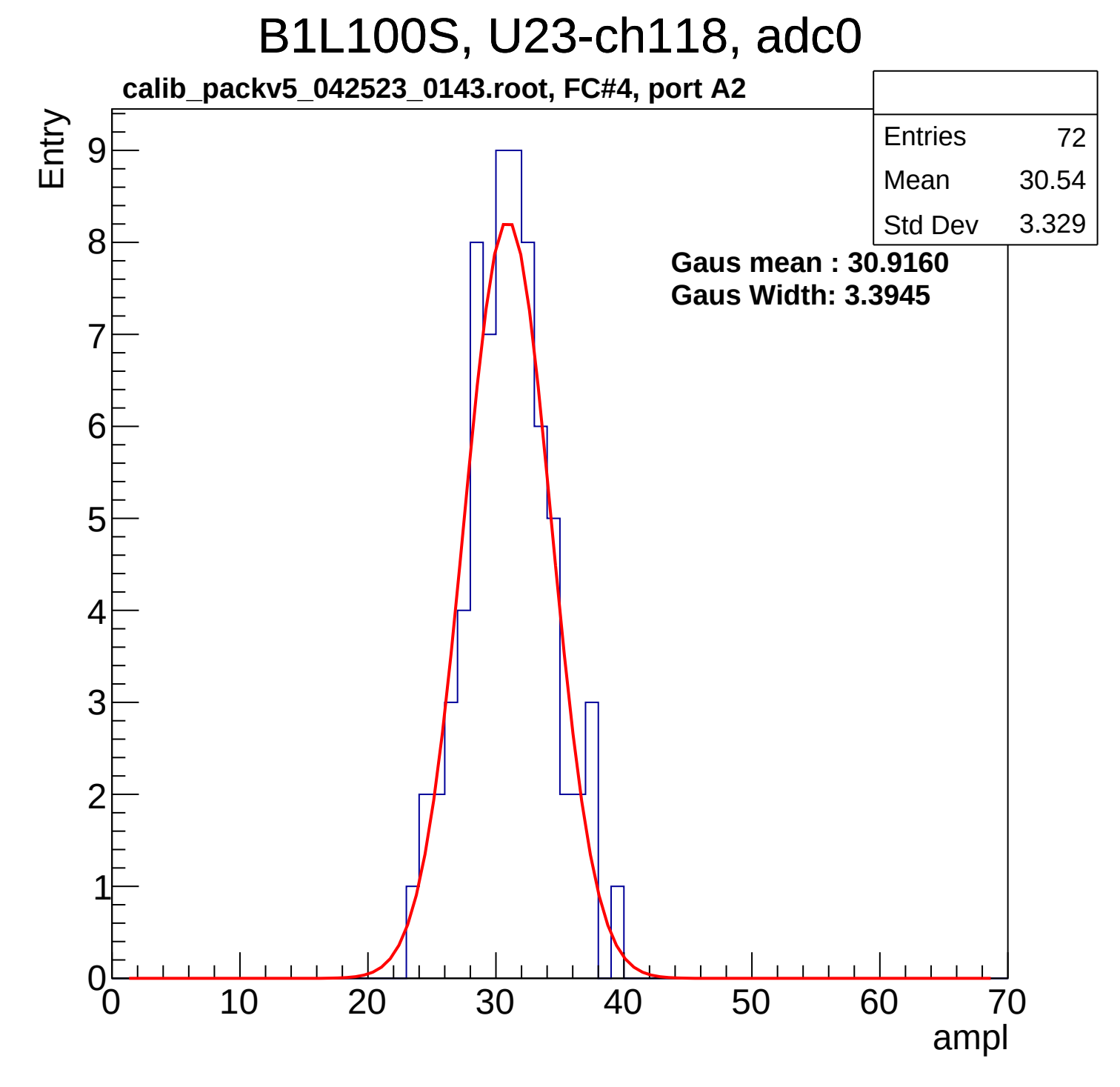
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.54
Std Dev 3.329
Gaus mean : 30.9160
Gaus Width: 3.3945
Entry
ampl
0
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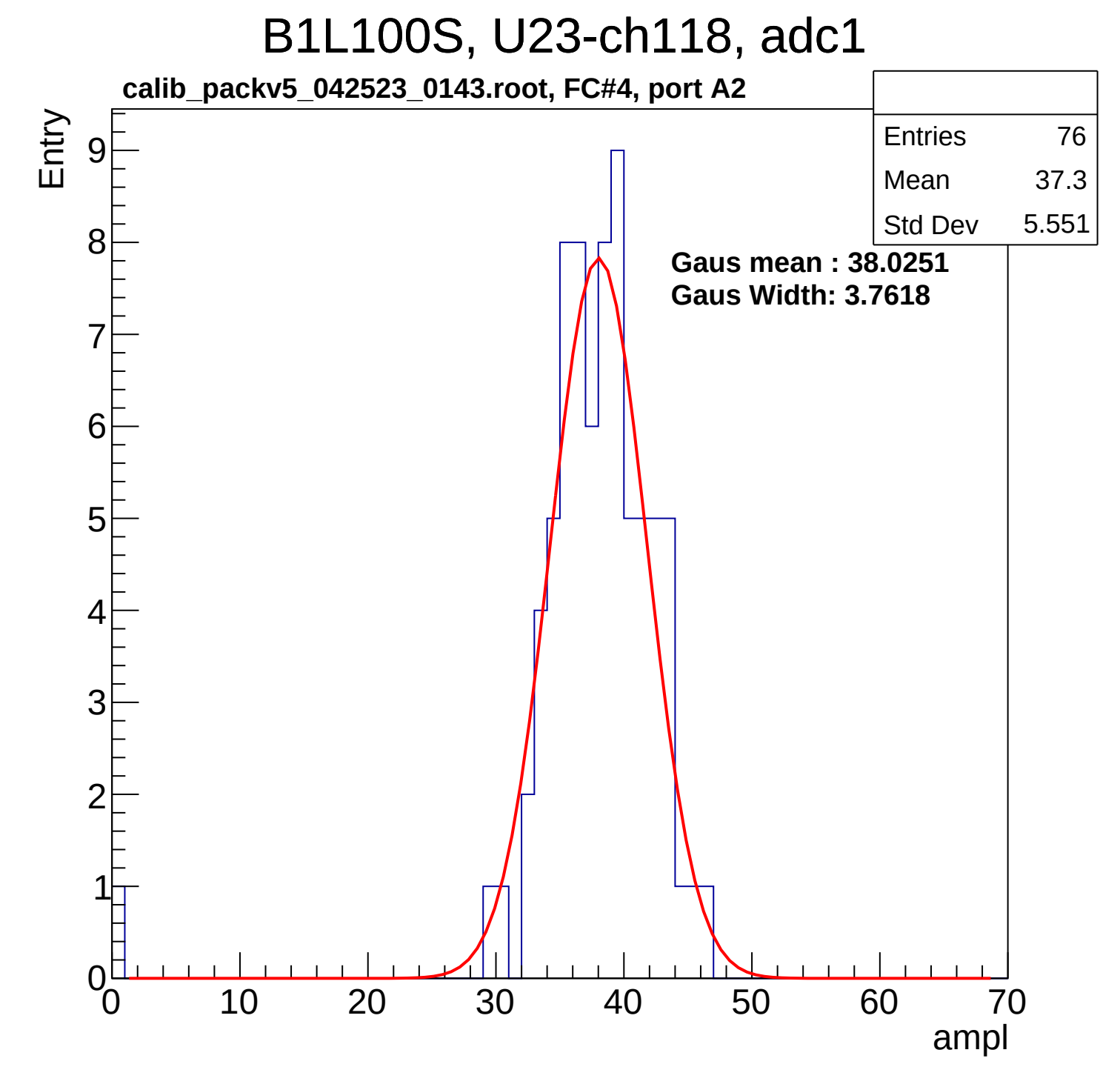
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.3
Std Dev 5.551
Gaus mean : 38.0251
Gaus Width: 3.7618
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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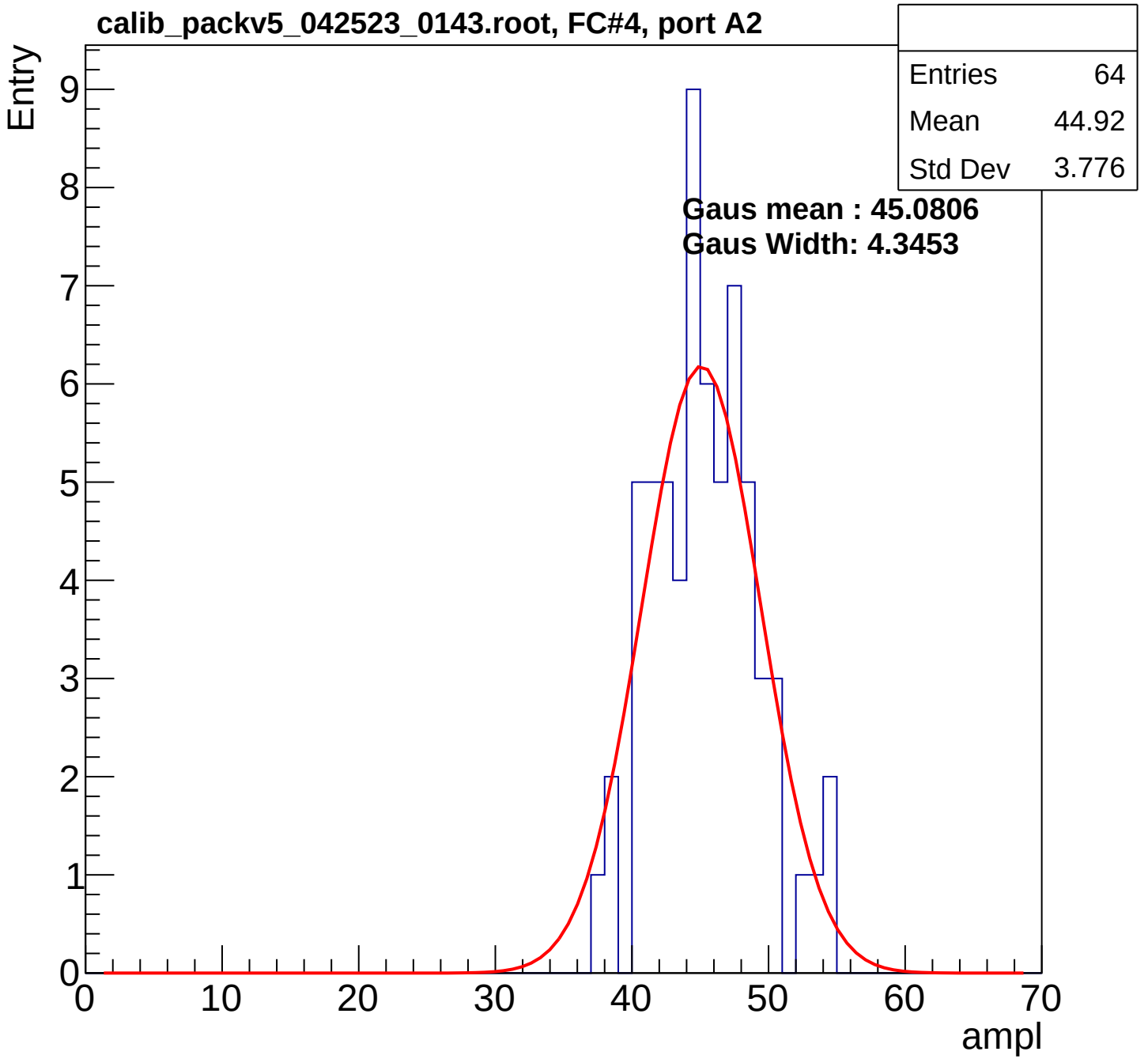
B1L100S, U23-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 44.92
Std Dev 3.776
Gaus mean : 45.0806
Gaus Width: 4.3453
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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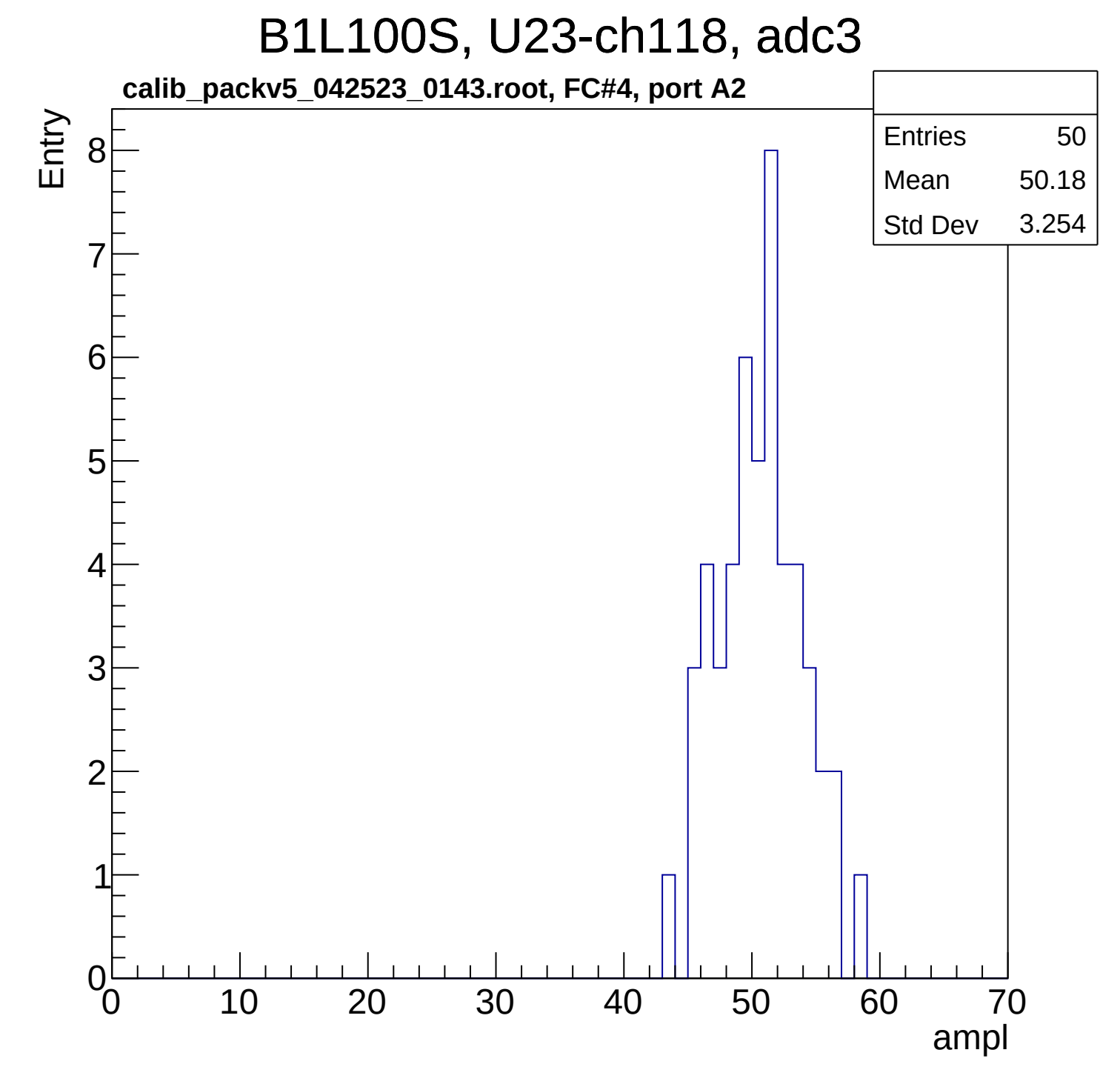
B1L100S, U23-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 50.18
Std Dev 3.254
Entry
ampl
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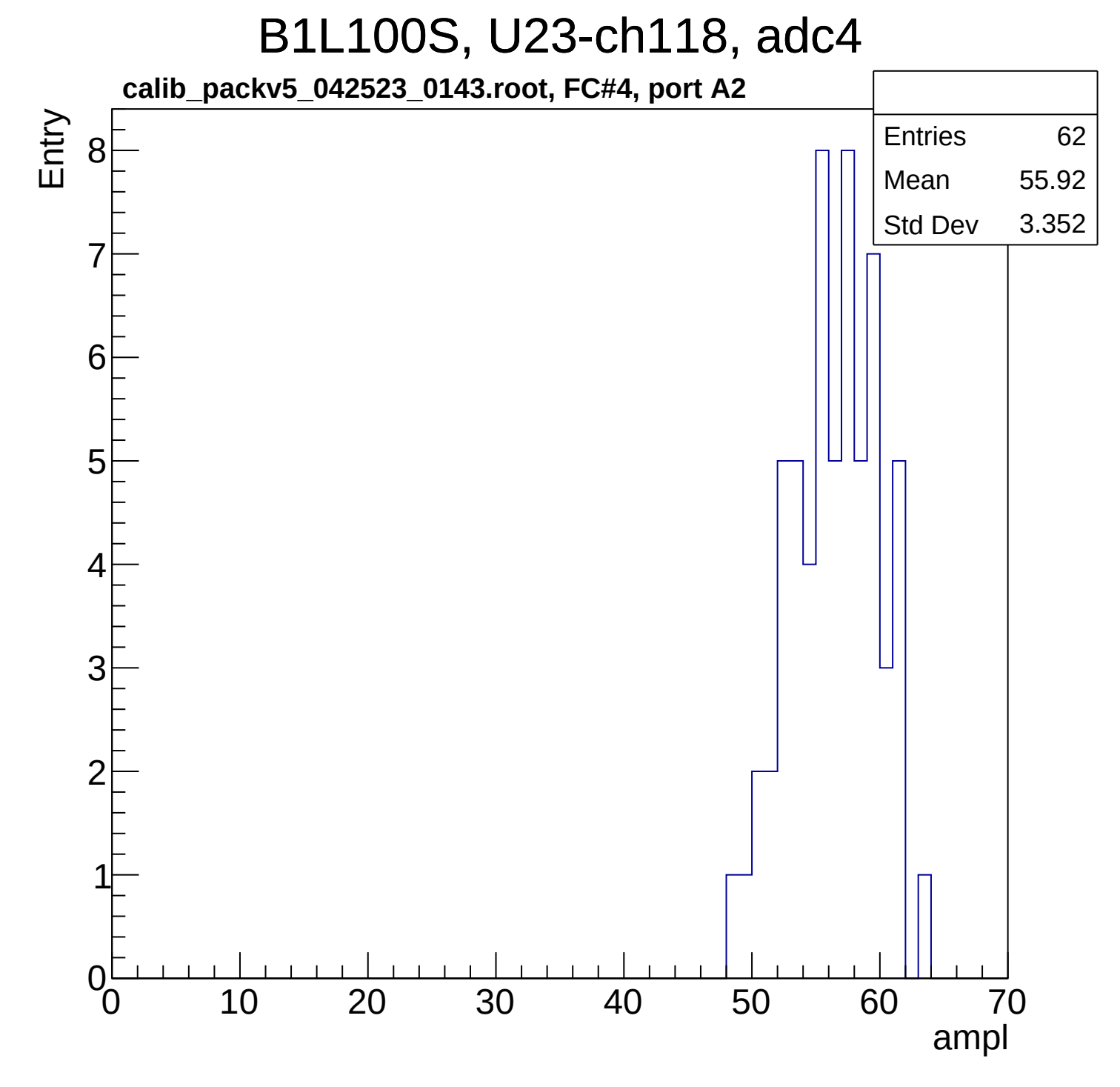
B1L100S, U23-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.92
Std Dev 3.352
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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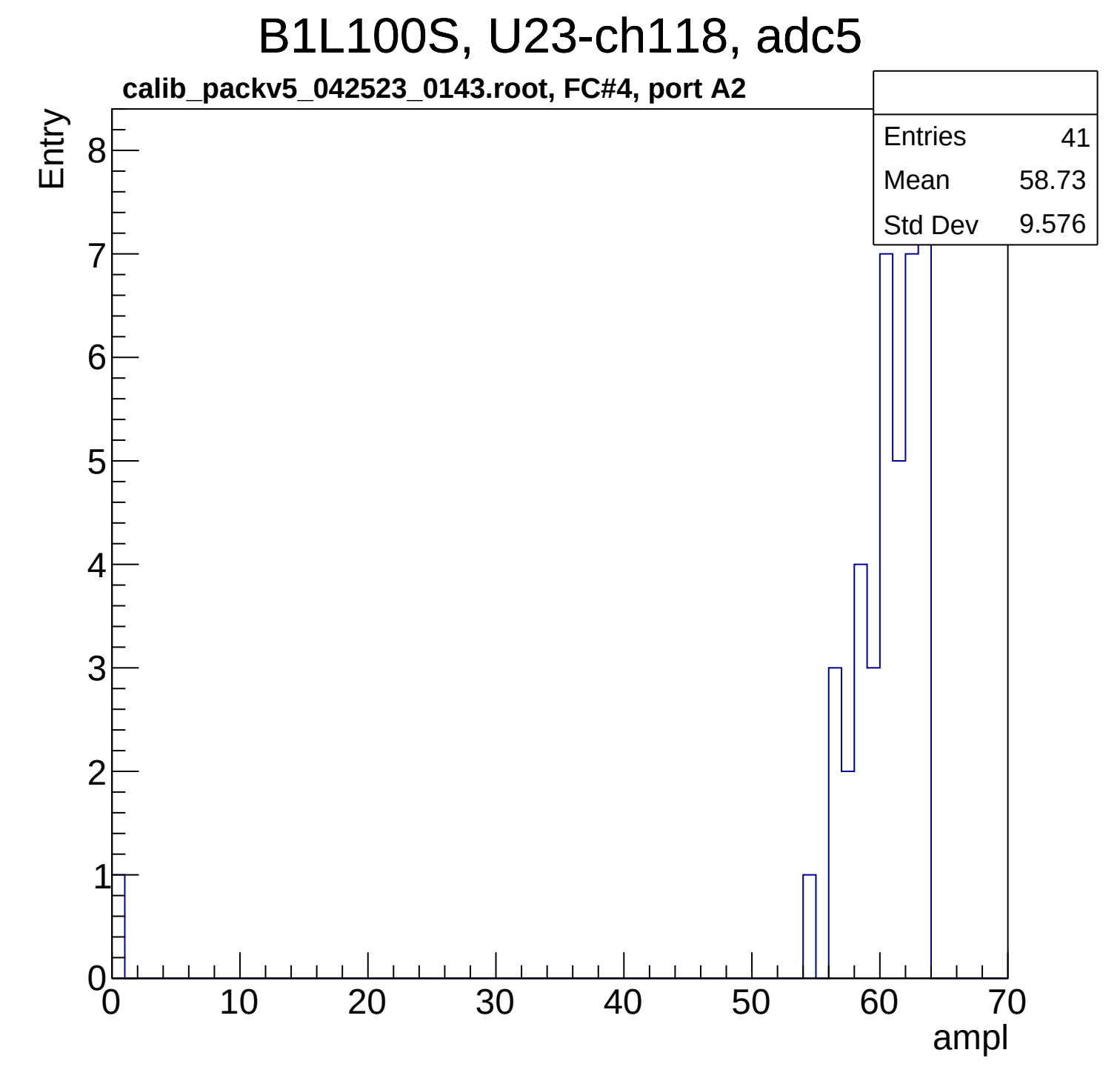
60
70


B1L100S, U23-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.73
Std Dev 9.576
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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60
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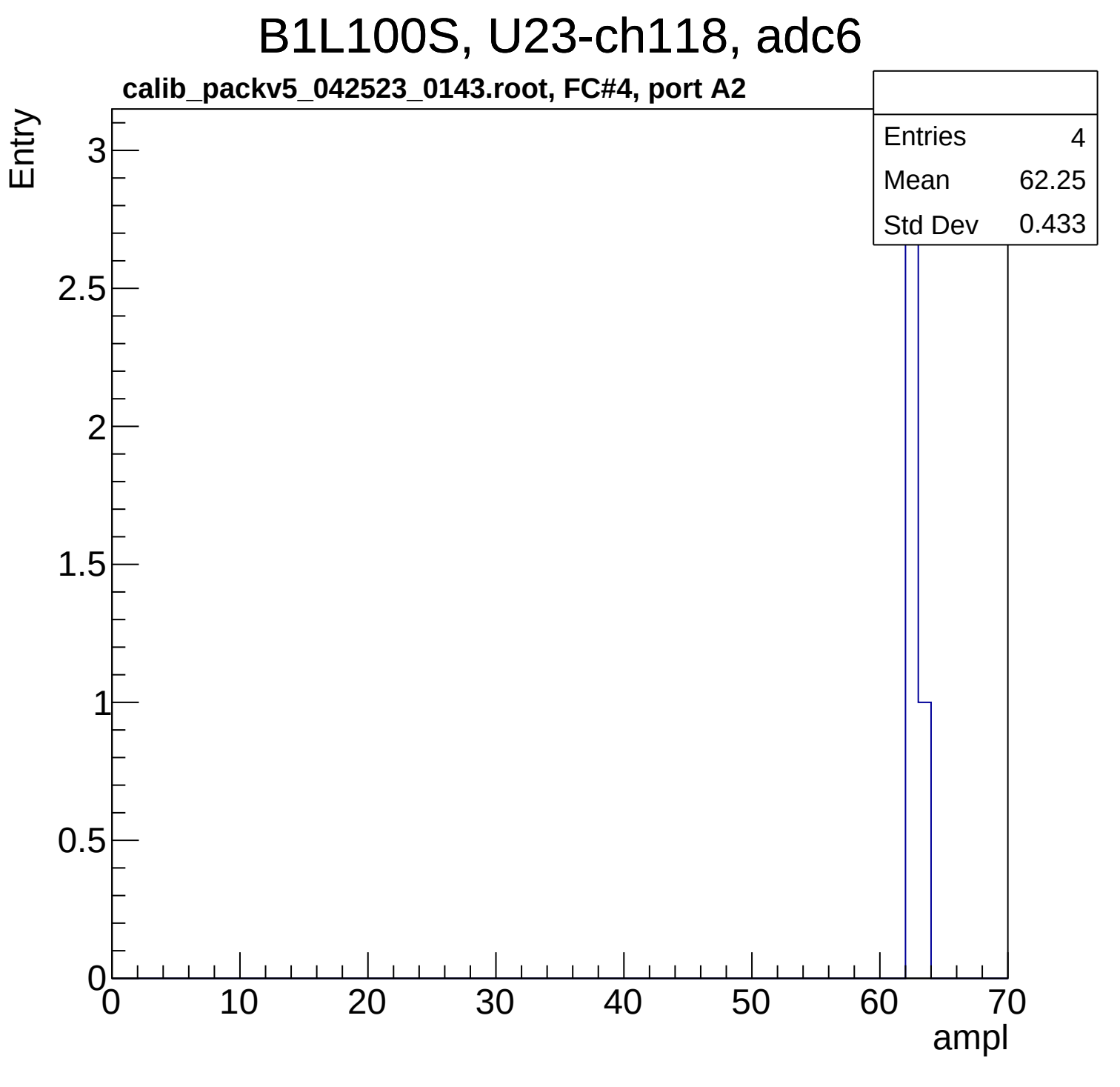
B1L100S, U23-ch118, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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1
1.5
2
2.5
3
0
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20
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40
50
60
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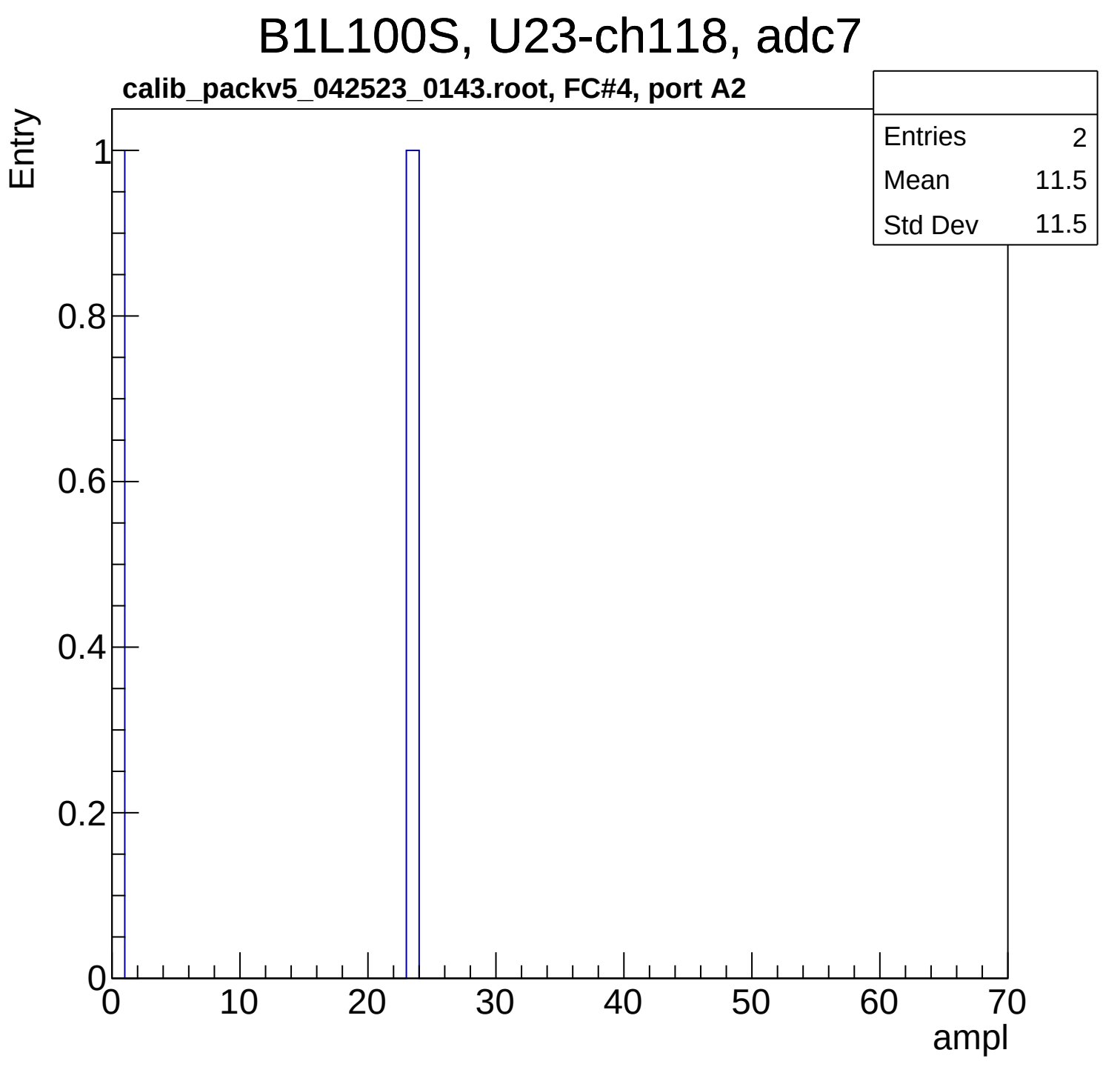
B1L100S, U23-ch118, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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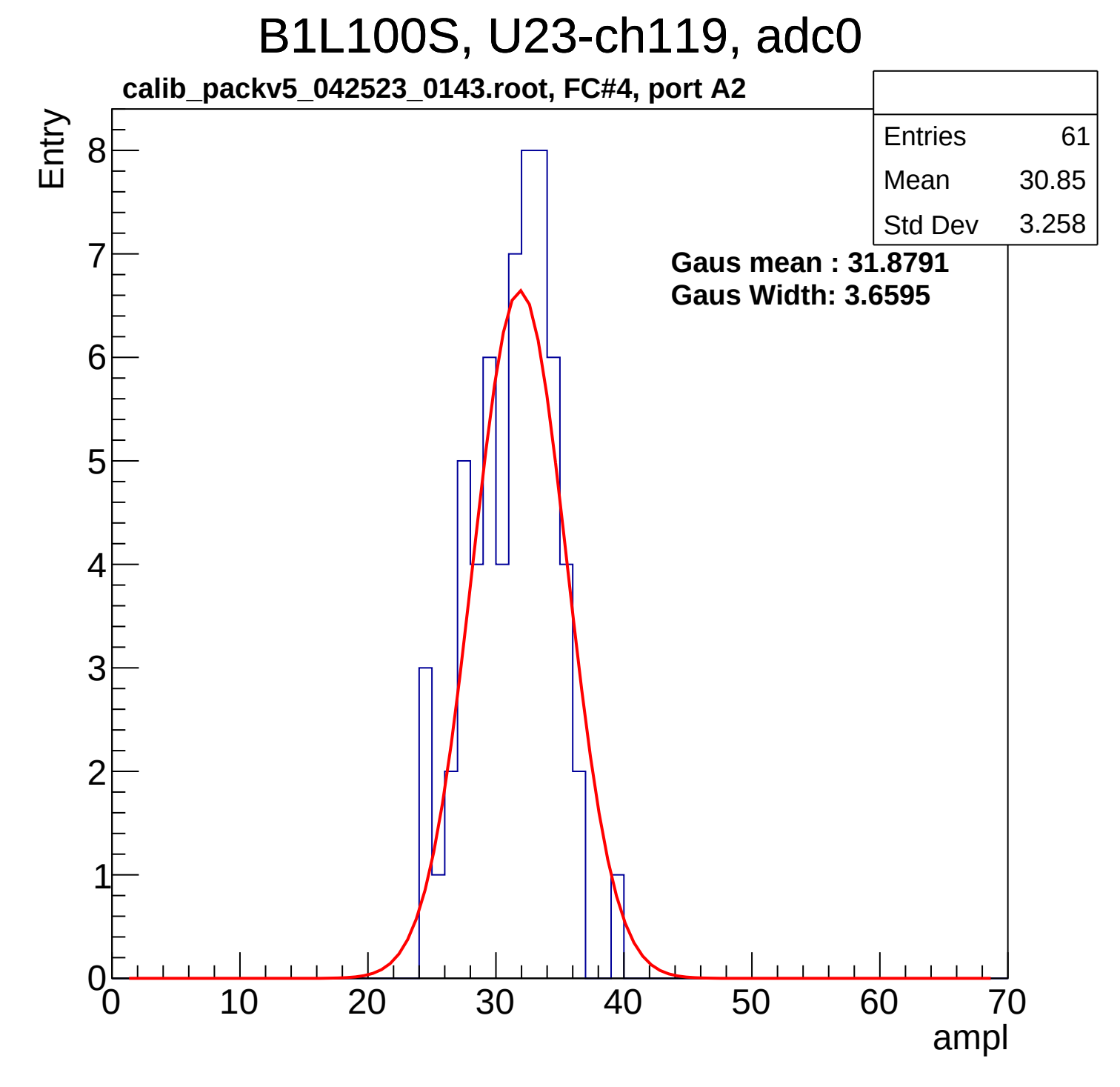
50
60
70
ampl


B1L100S, U23-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.85
Std Dev 3.258
Gaus mean : 31.8791
Gaus Width: 3.6595
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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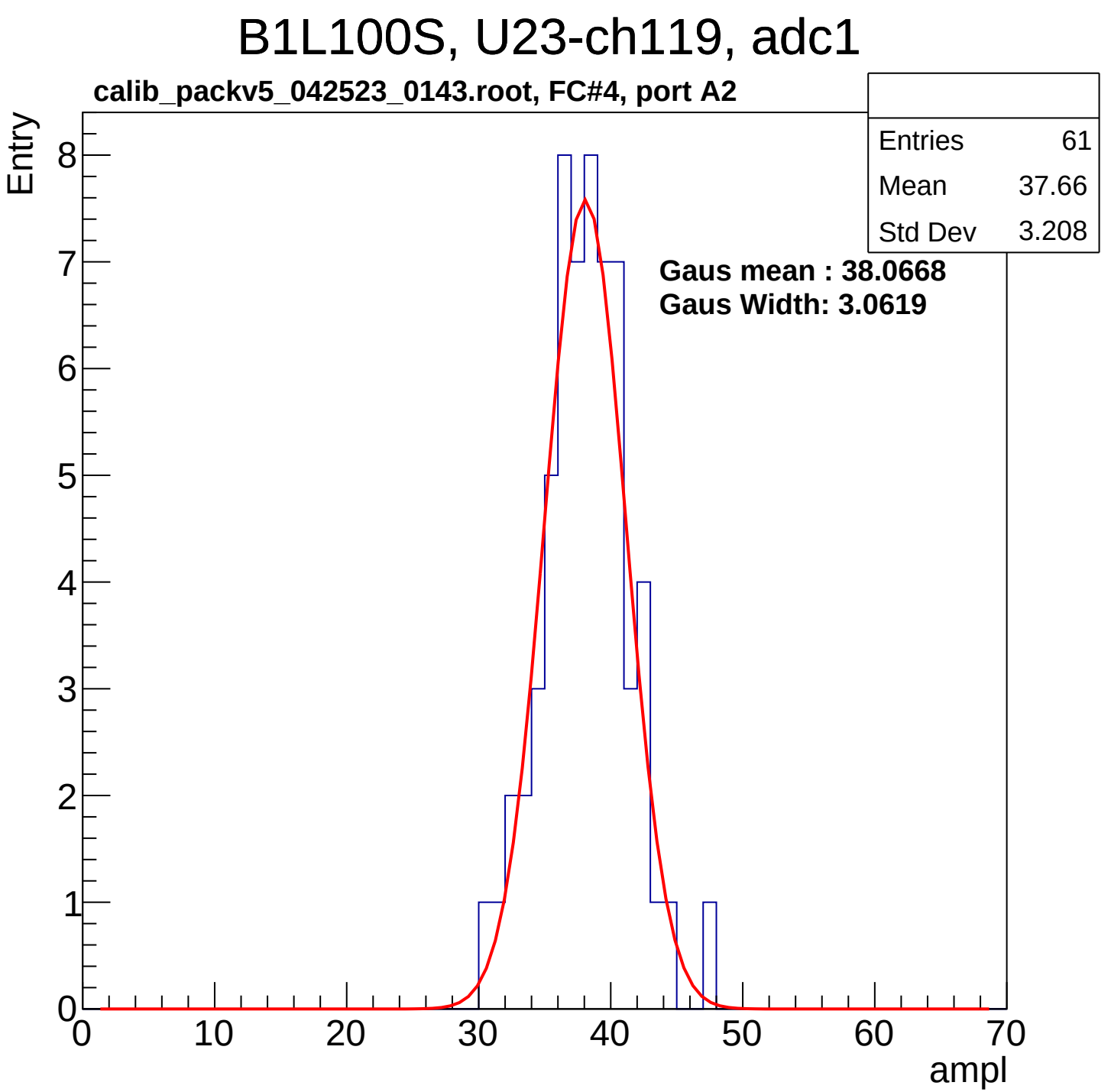
B1L100S, U23-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.66
Std Dev 3.208
Gaus mean : 38.0668
Gaus Width: 3.0619
Entry
ampl
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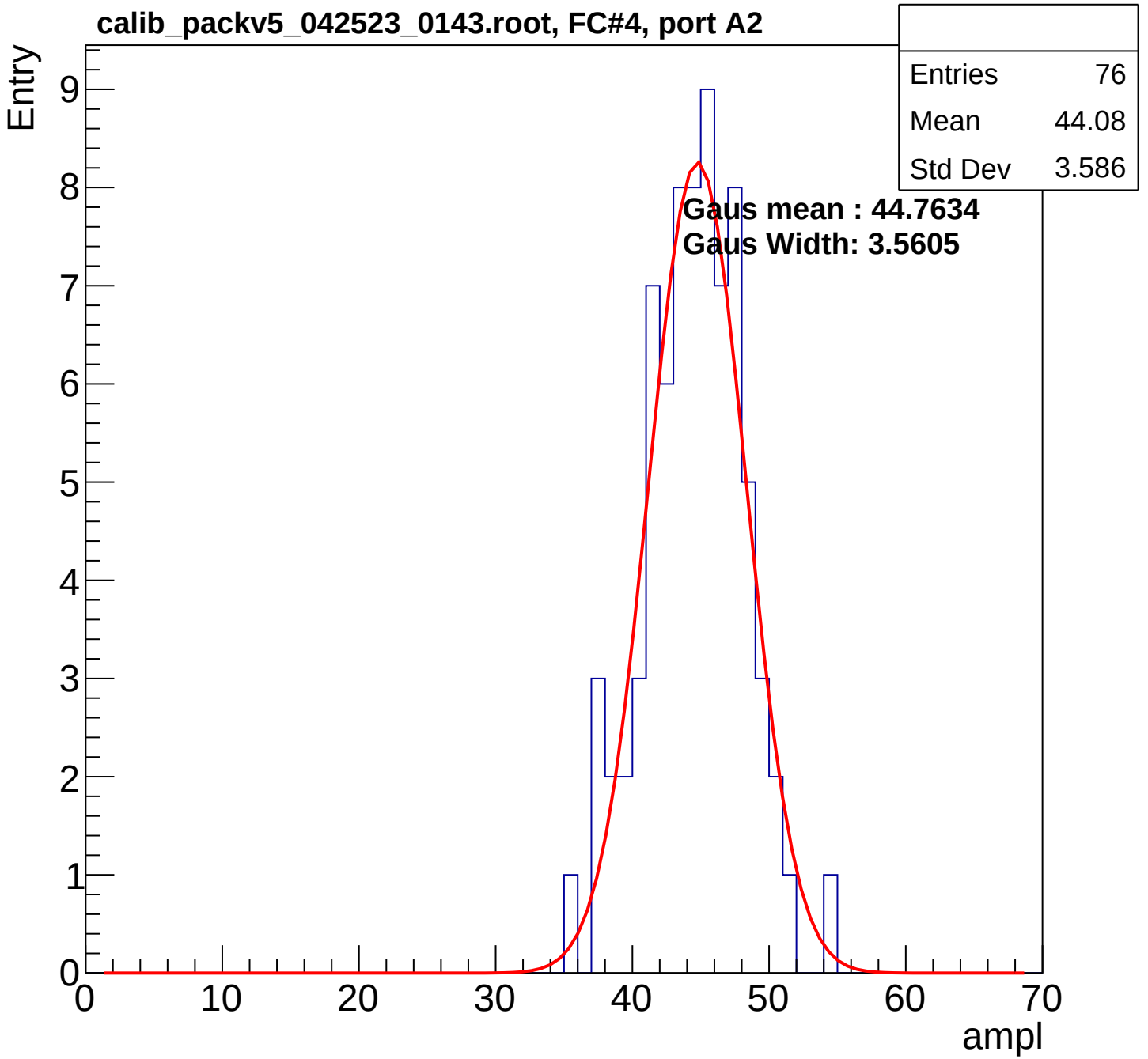
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.08
Std Dev 3.586
Gaus mean : 44.7634
Gaus Width: 3.5605
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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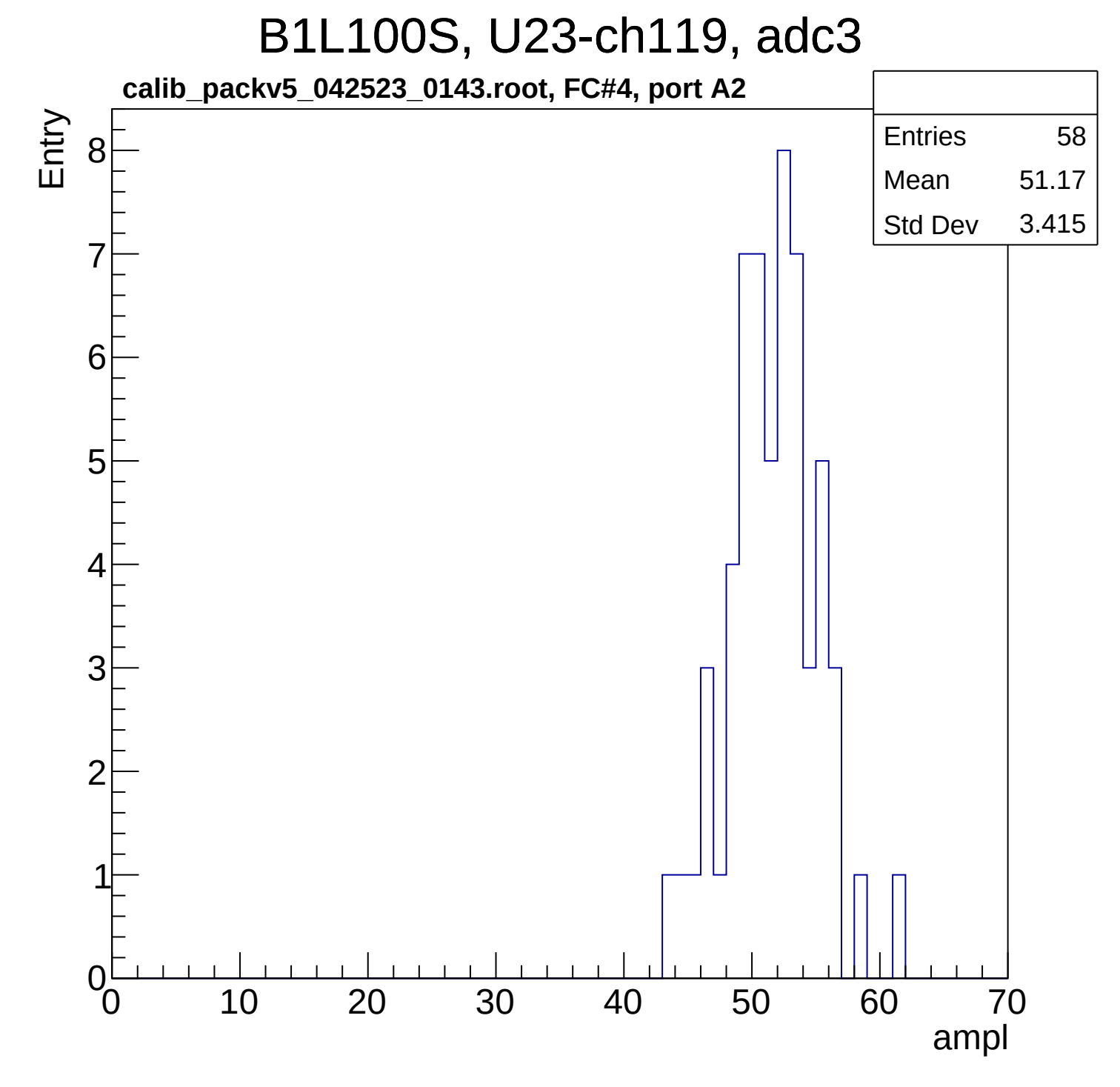
B1L100S, U23-ch119, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 51.17
Std Dev 3.415
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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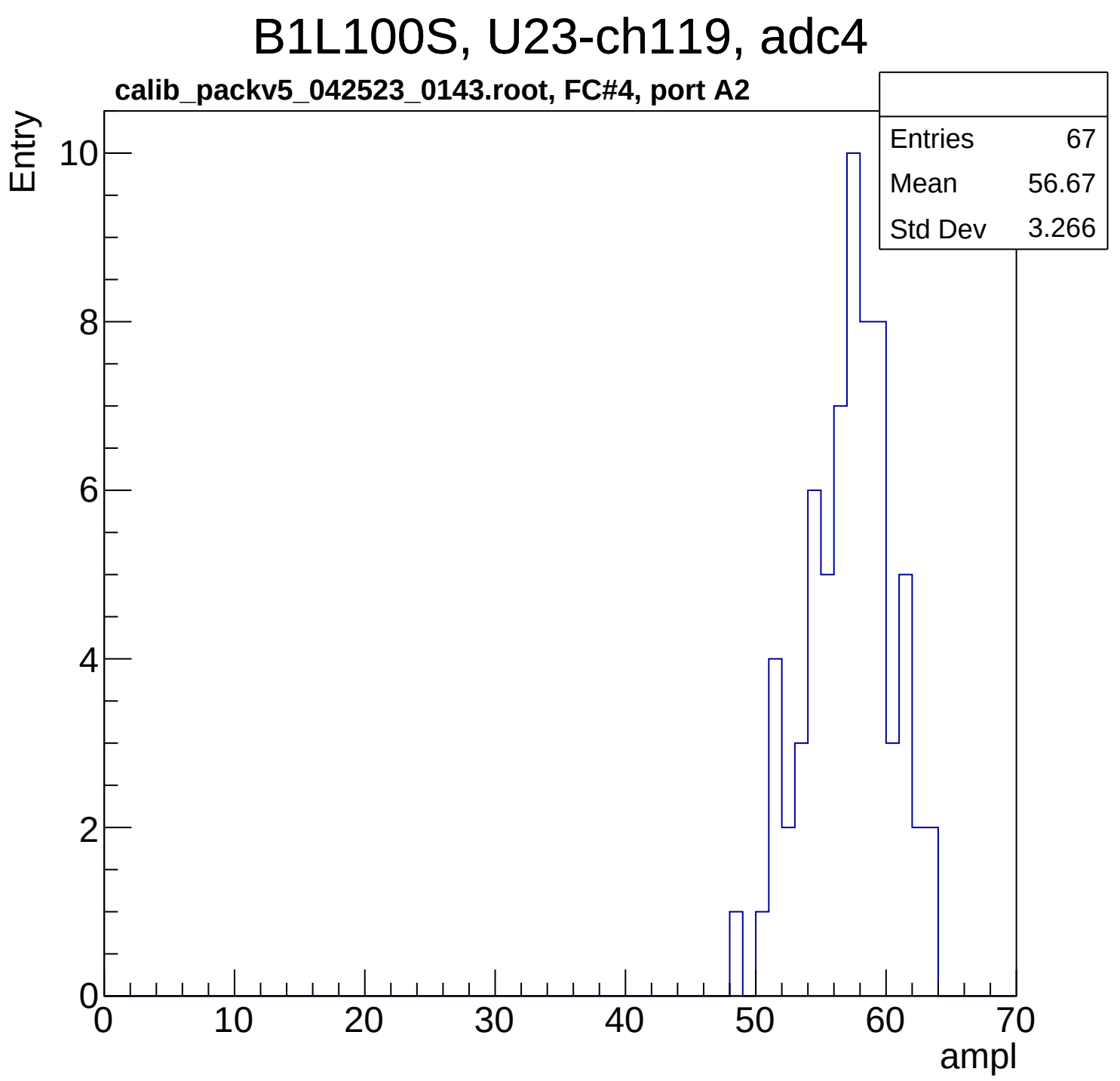
B1L100S, U23-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.67
Std Dev 3.266
Entry
ampl
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20
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40
50
60
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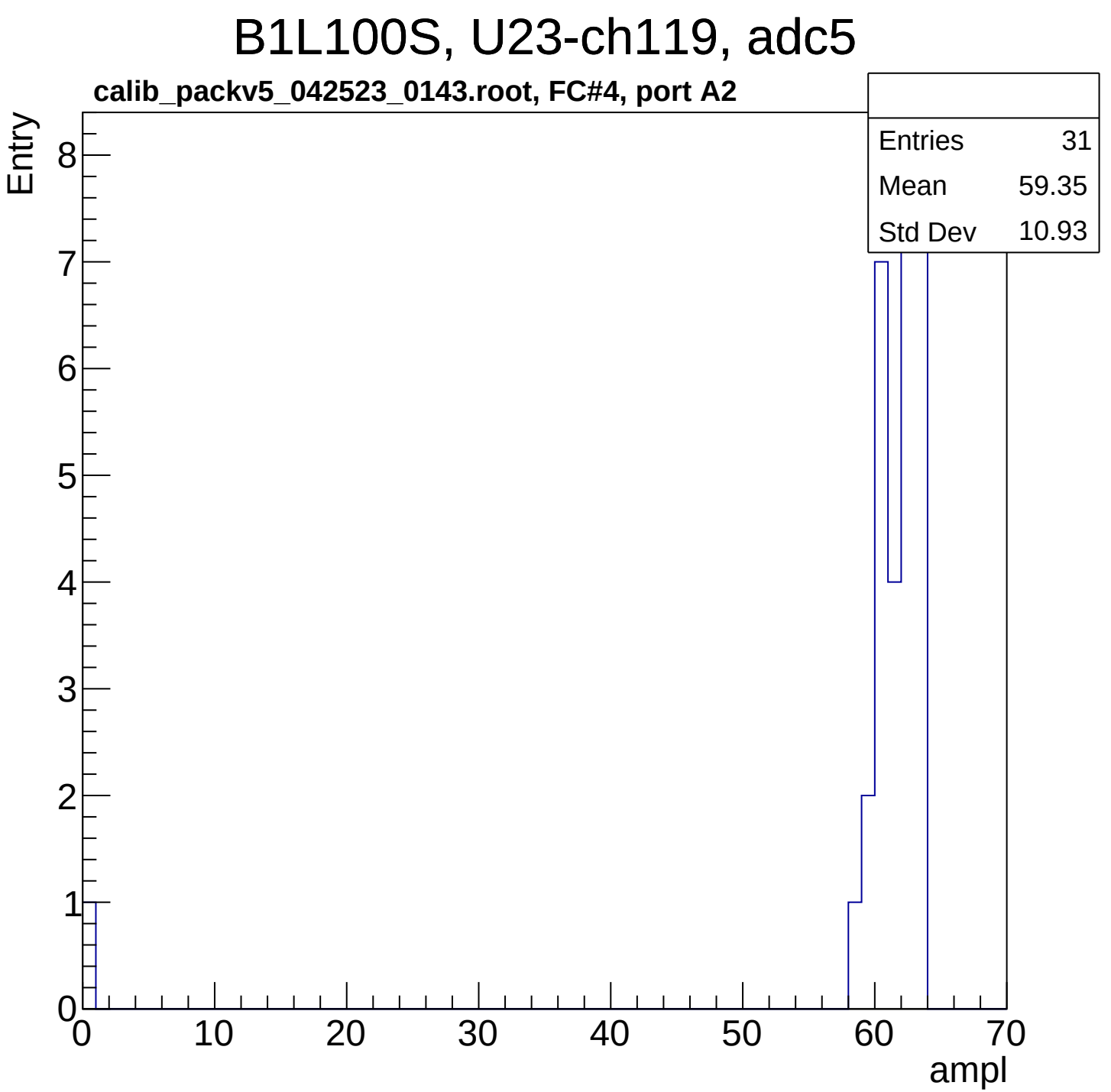
B1L100S, U23-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59.35
Std Dev 10.93
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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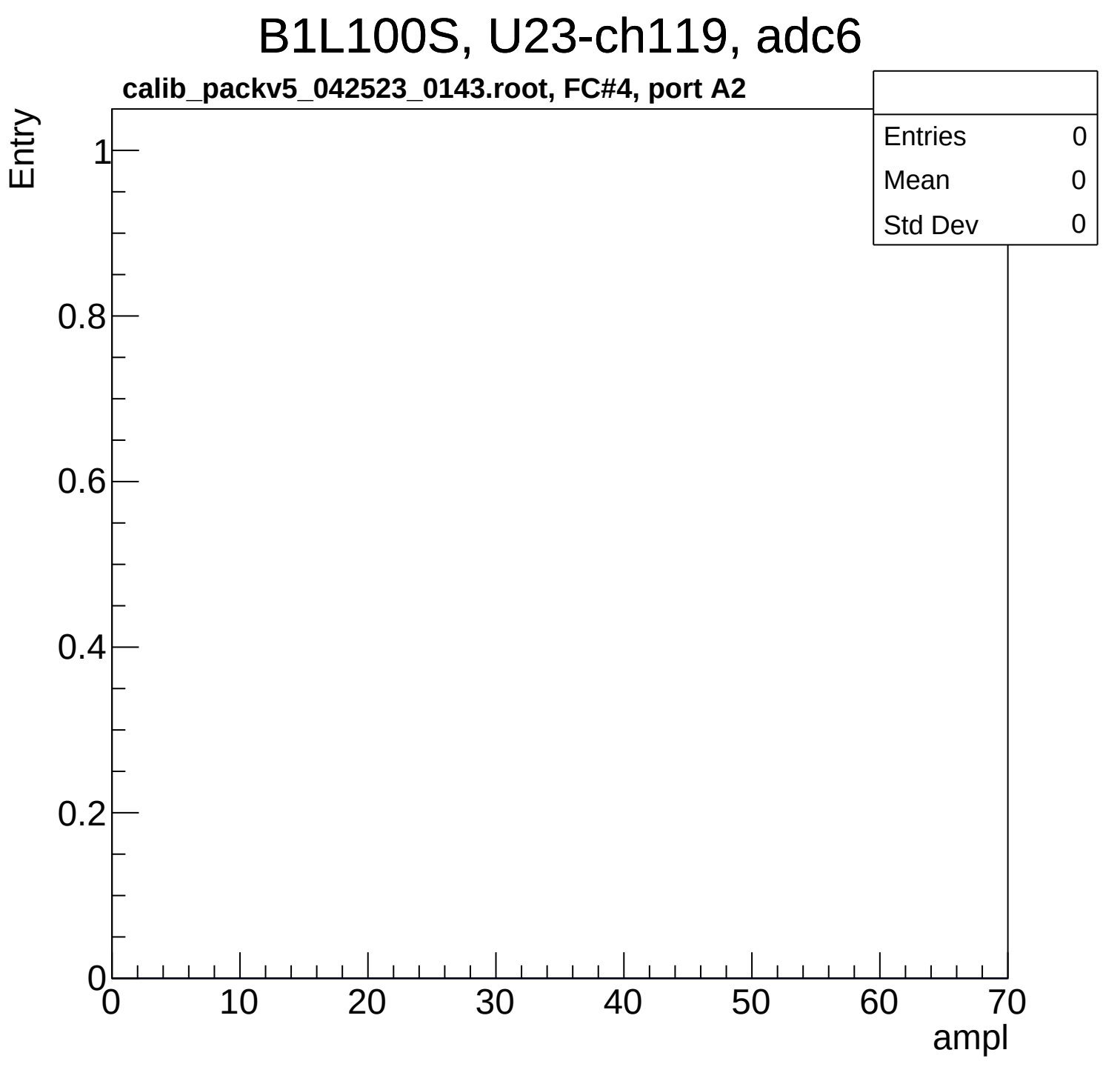
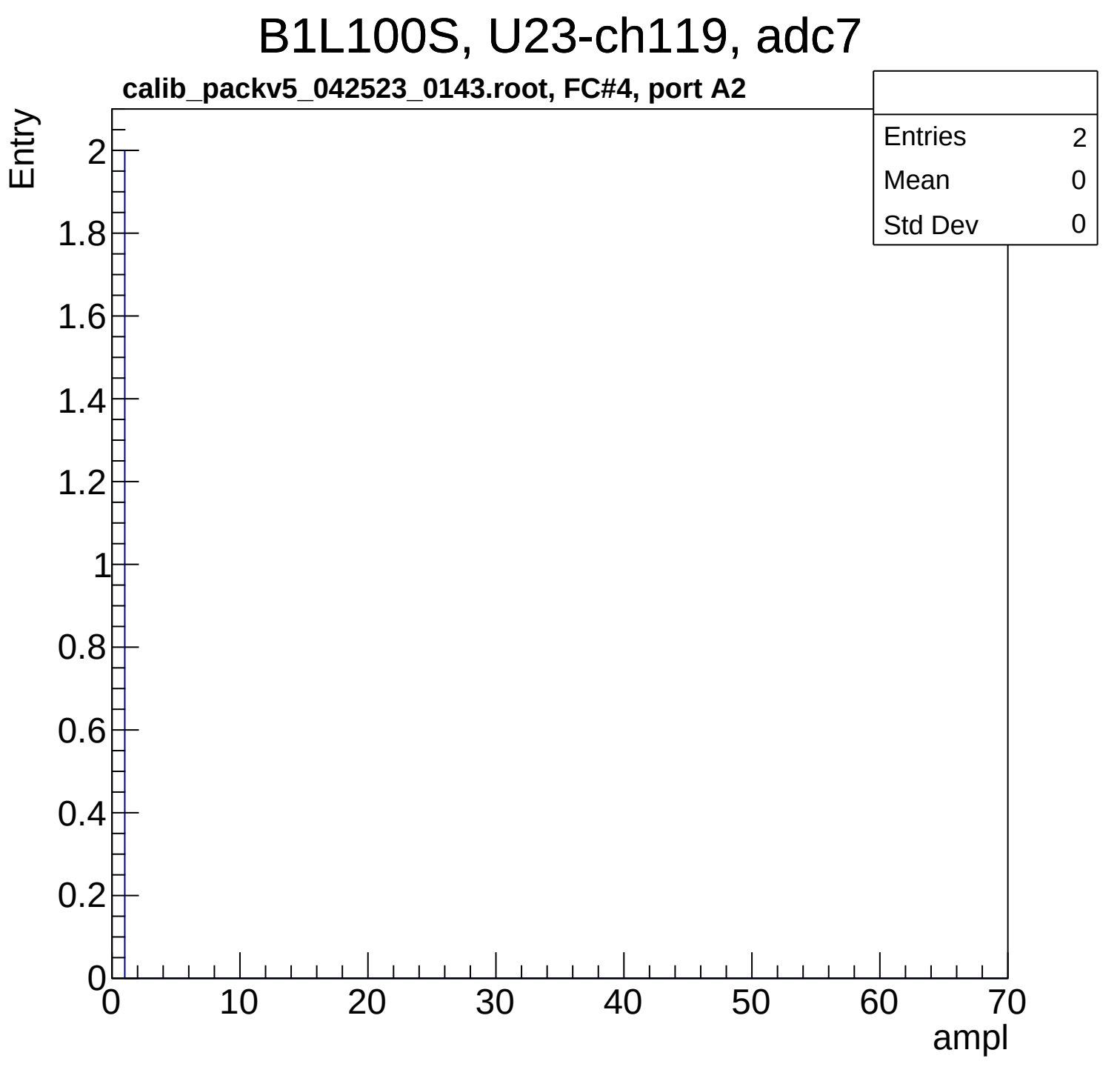

B1L100S, U23-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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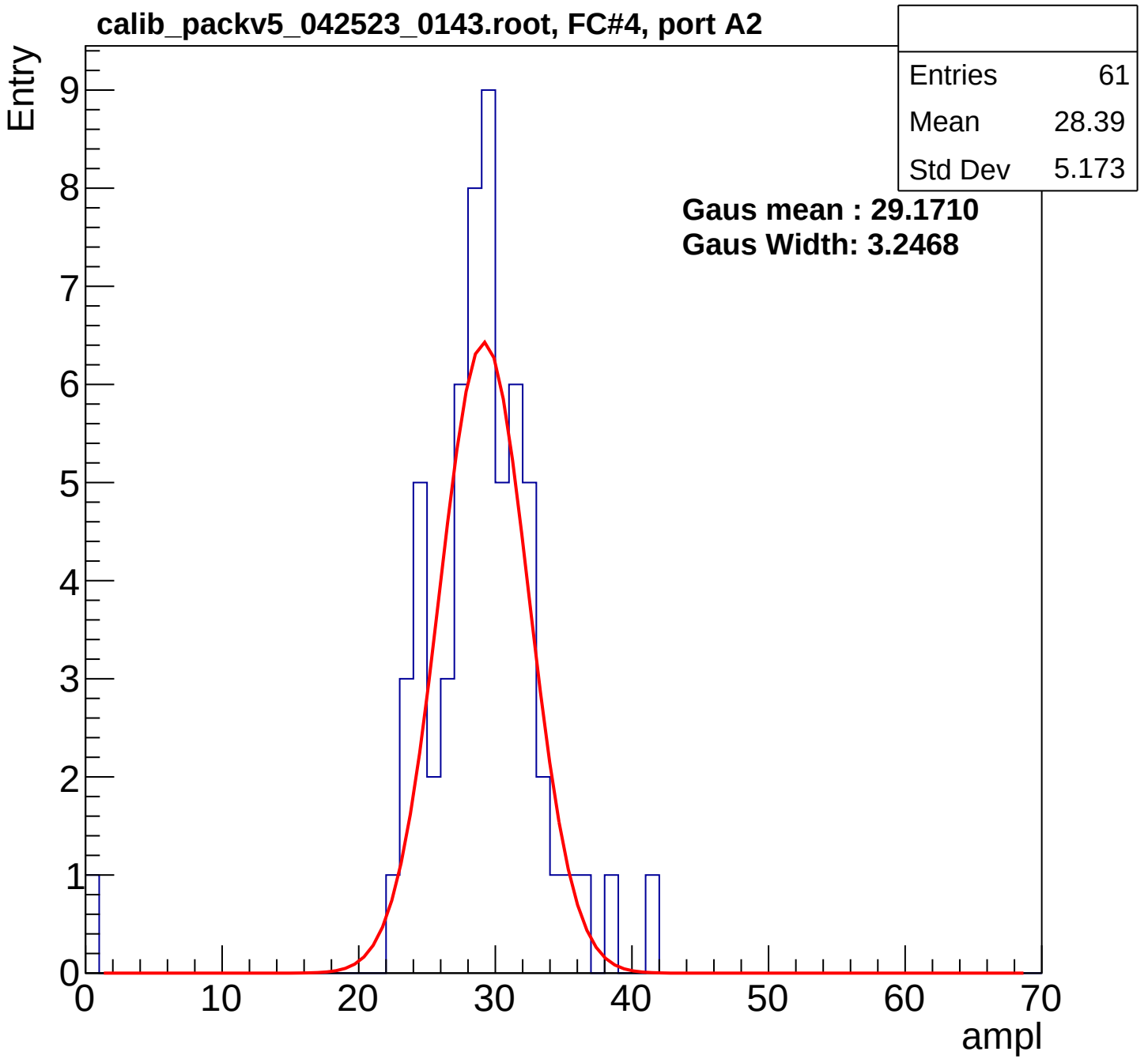
70
ampl


B1L100S, U23-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.39
Std Dev 5.173
Gaus mean : 29.1710
Gaus Width: 3.2468
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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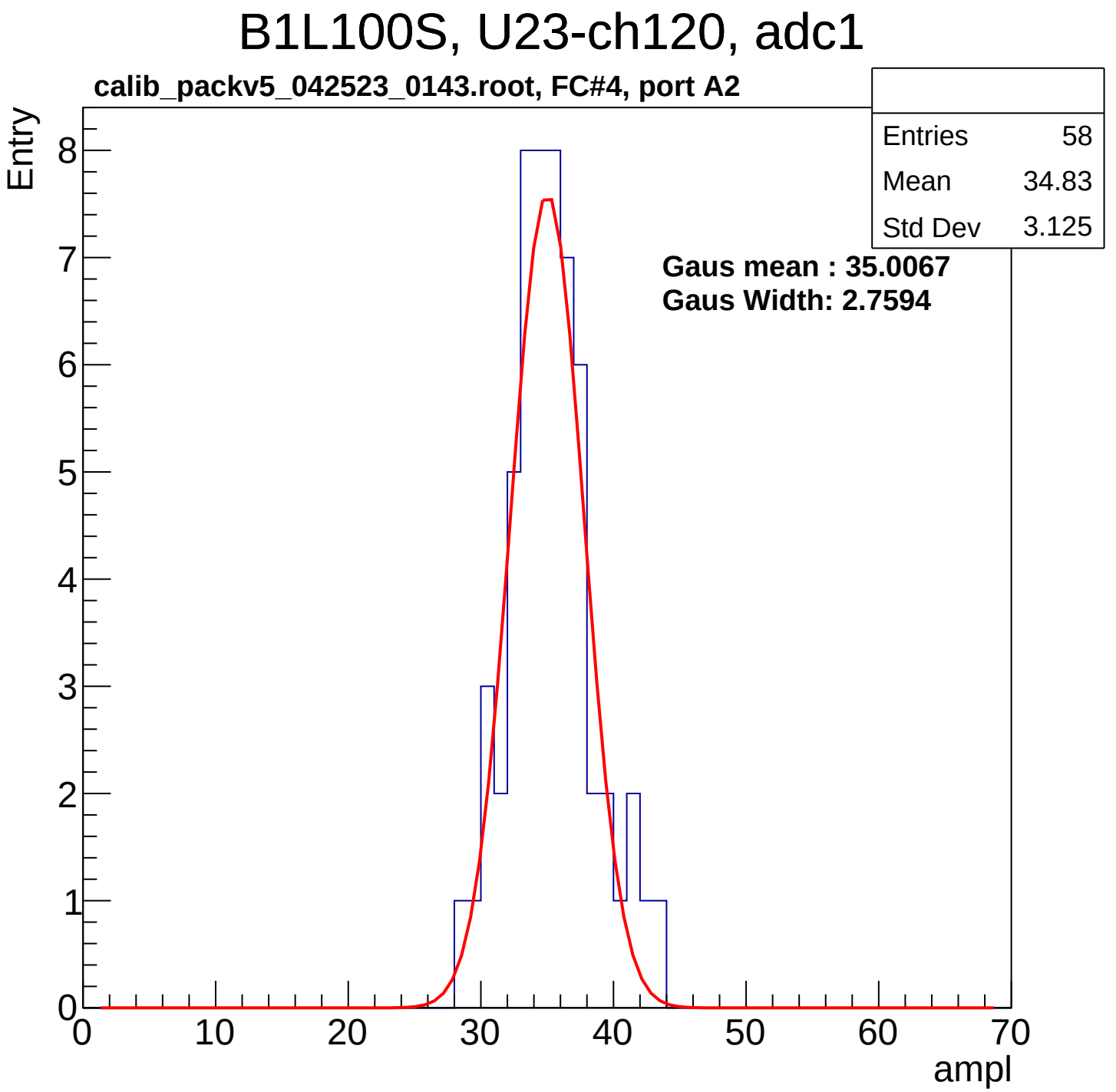
B1L100S, U23-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 34.83
Std Dev 3.125
Gaus mean : 35.0067
Gaus Width: 2.7594
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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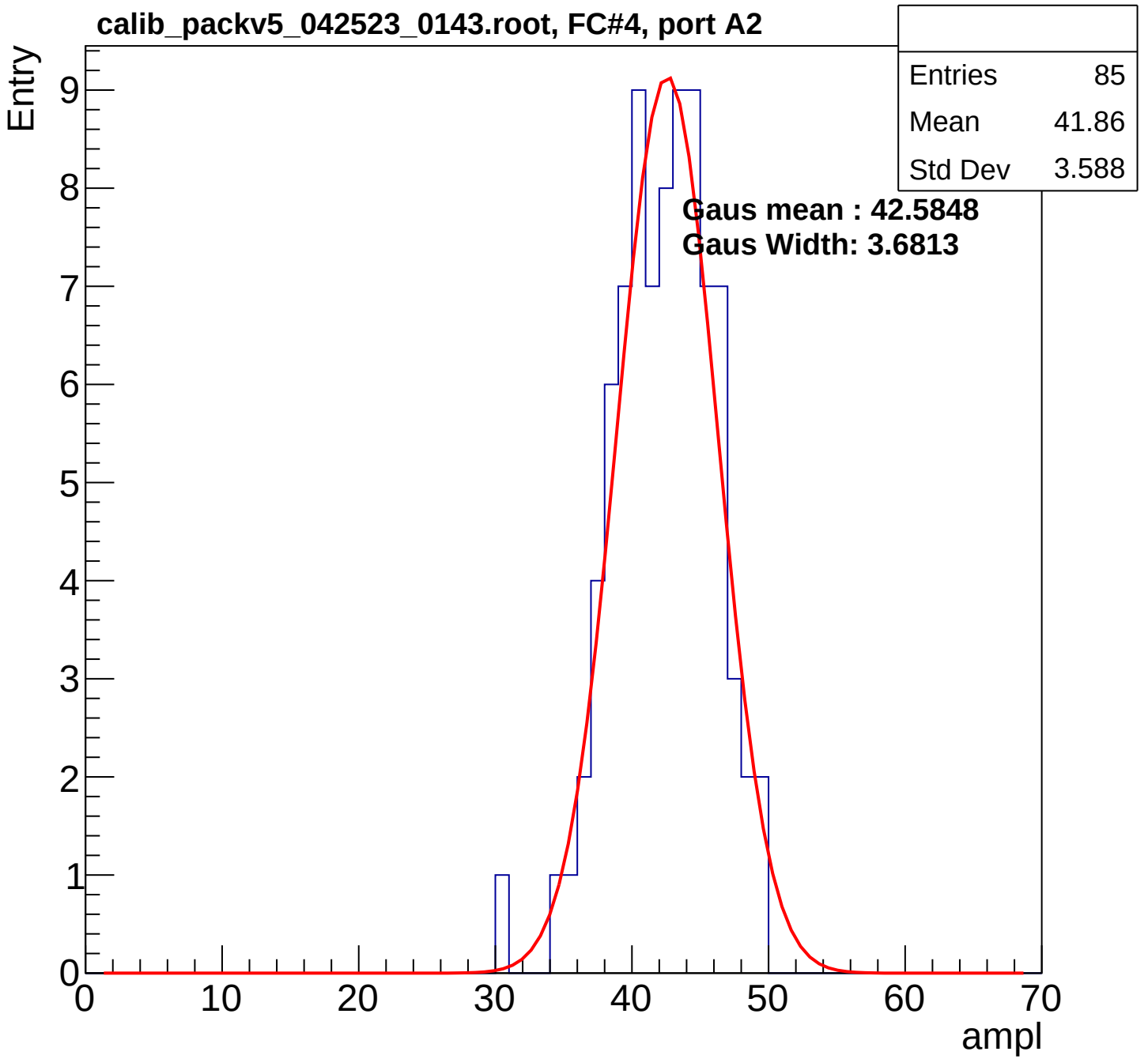
B1L100S, U23-ch120, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 41.86
Std Dev 3.588
Gaus mean : 42.5848
Gaus Width: 3.6813
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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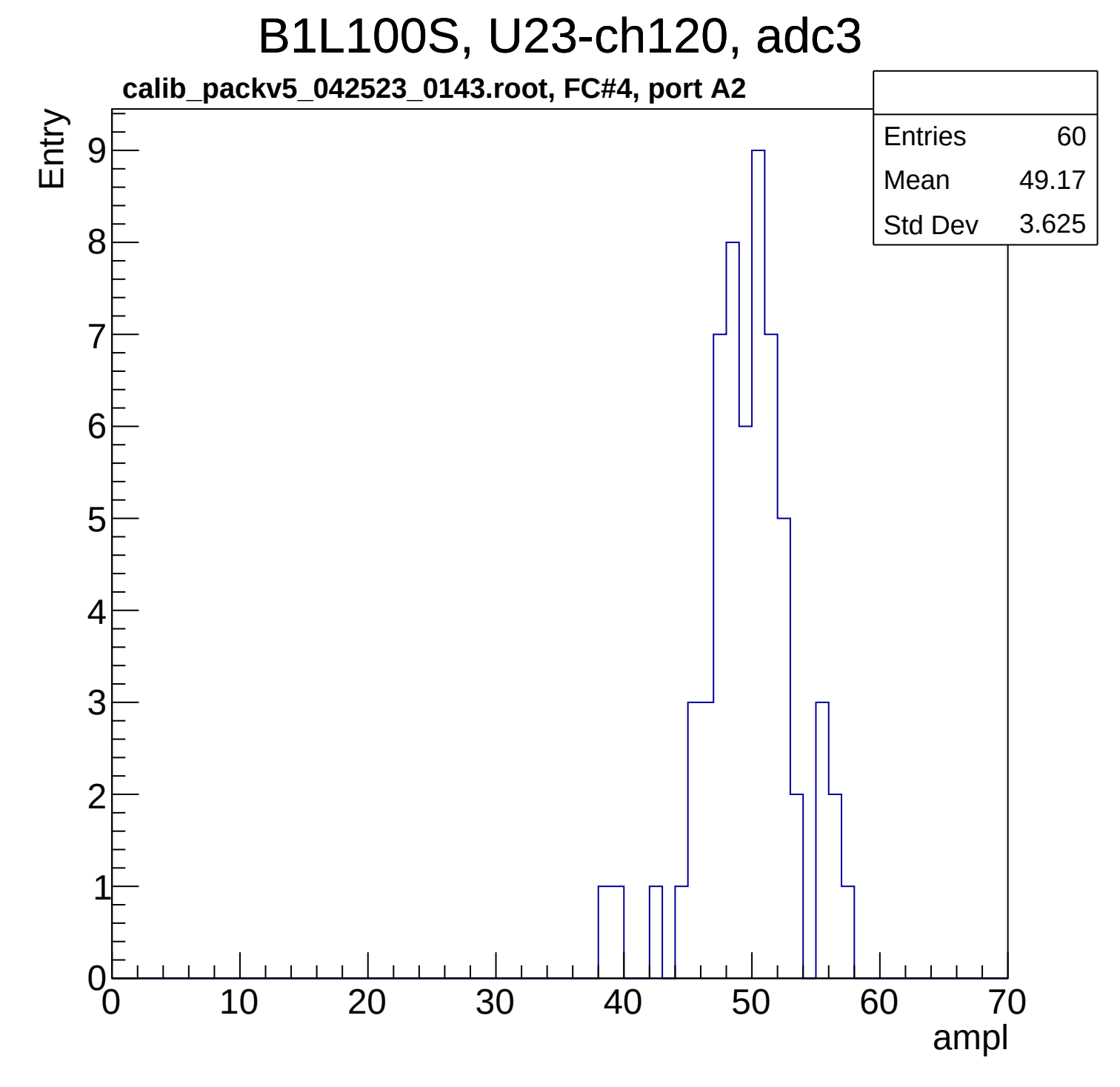
B1L100S, U23-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.17
Std Dev 3.625
Entry
ampl
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2
3
4
5
6
7
8
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60
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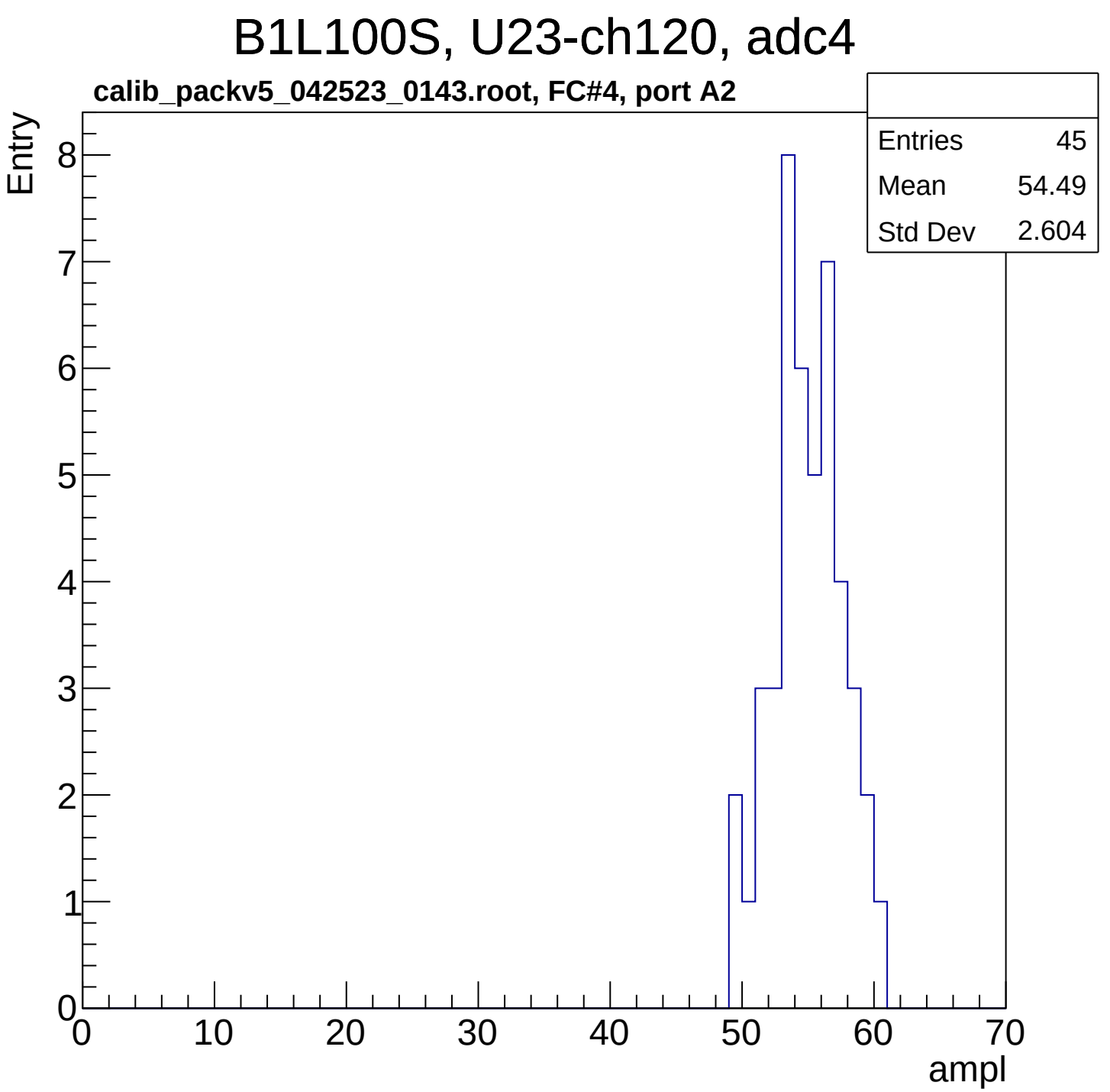
B1L100S, U23-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 54.49
Std Dev 2.604
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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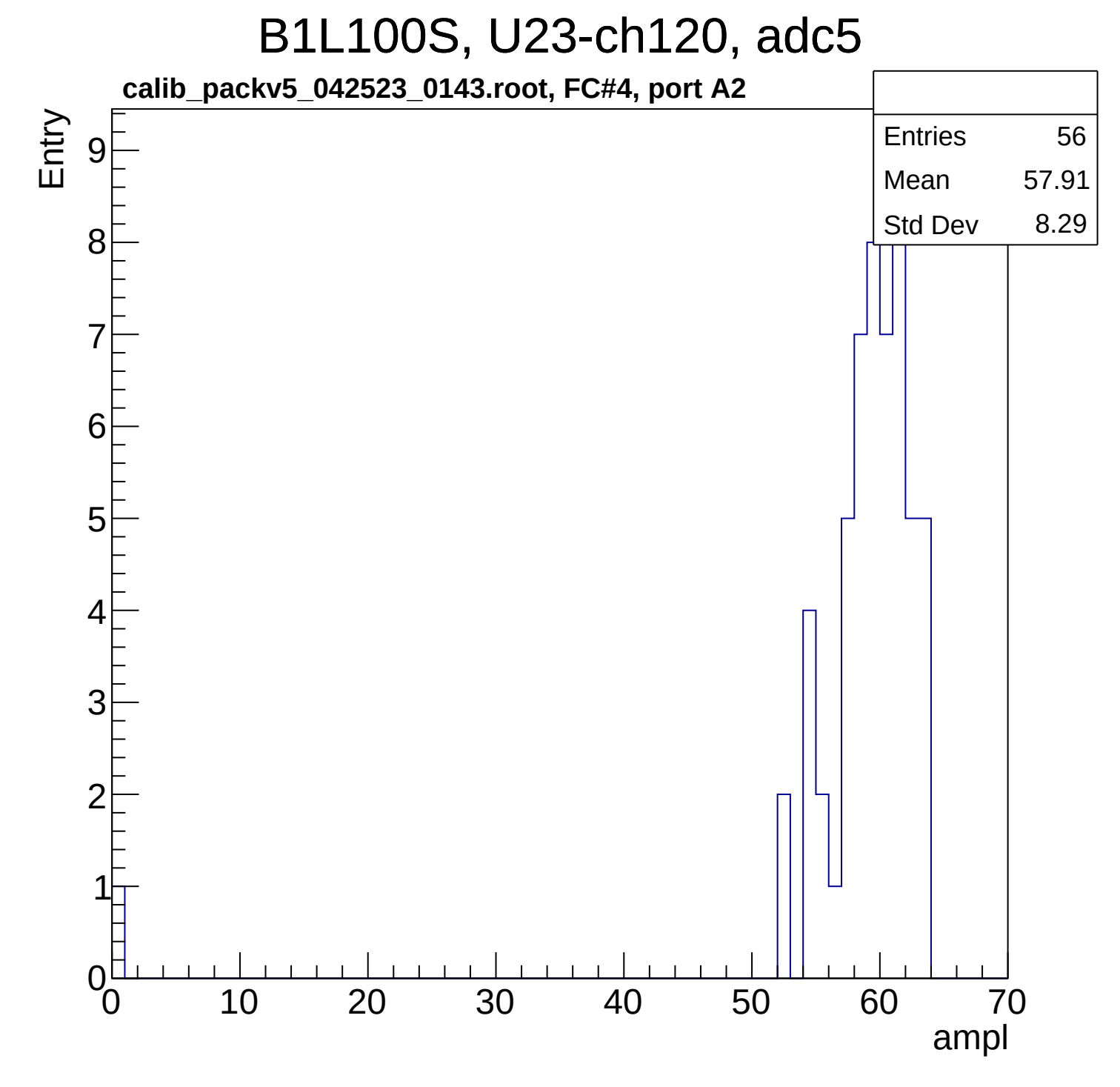
B1L100S, U23-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.91
Std Dev 8.29
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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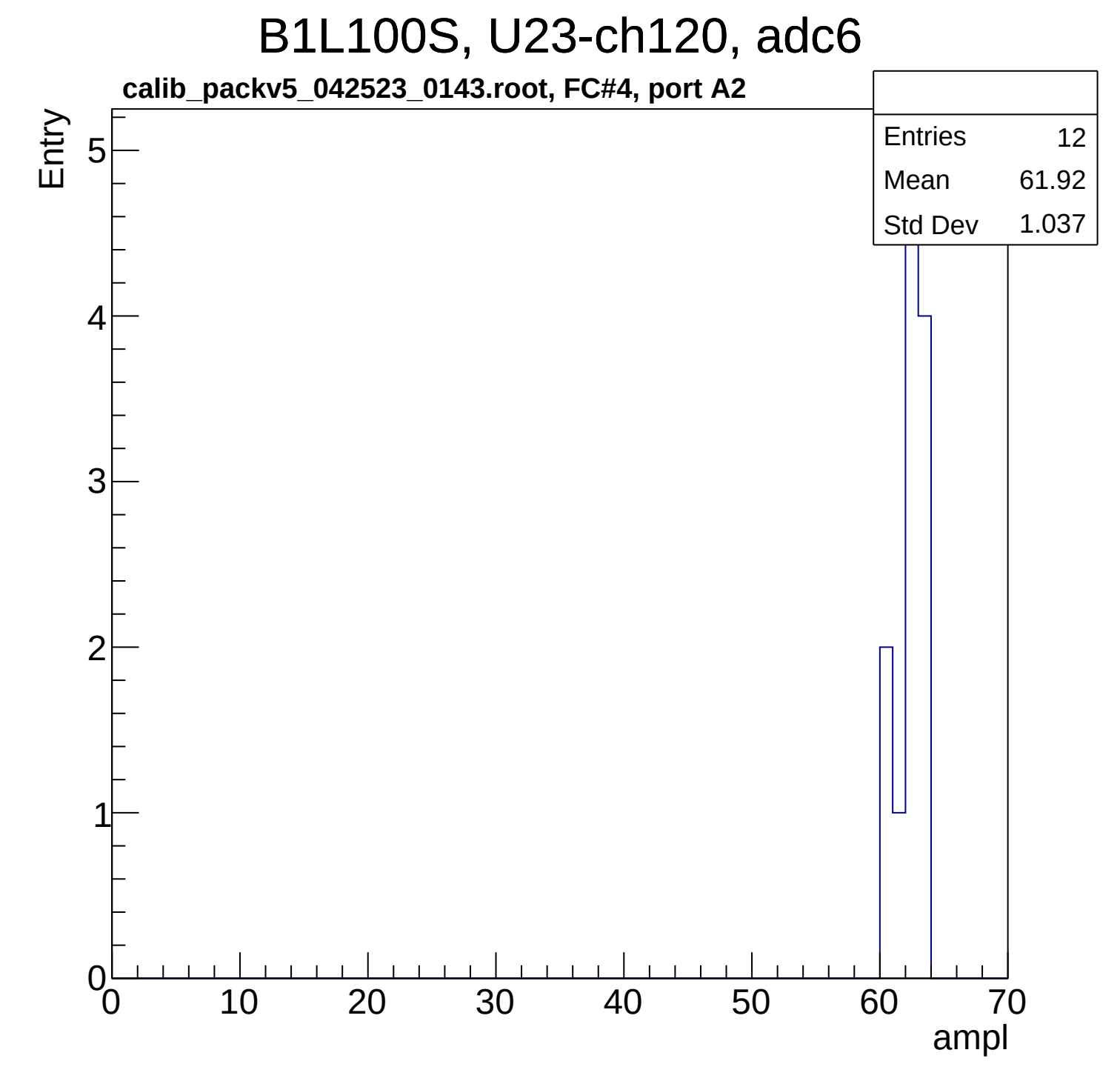
B1L100S, U23-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 61.92
Std Dev 1.037
Entry
ampl
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2
3
4
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0
10
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40
50
60
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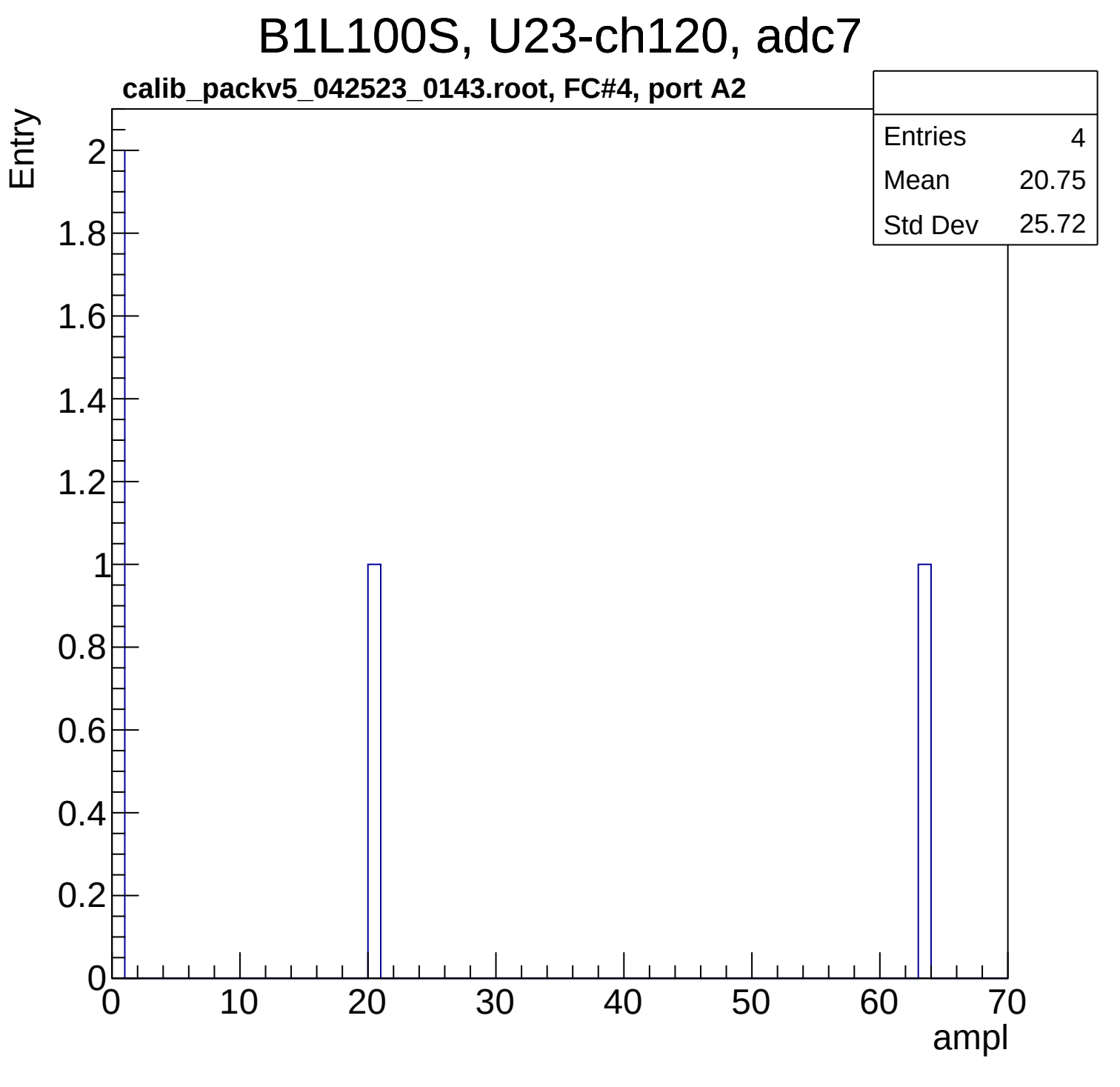
B1L100S, U23-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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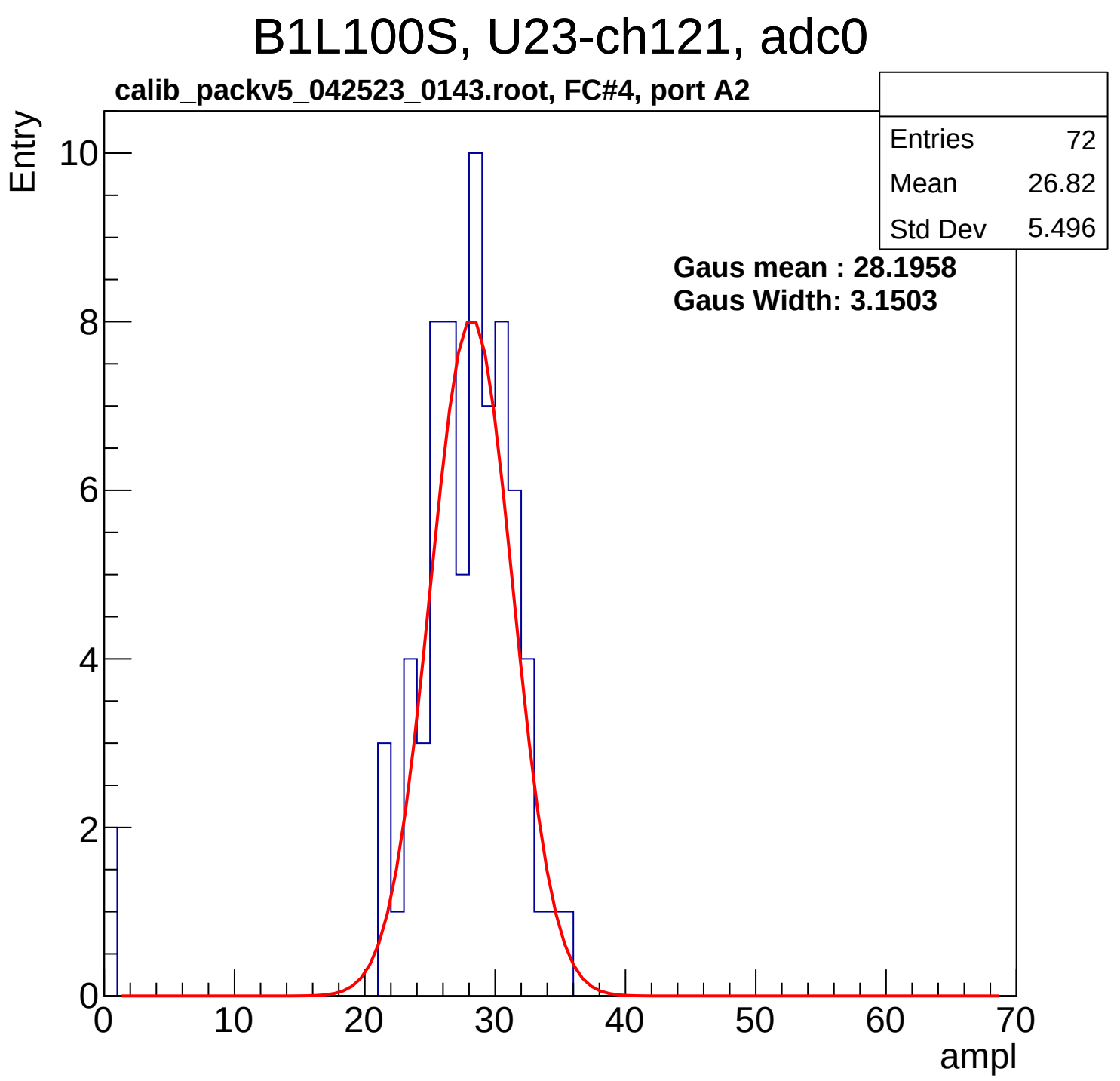
B1L100S, U23-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 26.82
Std Dev 5.496
Gaus mean : 28.1958
Gaus Width: 3.1503
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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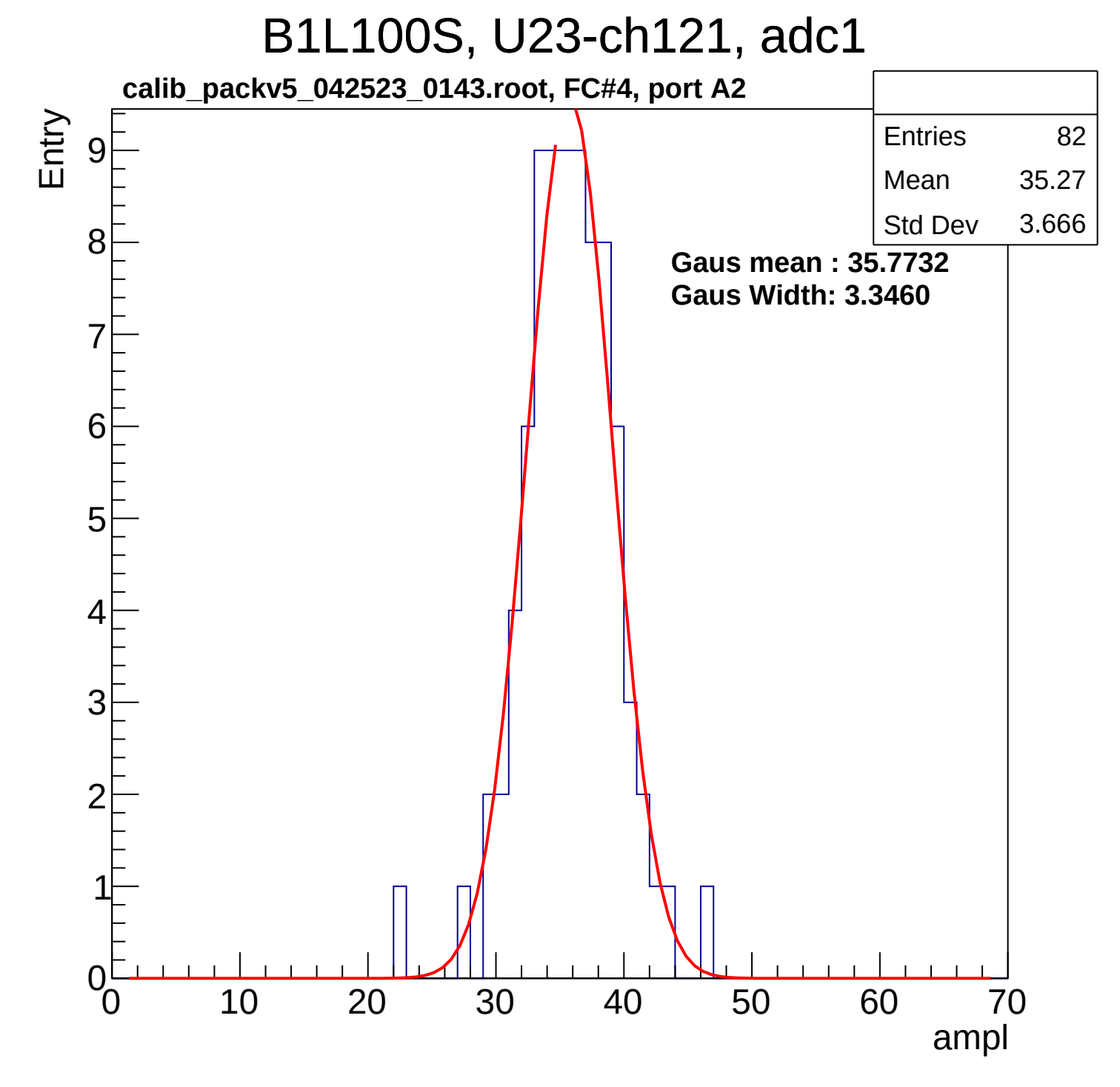
B1L100S, U23-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 35.27
Std Dev 3.666
Gaus mean : 35.7732
Gaus Width: 3.3460
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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40
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60
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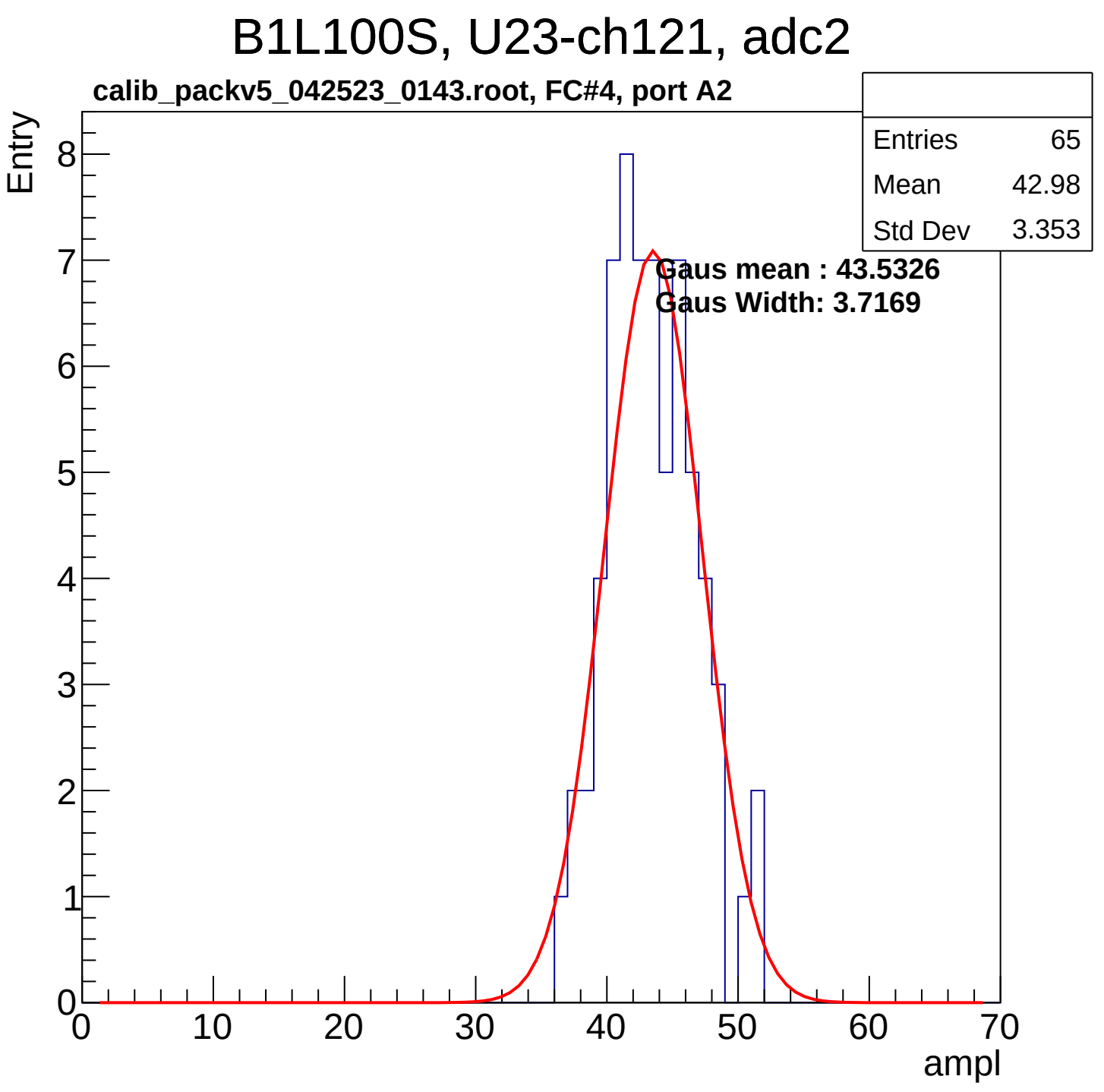
B1L100S, U23-ch121, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.98
Std Dev 3.353
Gaus mean : 43.5326
Gaus Width: 3.7169
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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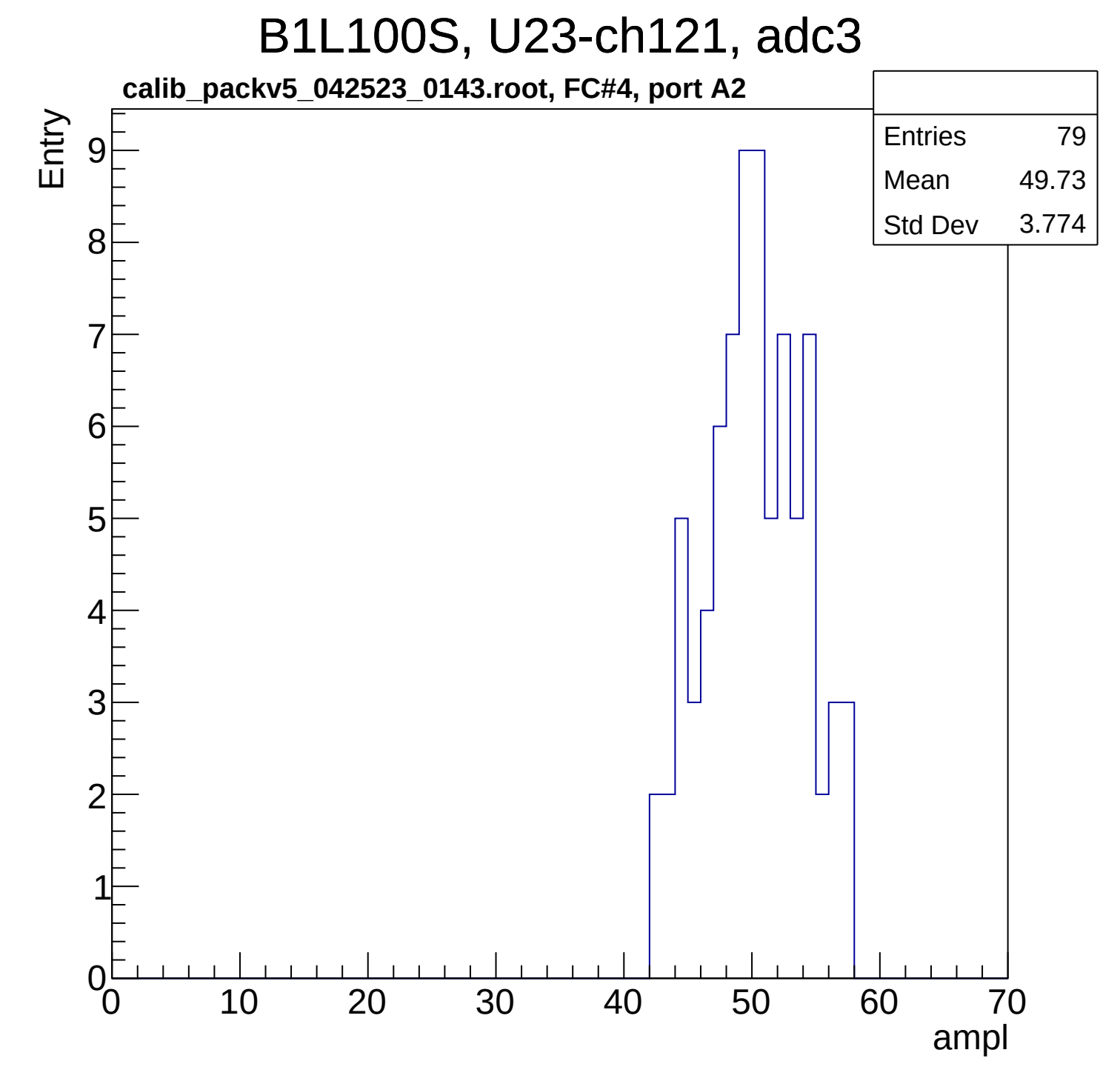
B1L100S, U23-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 49.73
Std Dev 3.774
Entry
ampl
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2
3
4
5
6
7
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9
0
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60
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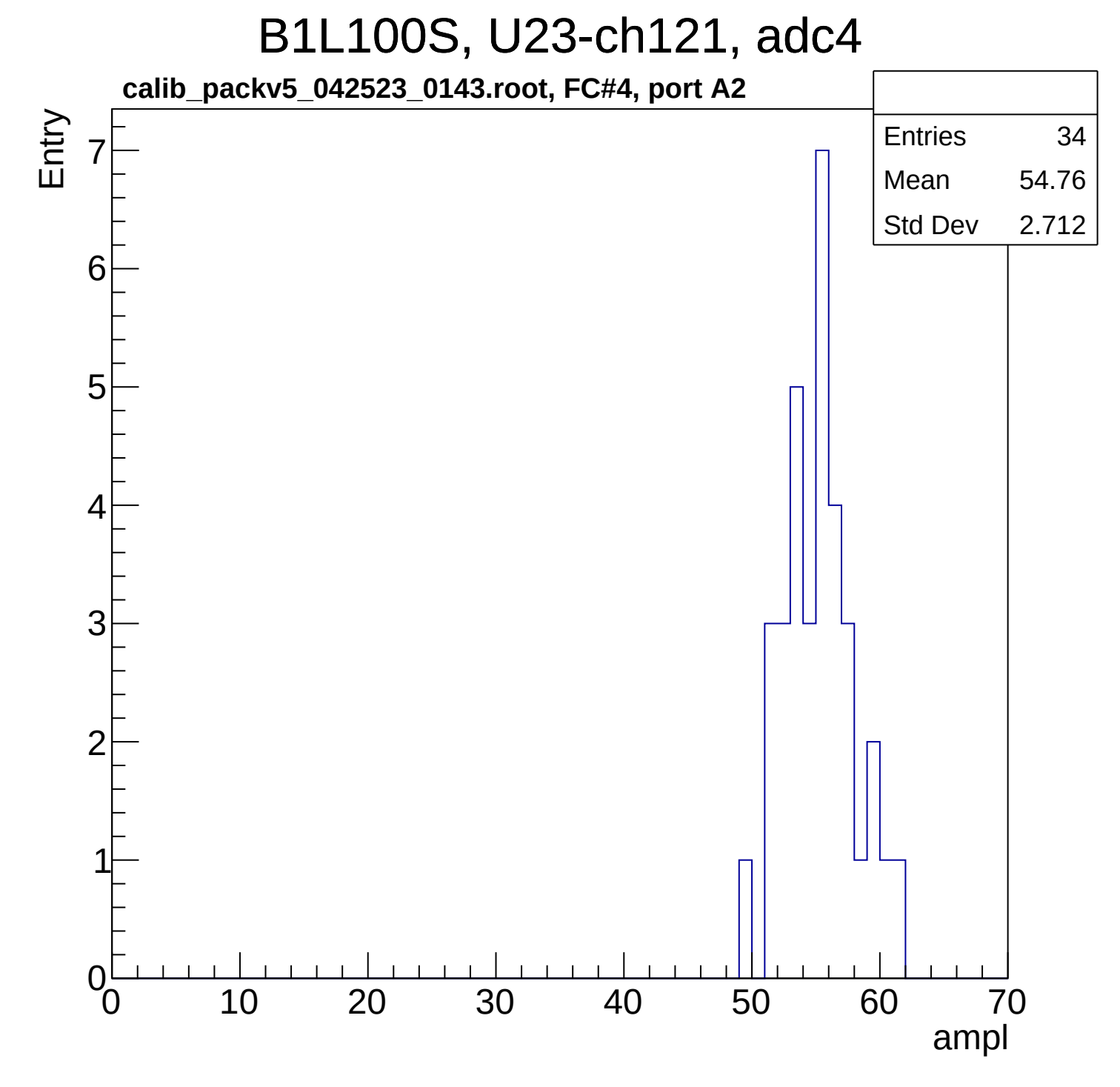
B1L100S, U23-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 54.76
Std Dev 2.712
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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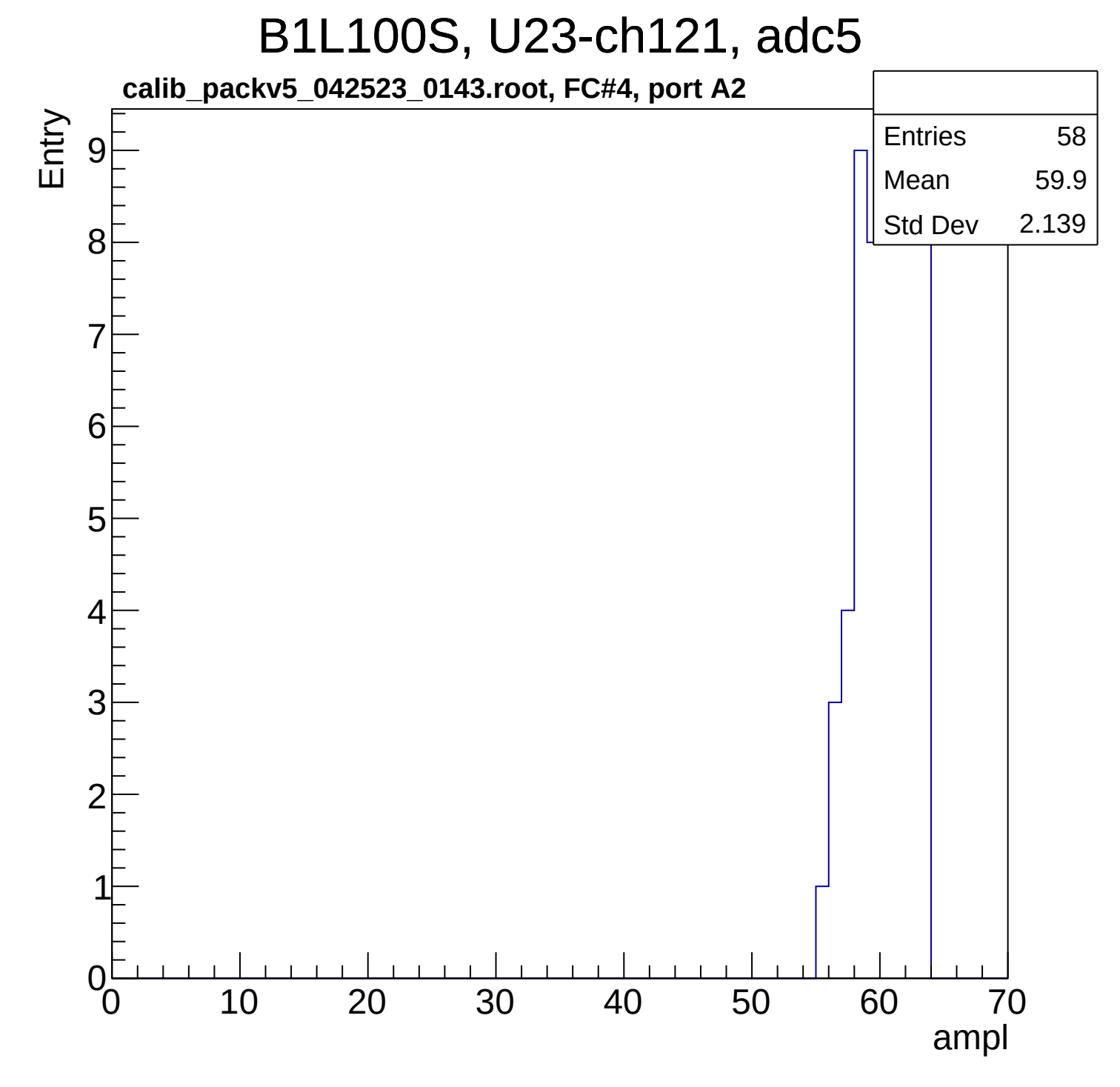
B1L100S, U23-ch121, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 59.9
Std Dev 2.139
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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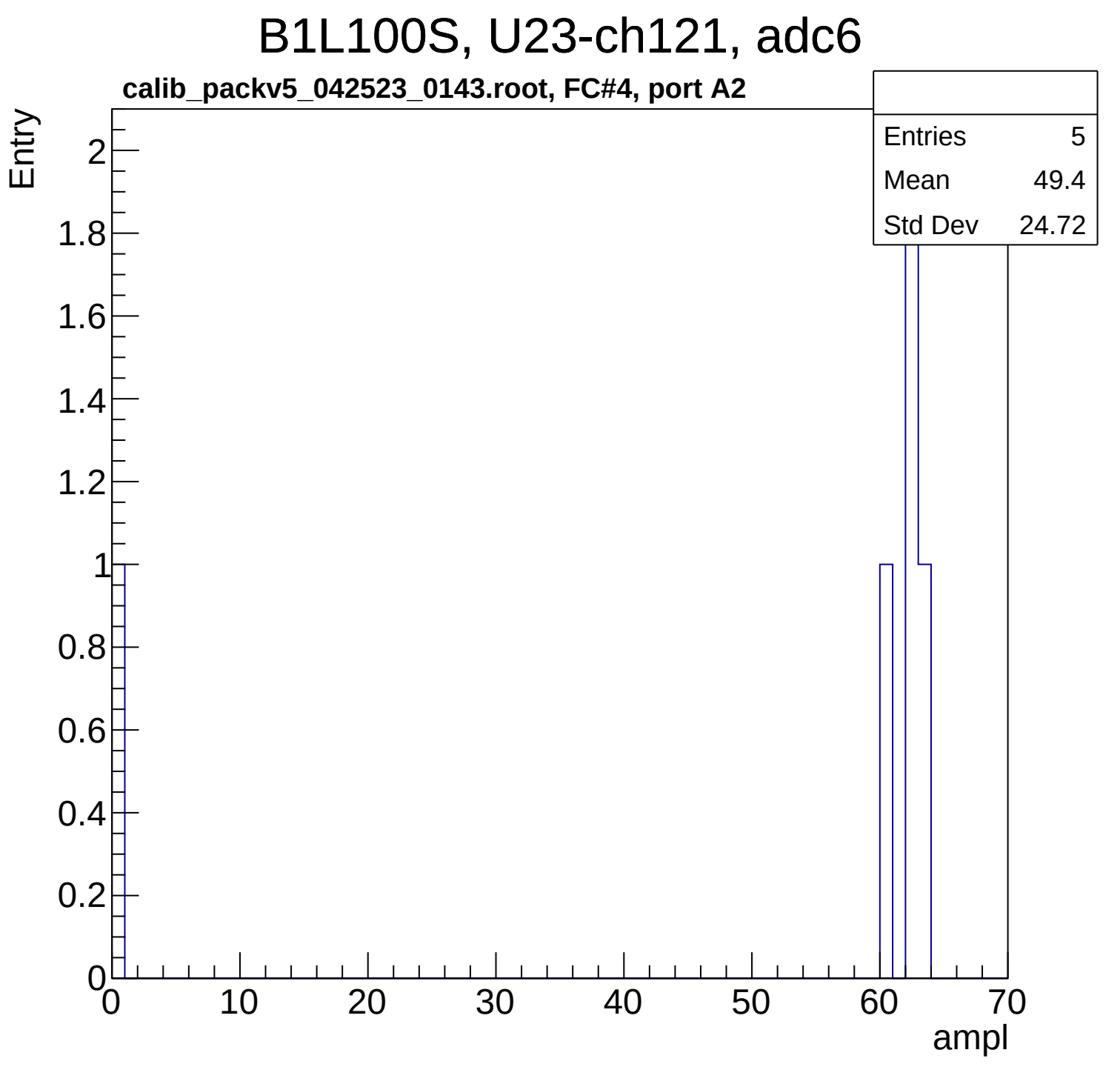
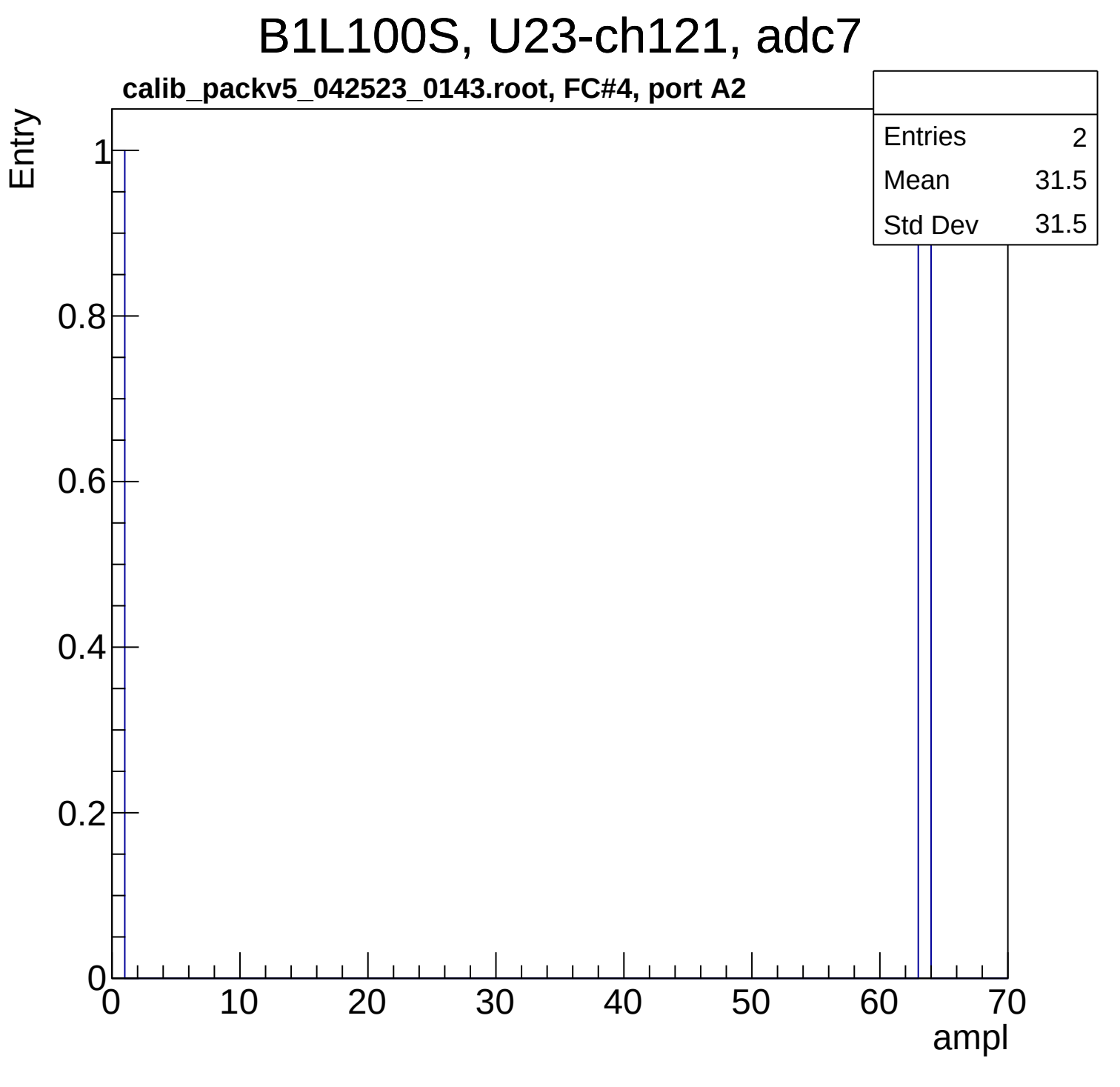

B1L100S, U23-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.4
Std Dev 24.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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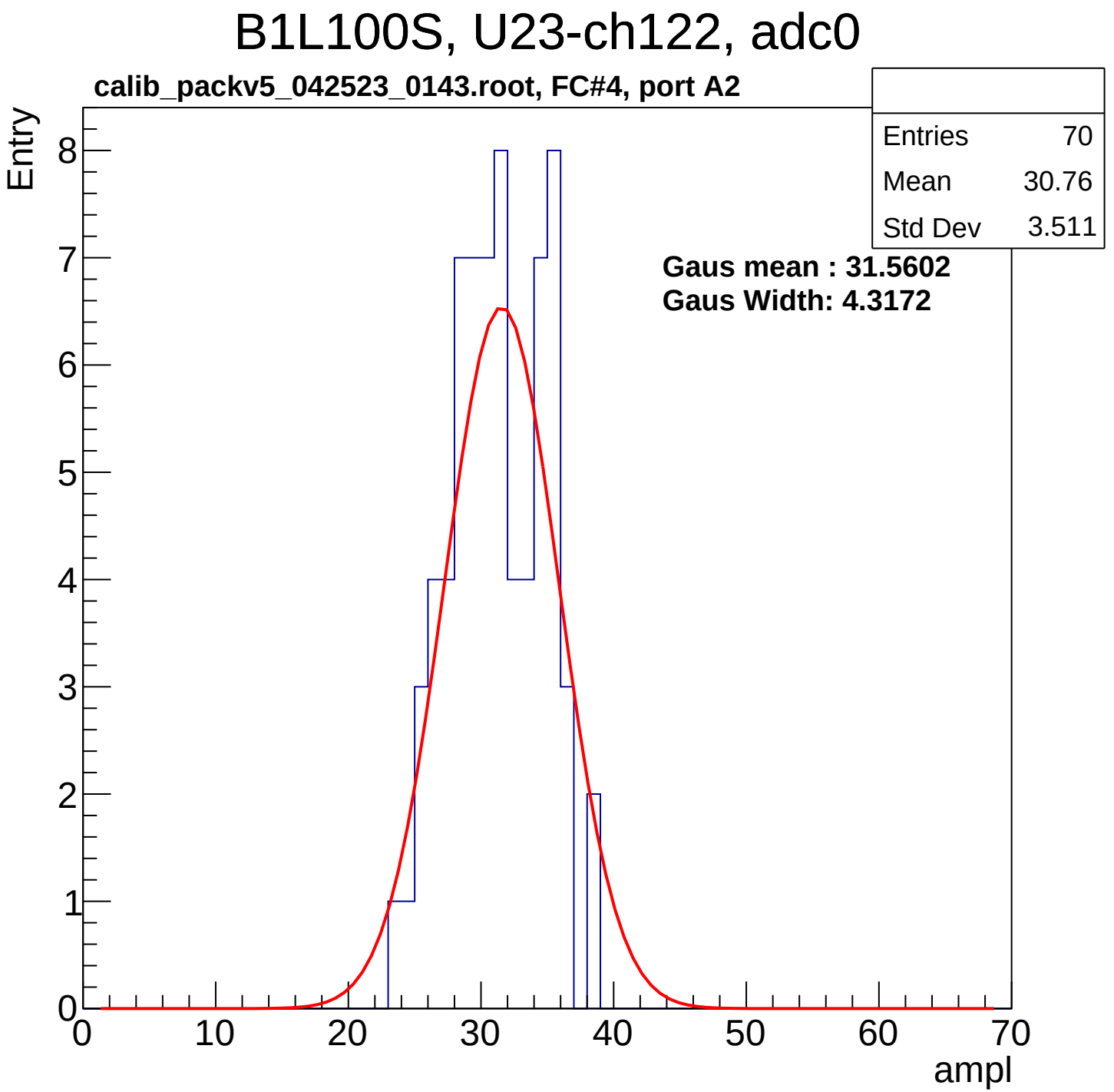
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 30.76
Std Dev 3.511
Gaus mean : 31.5602
Gaus Width: 4.3172
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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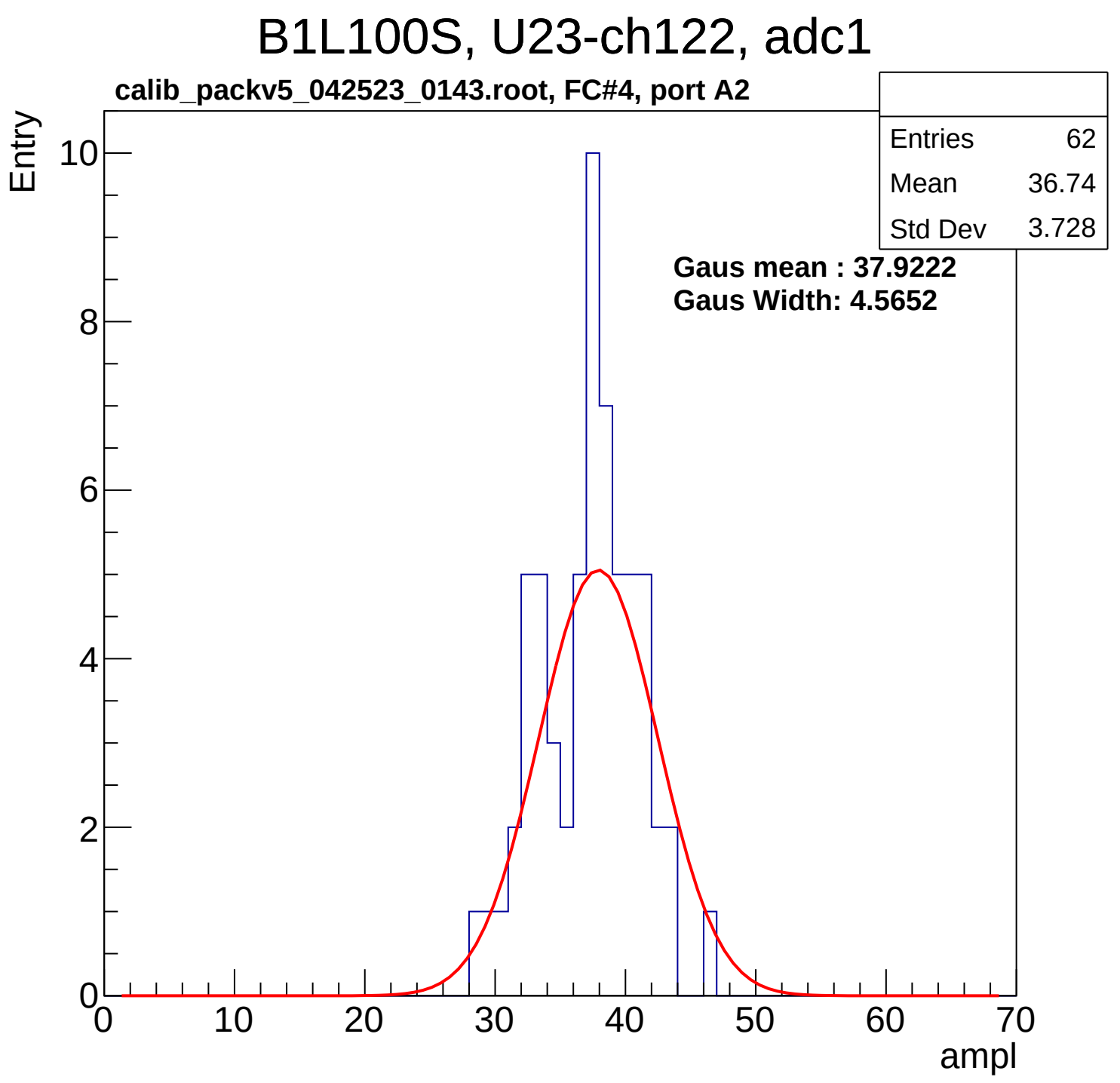
B1L100S, U23-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.74
Std Dev 3.728
Gaus mean : 37.9222
Gaus Width: 4.5652
Entry
ampl
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4
6
8
10
0
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20
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40
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60
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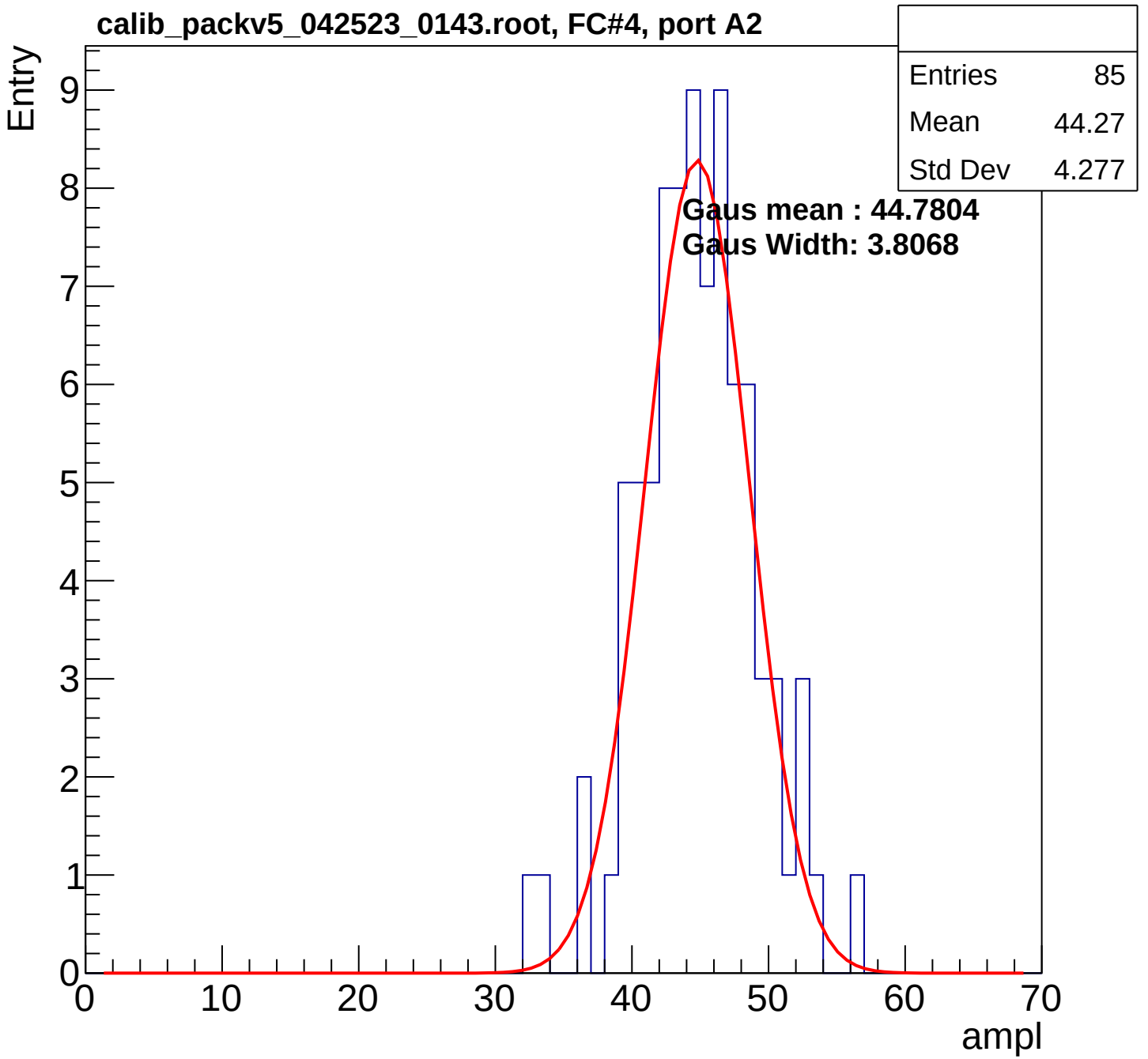
B1L100S, U23-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 44.27
Std Dev 4.277
Gaus mean : 44.7804
Gaus Width: 3.8068
Entry
ampl
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3
4
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6
7
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0
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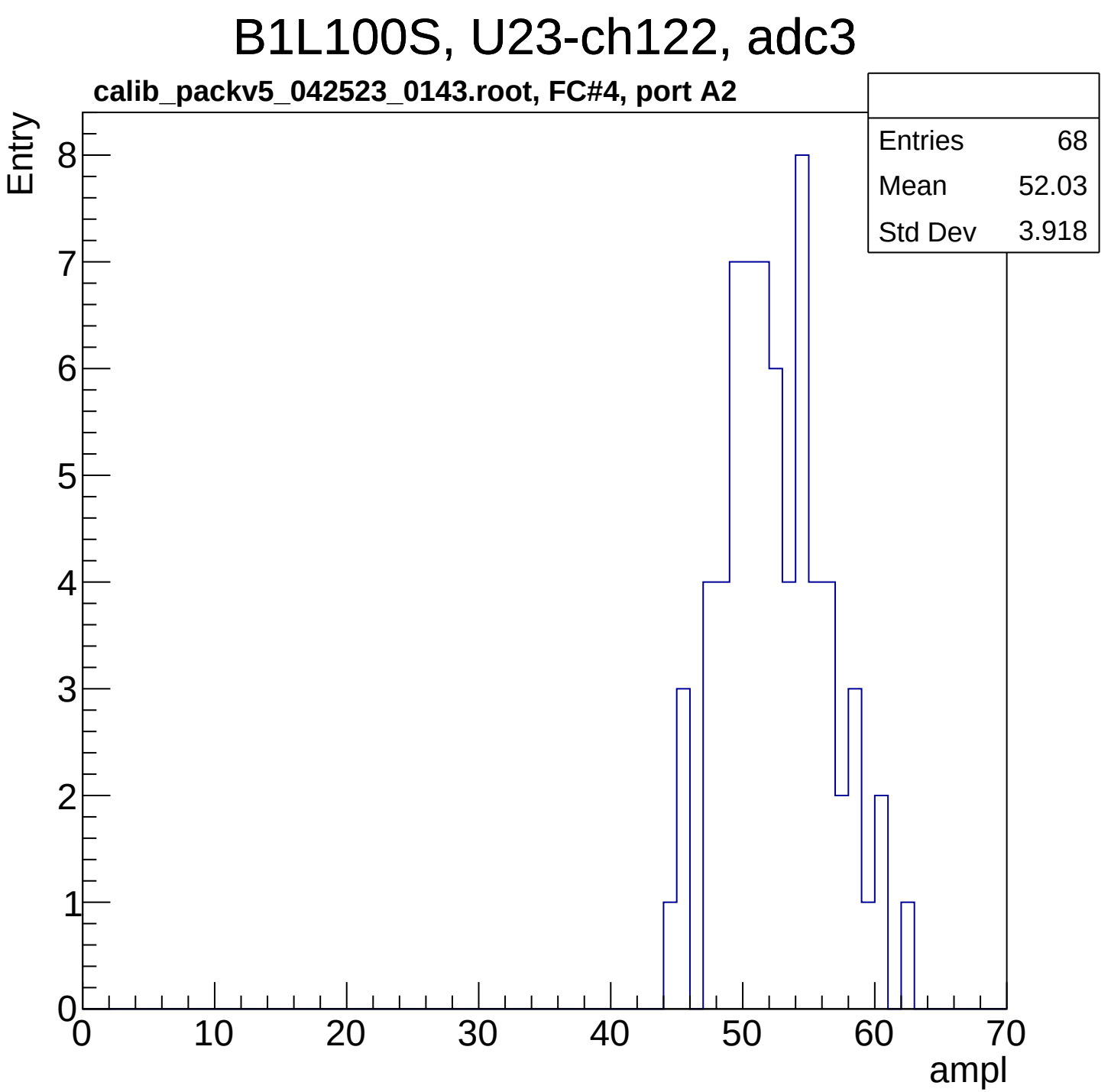
B1L100S, U23-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 52.03
Std Dev 3.918
Entry
ampl
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5
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0
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20
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60
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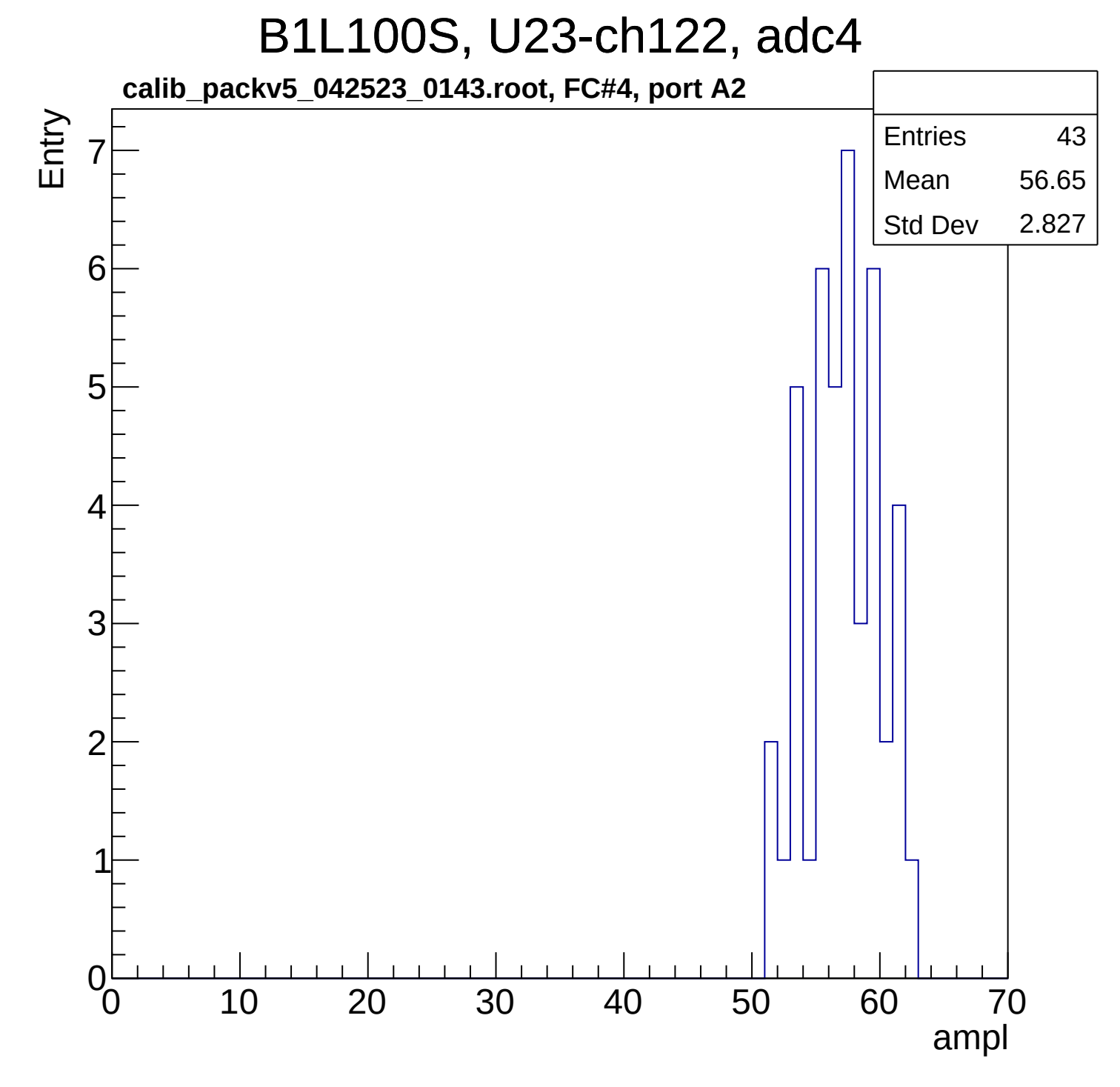
B1L100S, U23-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 56.65
Std Dev 2.827
Entry
ampl
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20
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40
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60
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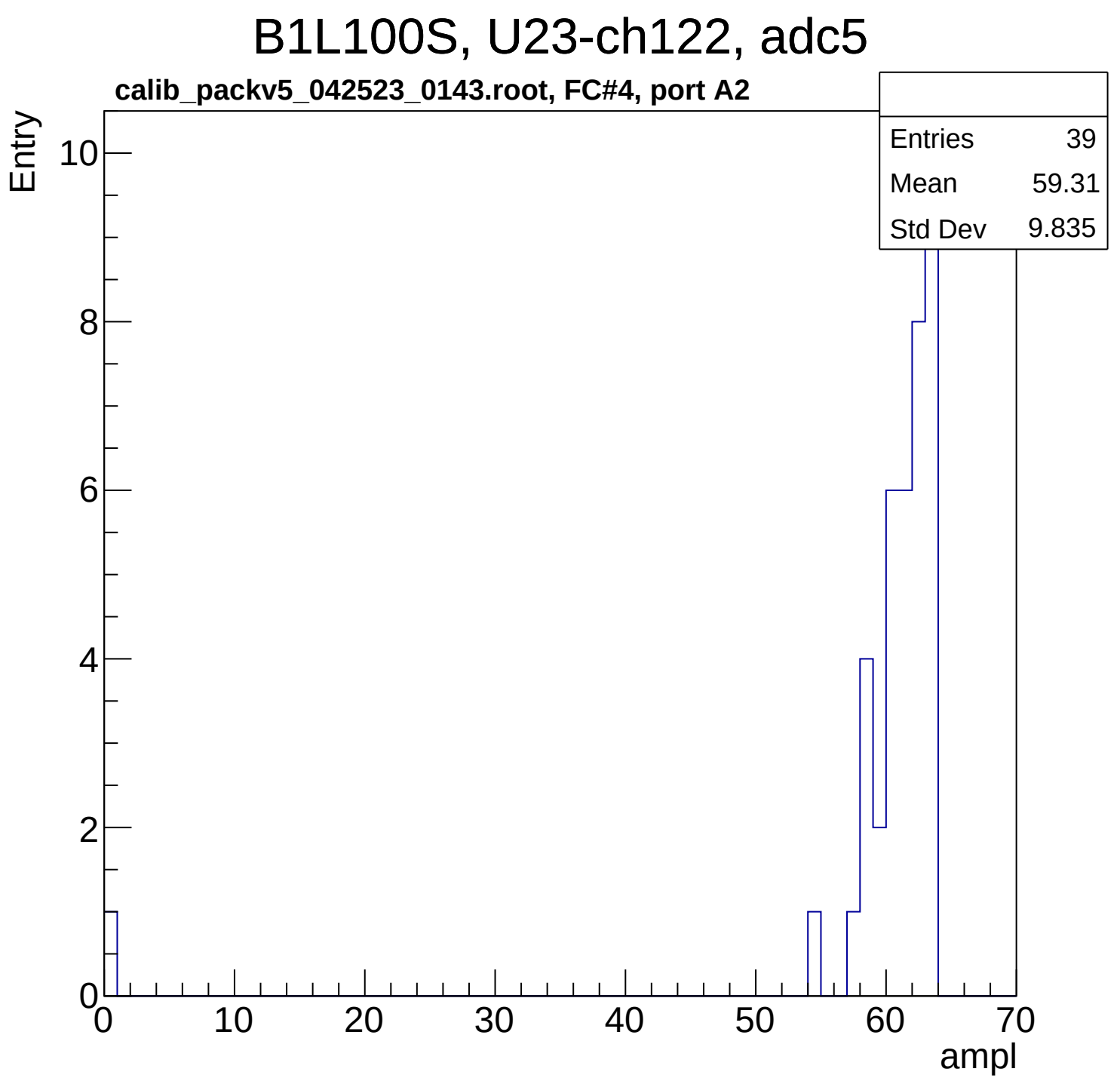
B1L100S, U23-ch122, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.31
Std Dev 9.835
Entry
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6
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10
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10
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30
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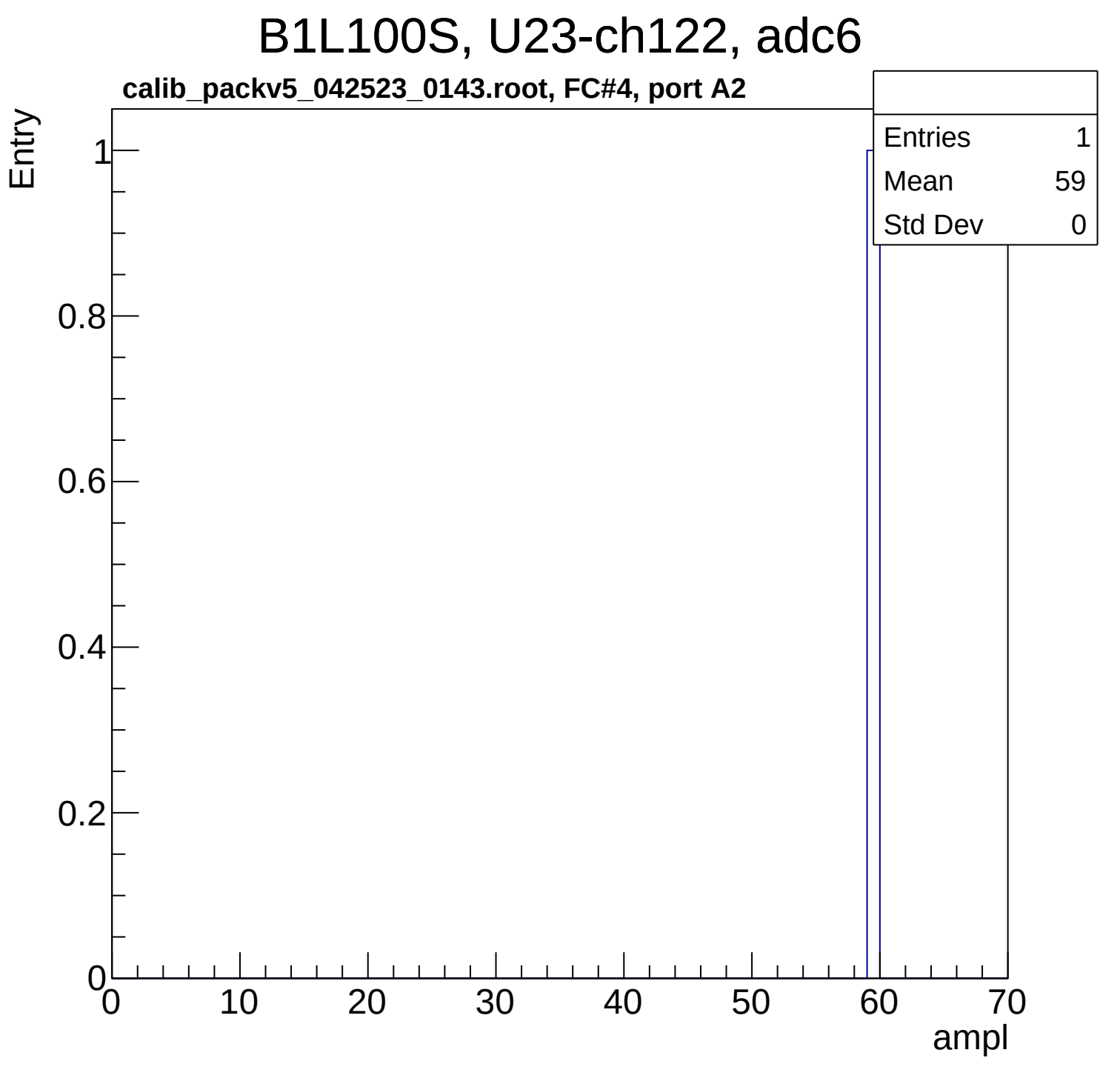
50
60
70
ampl


B1L100S, U23-ch122, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 59
Std Dev 0
Entry
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0.6
0.4
0.2
0
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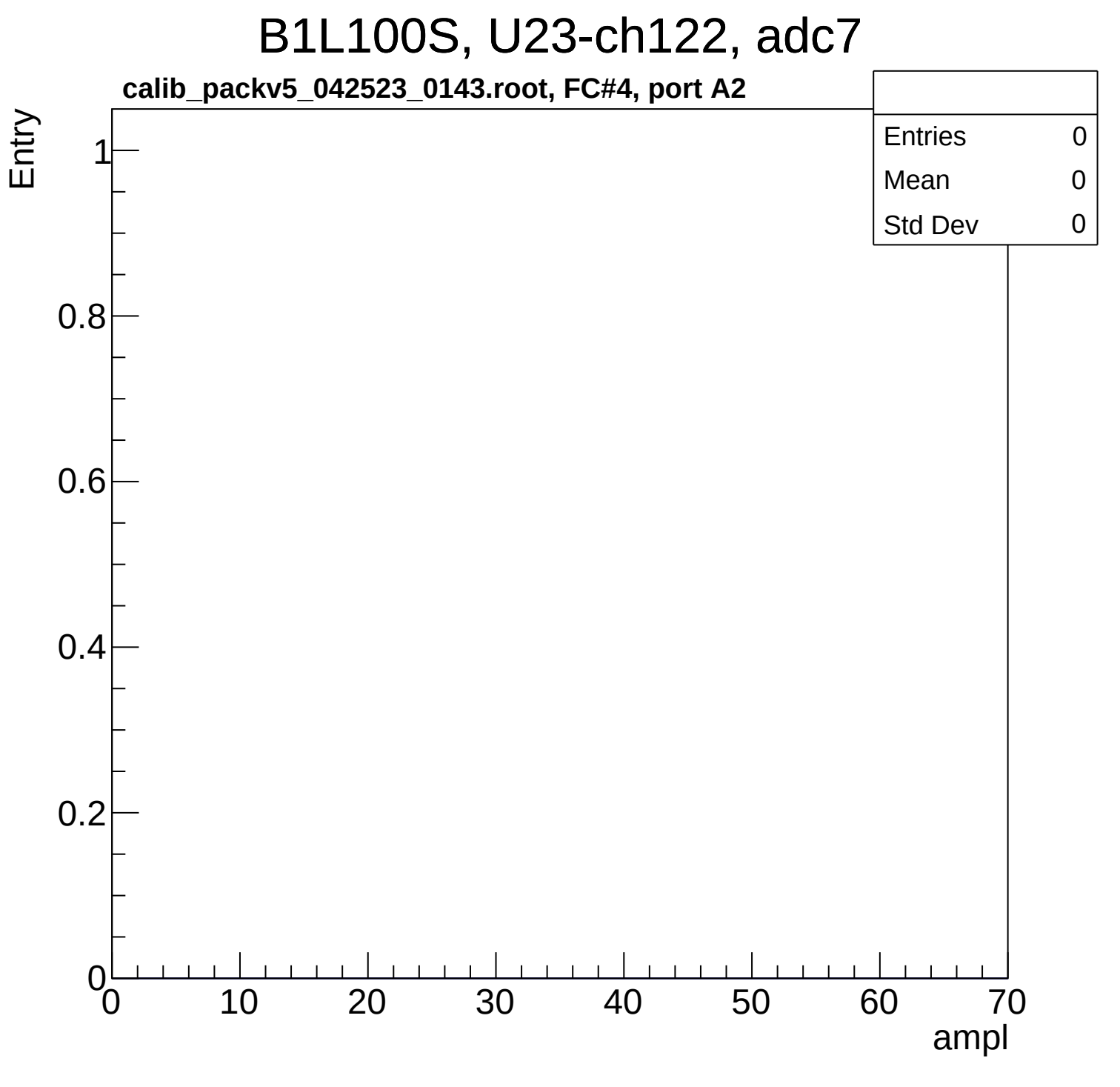
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch122, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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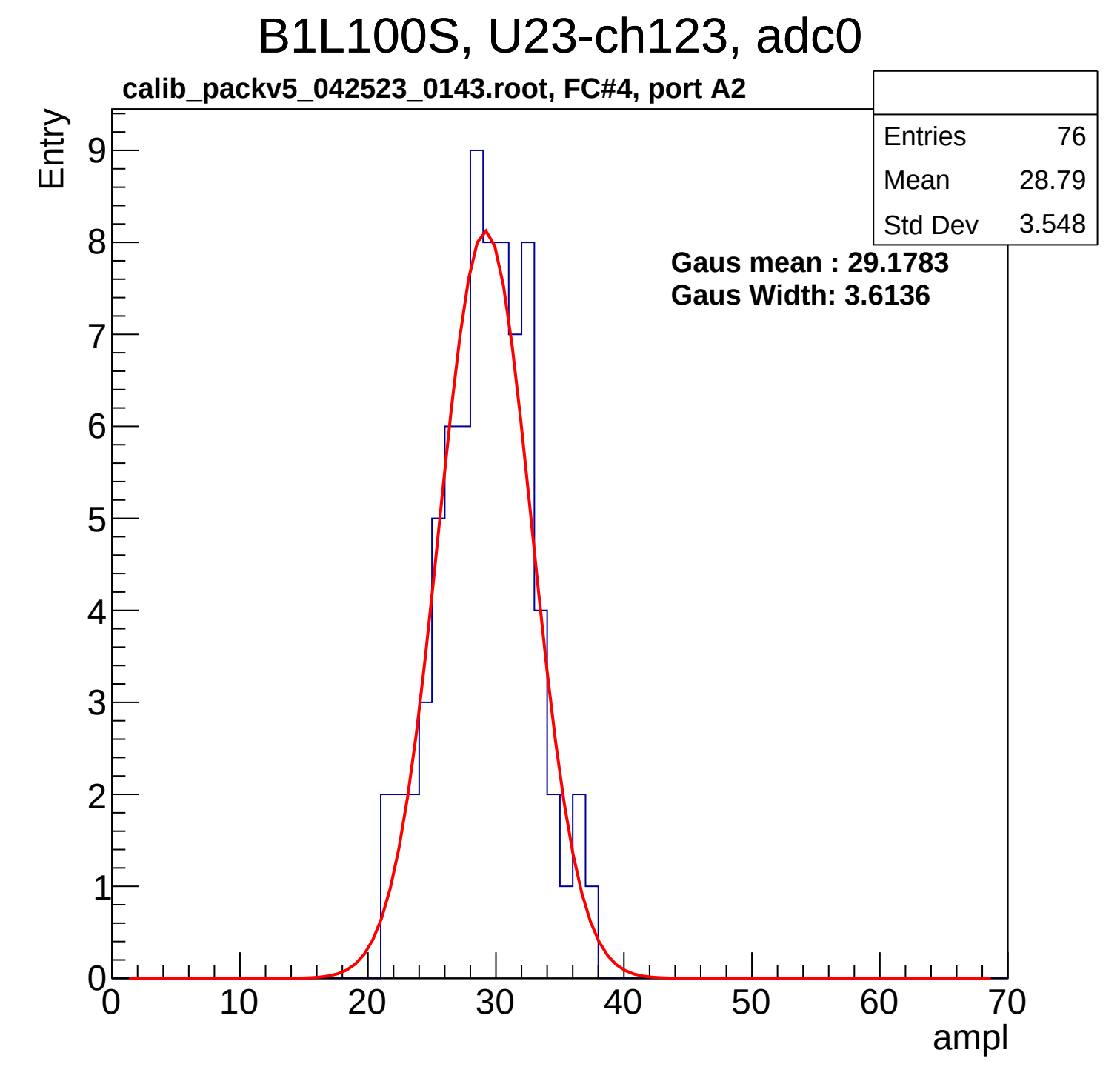
70
ampl


B1L100S, U23-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 28.79
Std Dev 3.548
Gaus mean : 29.1783
Gaus Width: 3.6136
Entry
ampl
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6
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0
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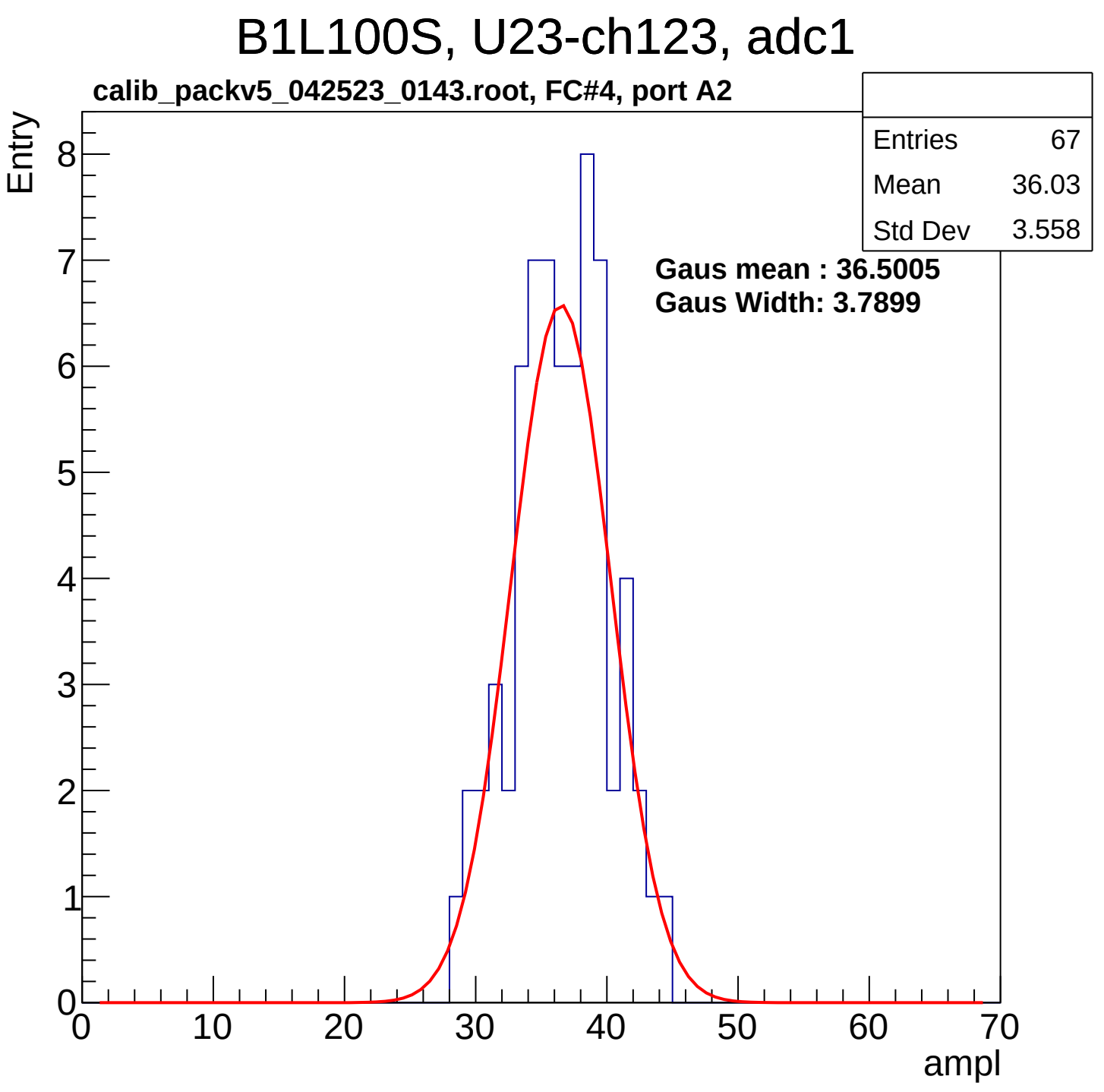
B1L100S, U23-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.03
Std Dev 3.558
Gaus mean : 36.5005
Gaus Width: 3.7899
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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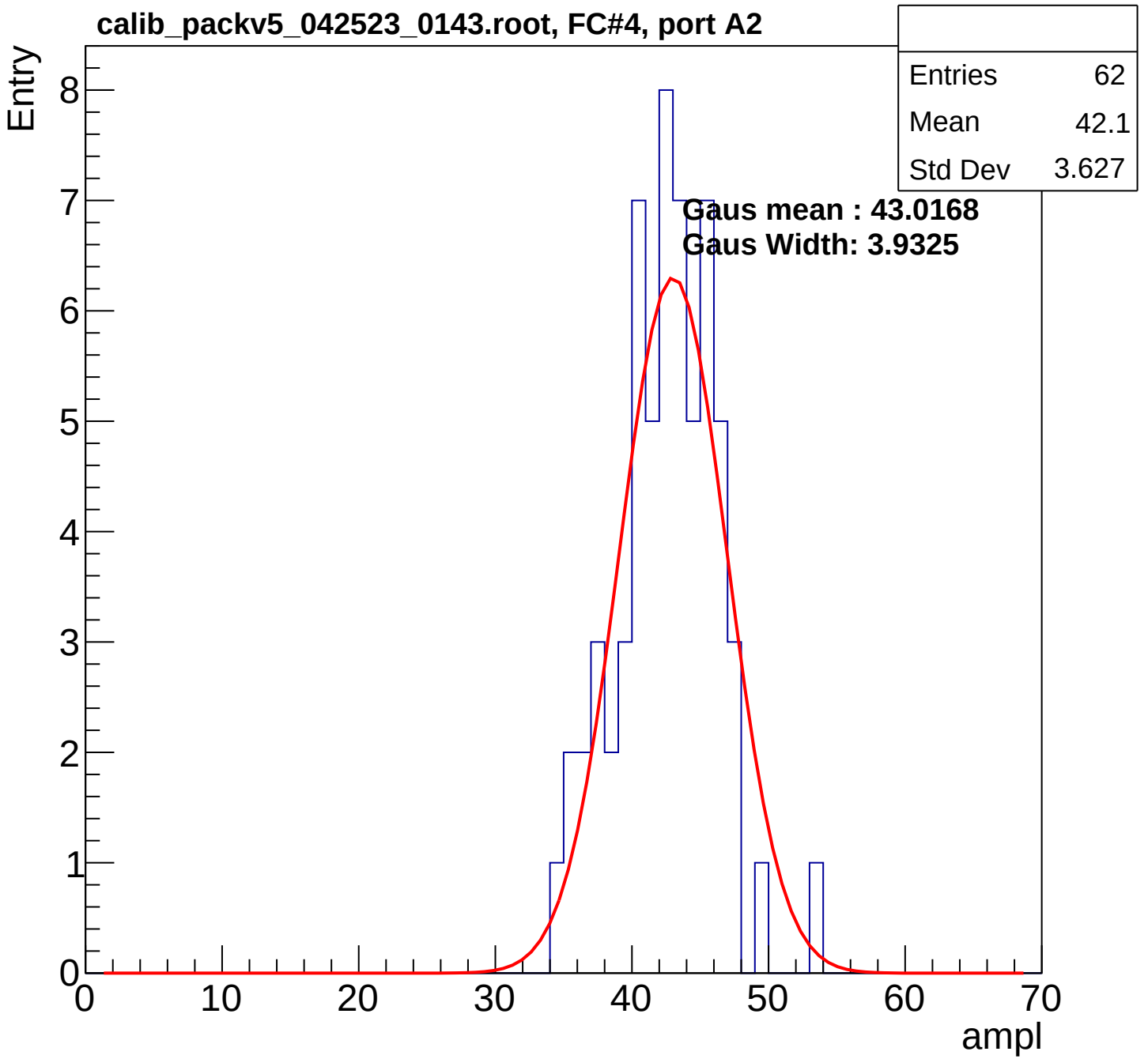
B1L100S, U23-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.1
Std Dev 3.627
Gaus mean : 43.0168
Gaus Width: 3.9325
Entry
ampl
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4
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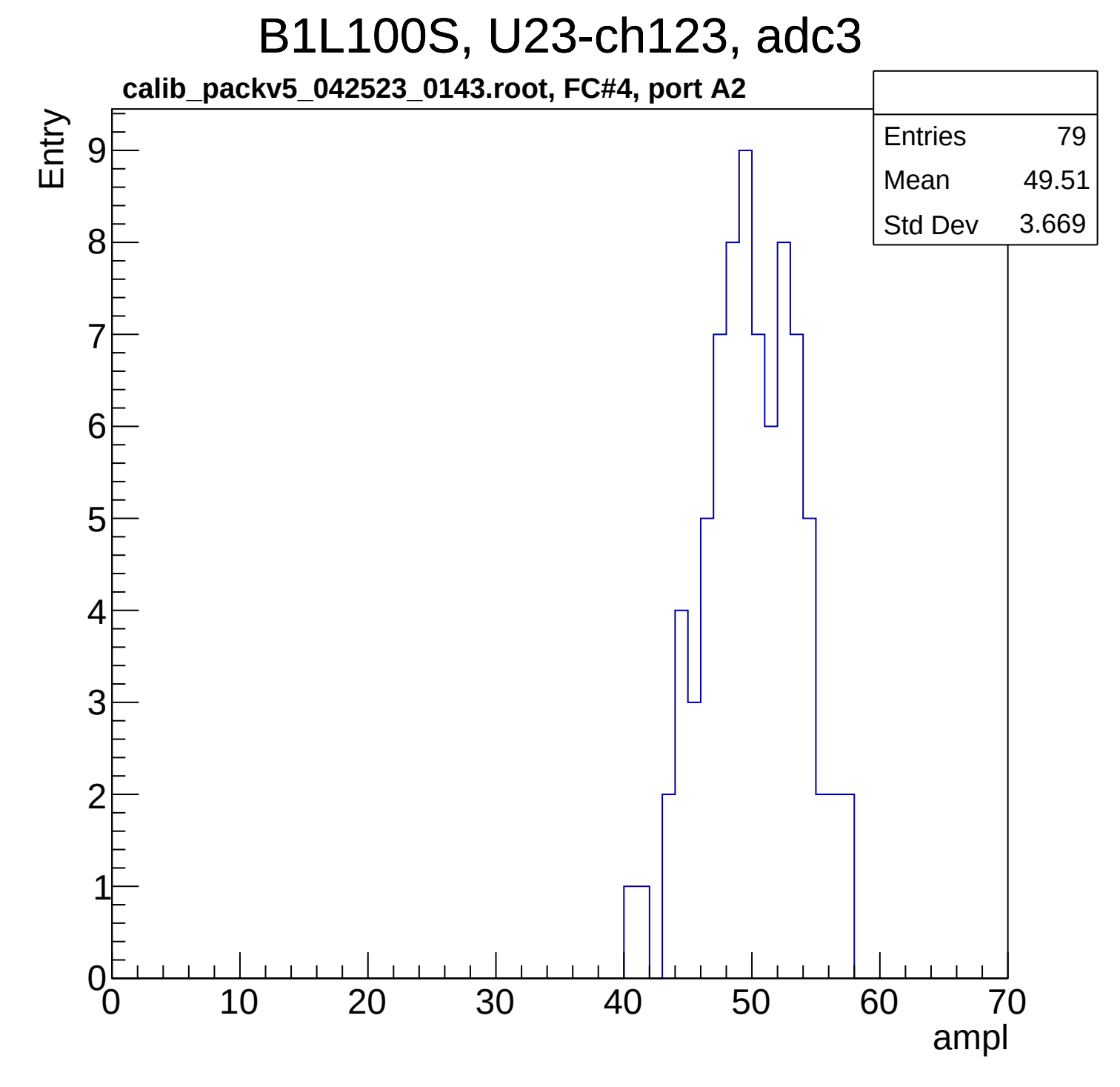
B1L100S, U23-ch123, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 49.51
Std Dev 3.669
Entry
ampl
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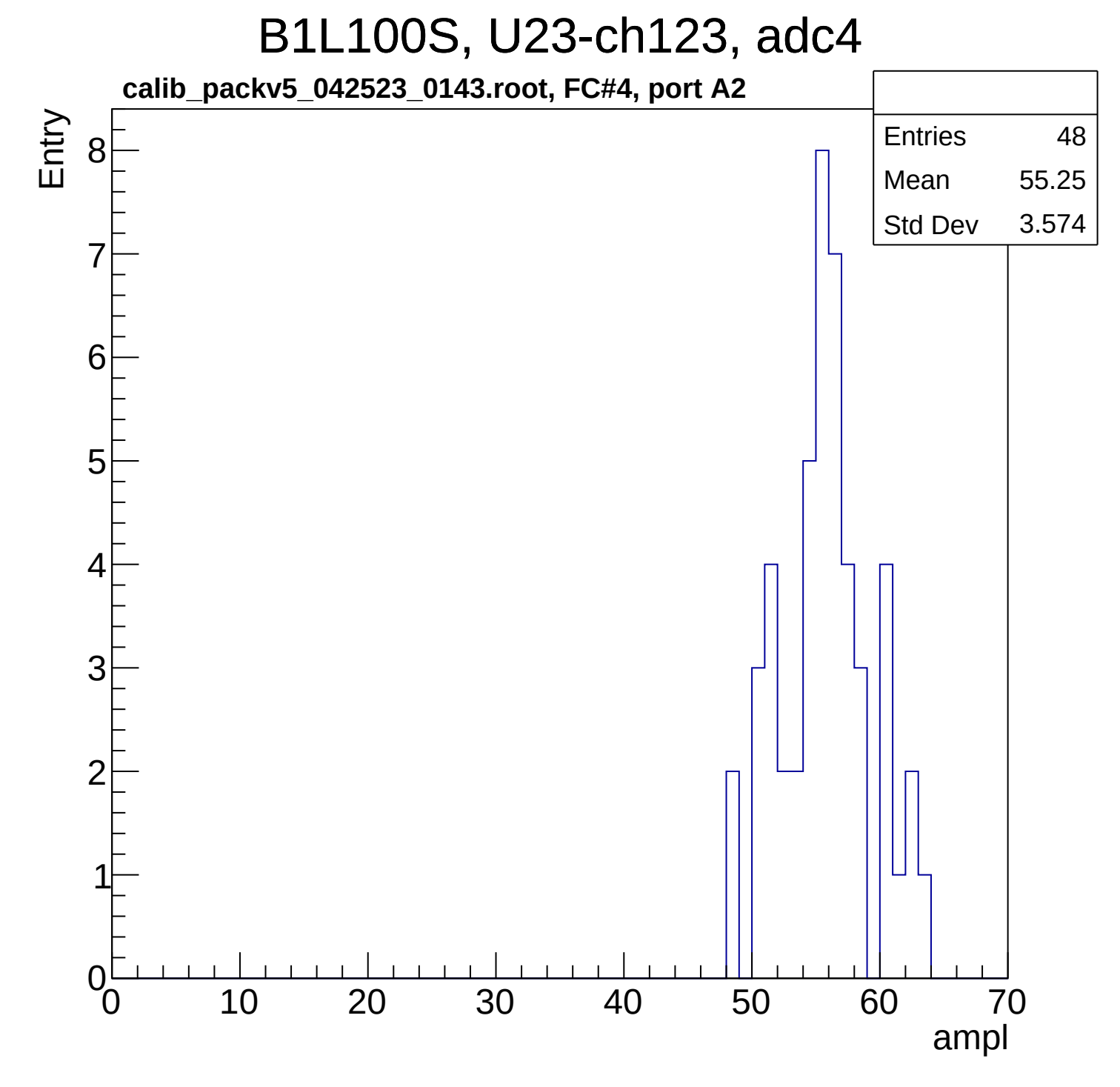
B1L100S, U23-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 55.25
Std Dev 3.574
Entry
ampl
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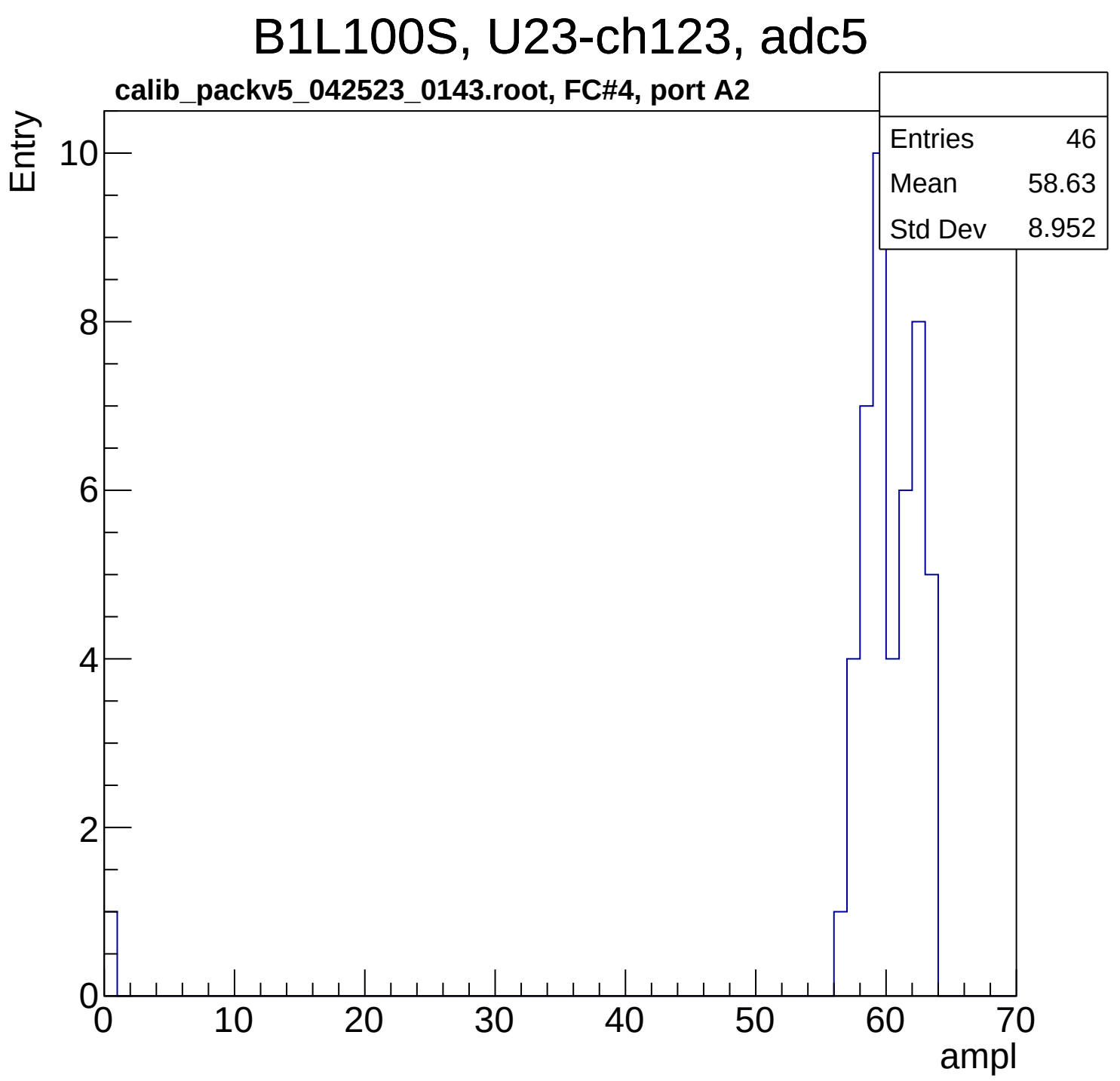
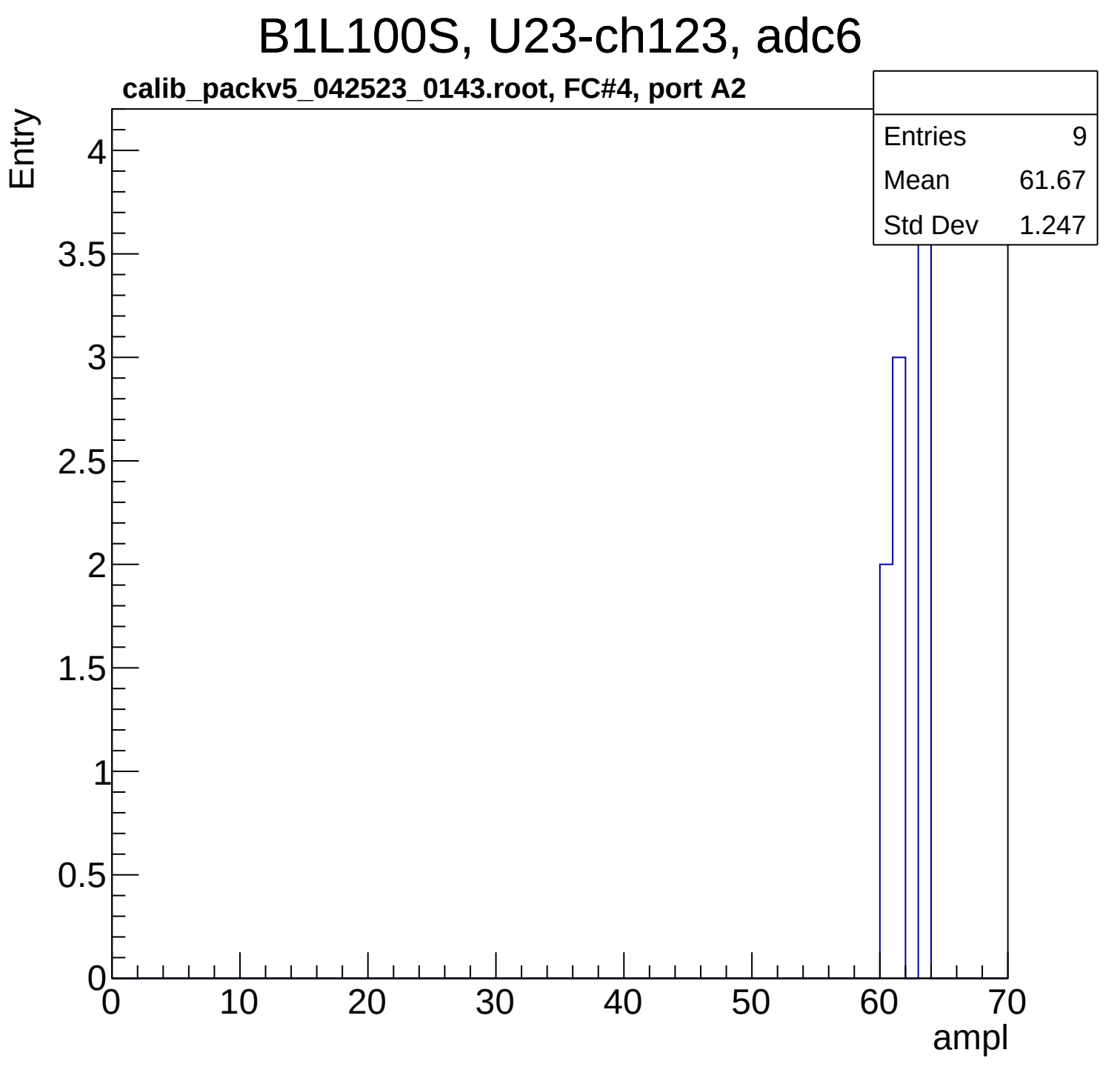

B1L100S, U23-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.63
Std Dev 8.952
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
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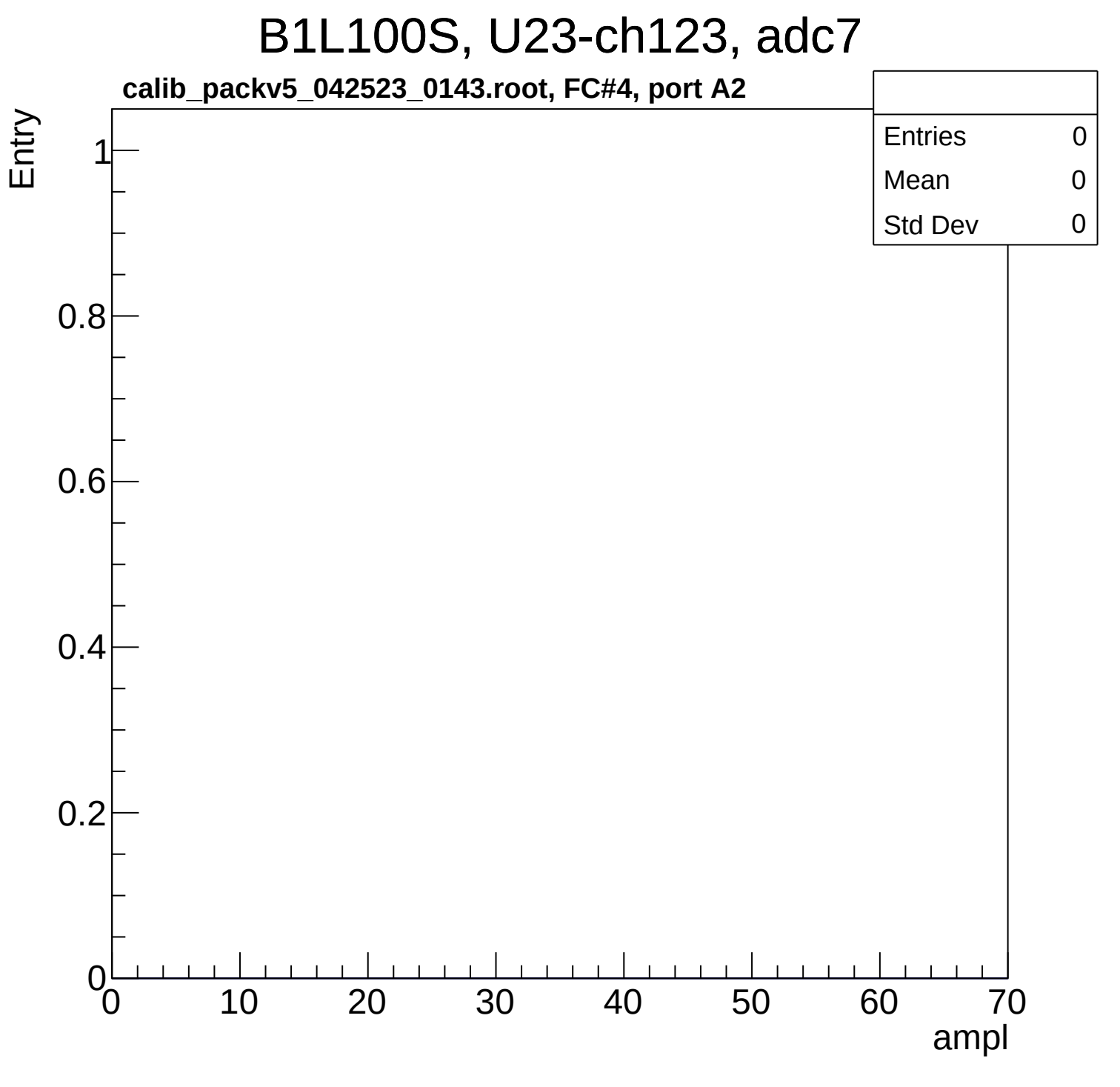
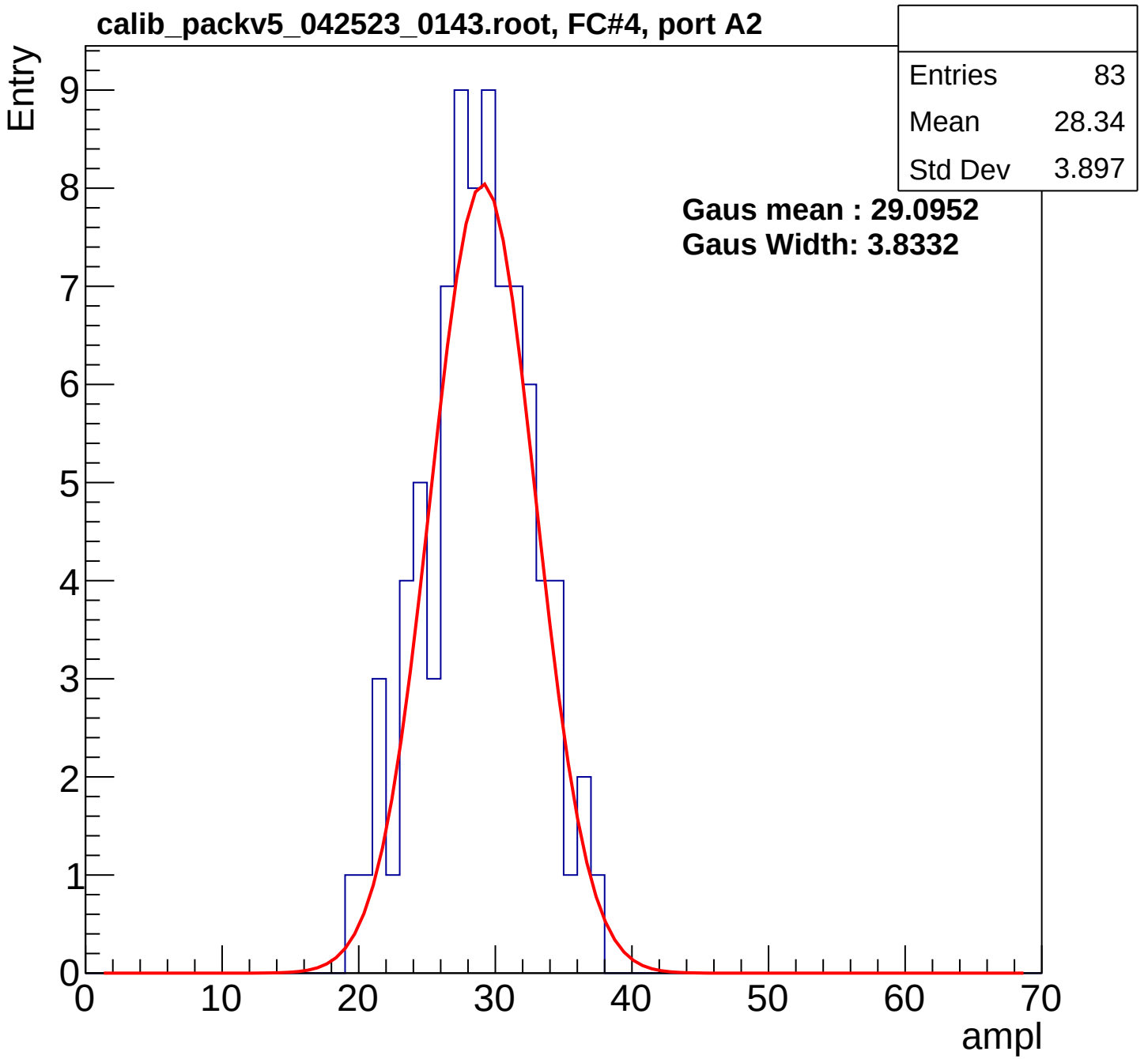
40
50
60
70


B1L100S, U23-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 28.34
Std Dev 3.897
Gaus mean : 29.0952
Gaus Width: 3.8332
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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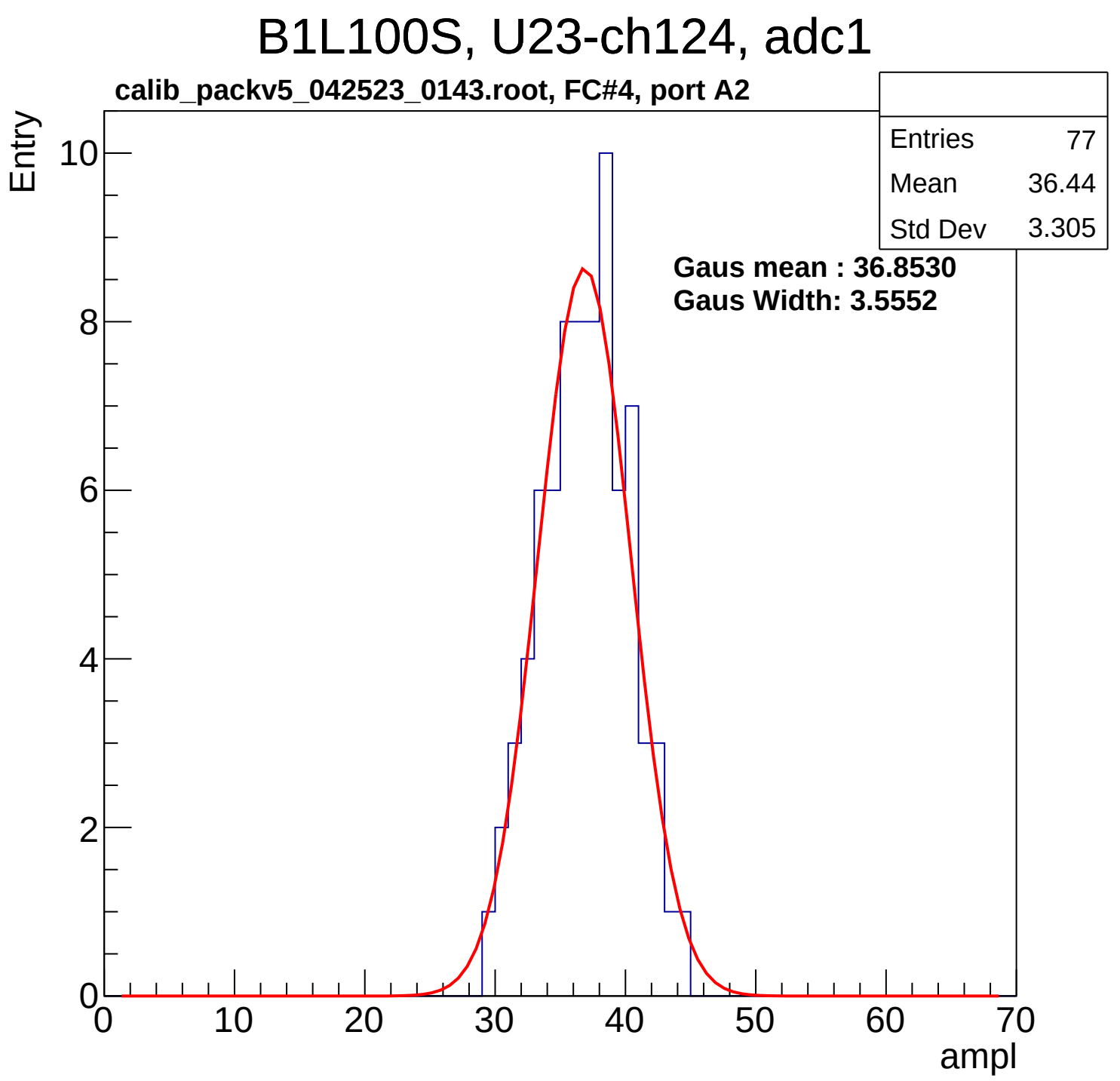
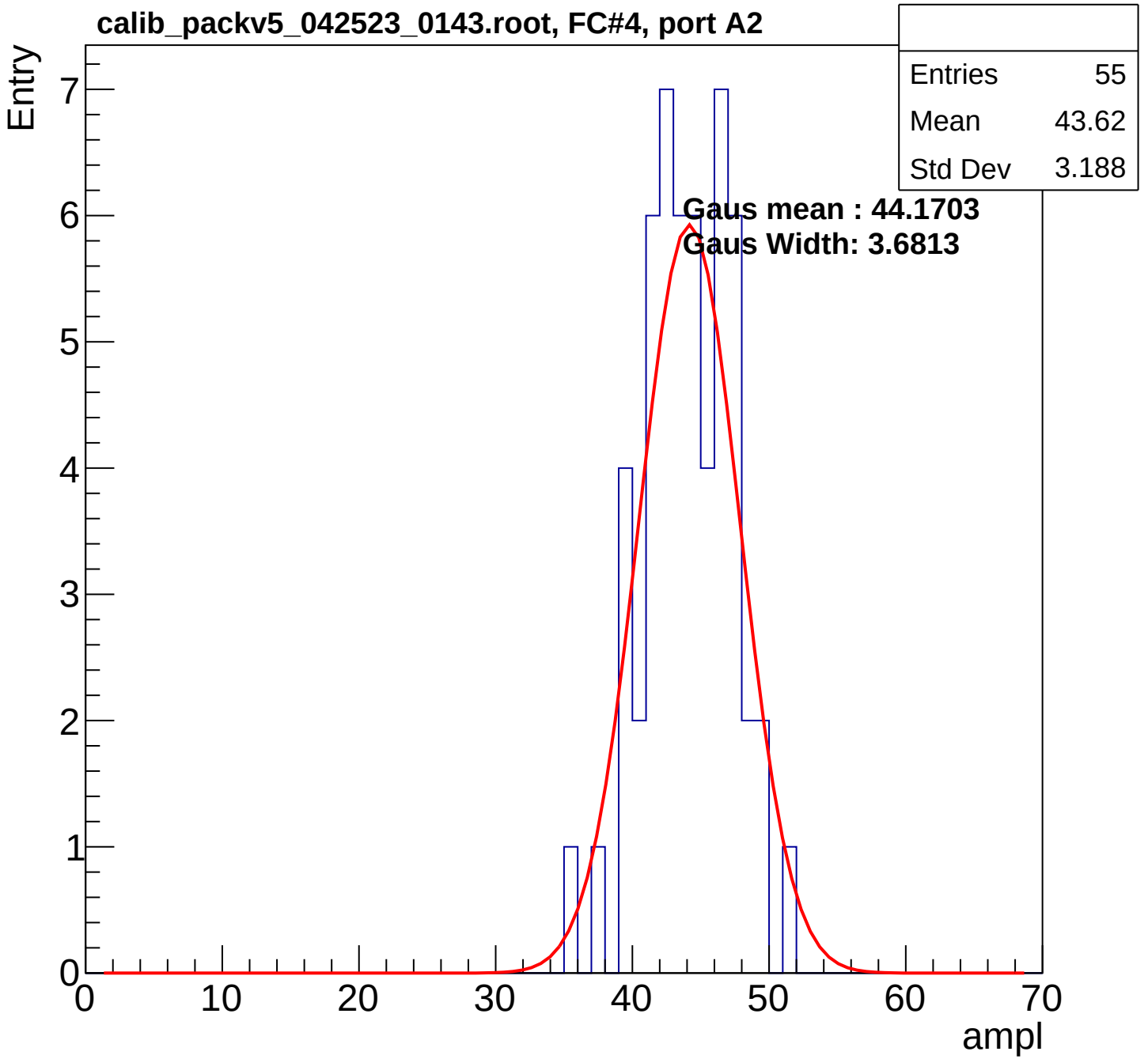
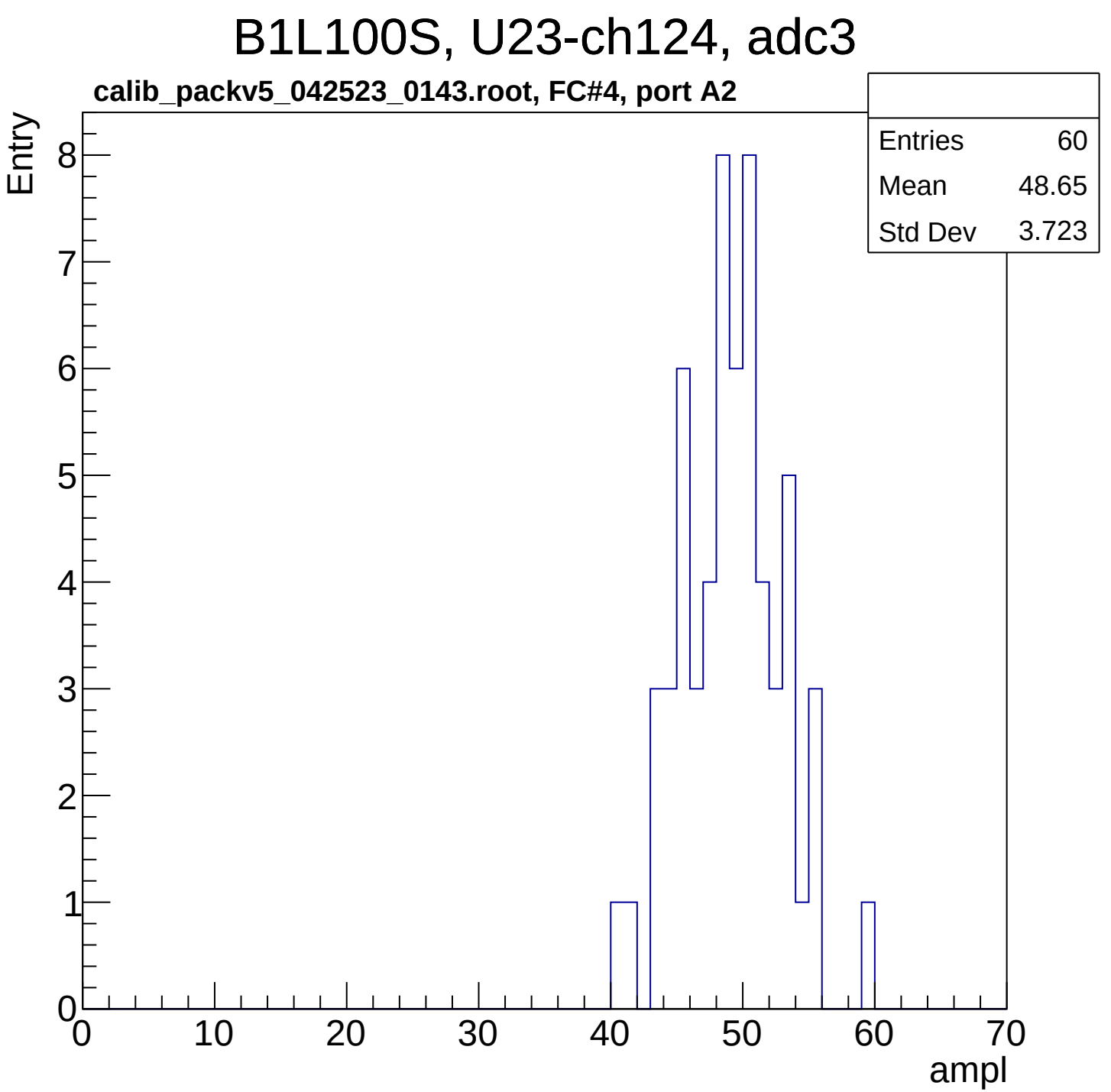
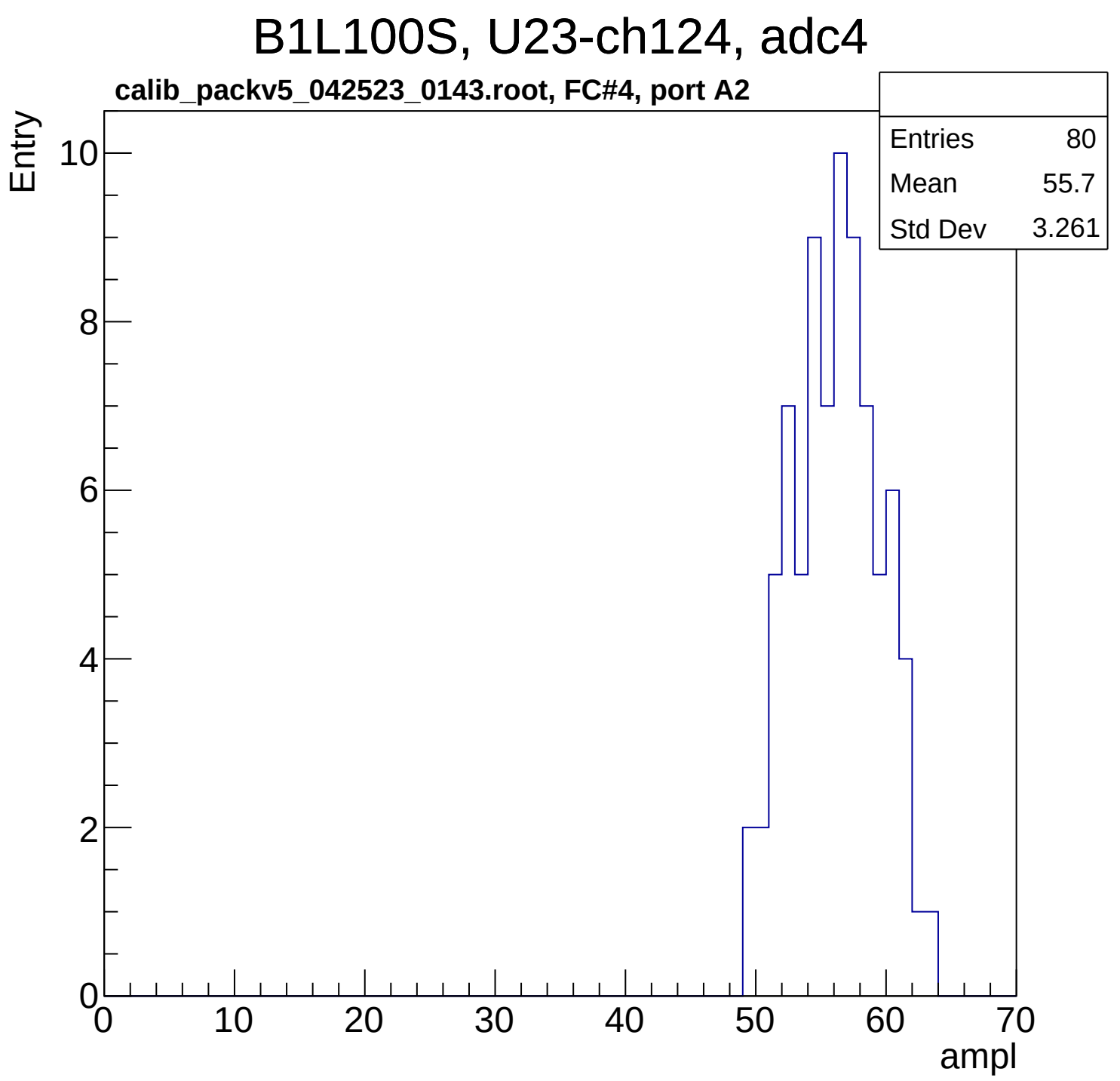
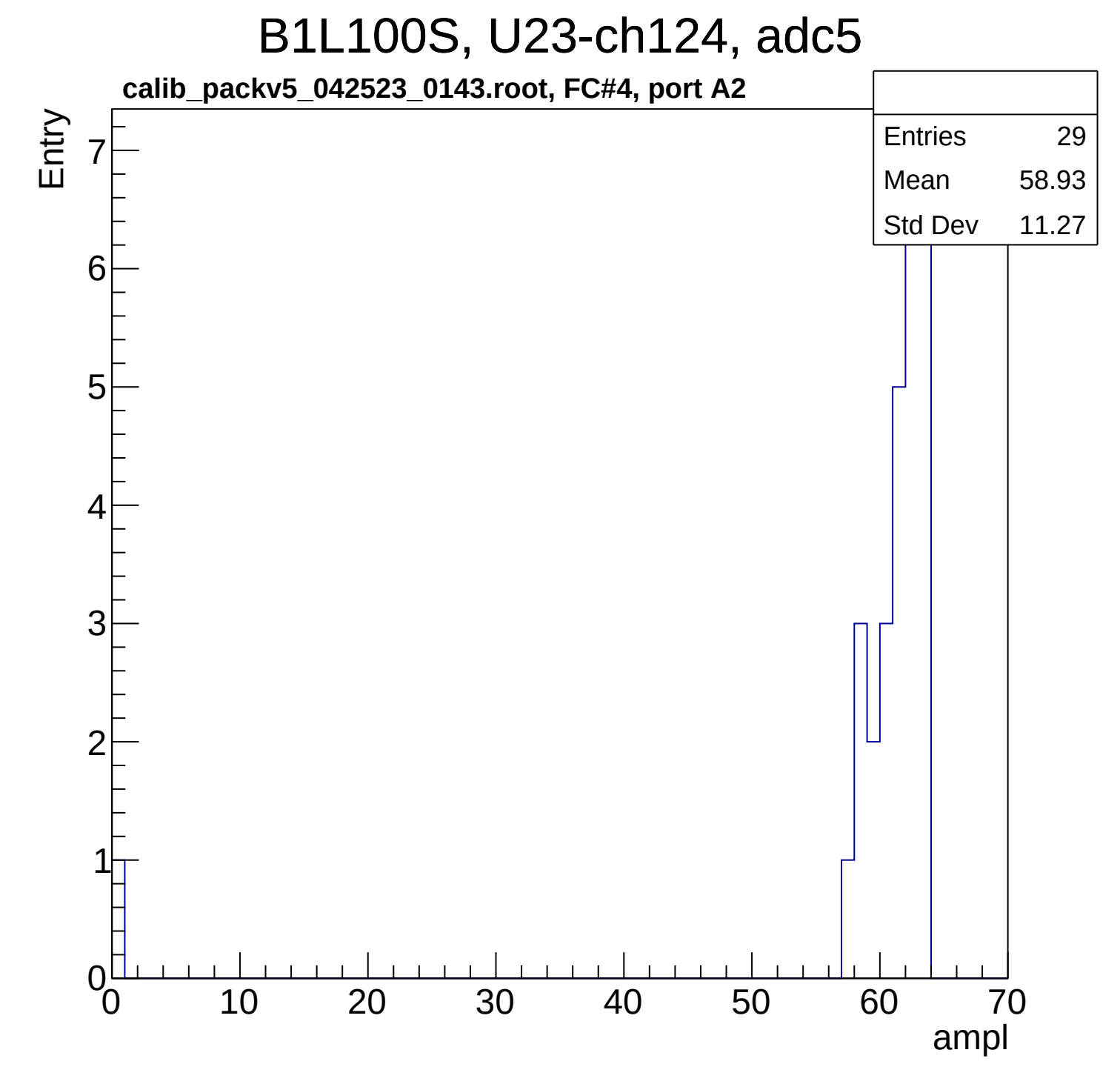
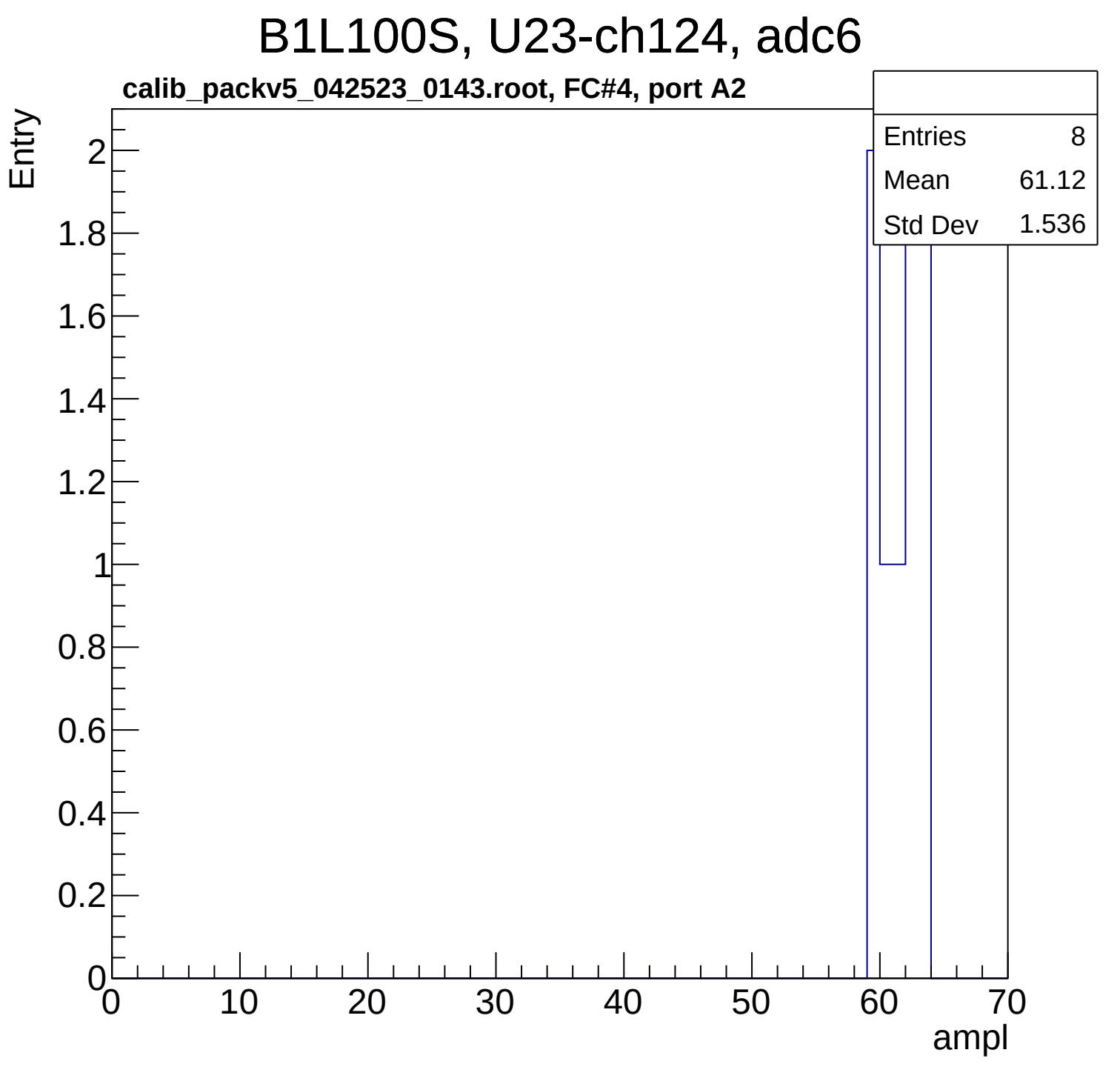

B1L100S, U23-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.44
Std Dev 3.305
Gaus mean : 36.8530
Gaus Width: 3.5552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

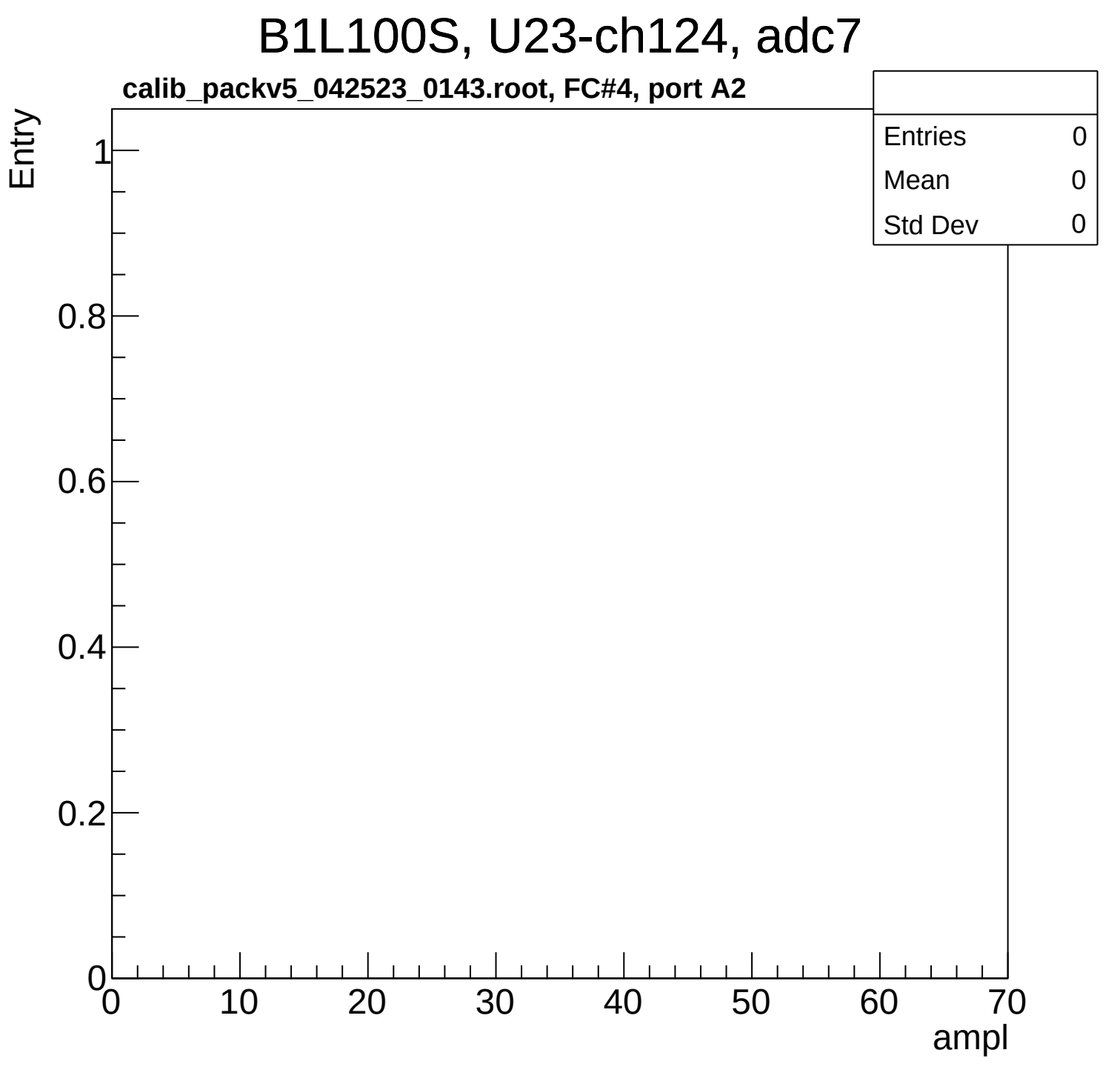

B1L100S, U23-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 43.62
Std Dev 3.188
Gaus mean : 44.1703
Gaus Width: 3.6813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 48.65
Std Dev 3.723
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 55.7
Std Dev 3.261
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch124, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.93
Std Dev 11.27
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.12
Std Dev 1.536
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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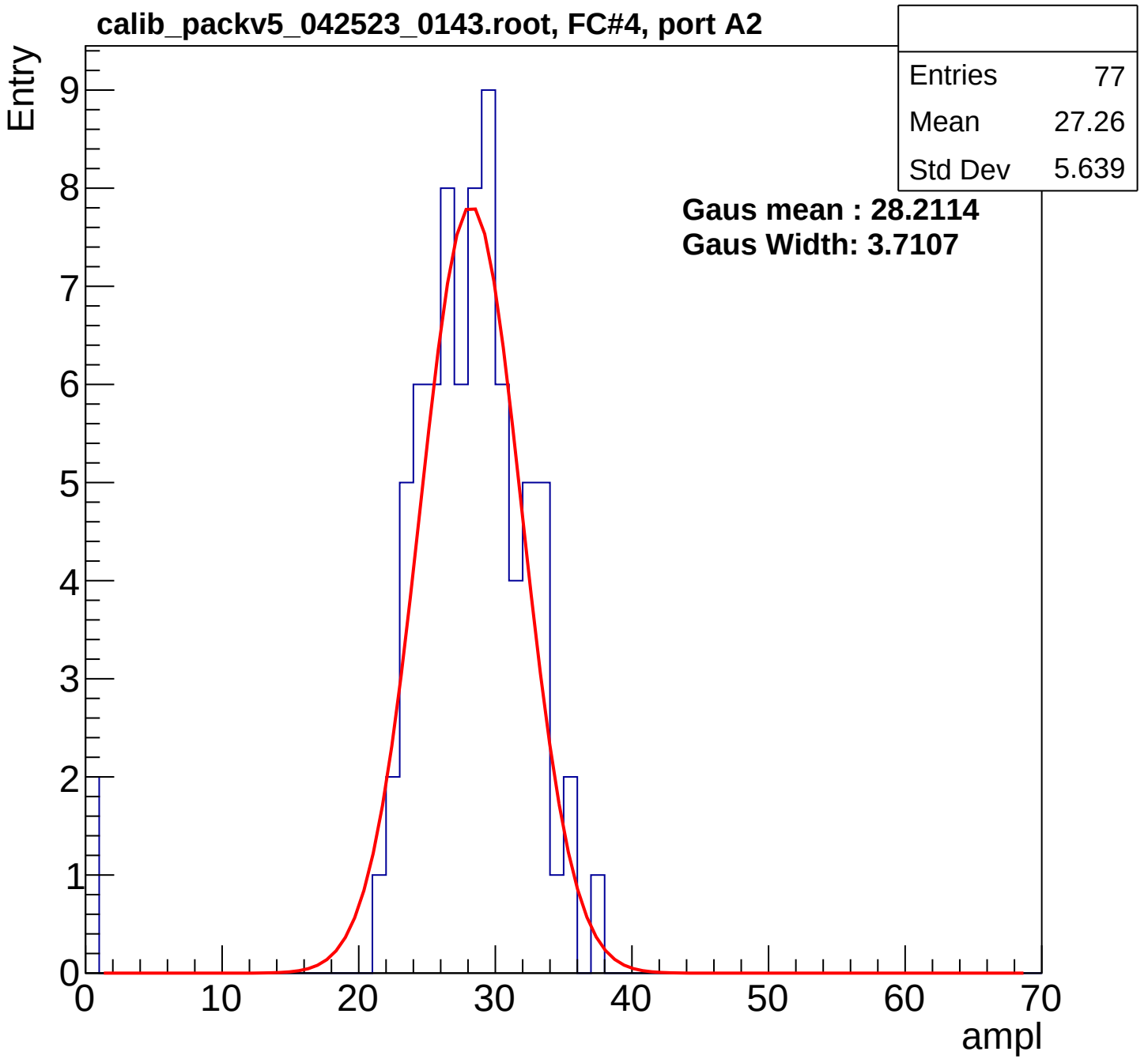
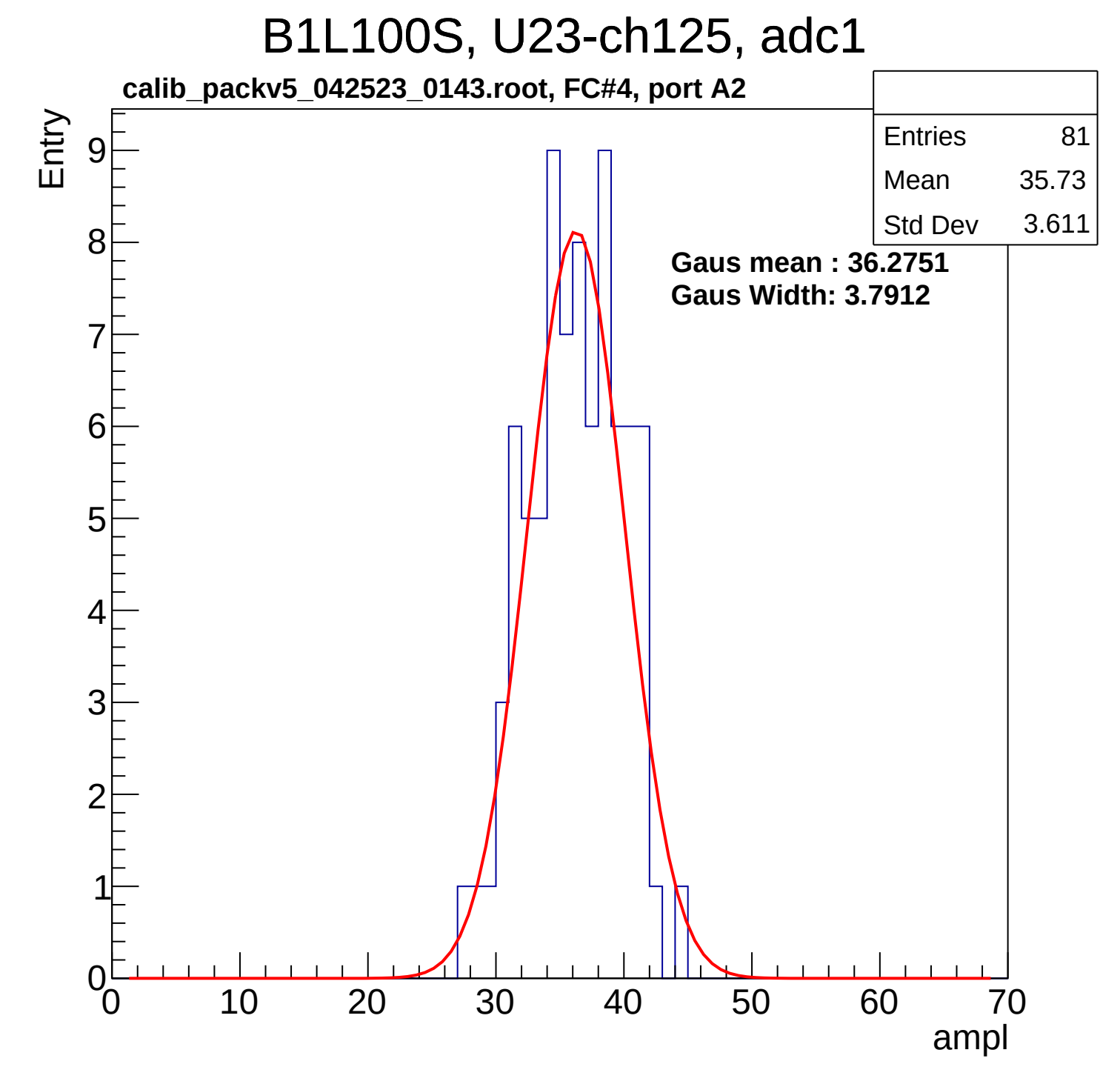
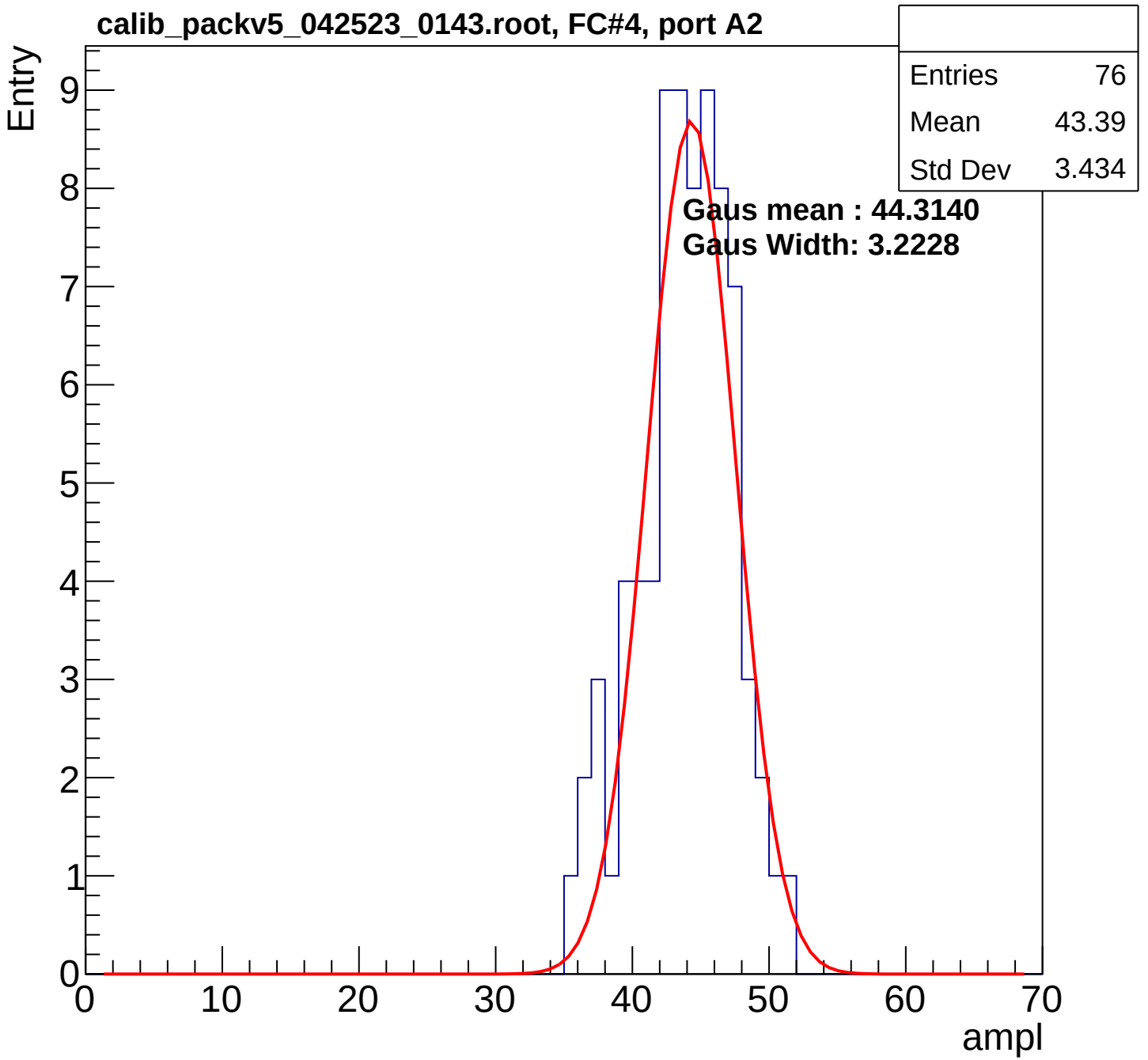
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 27.26
Std Dev 5.639
Gaus mean : 28.2114
Gaus Width: 3.7107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.73
Std Dev 3.611
Gaus mean : 36.2751
Gaus Width: 3.7912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U23-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 43.39
Std Dev 3.434
Gaus mean : 44.3140
Gaus Width: 3.2228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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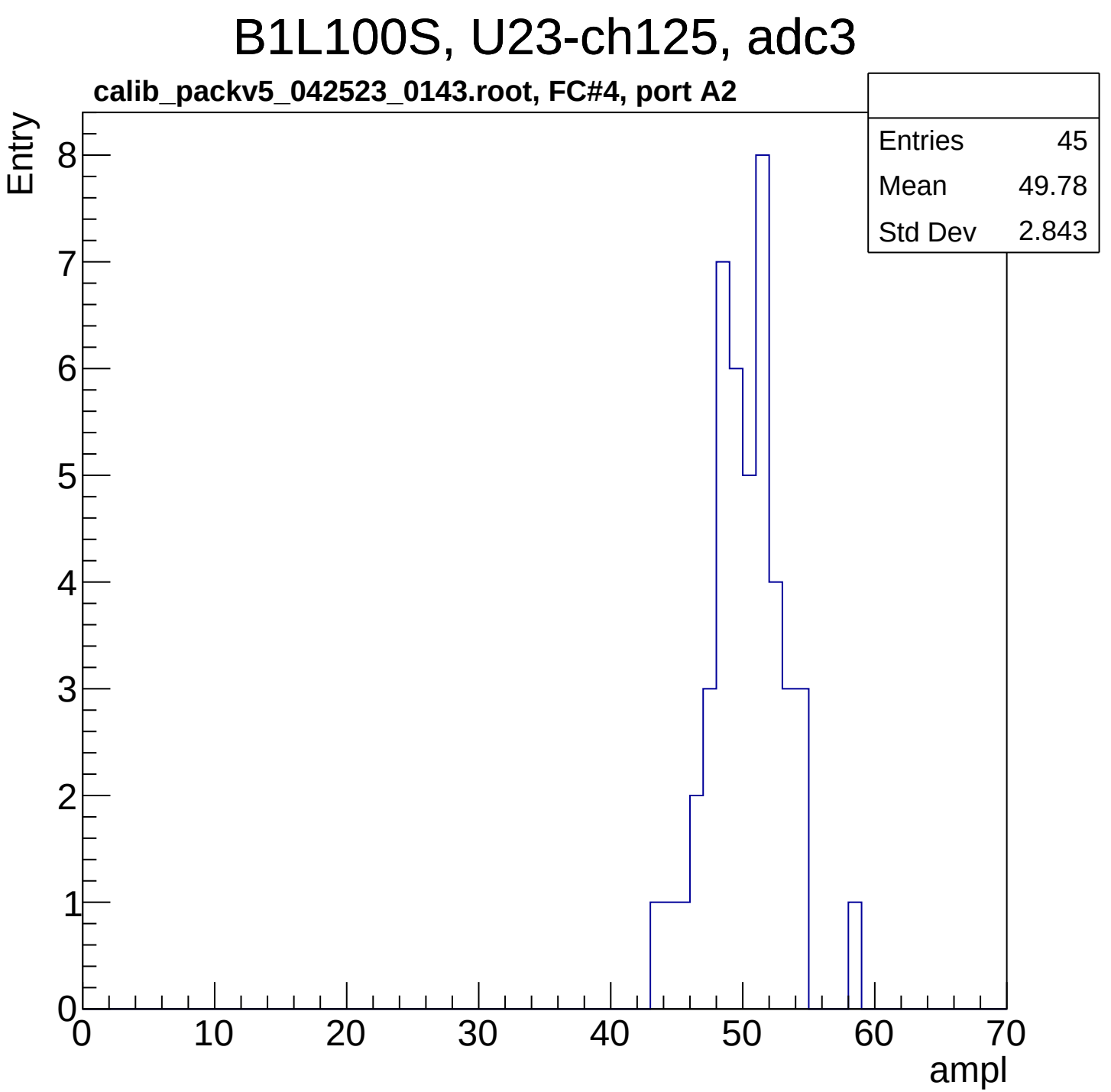
B1L100S, U23-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 49.78
Std Dev 2.843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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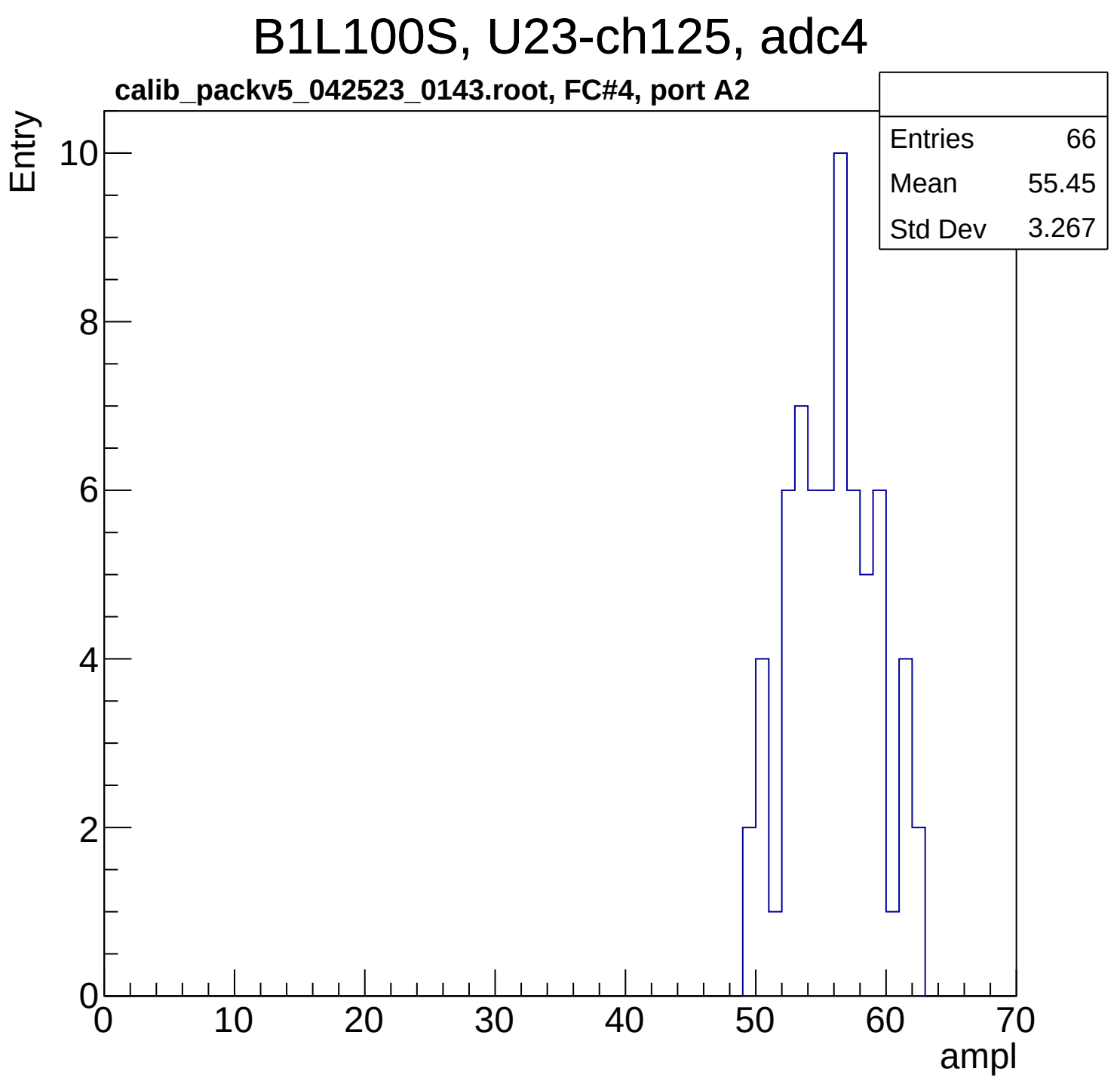
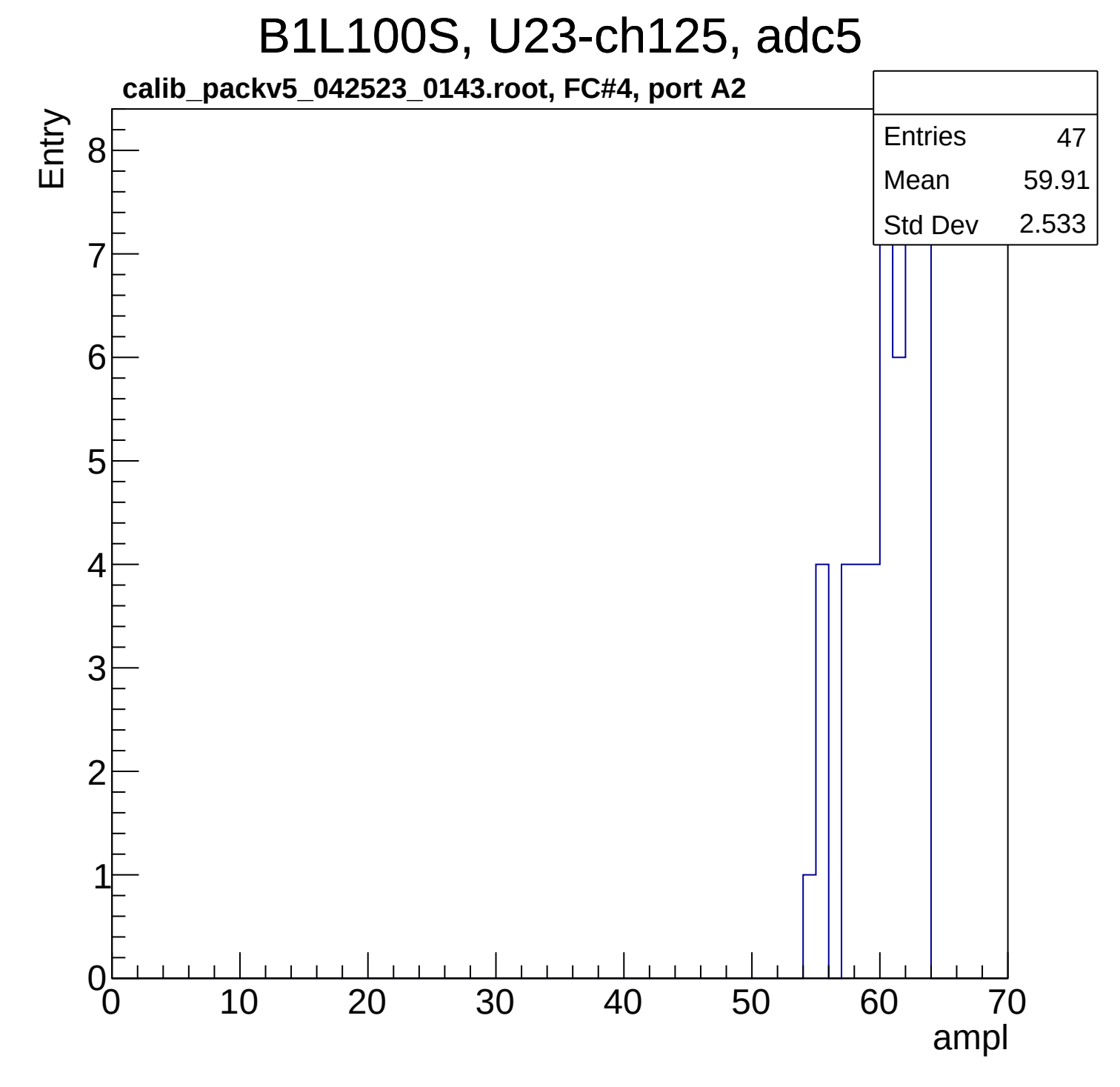

B1L100S, U23-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.45
Std Dev 3.267
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.91
Std Dev 2.533
Entry
ampl
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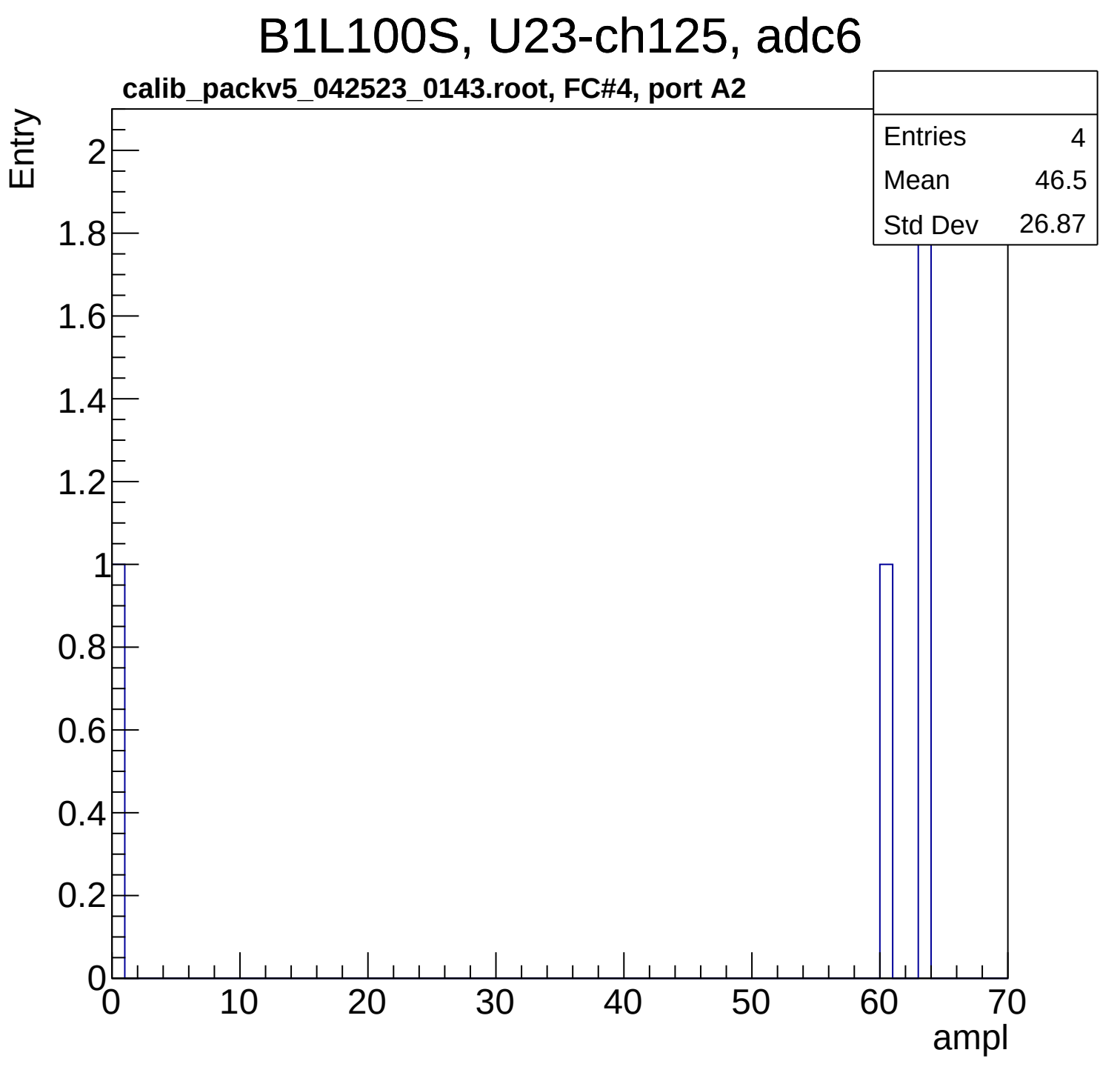
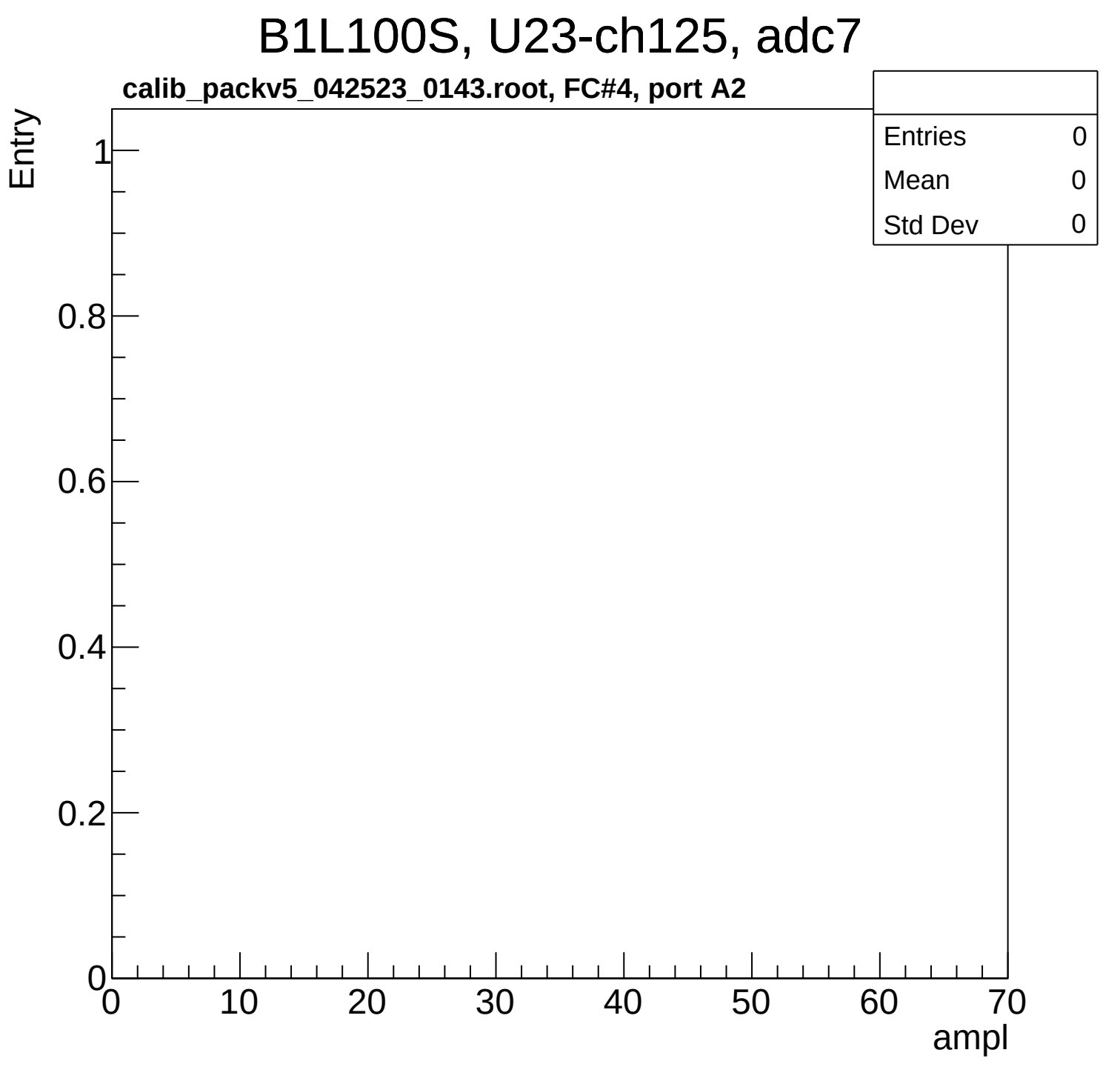
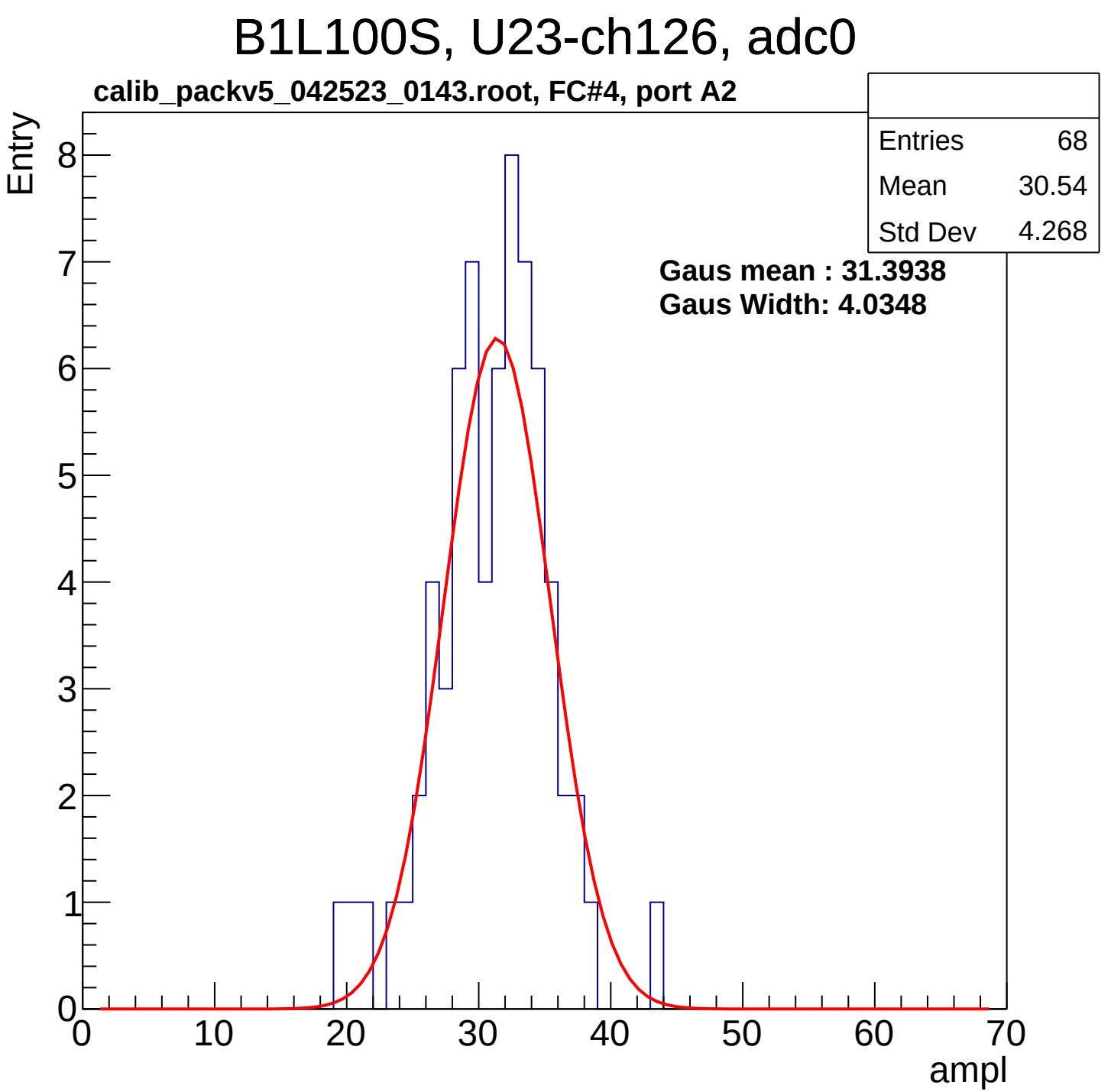
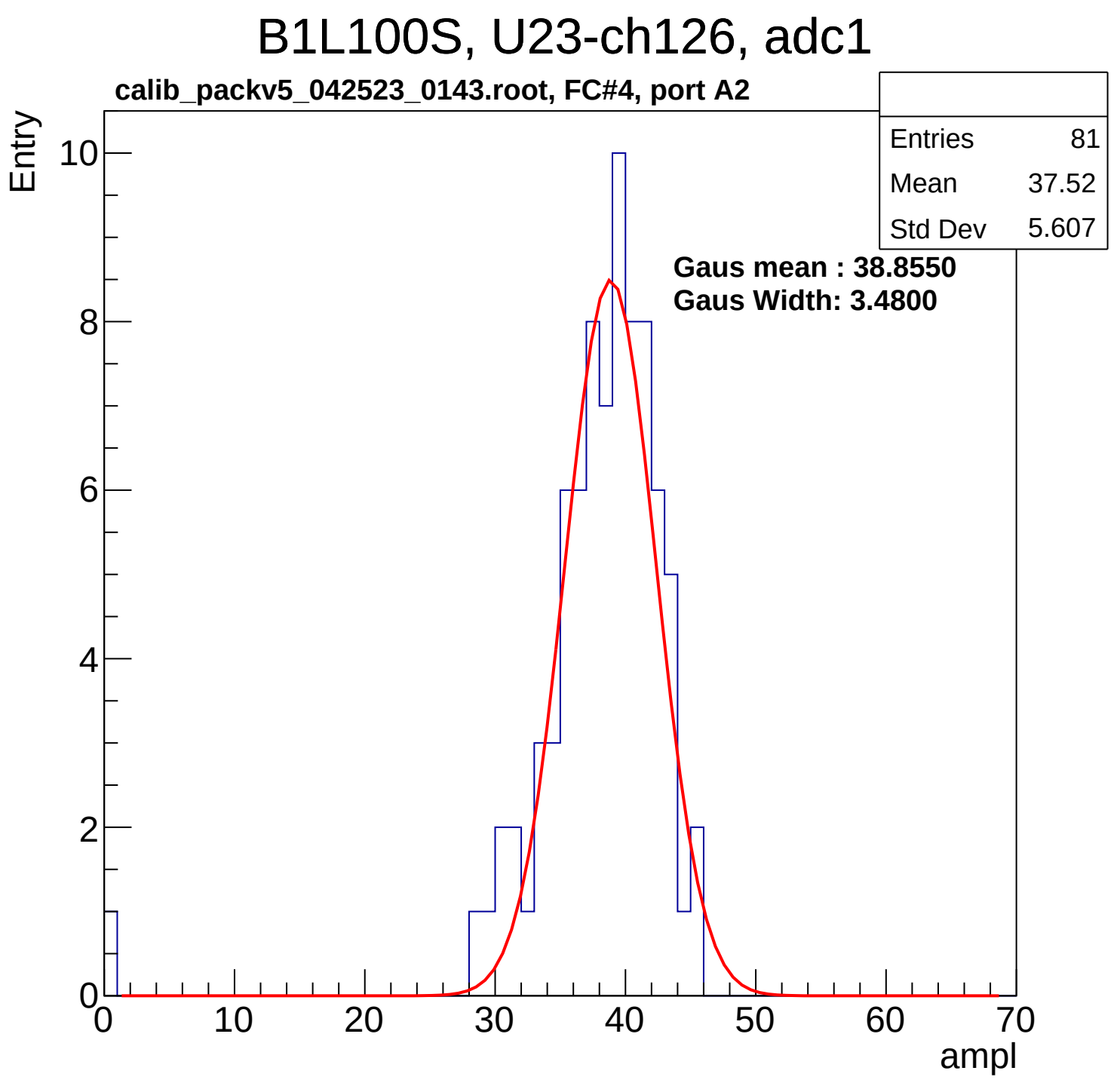
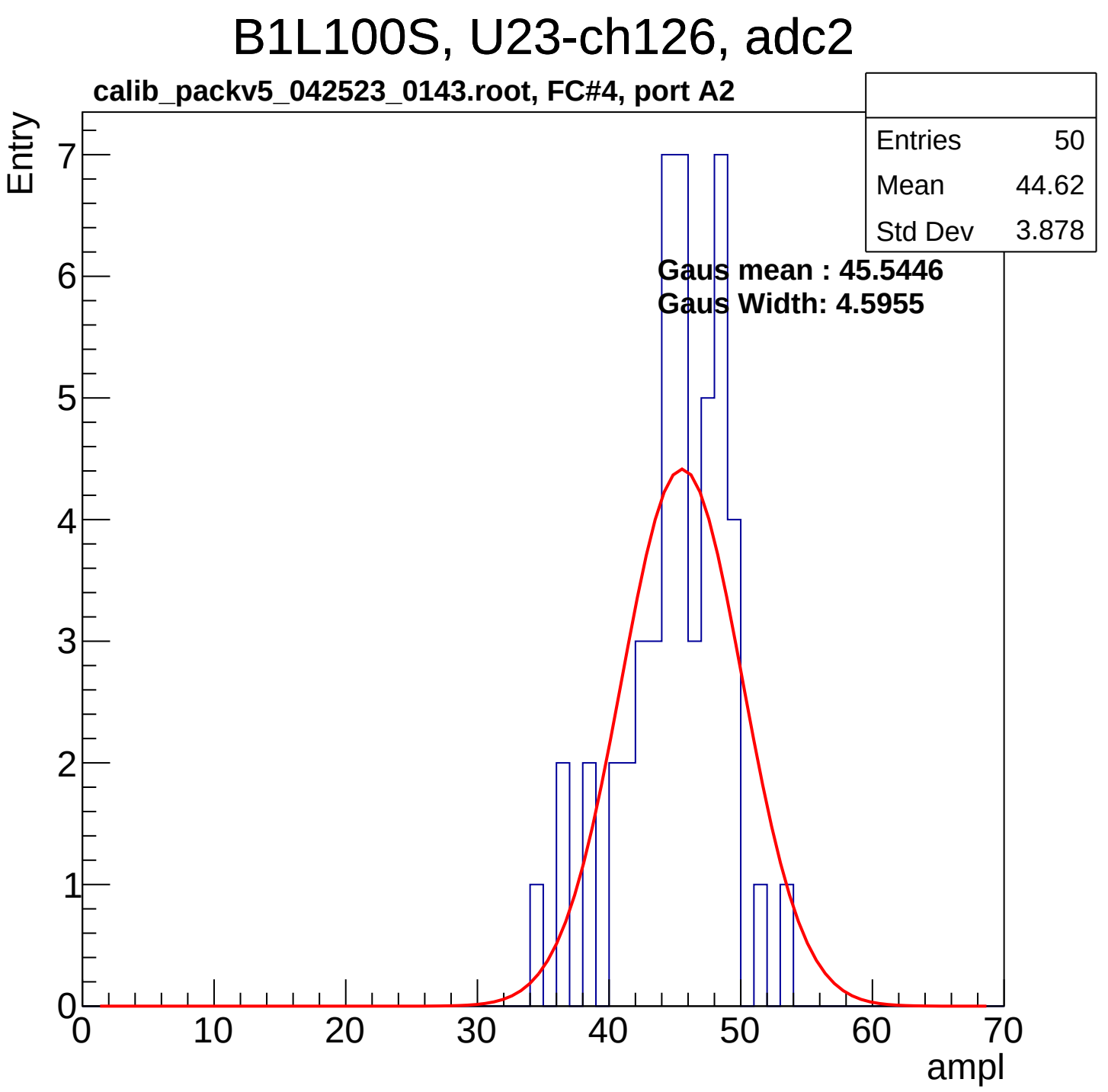
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

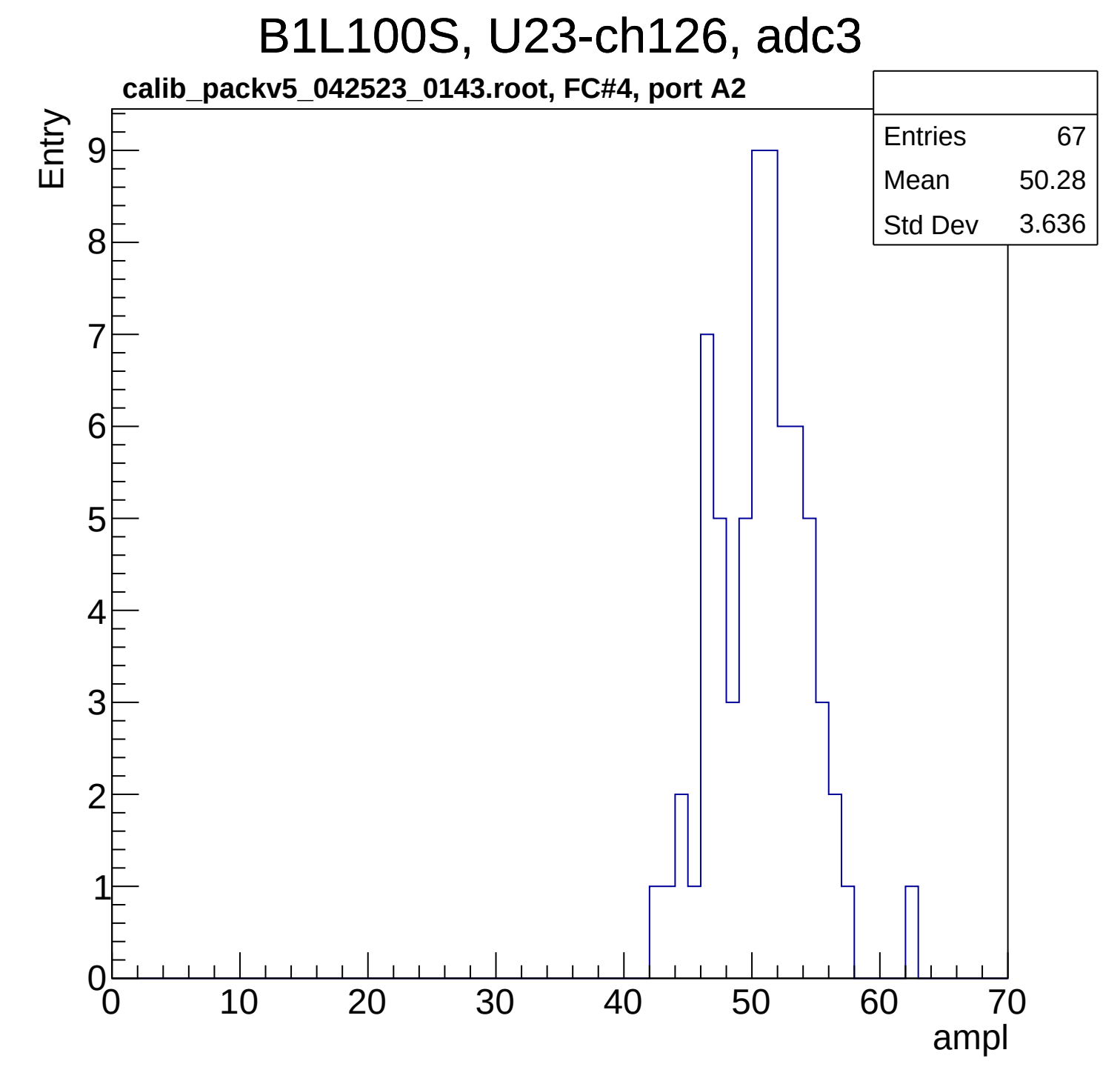

B1L100S, U23-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.5
Std Dev 26.87
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch125, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 30.54
Std Dev 4.268
Gaus mean : 31.3938
Gaus Width: 4.0348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch126, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 37.52
Std Dev 5.607
Gaus mean : 38.8550
Gaus Width: 3.4800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U23-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 44.62
Std Dev 3.878
Gaus mean : 45.5446
Gaus Width: 4.5955
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch126, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.28
Std Dev 3.636
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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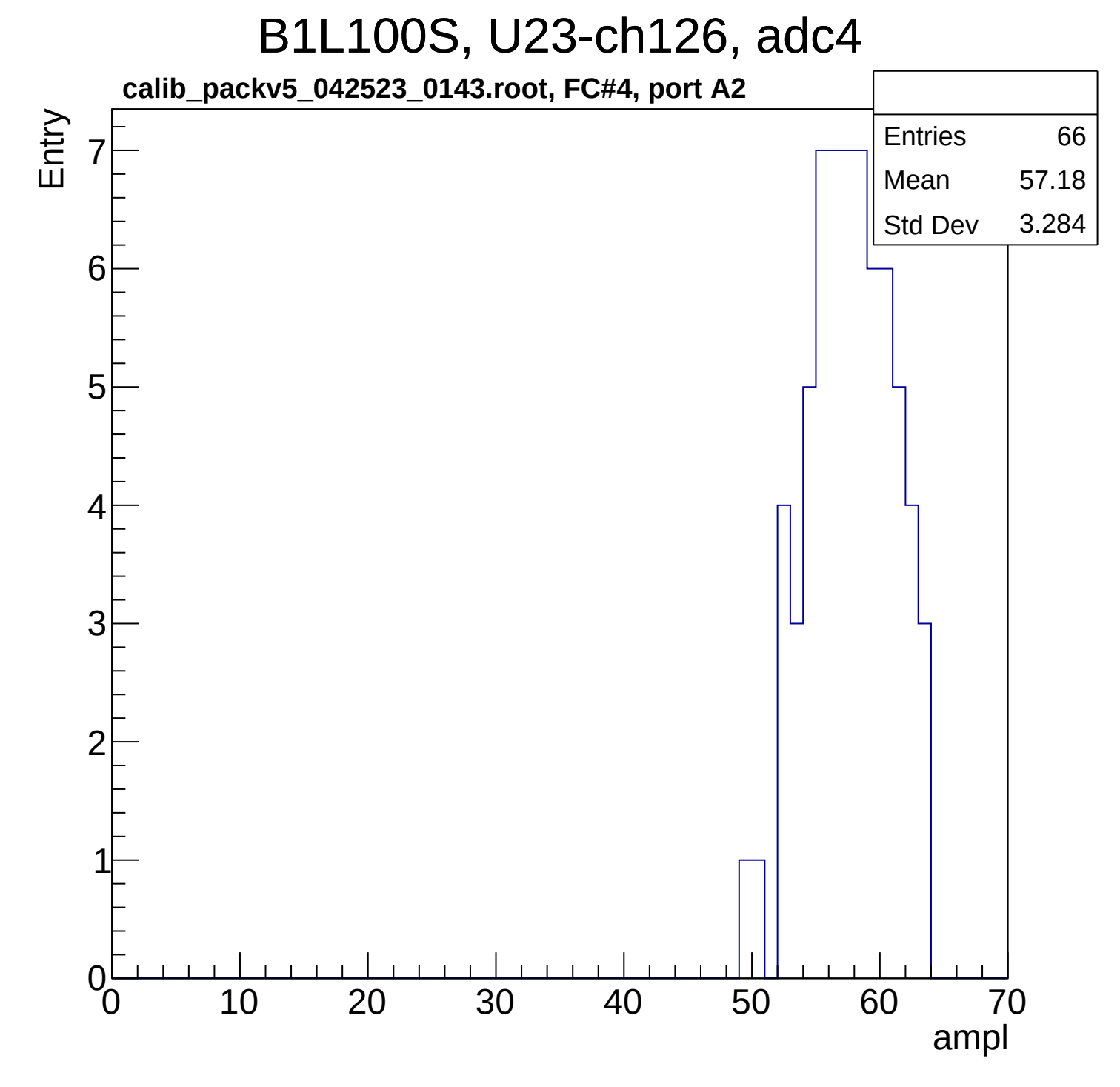
B1L100S, U23-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 57.18
Std Dev 3.284
Entry
ampl
0
1
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3
4
5
6
7
0
10
20
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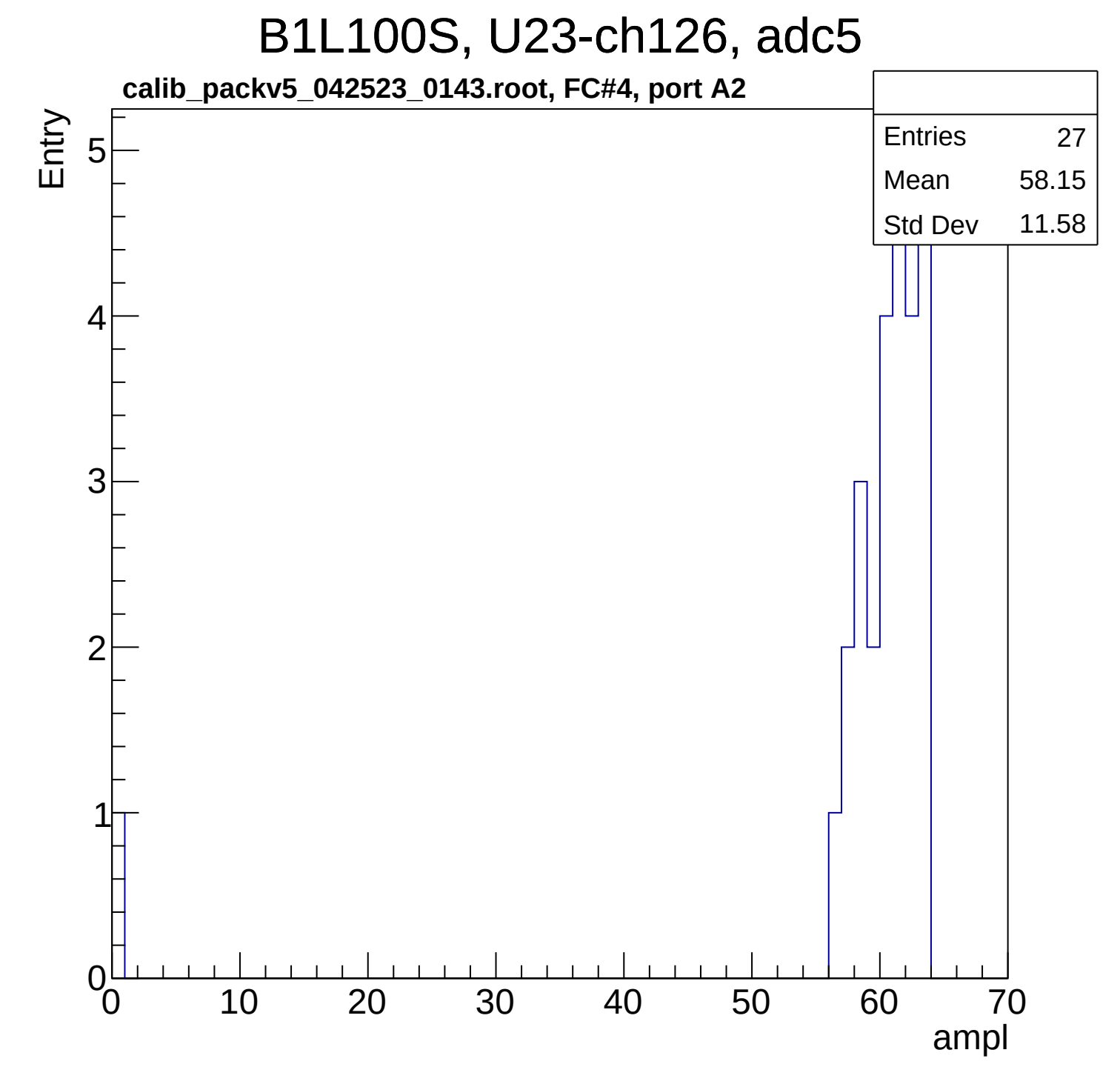
40
50
60
70


B1L100S, U23-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.15
Std Dev 11.58
Entry
0
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2
3
4
5
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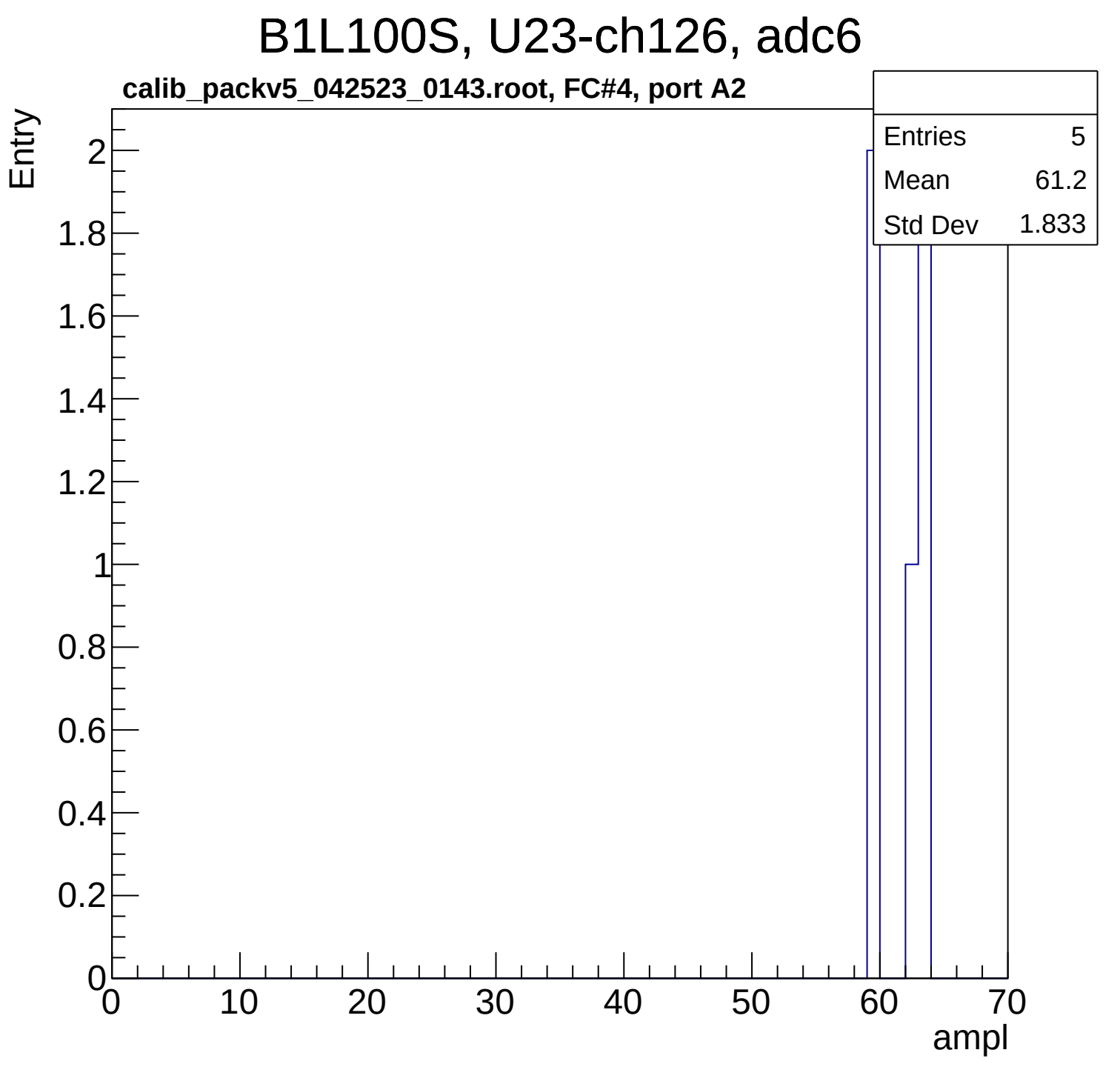
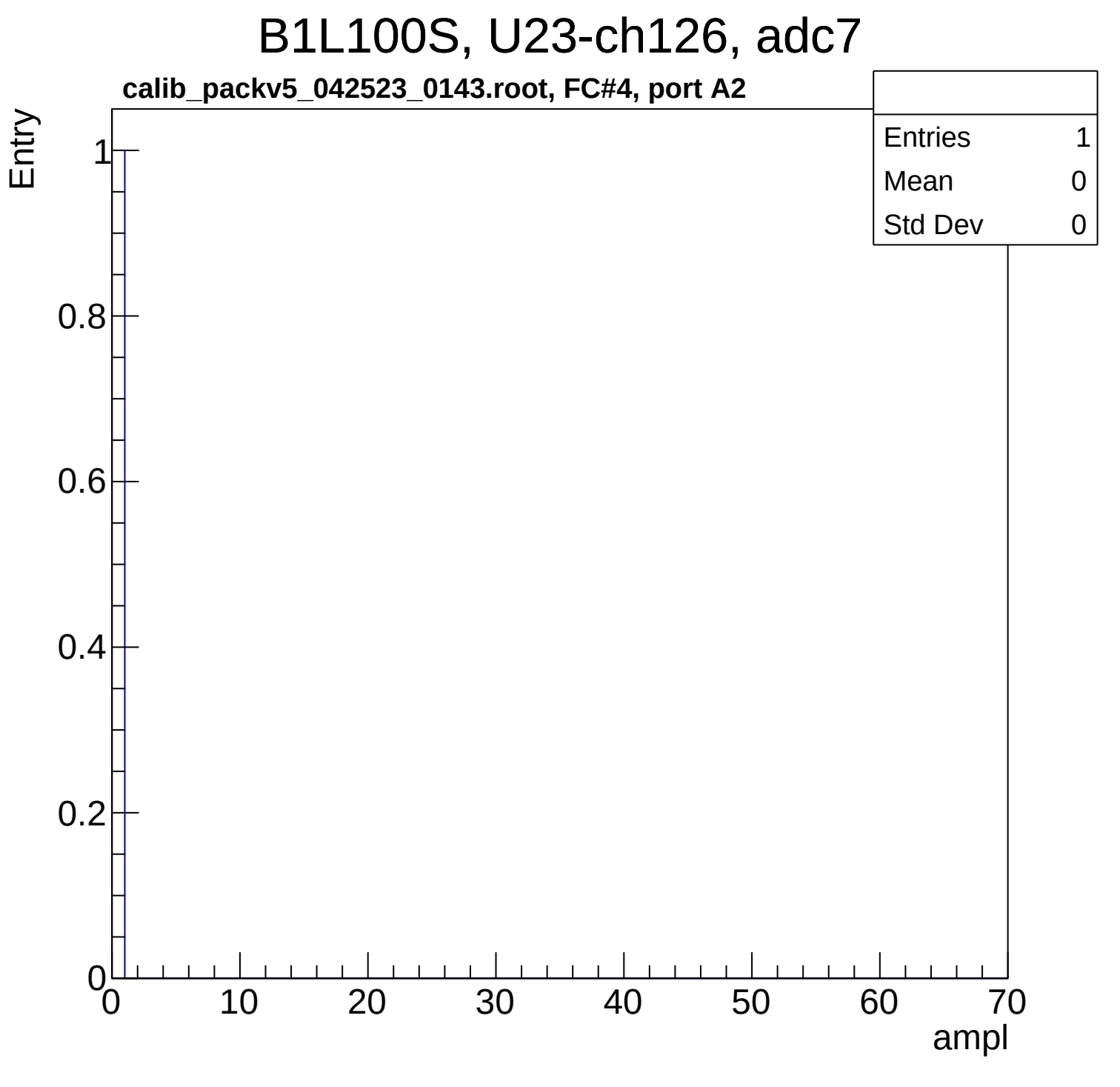
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch126, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.2
Std Dev 1.833
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U23-ch126, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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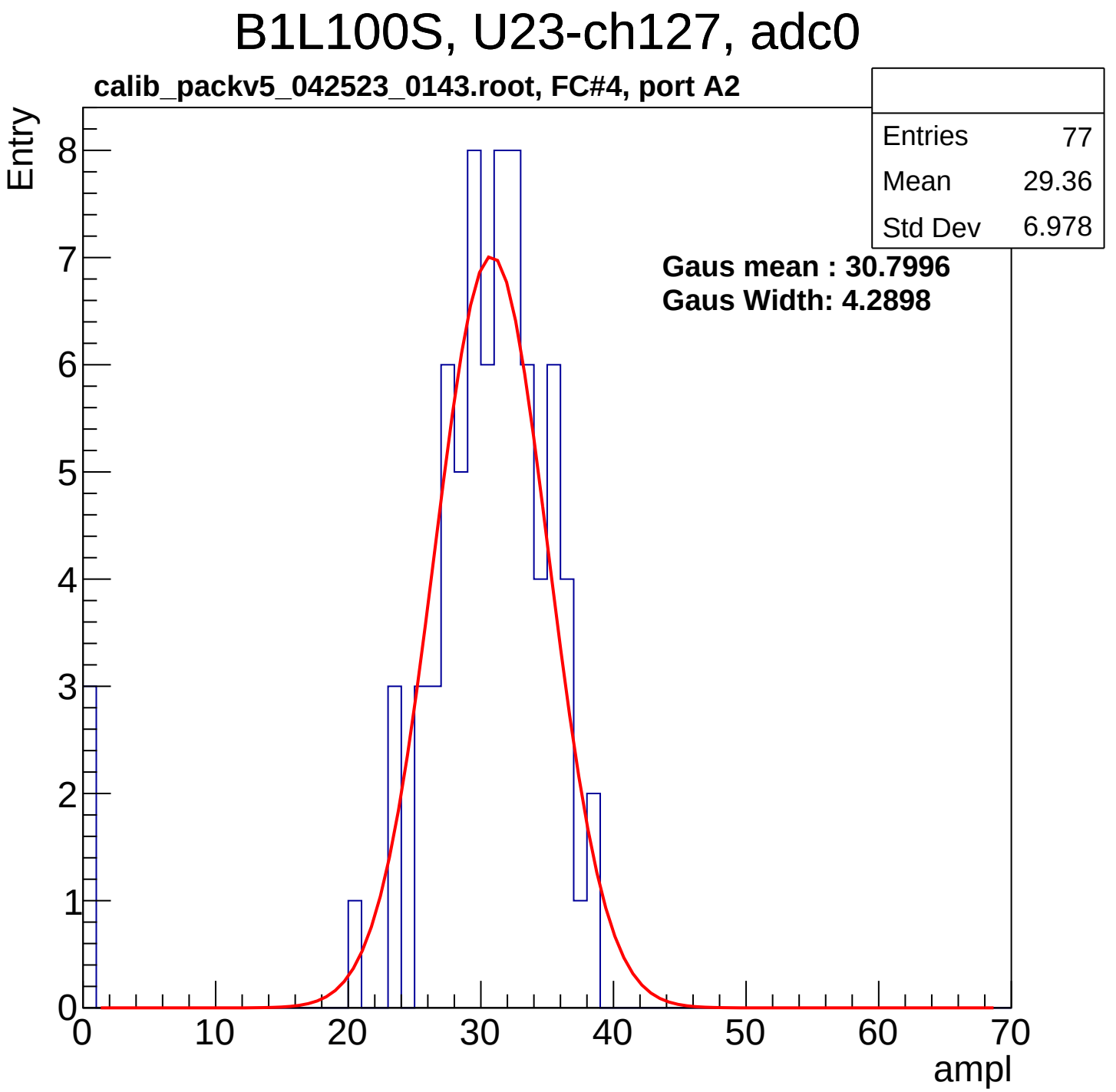
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.36
Std Dev 6.978
Gaus mean : 30.7996
Gaus Width: 4.2898
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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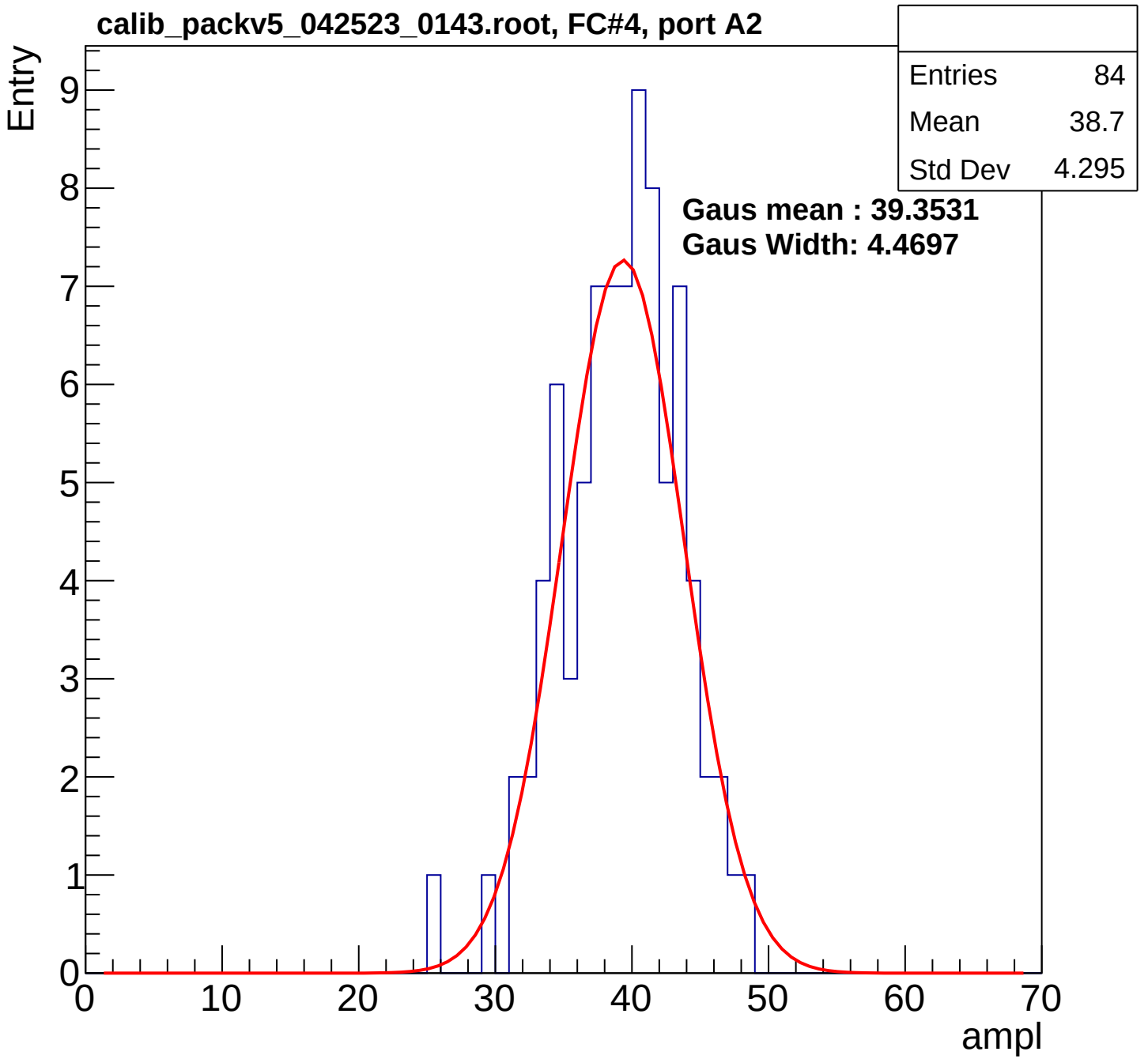
B1L100S, U23-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 38.7
Std Dev 4.295
Gaus mean : 39.3531
Gaus Width: 4.4697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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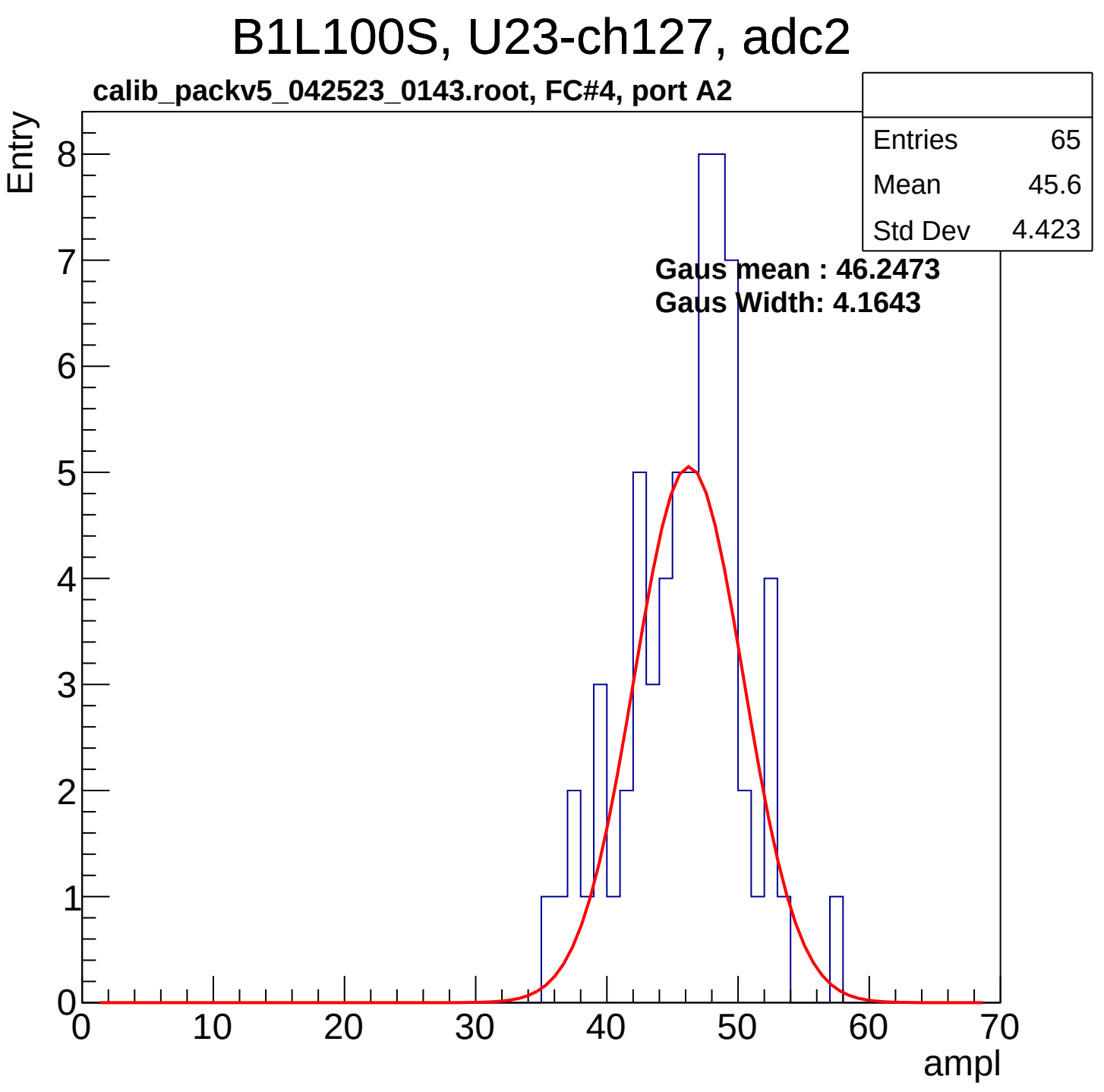
B1L100S, U23-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 45.6
Std Dev 4.423
Gaus mean : 46.2473
Gaus Width: 4.1643
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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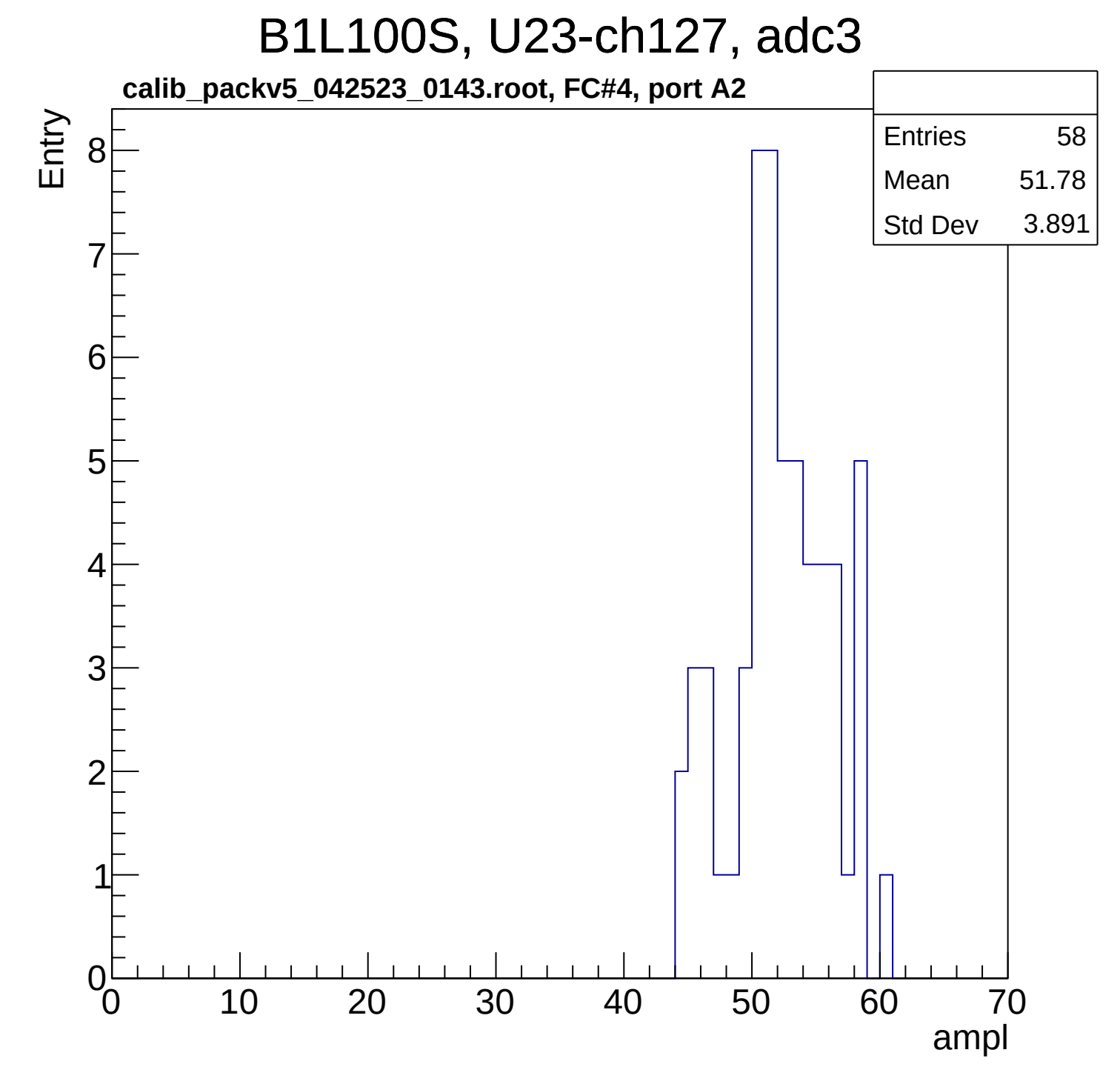
B1L100S, U23-ch127, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 51.78
Std Dev 3.891
Entry
ampl
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2
3
4
5
6
7
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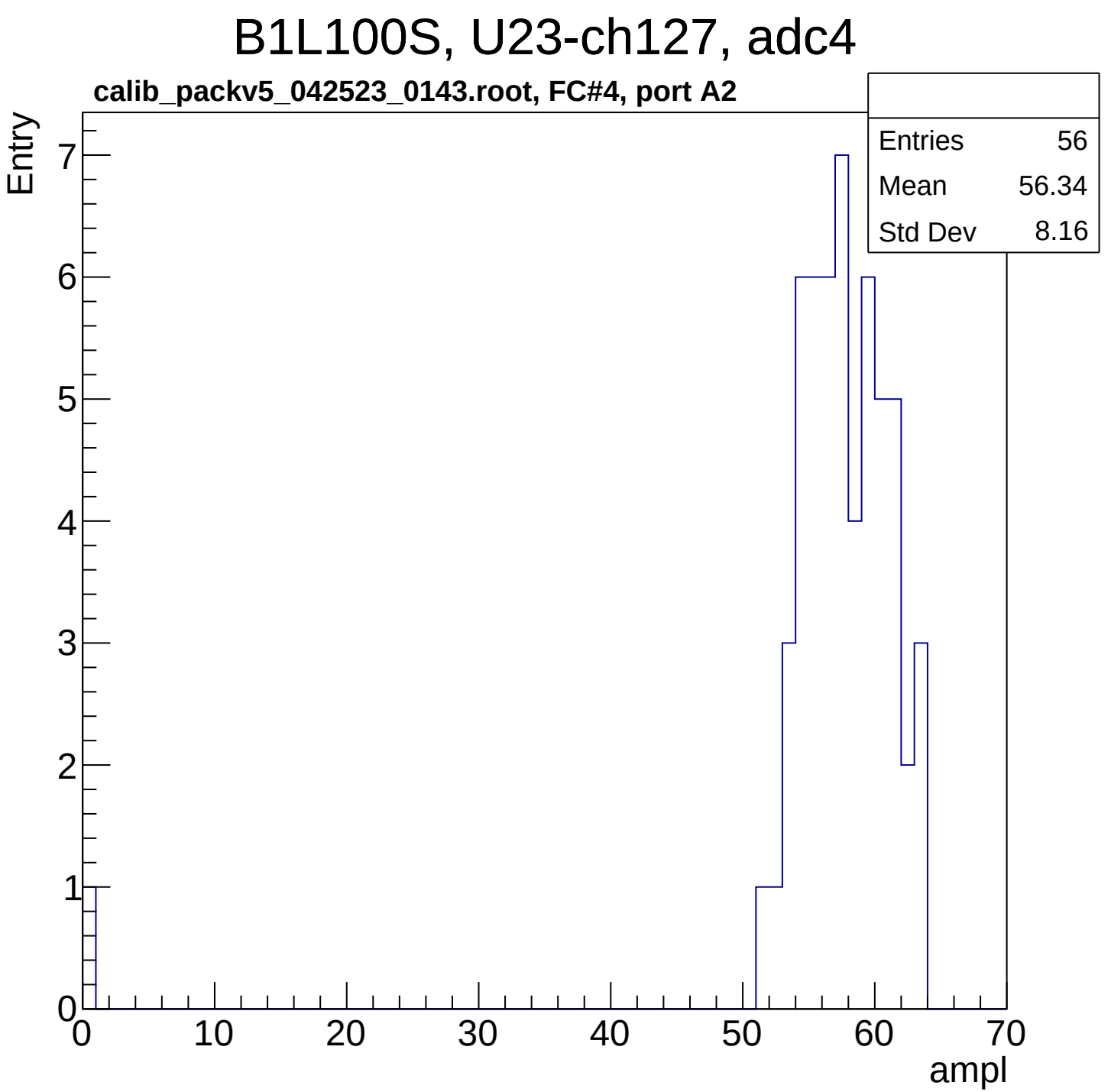
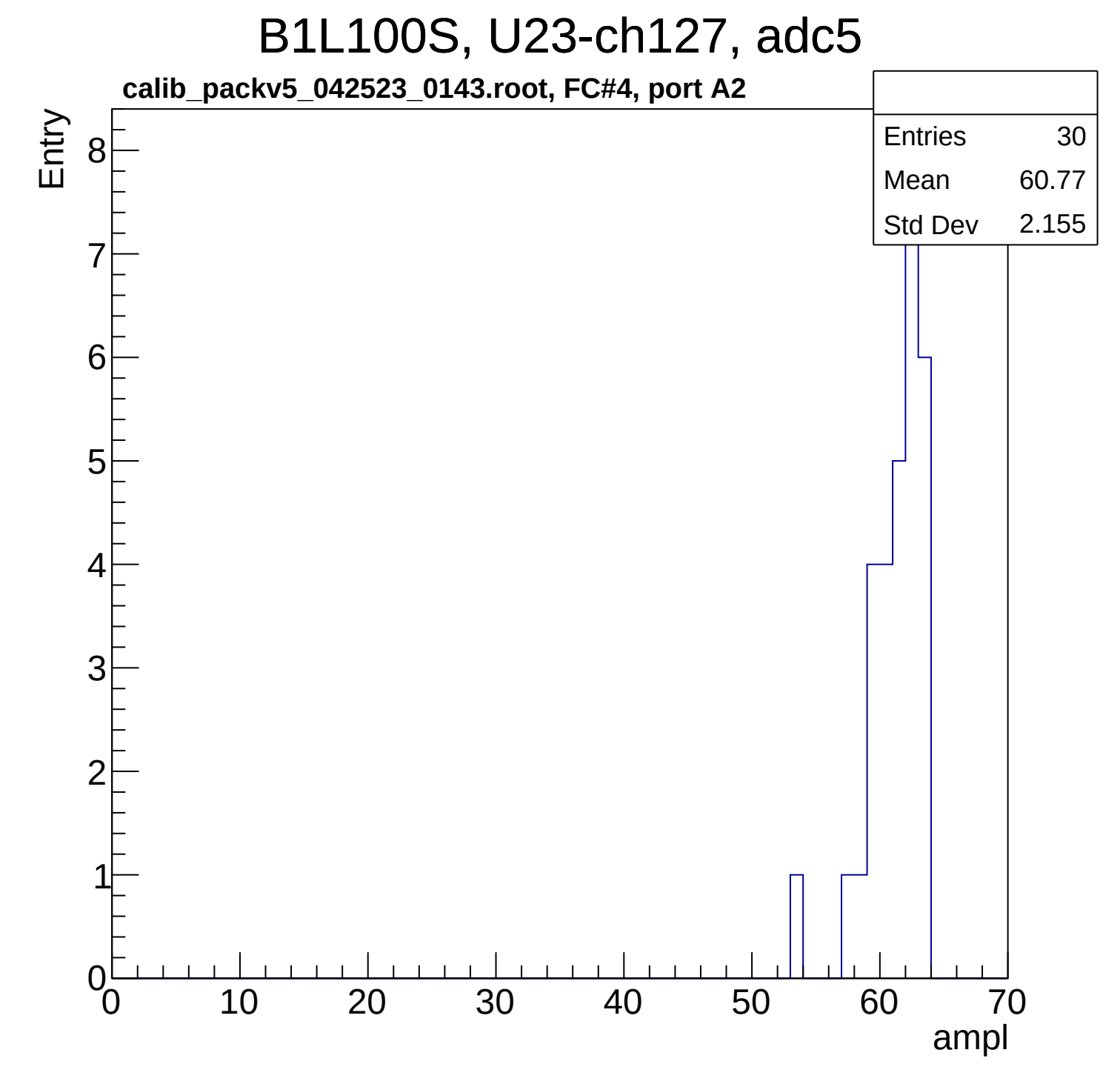
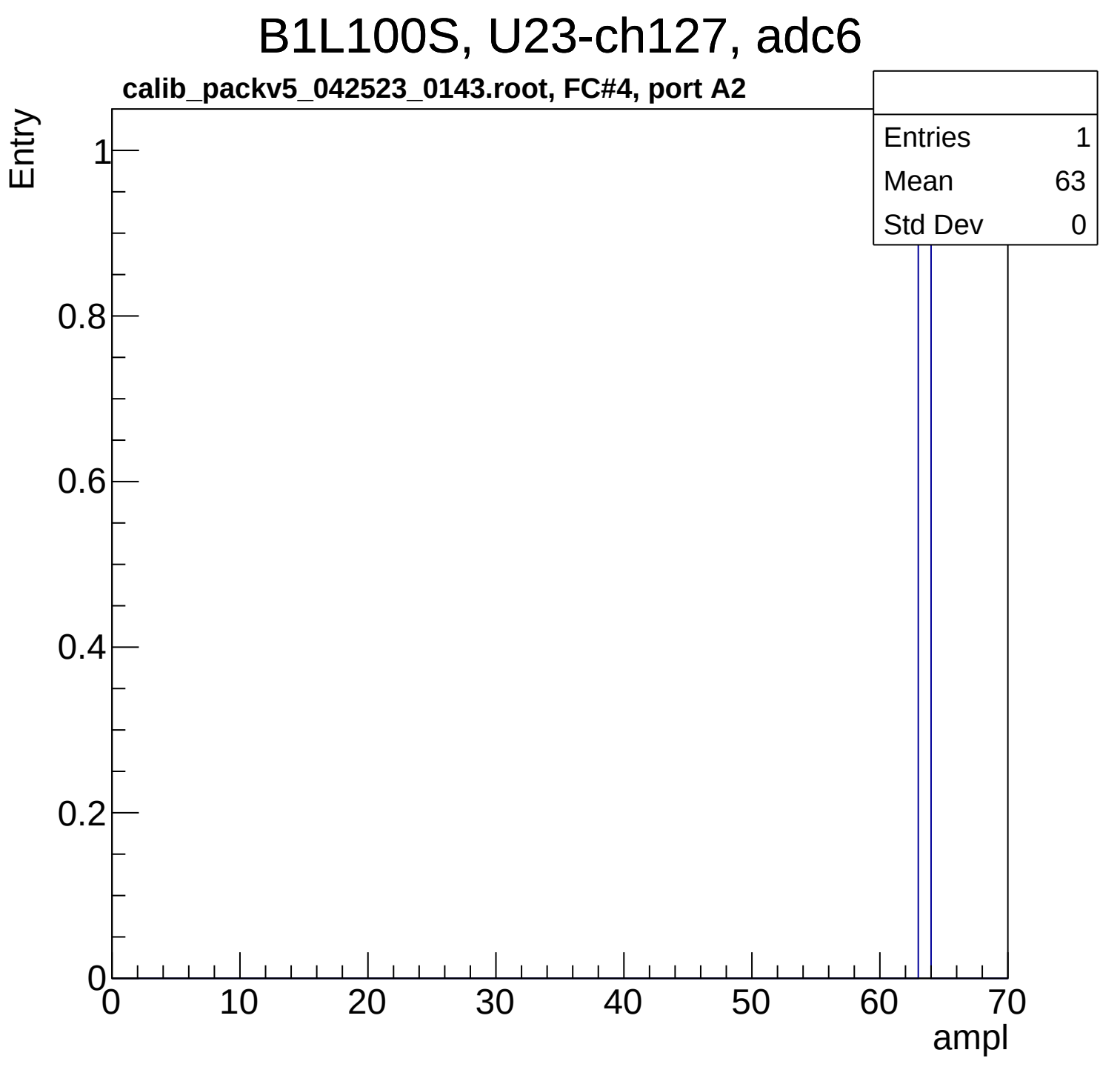
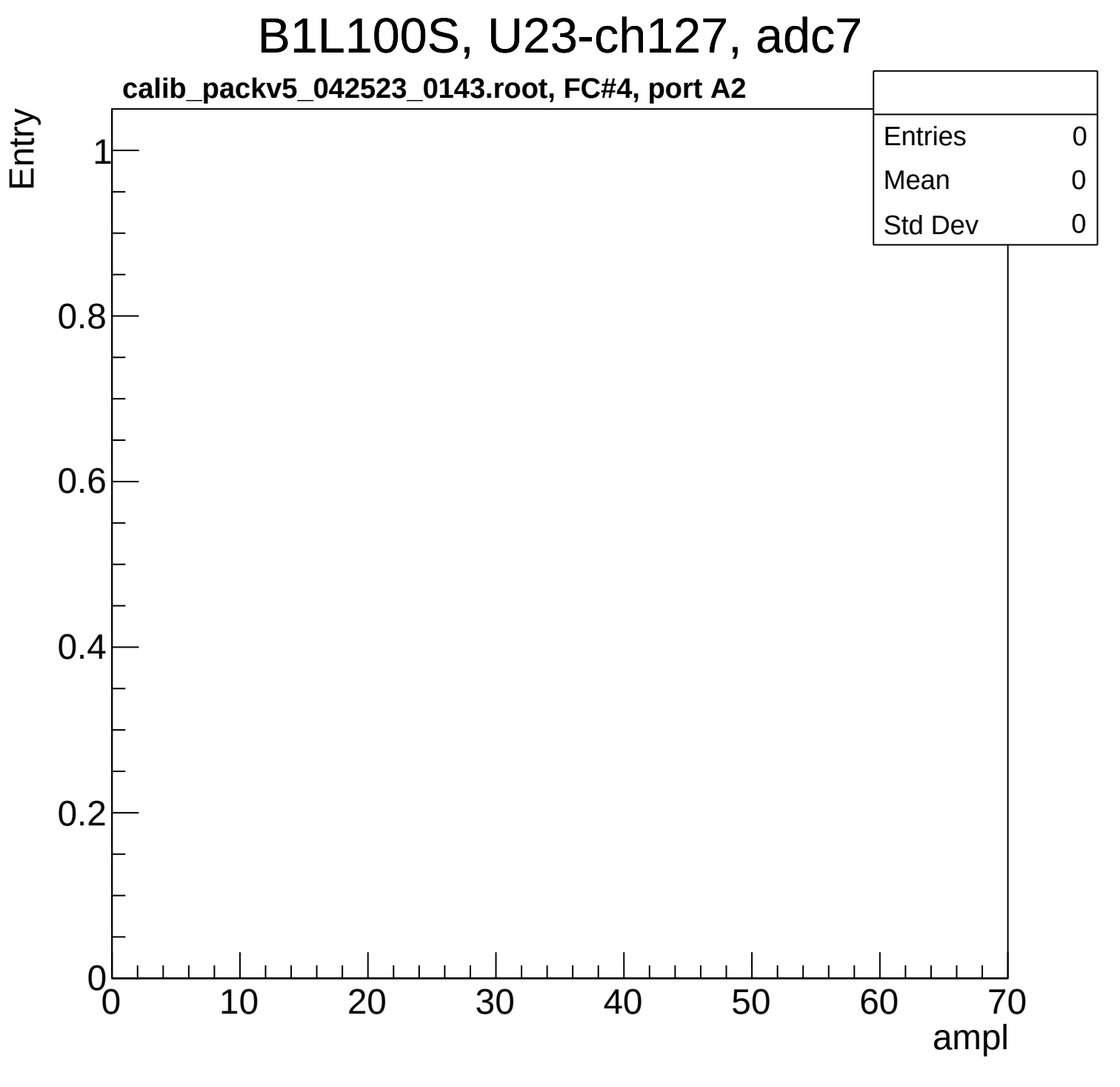
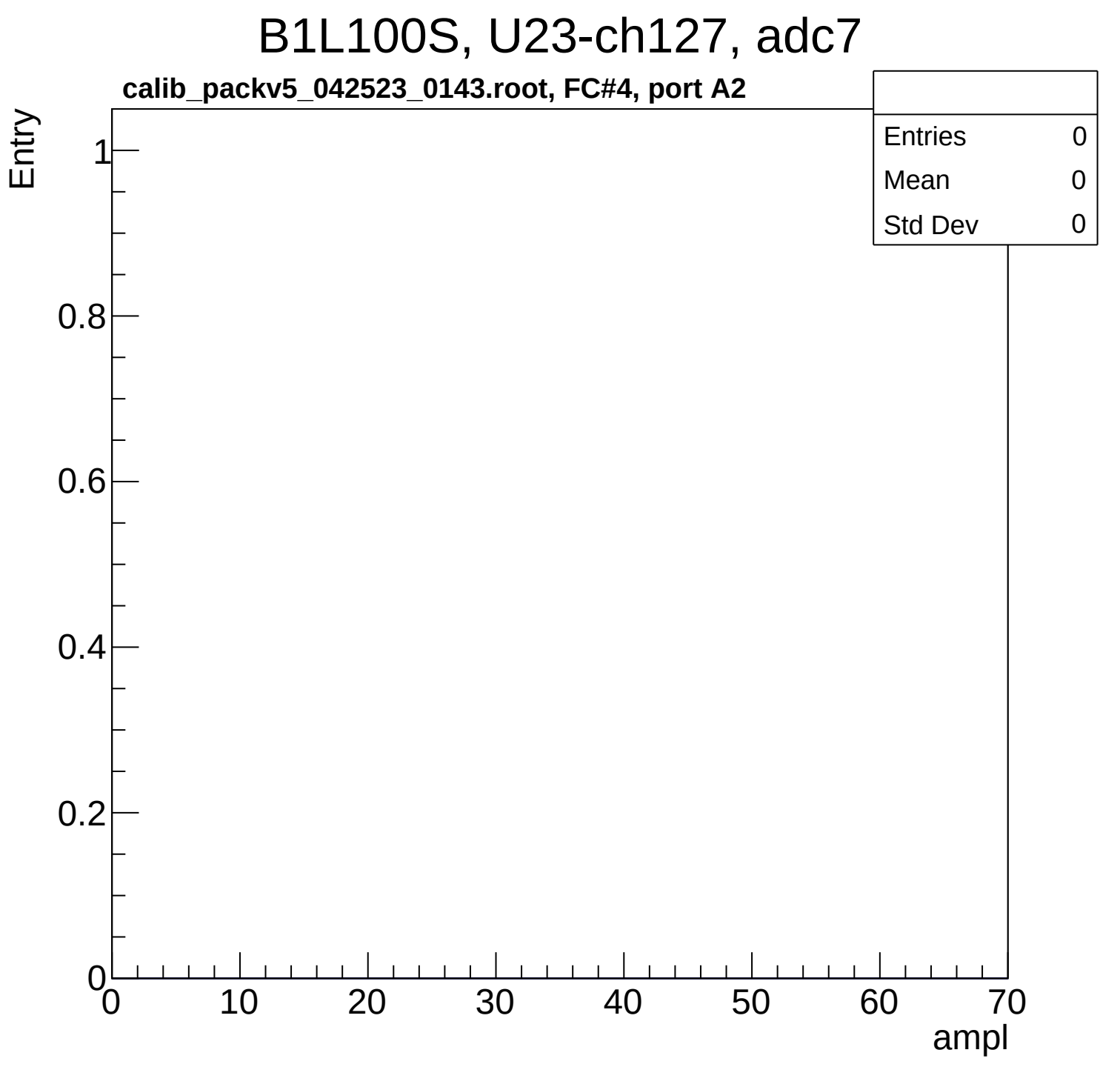
0
10
20
30
40
50
60
70


B1L100S, U23-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.34
Std Dev 8.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U23-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 60.77
Std Dev 2.155
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U23-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl