

B0L000S, U16-ch0

calib_packv5_042523_0143.root, FC#5, port B1

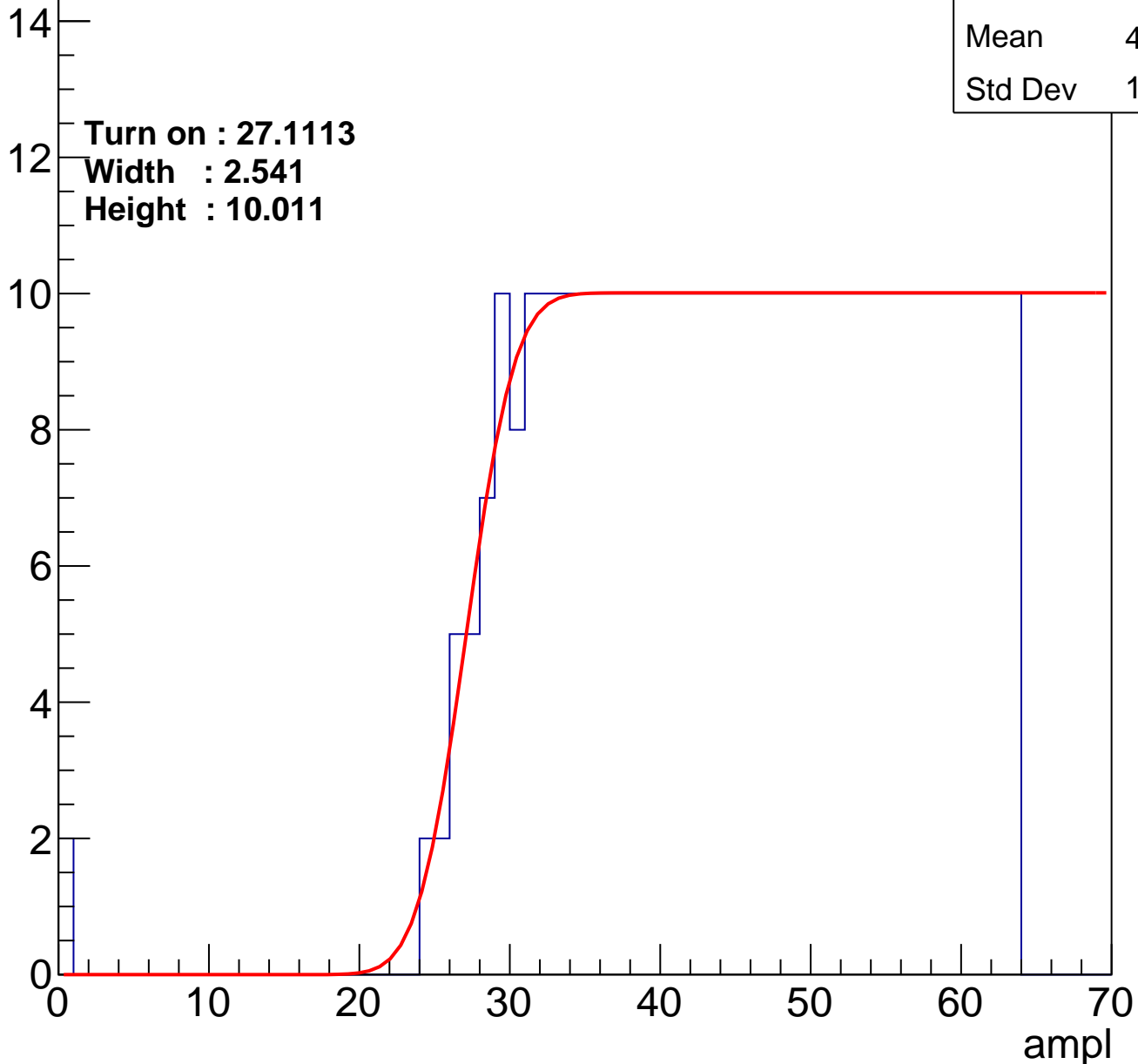
Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.1113

Width : 2.541

Height : 10.011

Entry



B0L000S, U16-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.7643

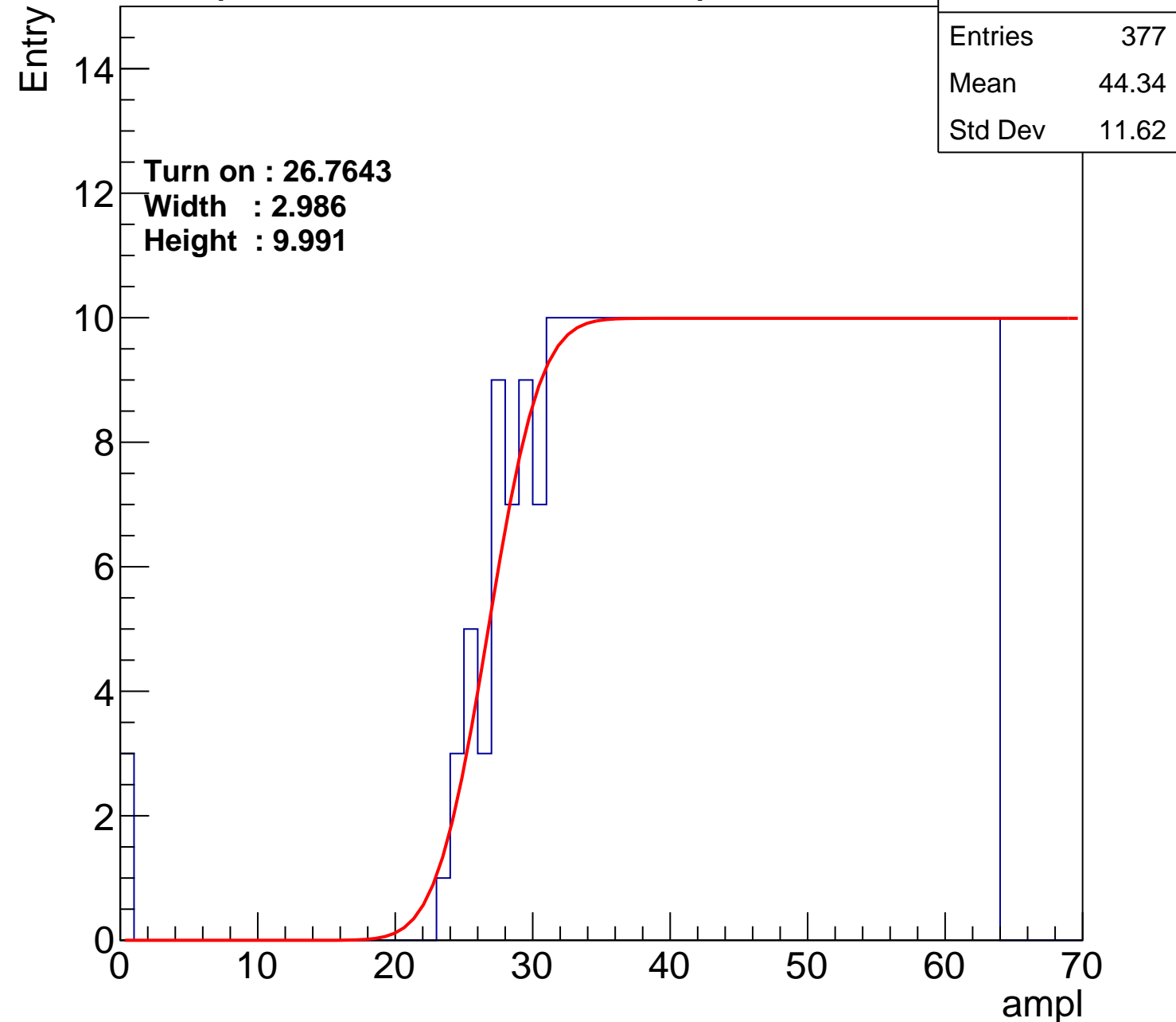
Width : 2.986

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.43
Std Dev	10.93

Turn on : 28.7854

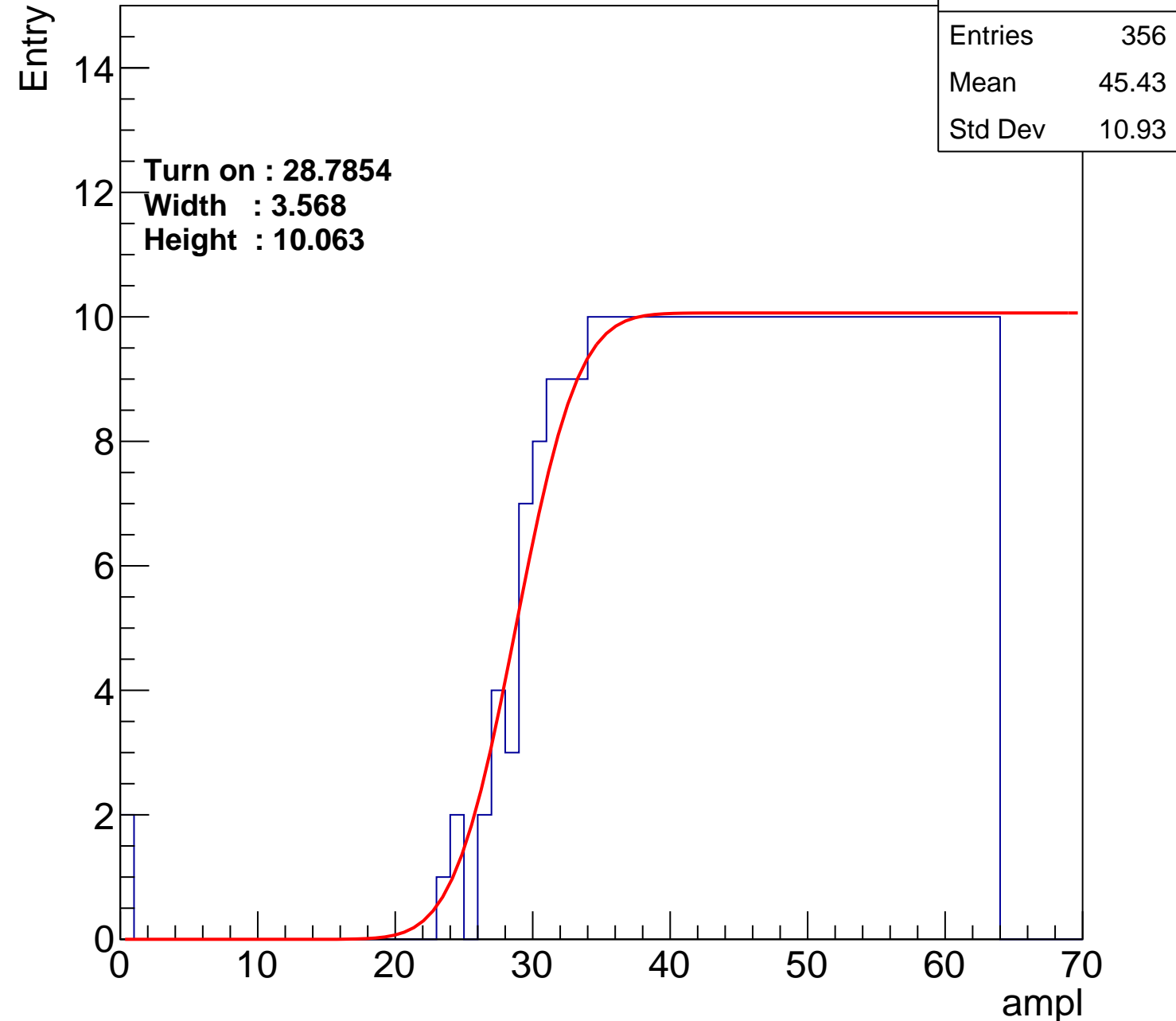
Width : 3.568

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.3147

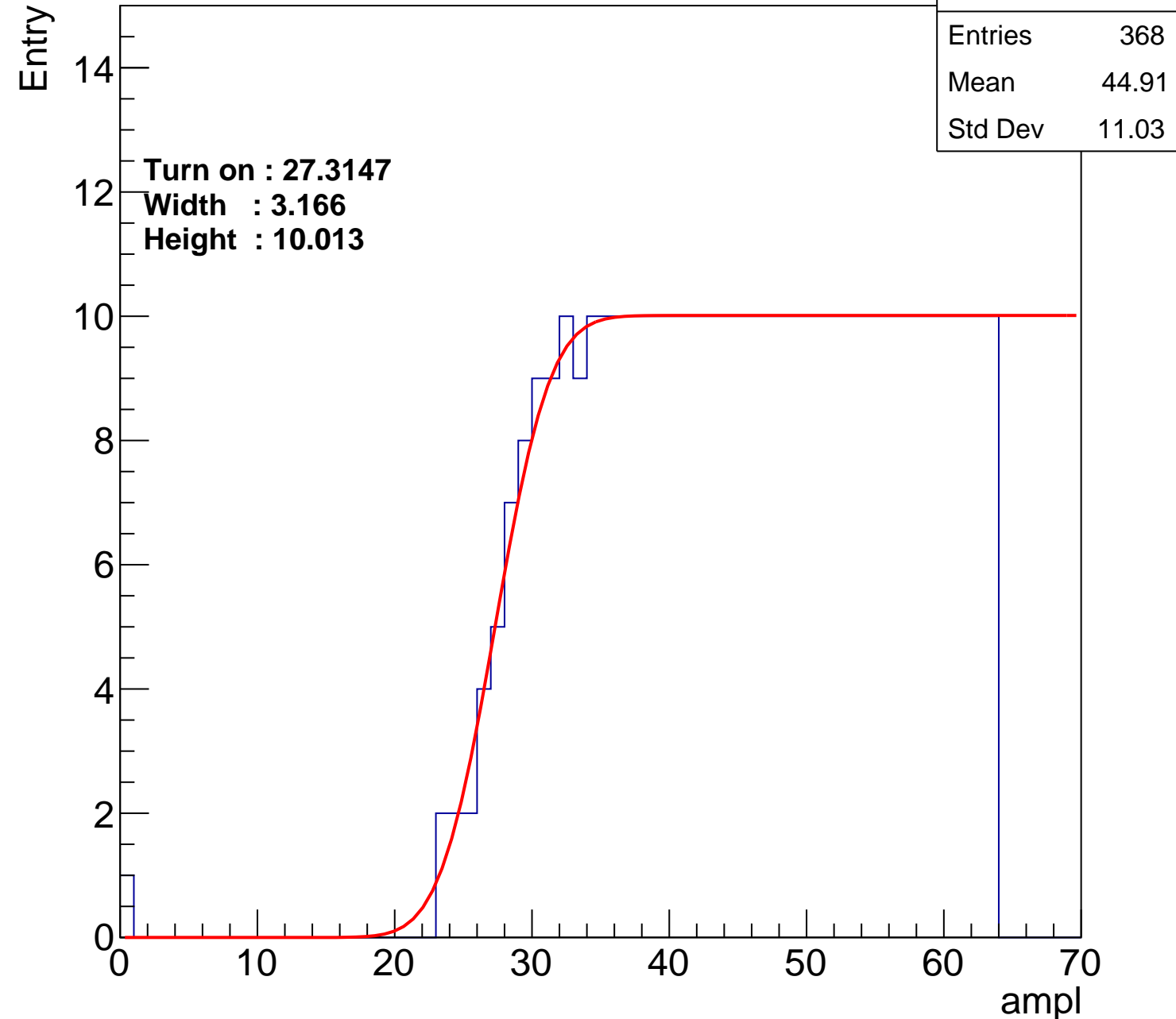
Width : 3.166

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	399
Mean	43.27
Std Dev	12.15

Turn on : 24.6956

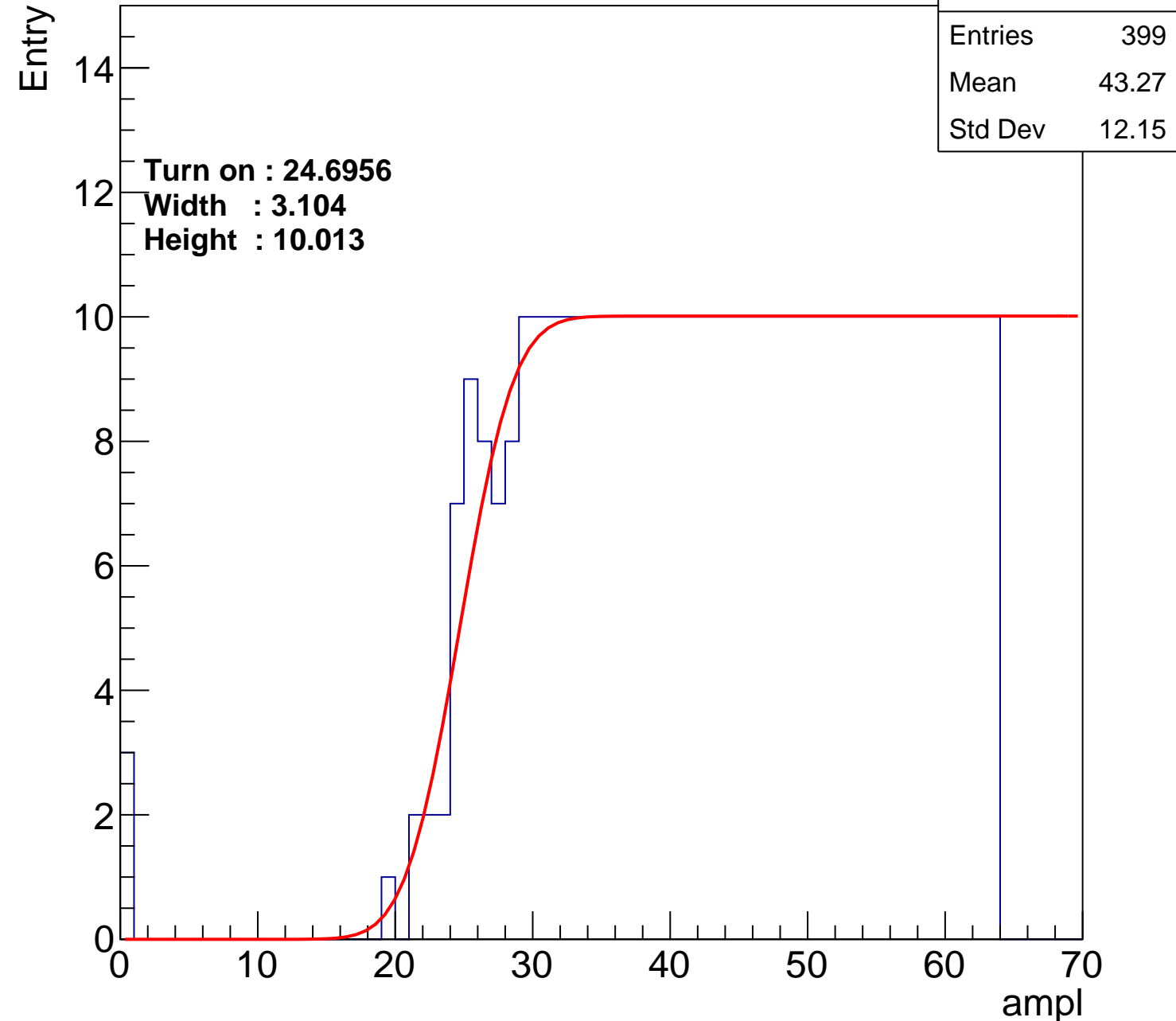
Width : 3.104

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch5

calib_packv5_042523_0143.root, FC#5, port B1

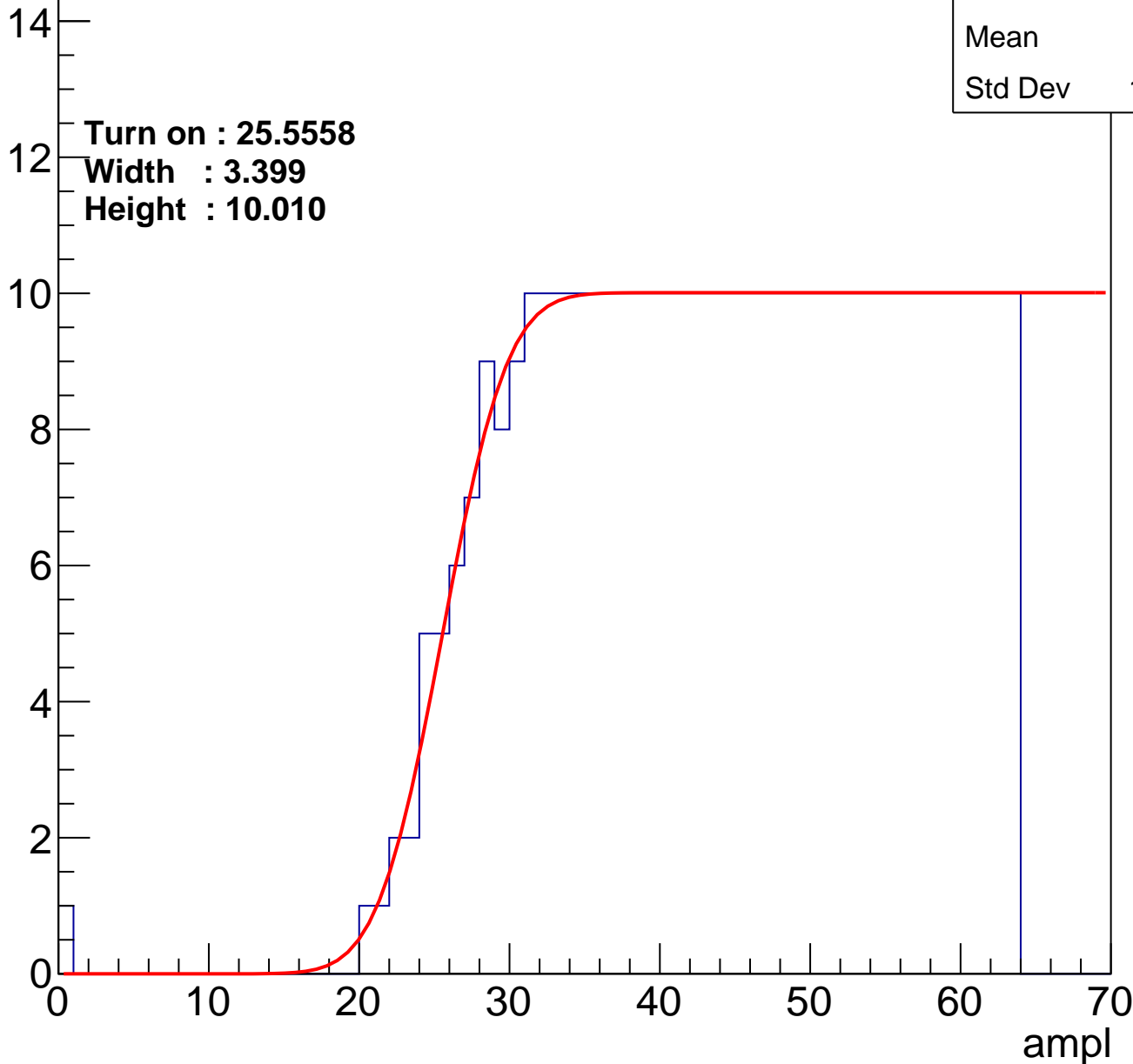
Entries	386
Mean	44
Std Dev	11.54

Turn on : 25.5558

Width : 3.399

Height : 10.010

Entry



B0L000S, U16-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.89
Std Dev	11.94

Turn on : 26.0428

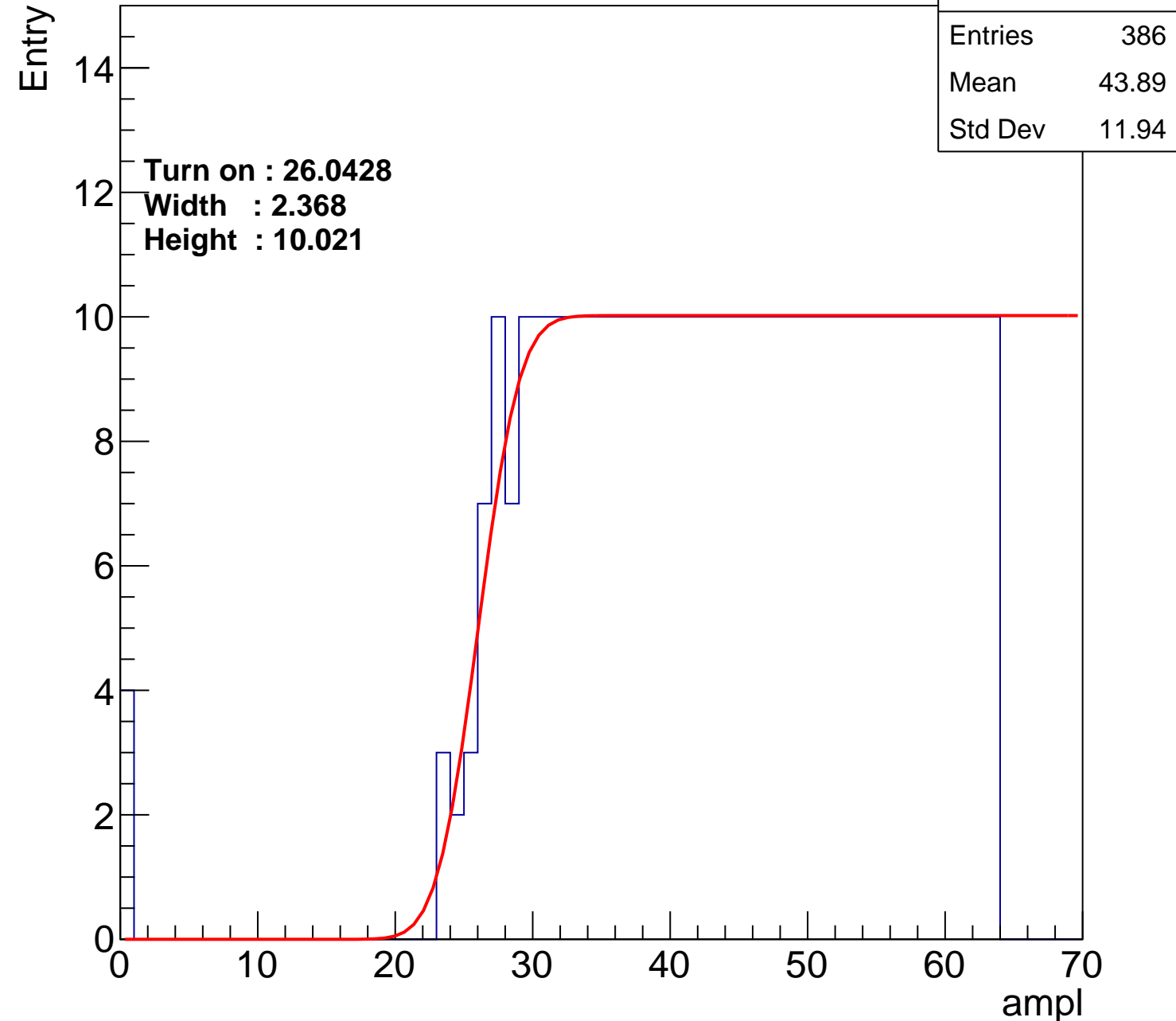
Width : 2.368

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.57
Std Dev	12.02

Turn on : 27.7131

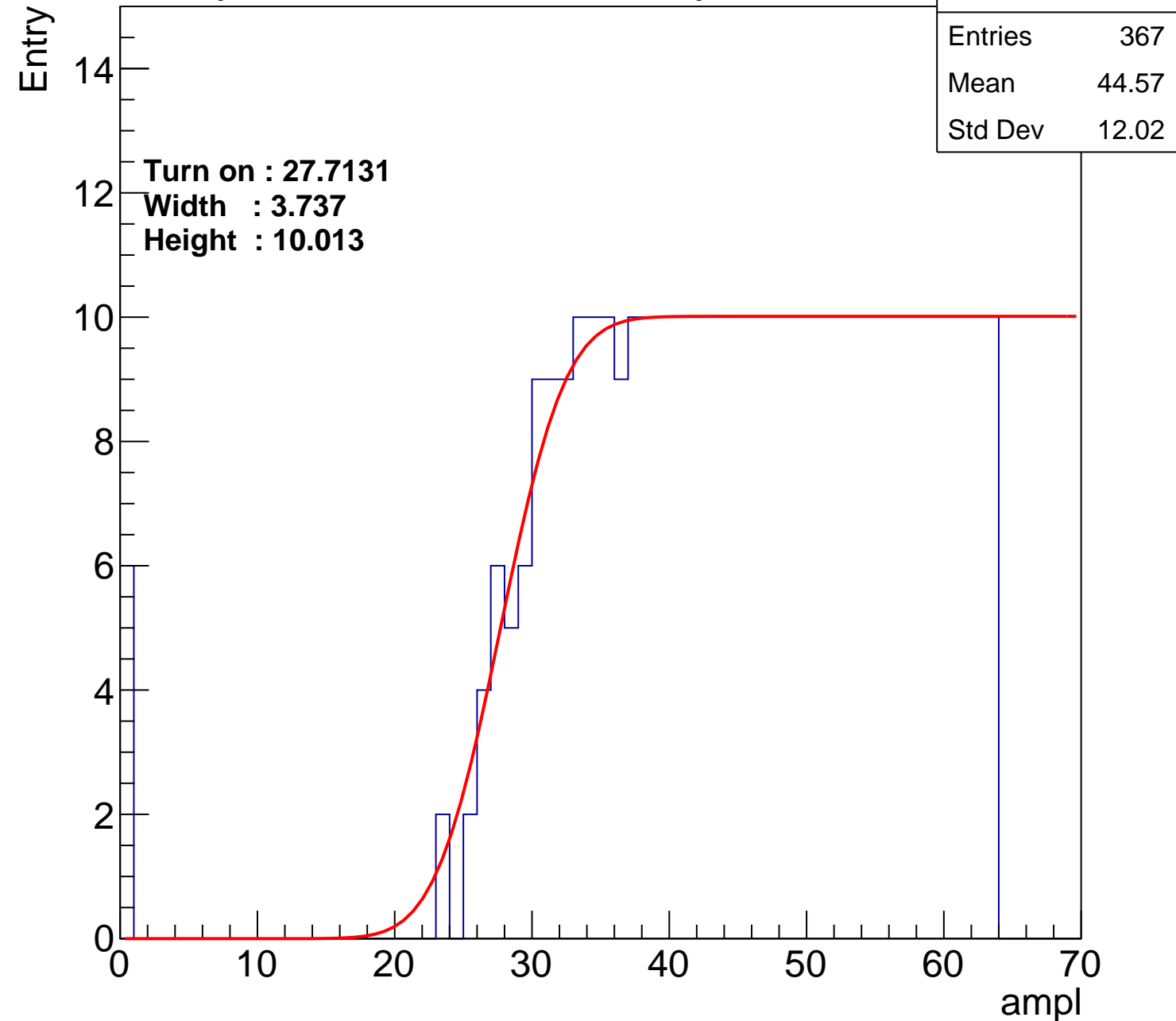
Width : 3.737

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch8

calib_packv5_042523_0143.root, FC#5, port B1

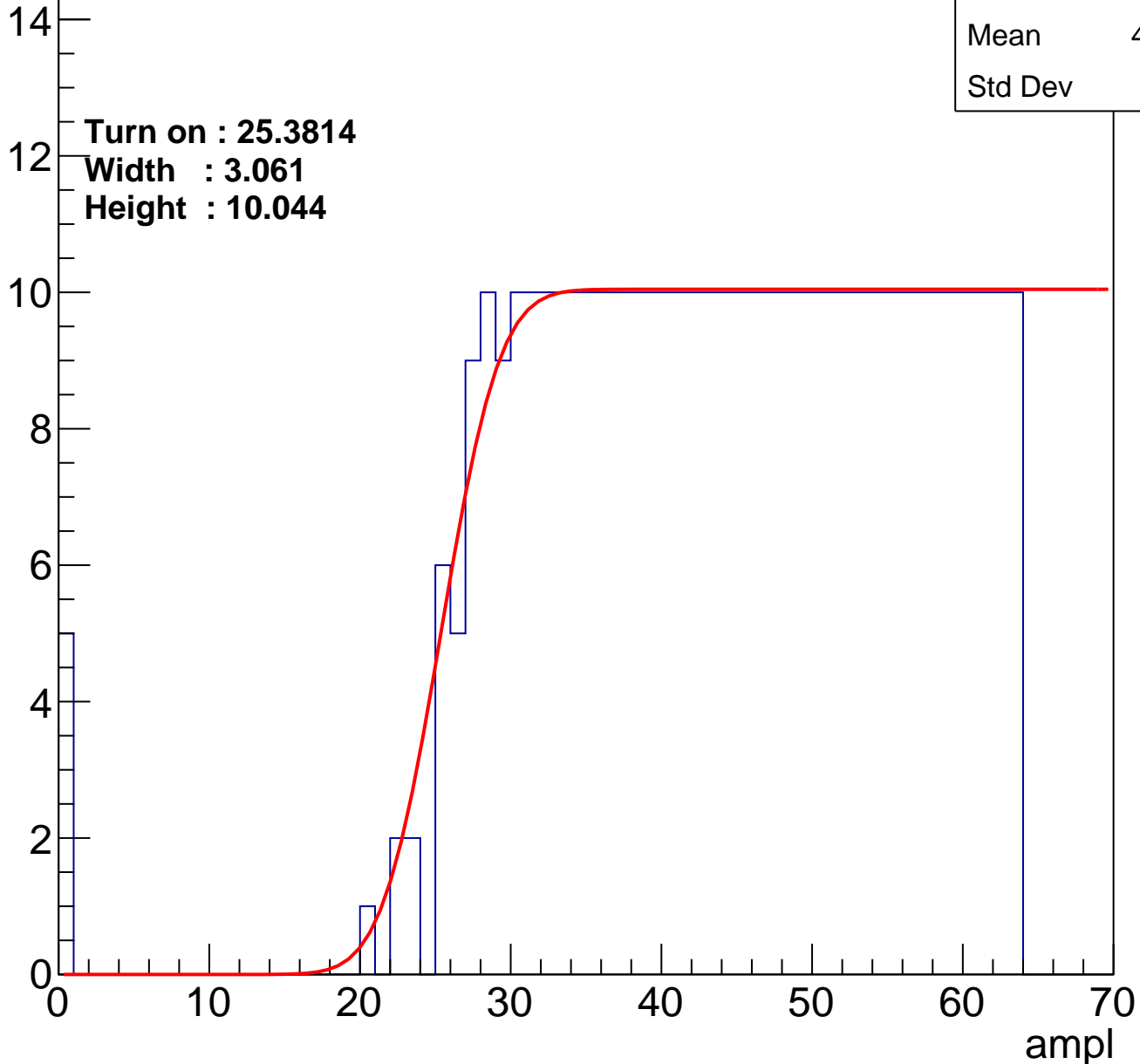
Entries	389
Mean	43.66
Std Dev	12.2

Turn on : 25.3814

Width : 3.061

Height : 10.044

Entry



B0L000S, U16-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.1946

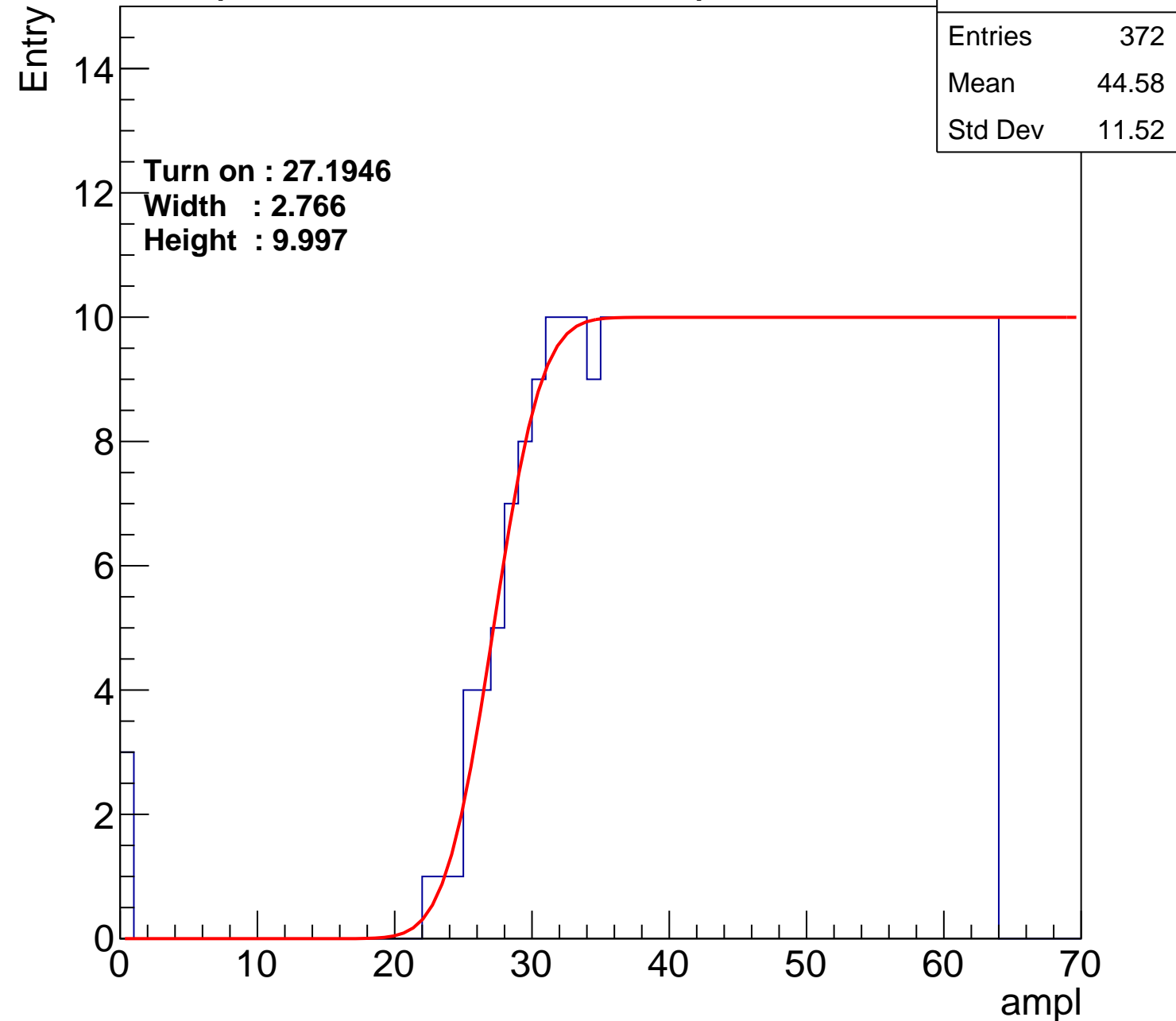
Width : 2.766

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.03
Std Dev	11.71

Turn on : 26.0573

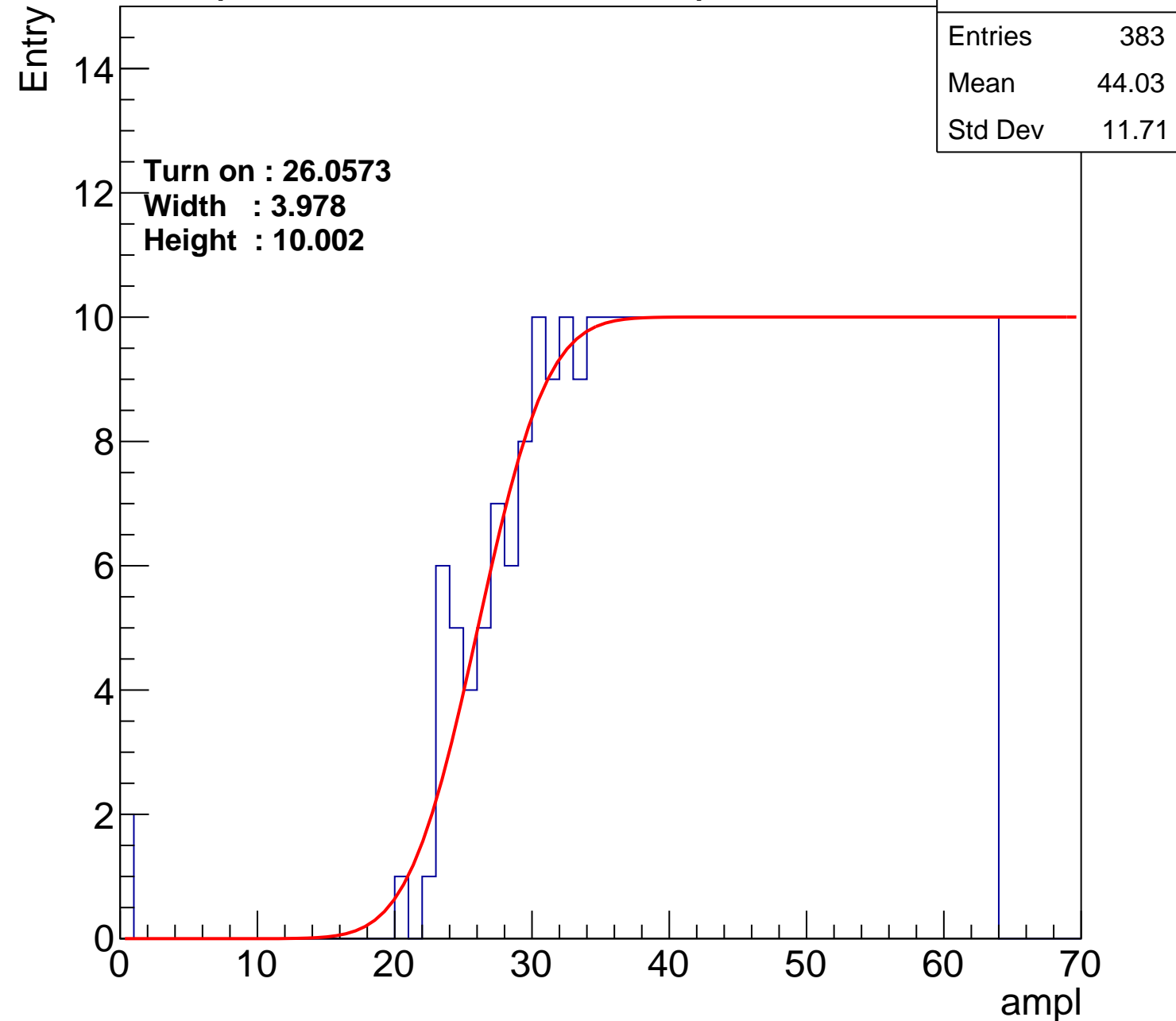
Width : 3.978

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 25.6413

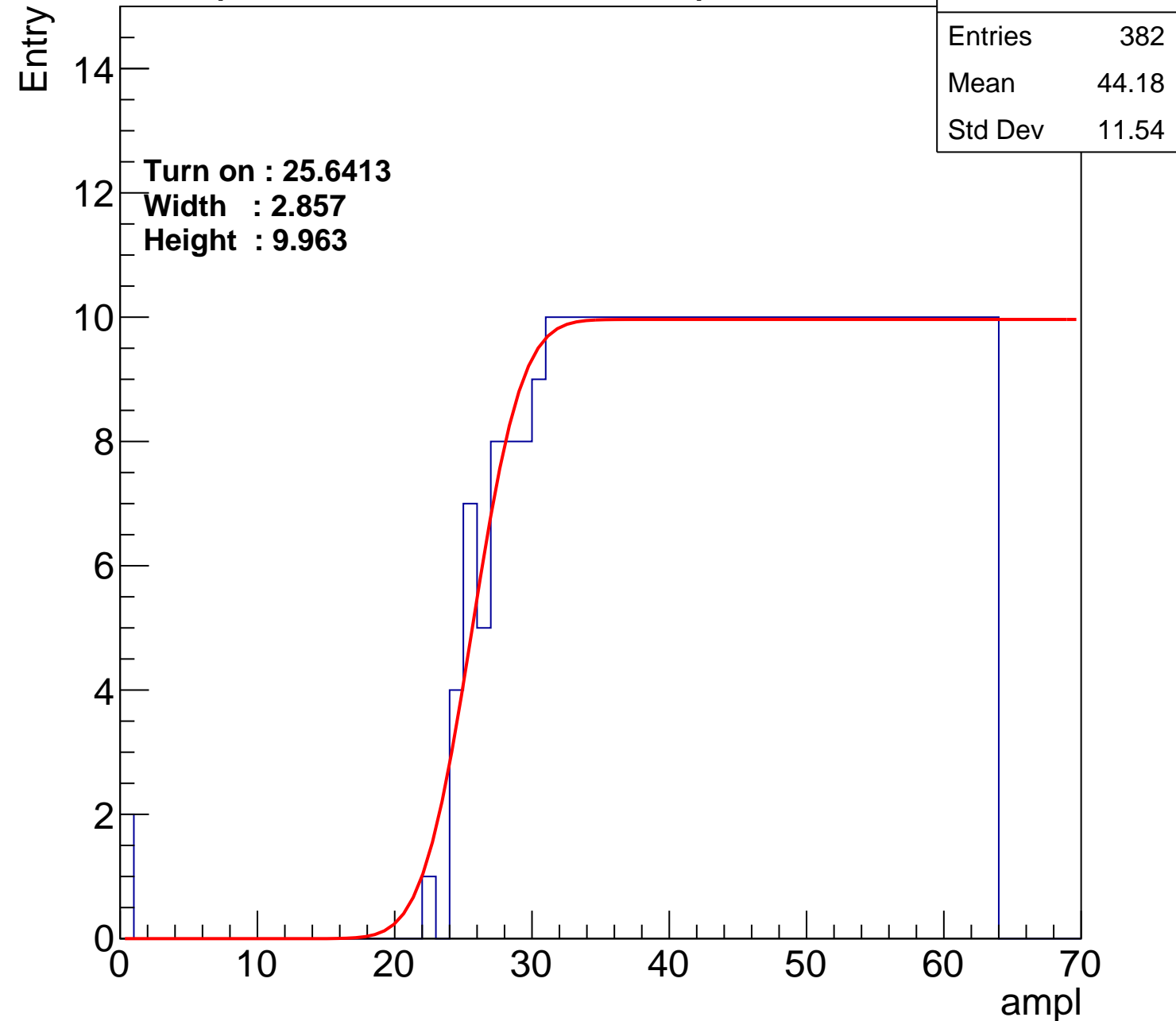
Width : 2.857

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.9
Std Dev	11.66

Turn on : 25.7491

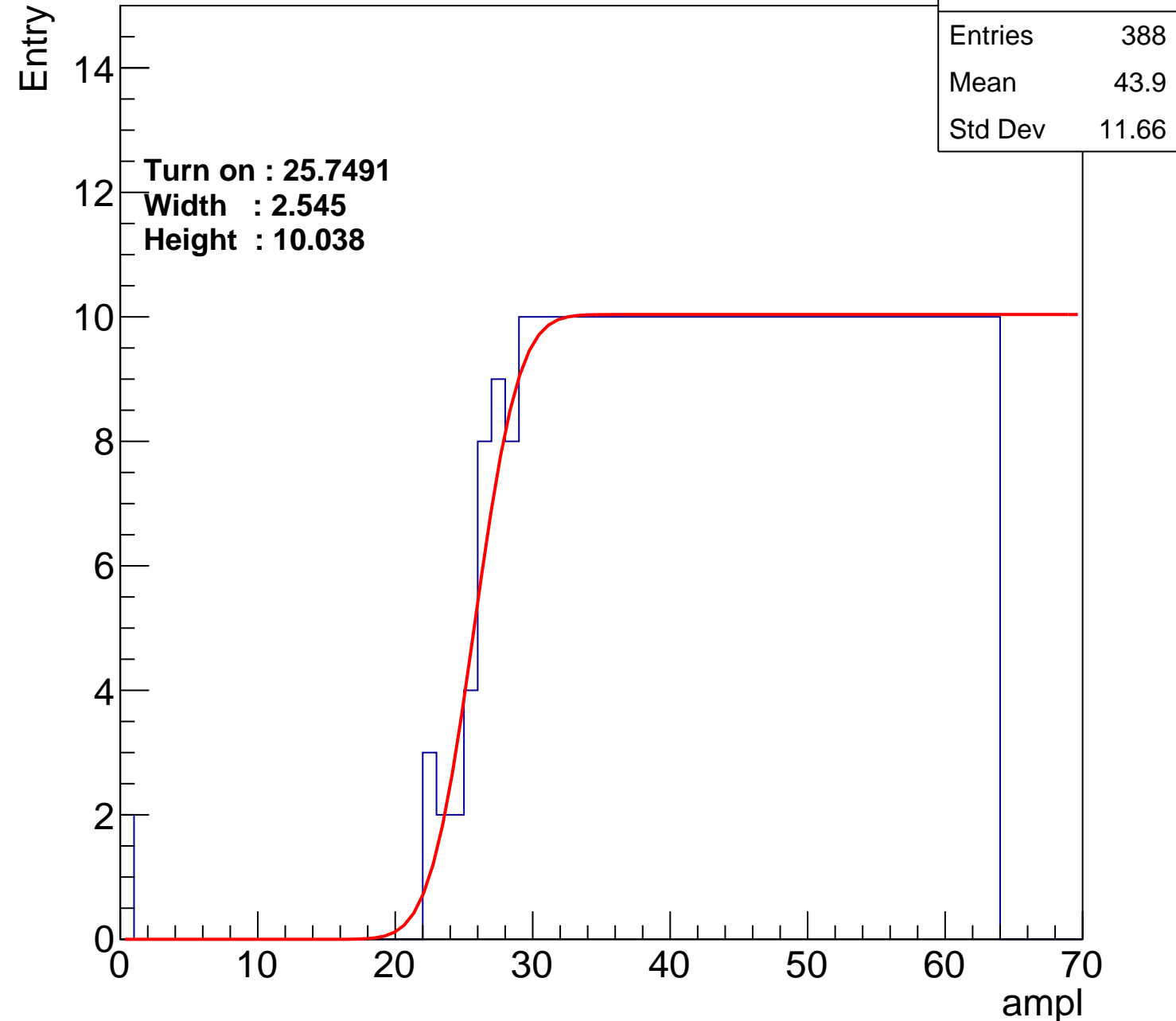
Width : 2.545

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.63
Std Dev	11.46

Turn on : 28.0568

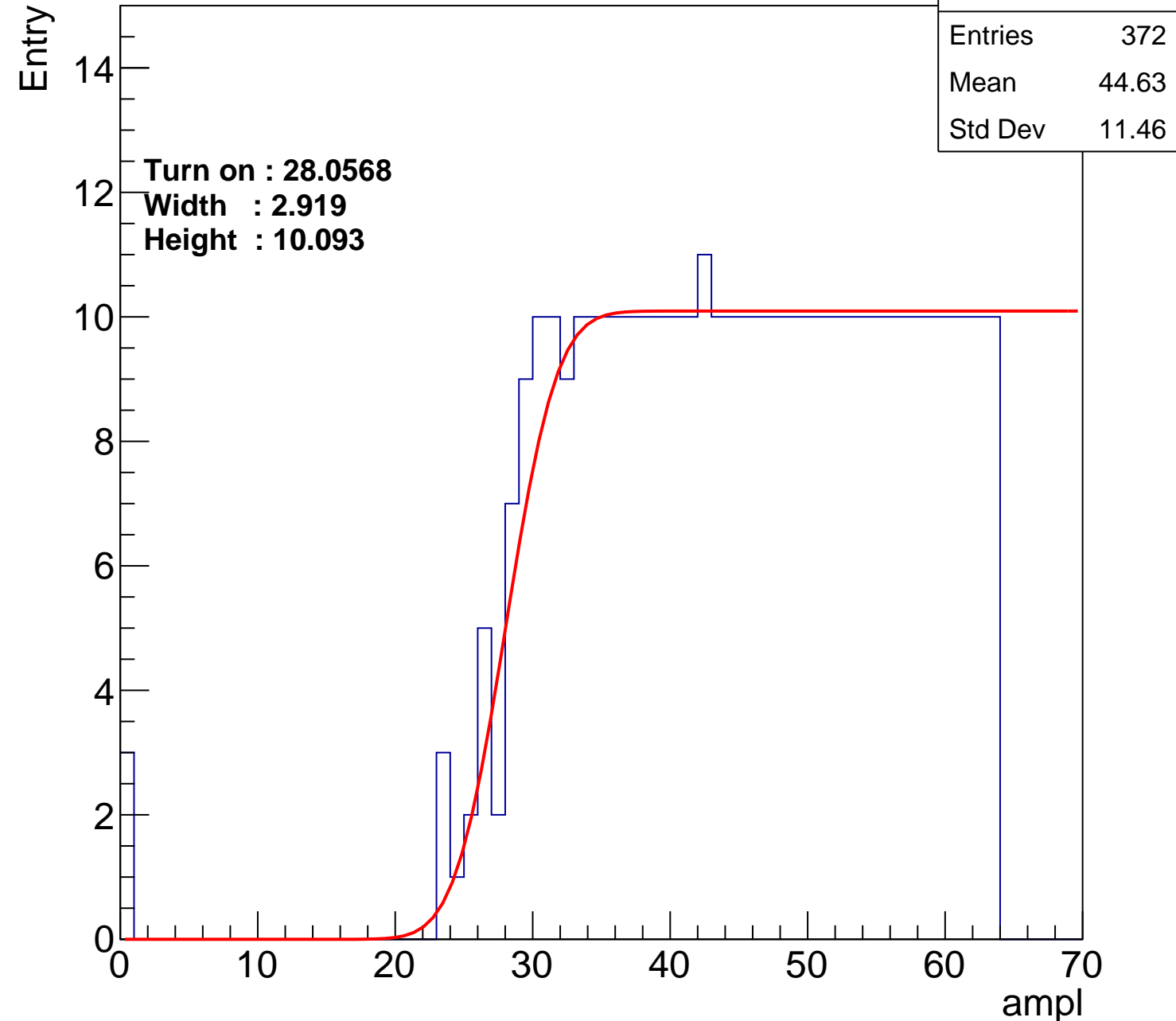
Width : 2.919

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch14

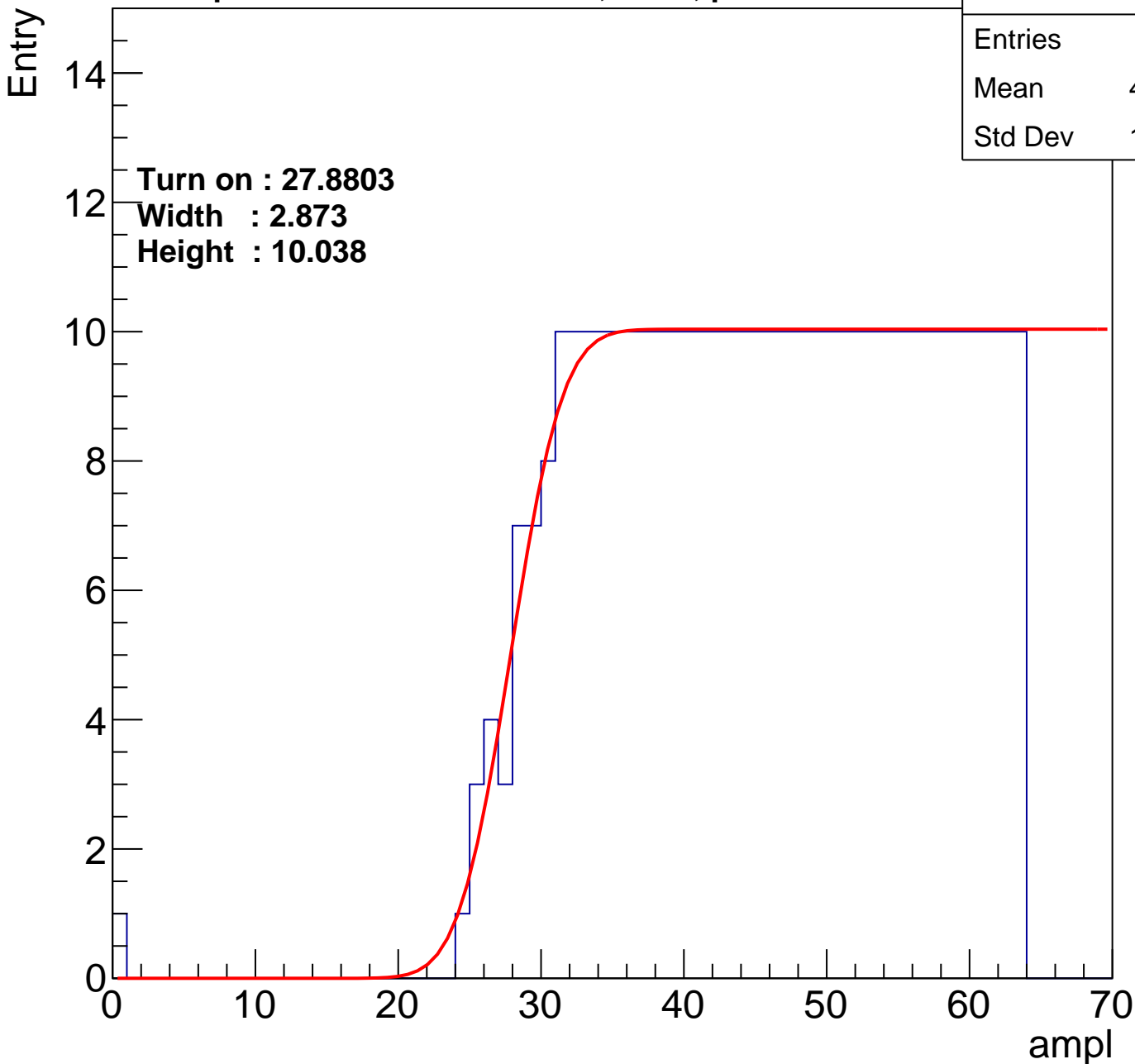
calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.15
Std Dev	10.86

Turn on : 27.8803

Width : 2.873

Height : 10.038



B0L000S, U16-ch15

calib_packv5_042523_0143.root, FC#5, port B1

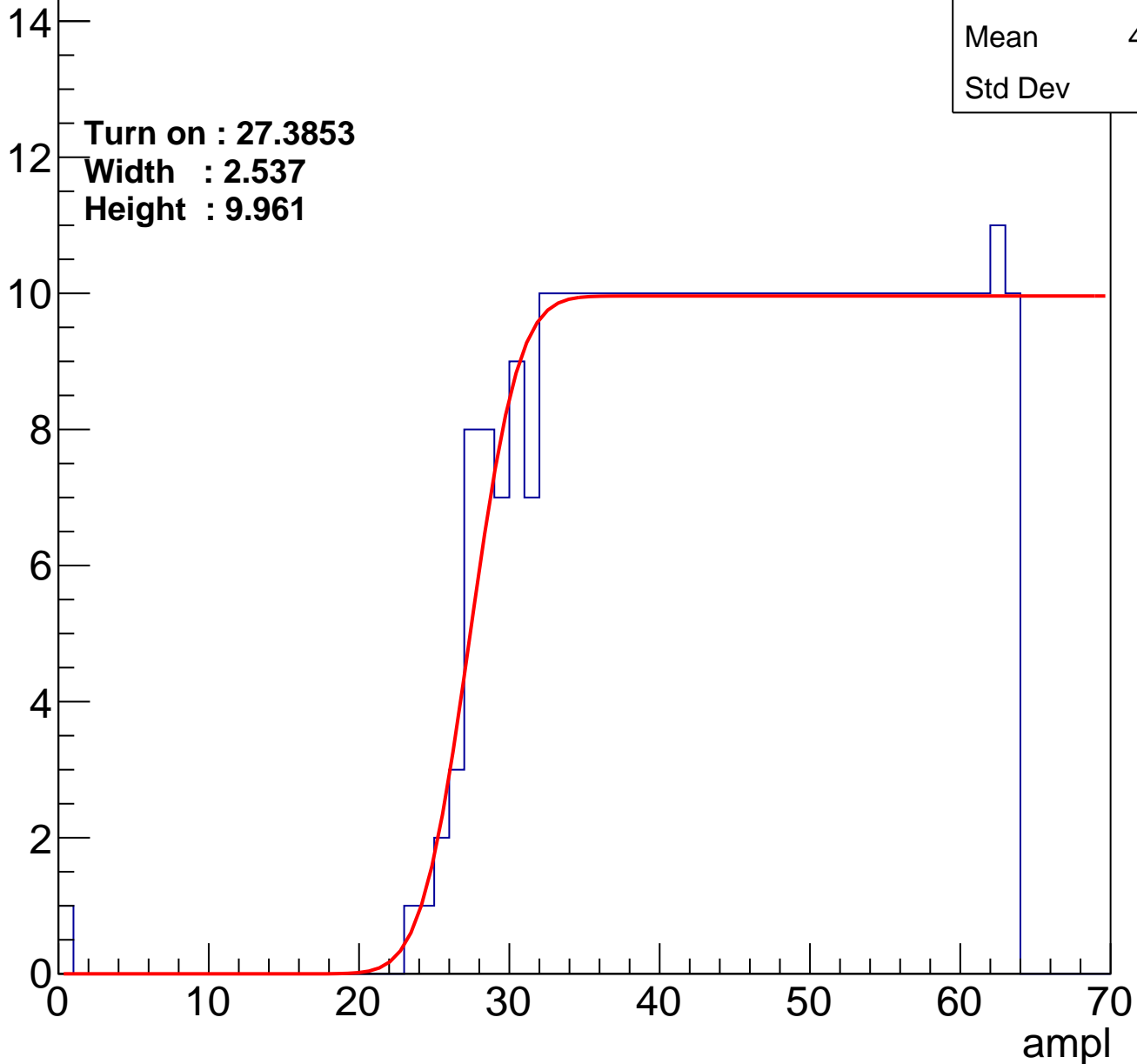
Entries	368
Mean	45.02
Std Dev	11

Turn on : 27.3853

Width : 2.537

Height : 9.961

Entry



B0L000S, U16-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.44
Std Dev	11.7

Turn on : 26.9210

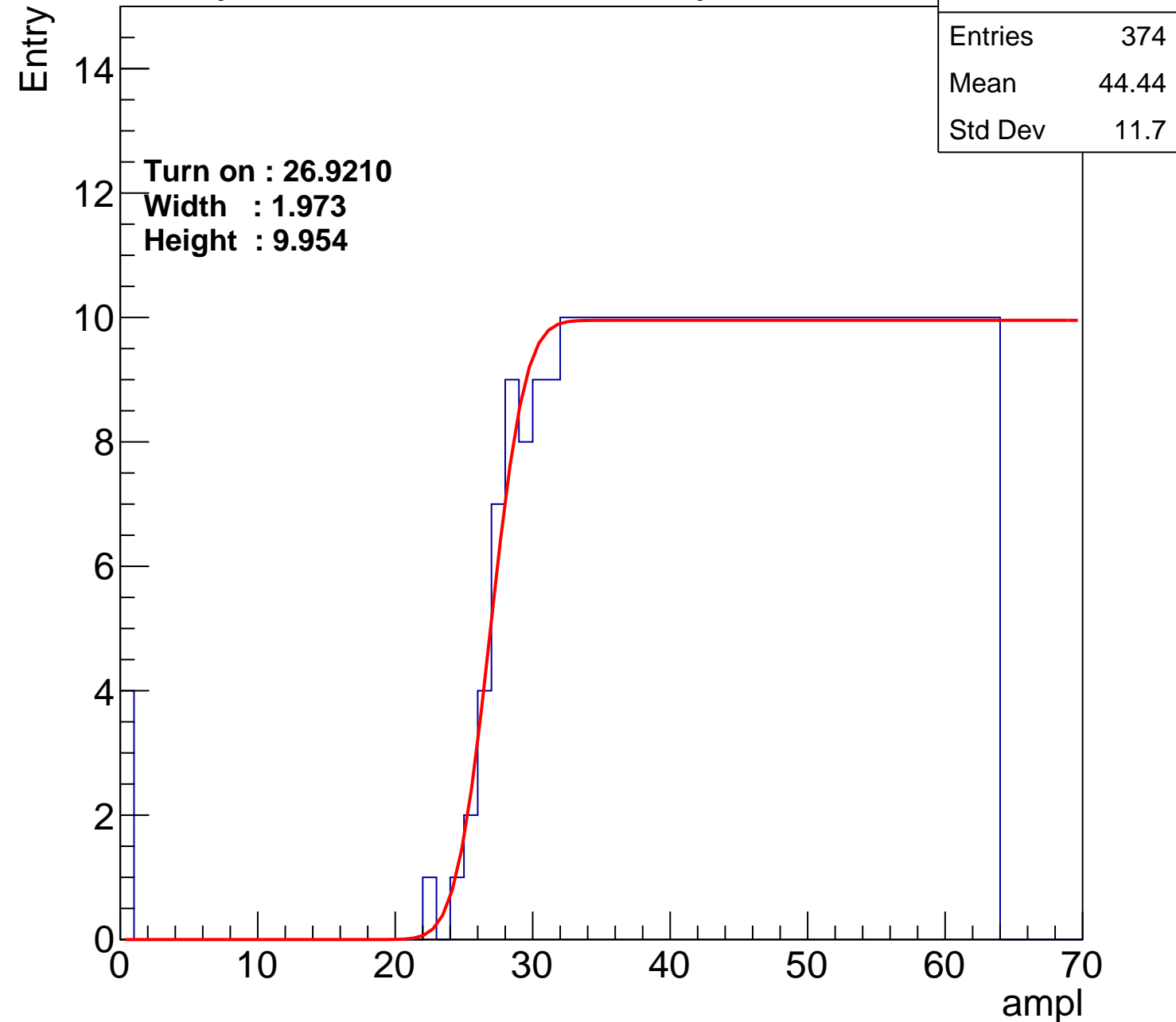
Width : 1.973

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch17

calib_packv5_042523_0143.root, FC#5, port B1

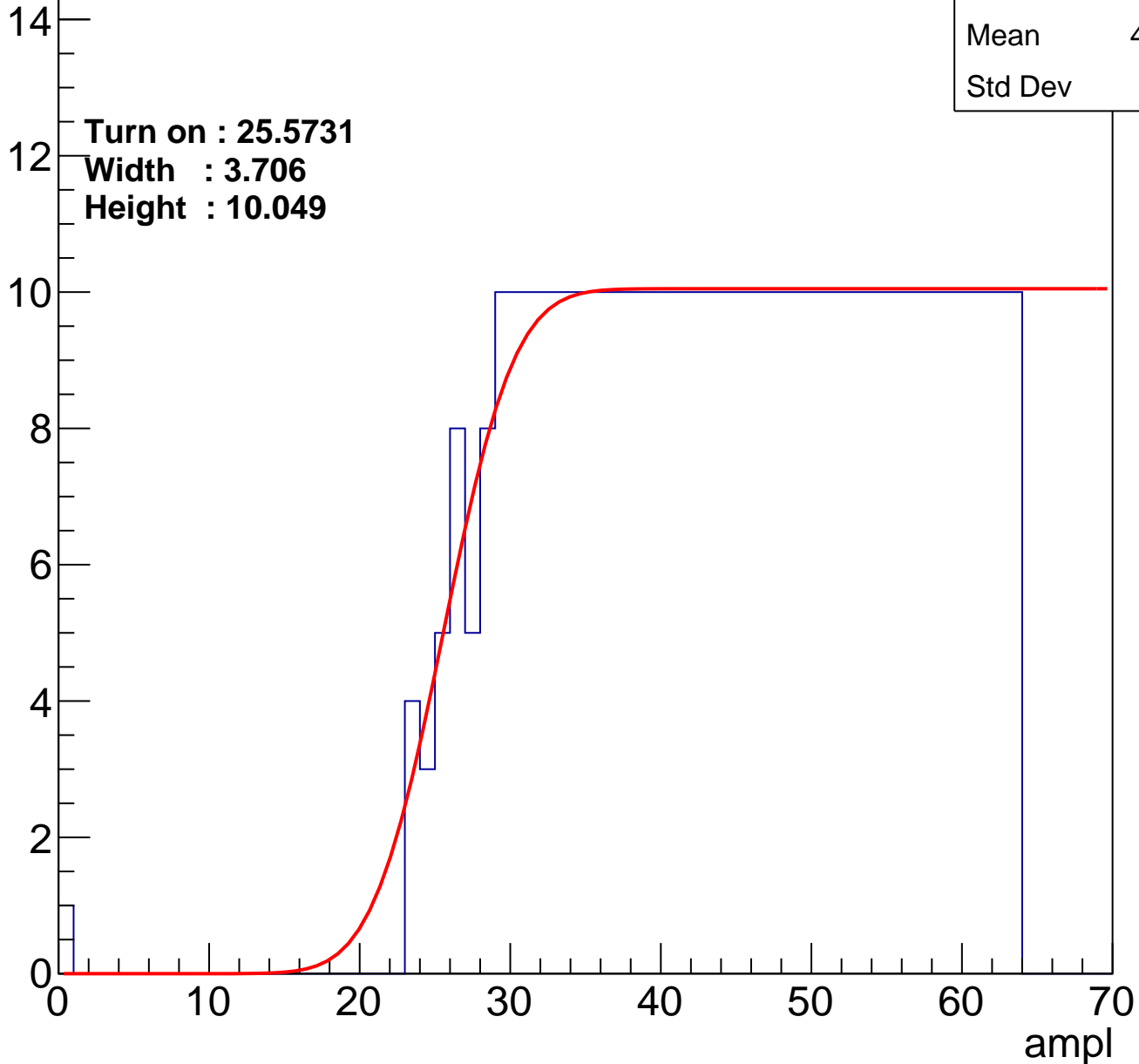
Entries	384
Mean	44.16
Std Dev	11.4

Turn on : 25.5731

Width : 3.706

Height : 10.049

Entry



B0L000S, U16-ch18

calib_packv5_042523_0143.root, FC#5, port B1

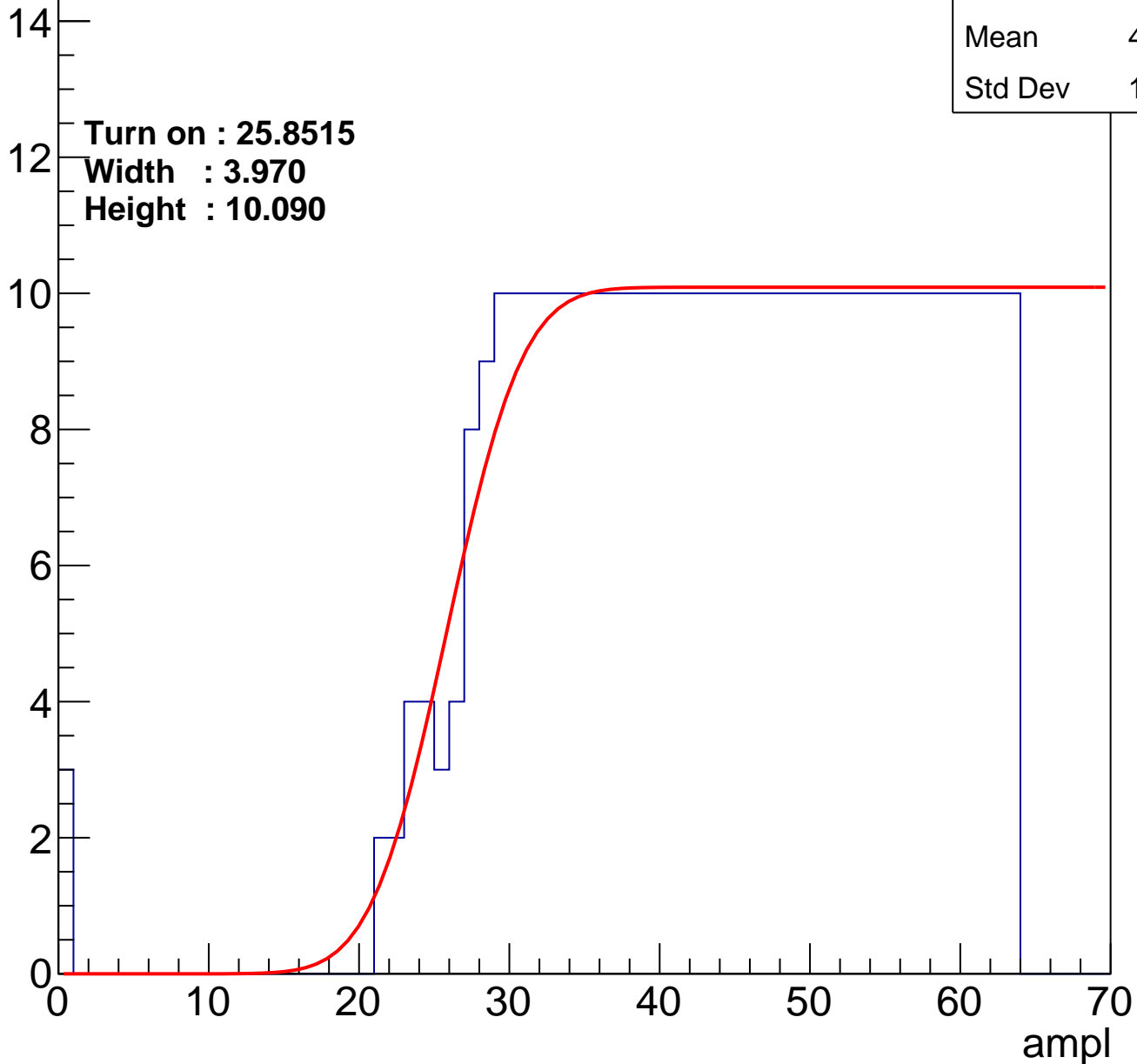
Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.8515

Width : 3.970

Height : 10.090

Entry



B0L000S, U16-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	401
Mean	43.19
Std Dev	12.17

Turn on : 24.0979

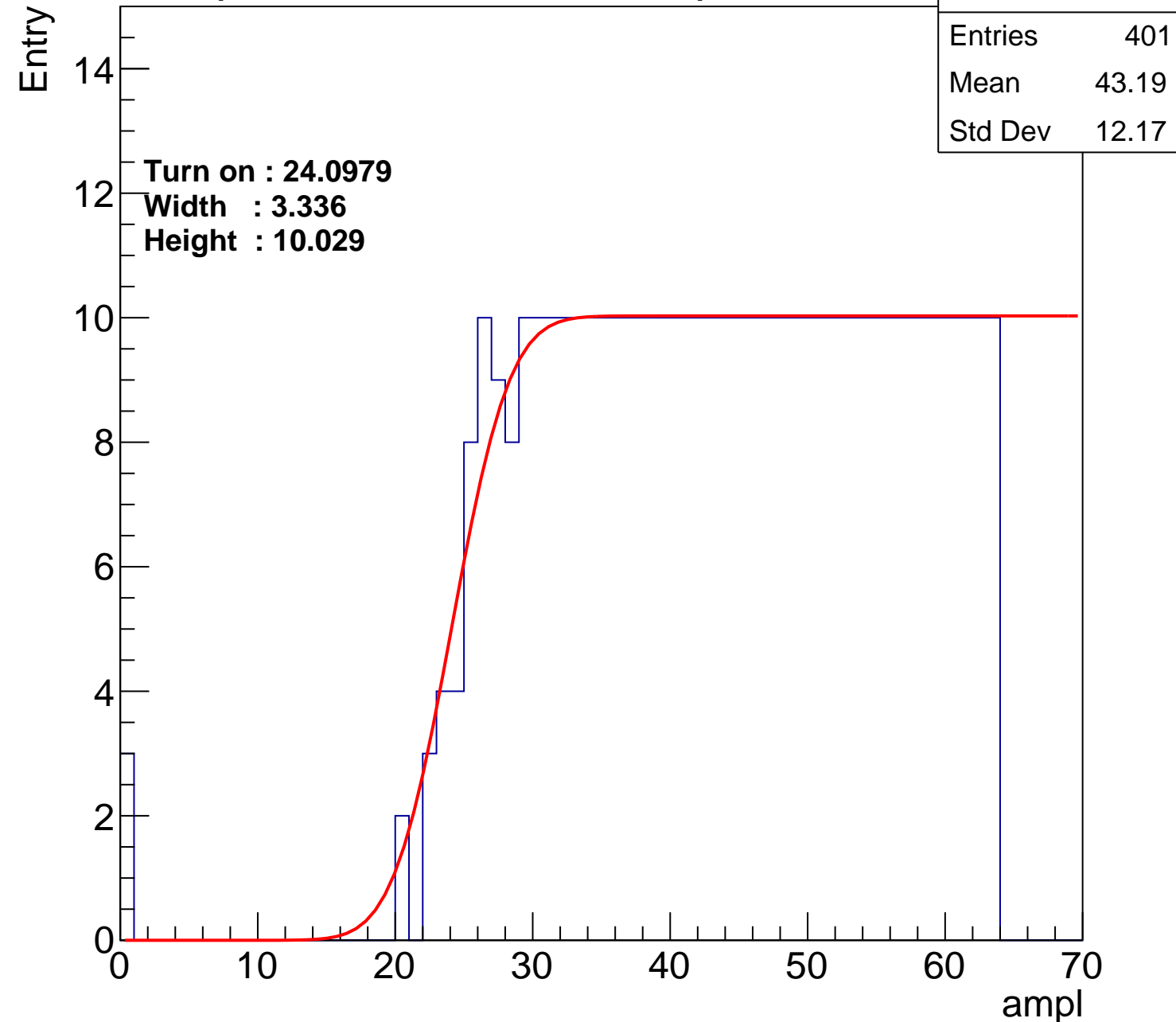
Width : 3.336

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch20

calib_packv5_042523_0143.root, FC#5, port B1

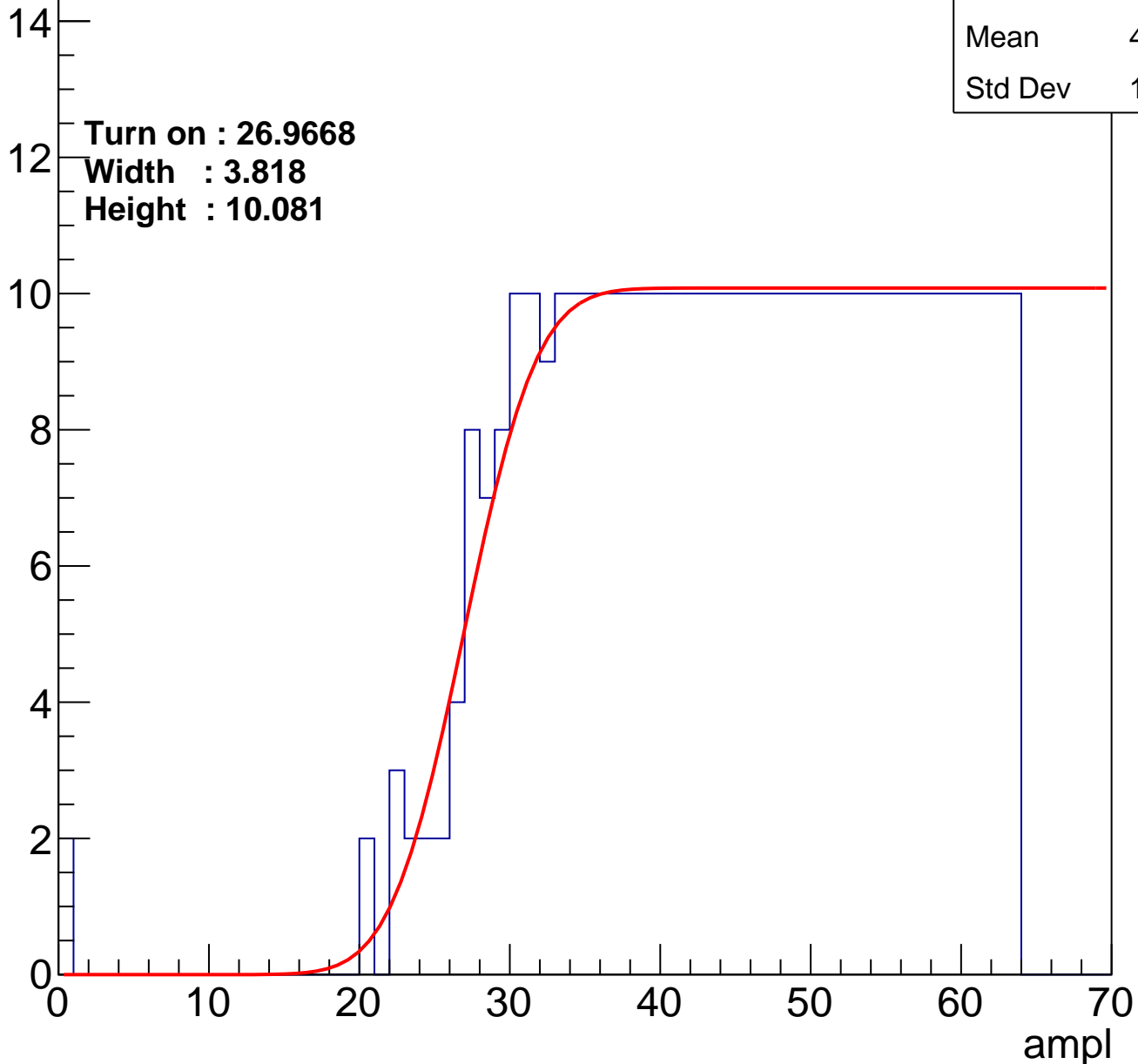
Entries	379
Mean	44.26
Std Dev	11.57

Turn on : 26.9668

Width : 3.818

Height : 10.081

Entry



B0L000S, U16-ch21

calib_packv5_042523_0143.root, FC#5, port B1

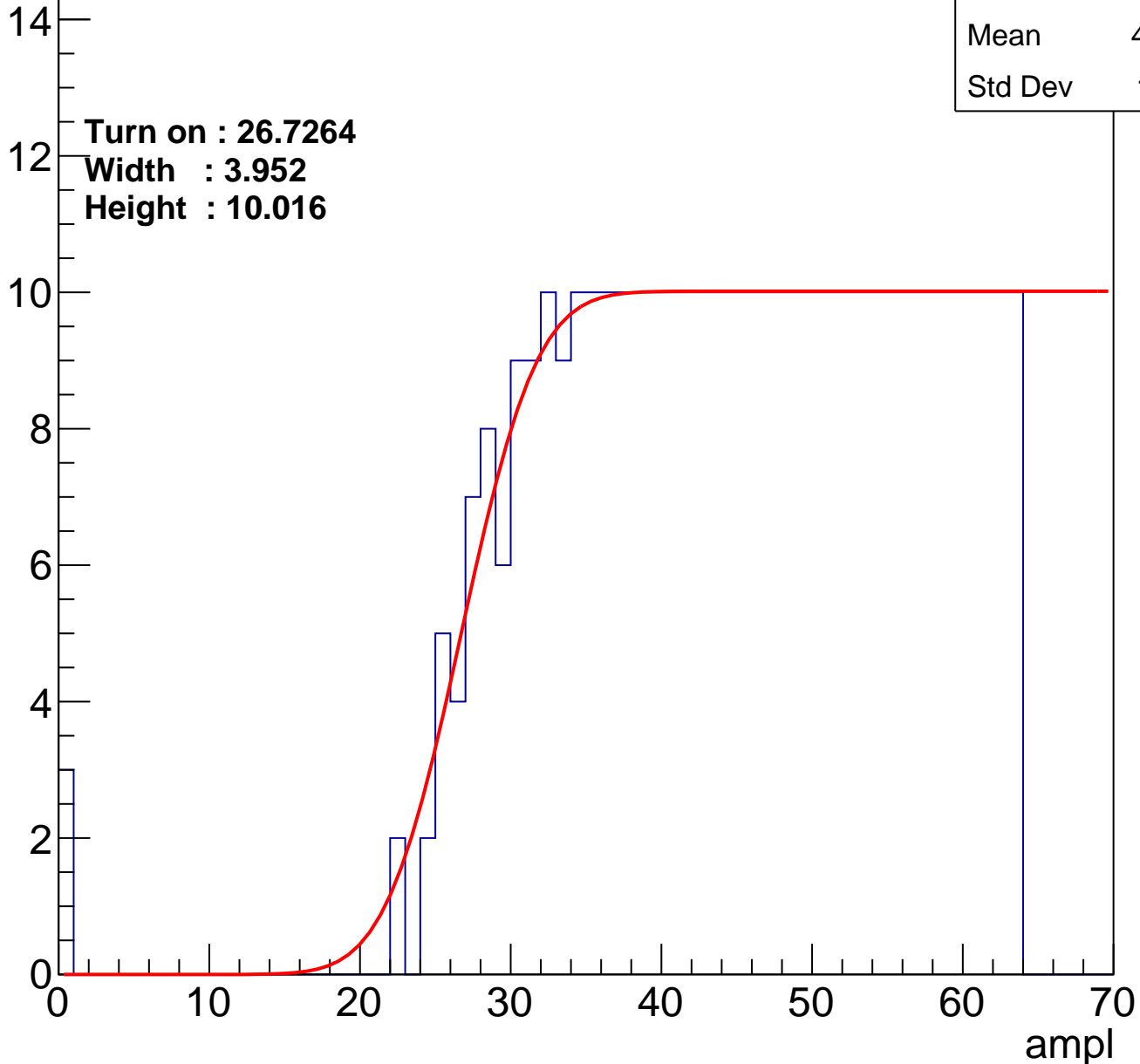
Entries	374
Mean	44.45
Std Dev	11.61

Turn on : 26.7264

Width : 3.952

Height : 10.016

Entry



B0L000S, U16-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.66
Std Dev	11.27

Turn on : 27.3004

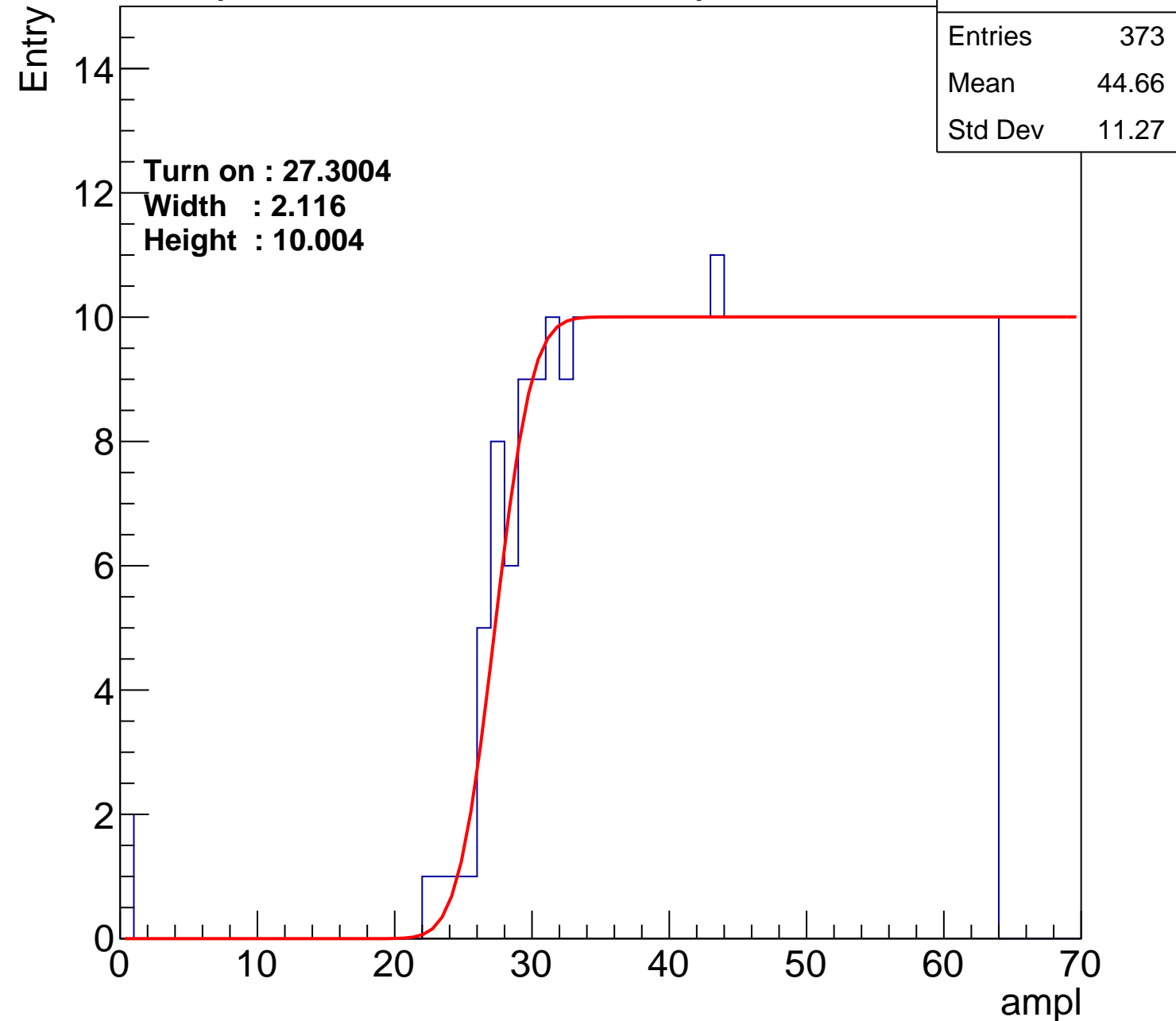
Width : 2.116

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.3622

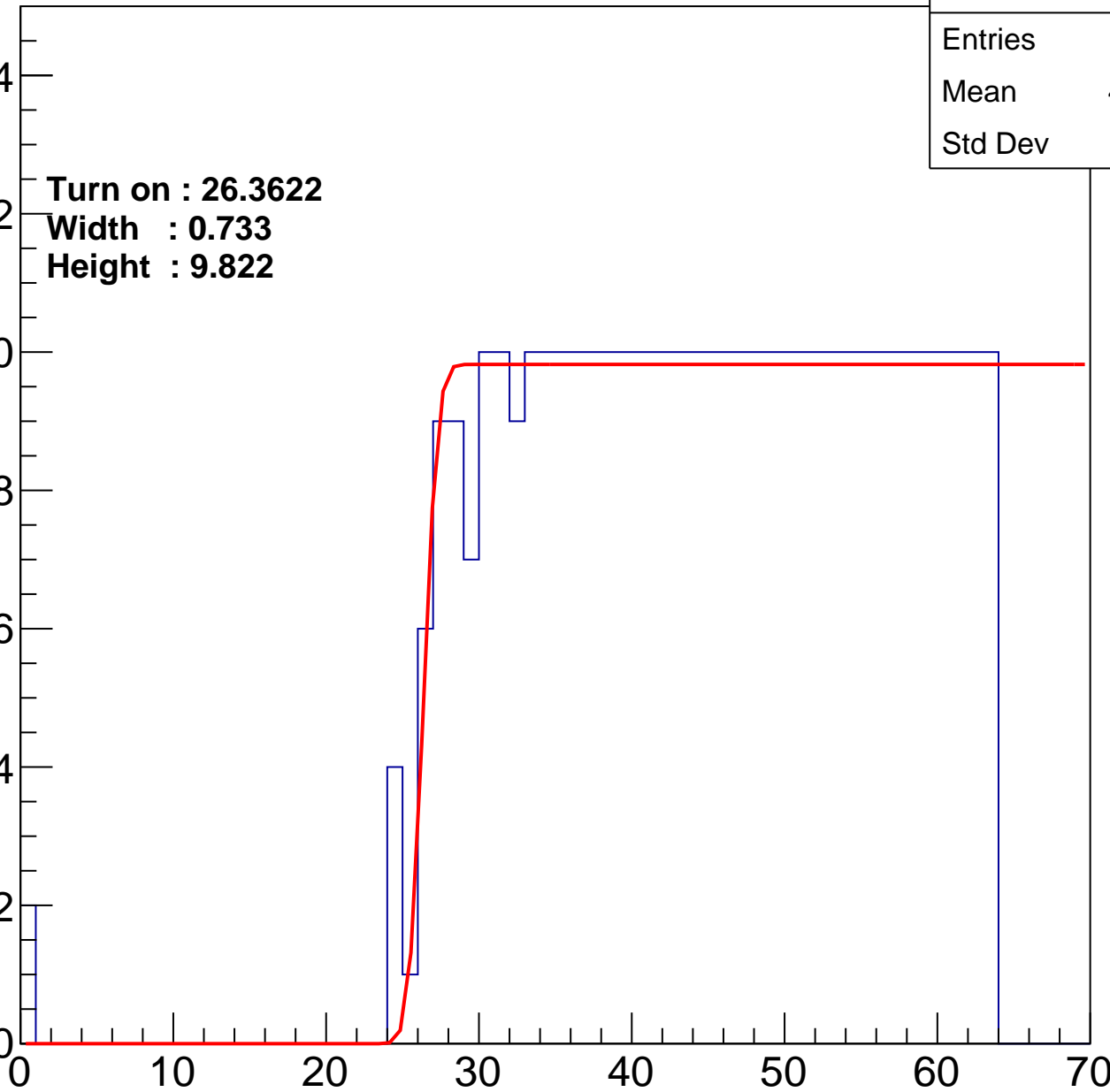
Width : 0.733

Height : 9.822

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch24

calib_packv5_042523_0143.root, FC#5, port B1

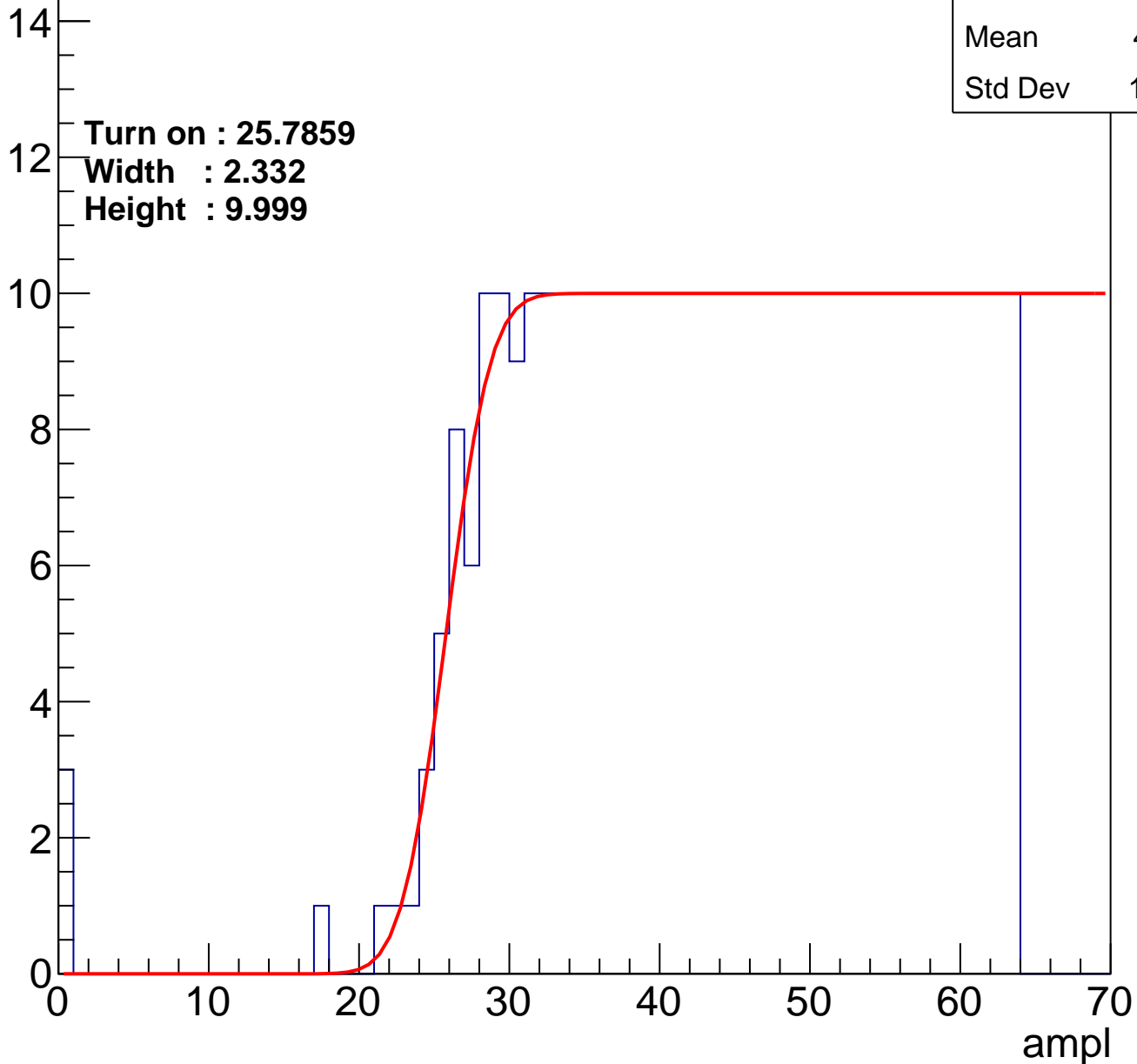
Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.7859

Width : 2.332

Height : 9.999

Entry



B0L000S, U16-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.64
Std Dev	11.29

Turn on : 27.3192

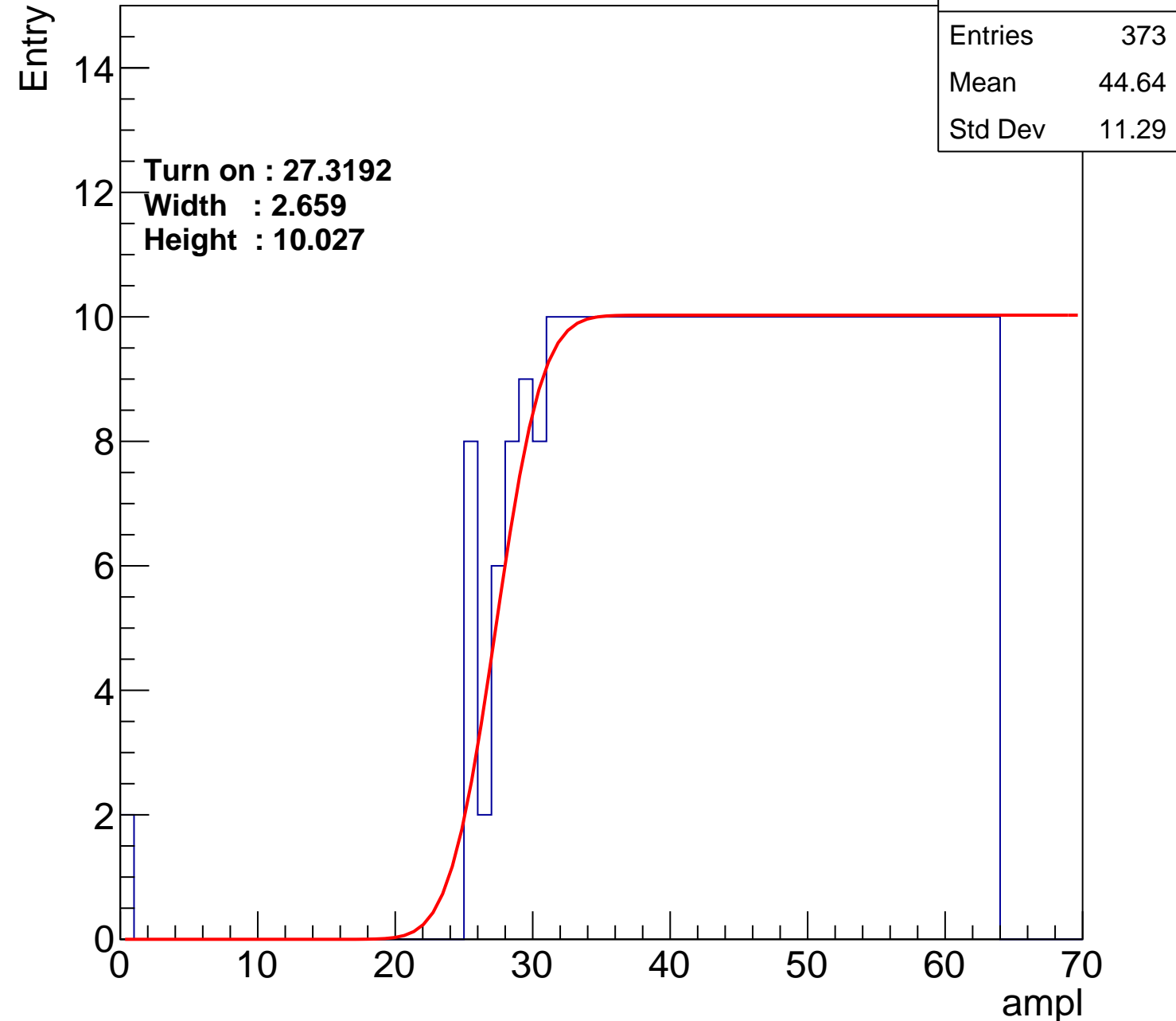
Width : 2.659

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch26

calib_packv5_042523_0143.root, FC#5, port B1

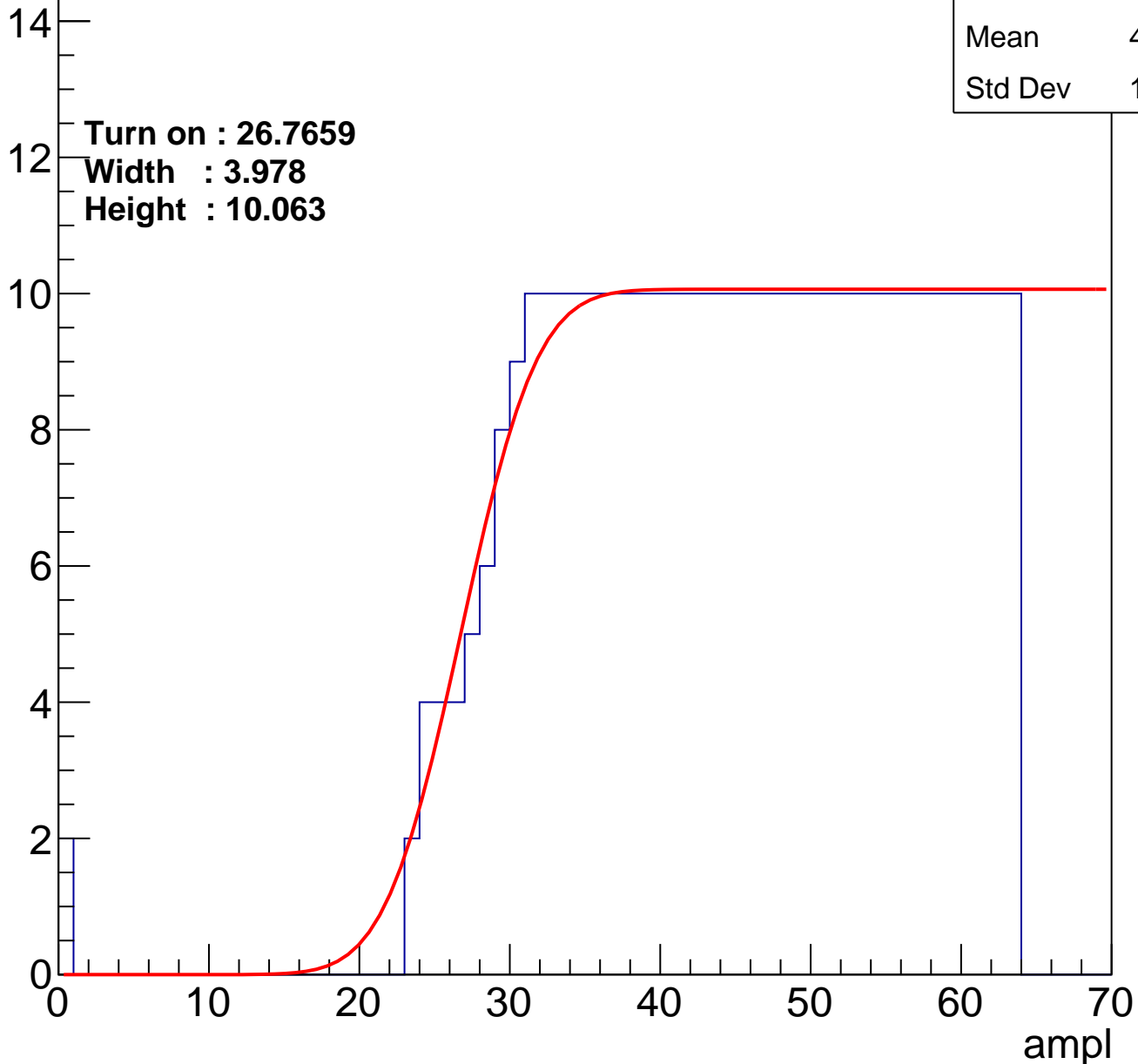
Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.7659

Width : 3.978

Height : 10.063

Entry



B0L000S, U16-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.08
Std Dev	12.02

Turn on : 26.8331

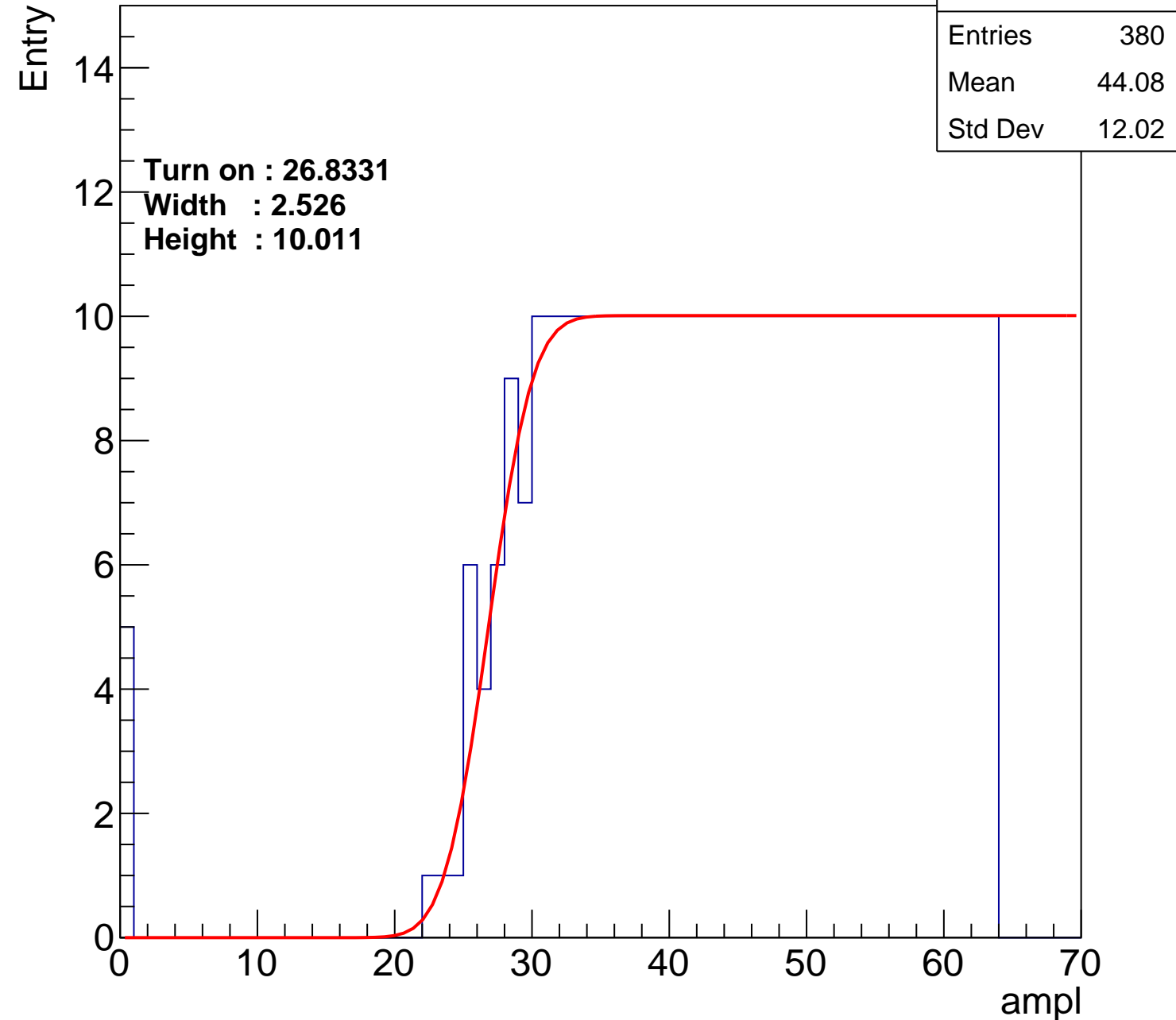
Width : 2.526

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch28

calib_packv5_042523_0143.root, FC#5, port B1

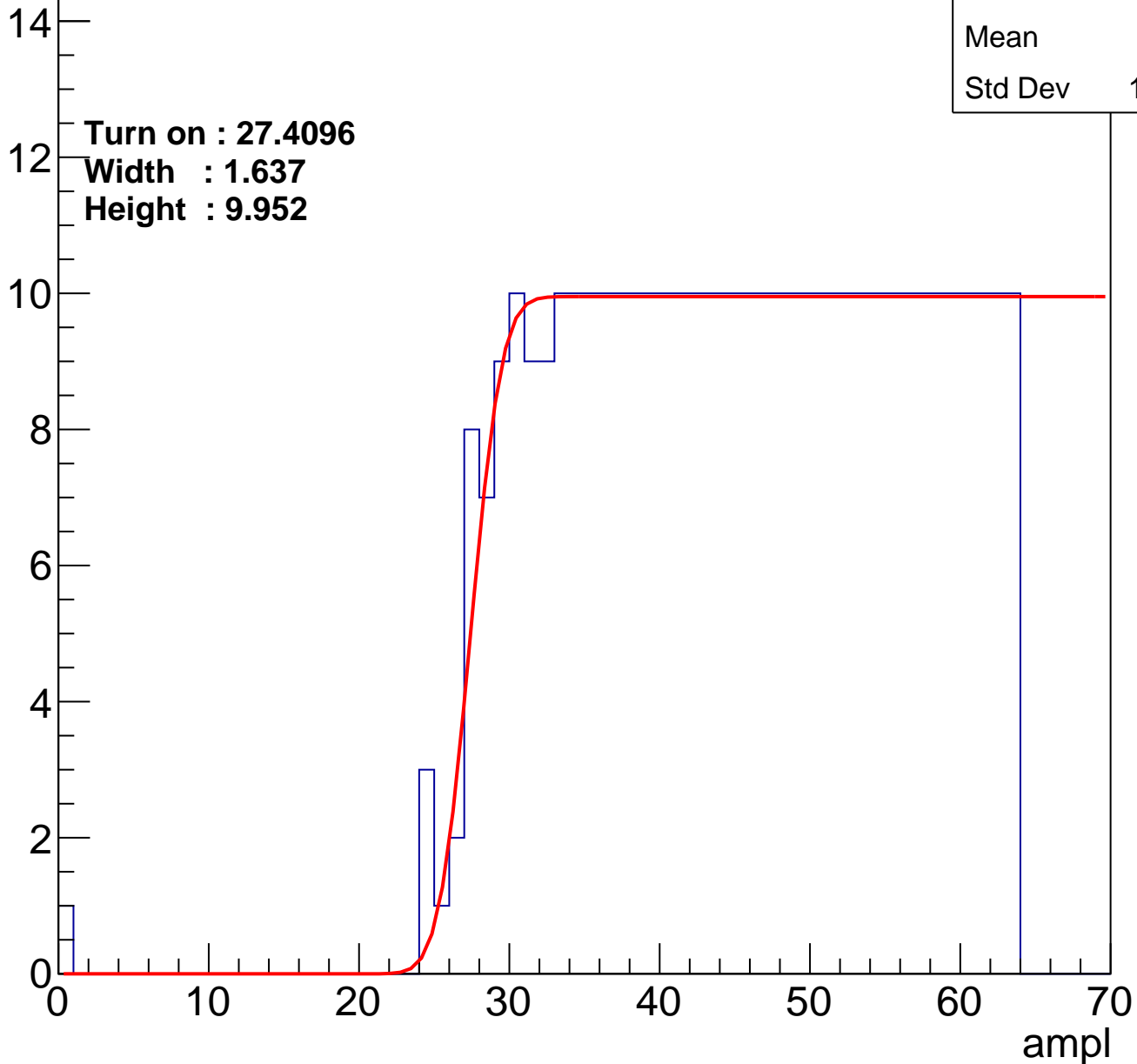
Entries	369
Mean	44.9
Std Dev	10.98

Turn on : 27.4096

Width : 1.637

Height : 9.952

Entry



B0L000S, U16-ch29

calib_packv5_042523_0143.root, FC#5, port B1

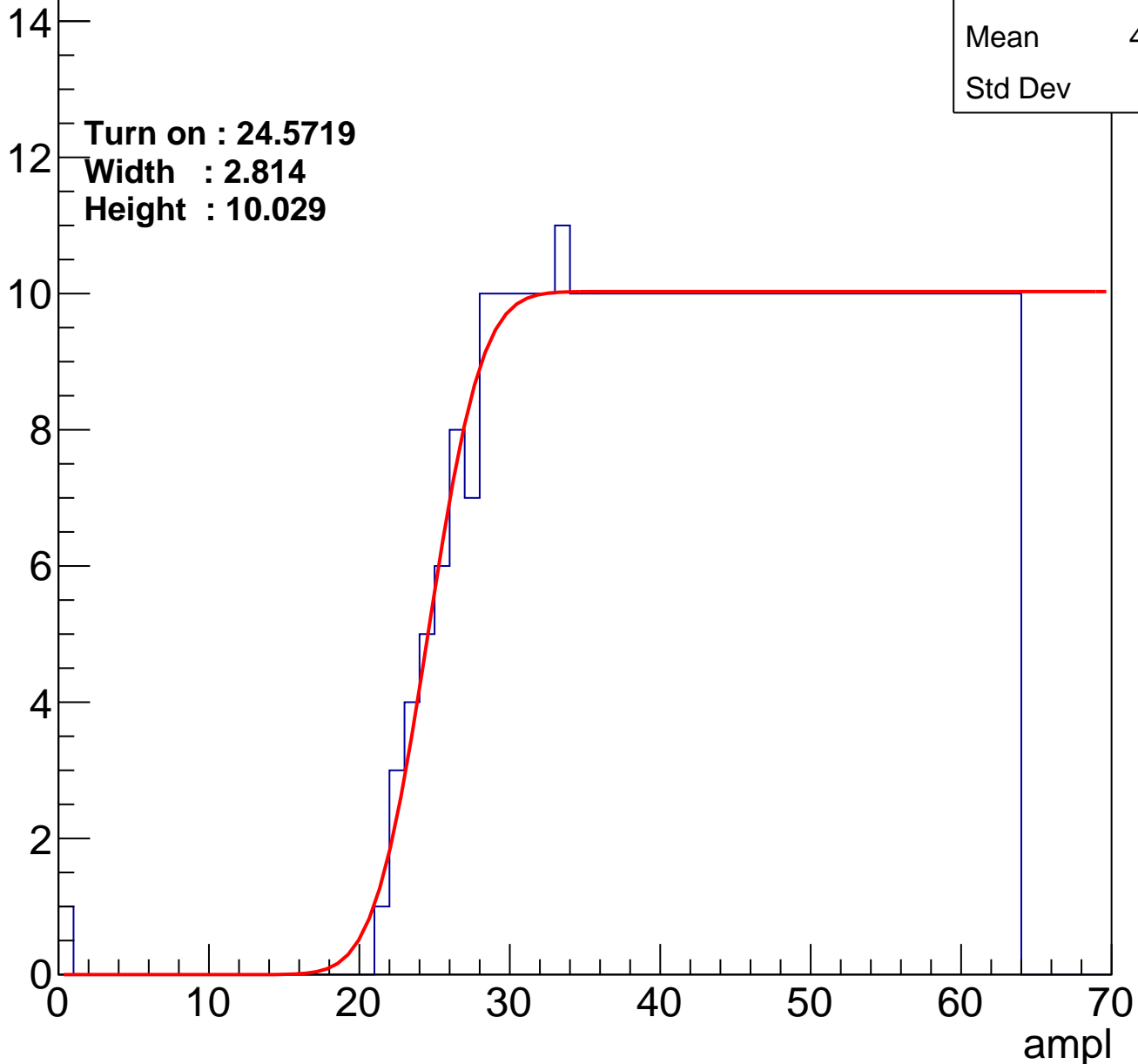
Entries	396
Mean	43.58
Std Dev	11.7

Turn on : 24.5719

Width : 2.814

Height : 10.029

Entry



B0L000S, U16-ch30

calib_packv5_042523_0143.root, FC#5, port B1

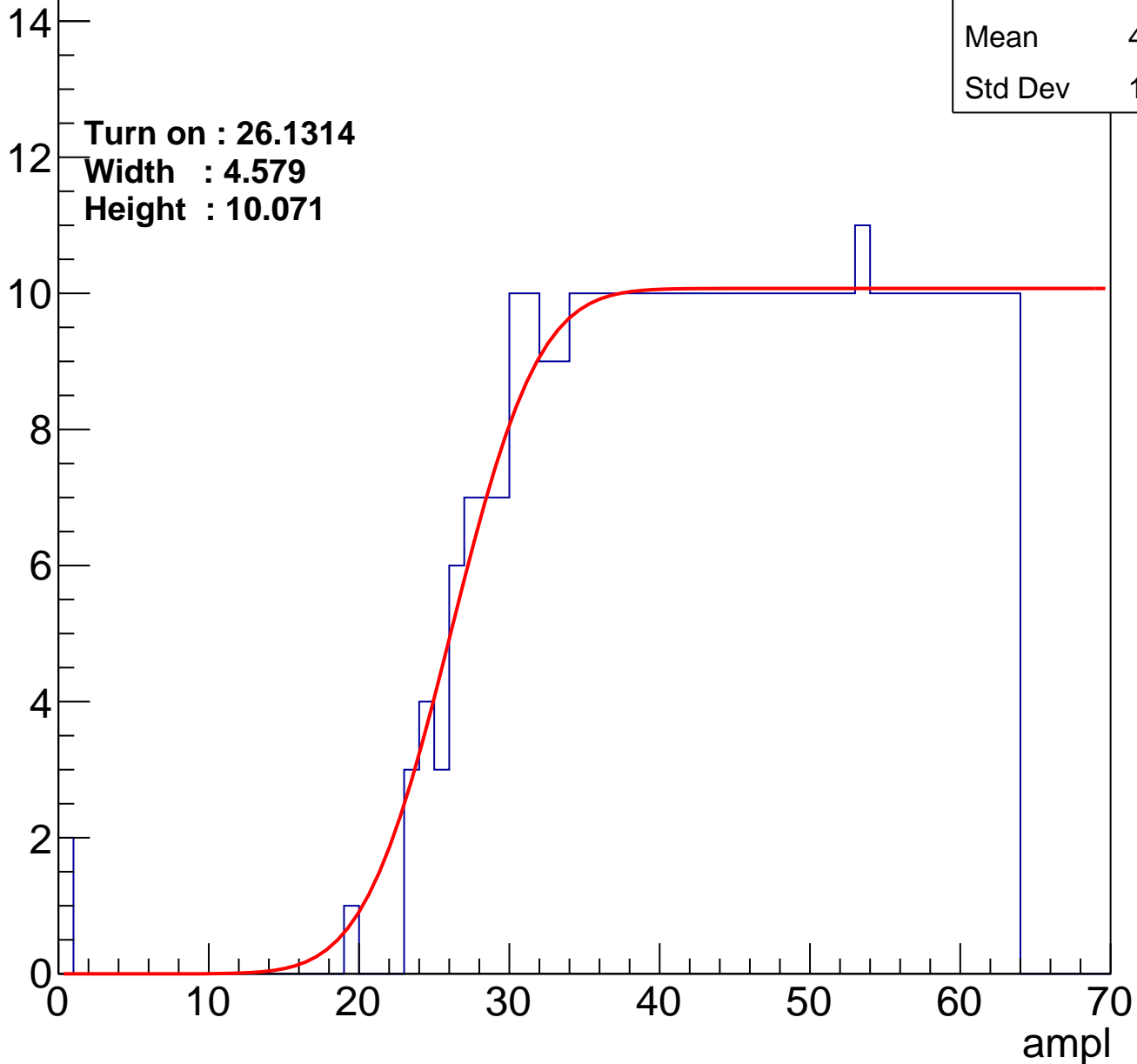
Entries	379
Mean	44.33
Std Dev	11.54

Turn on : 26.1314

Width : 4.579

Height : 10.071

Entry



B0L000S, U16-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 28.1453

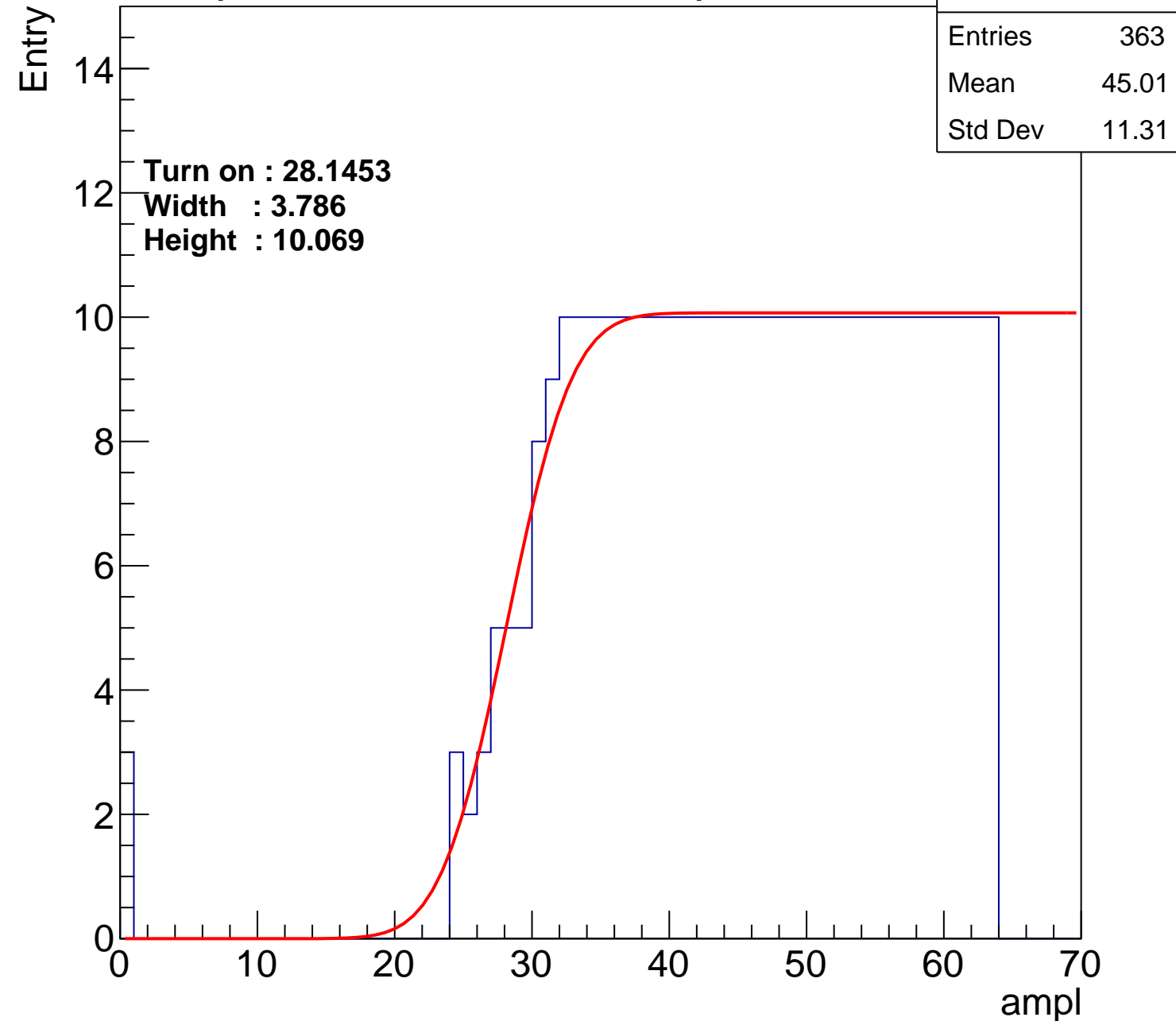
Width : 3.786

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	43.89
Std Dev	12.1

Turn on : 26.0450

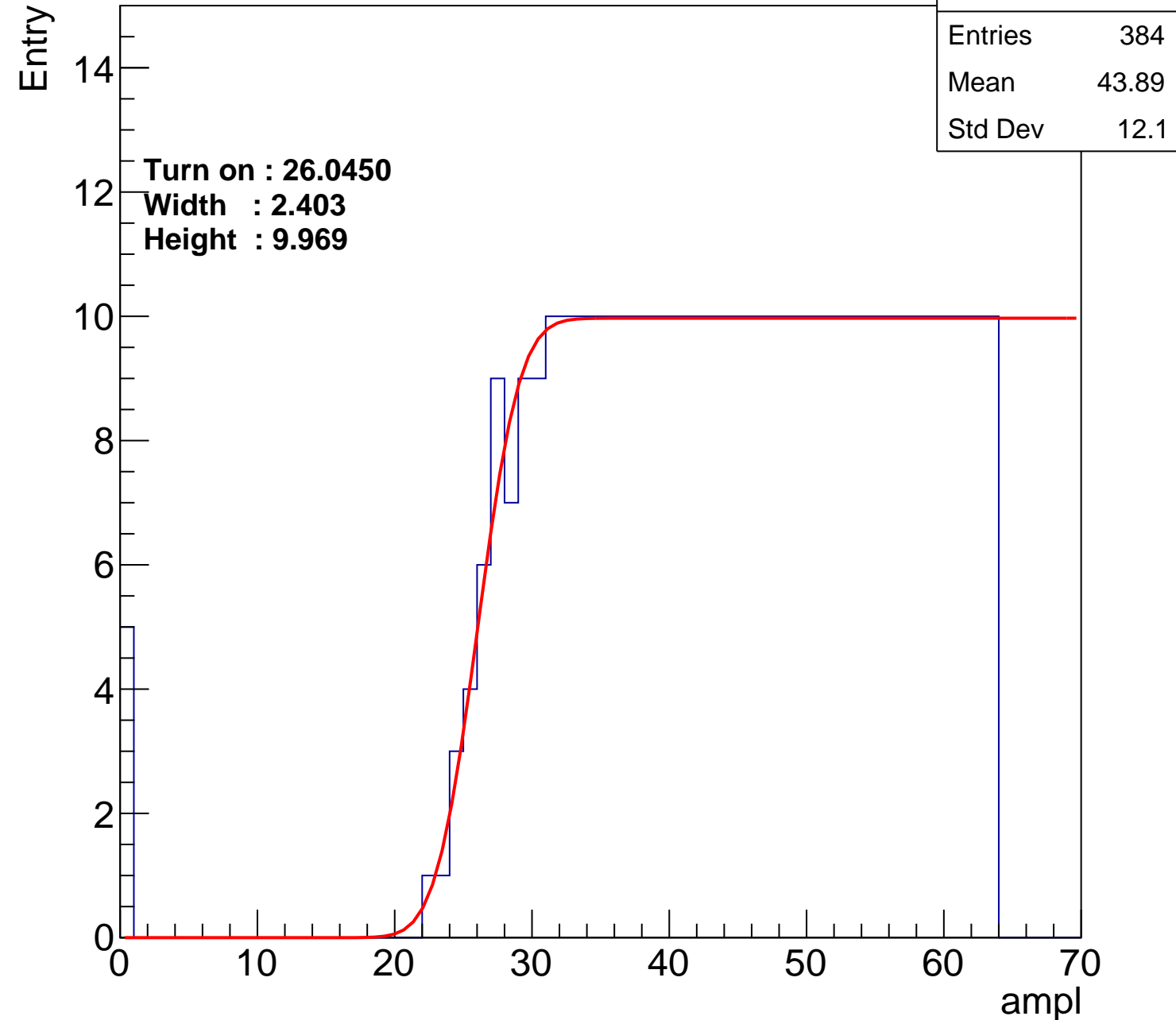
Width : 2.403

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch33

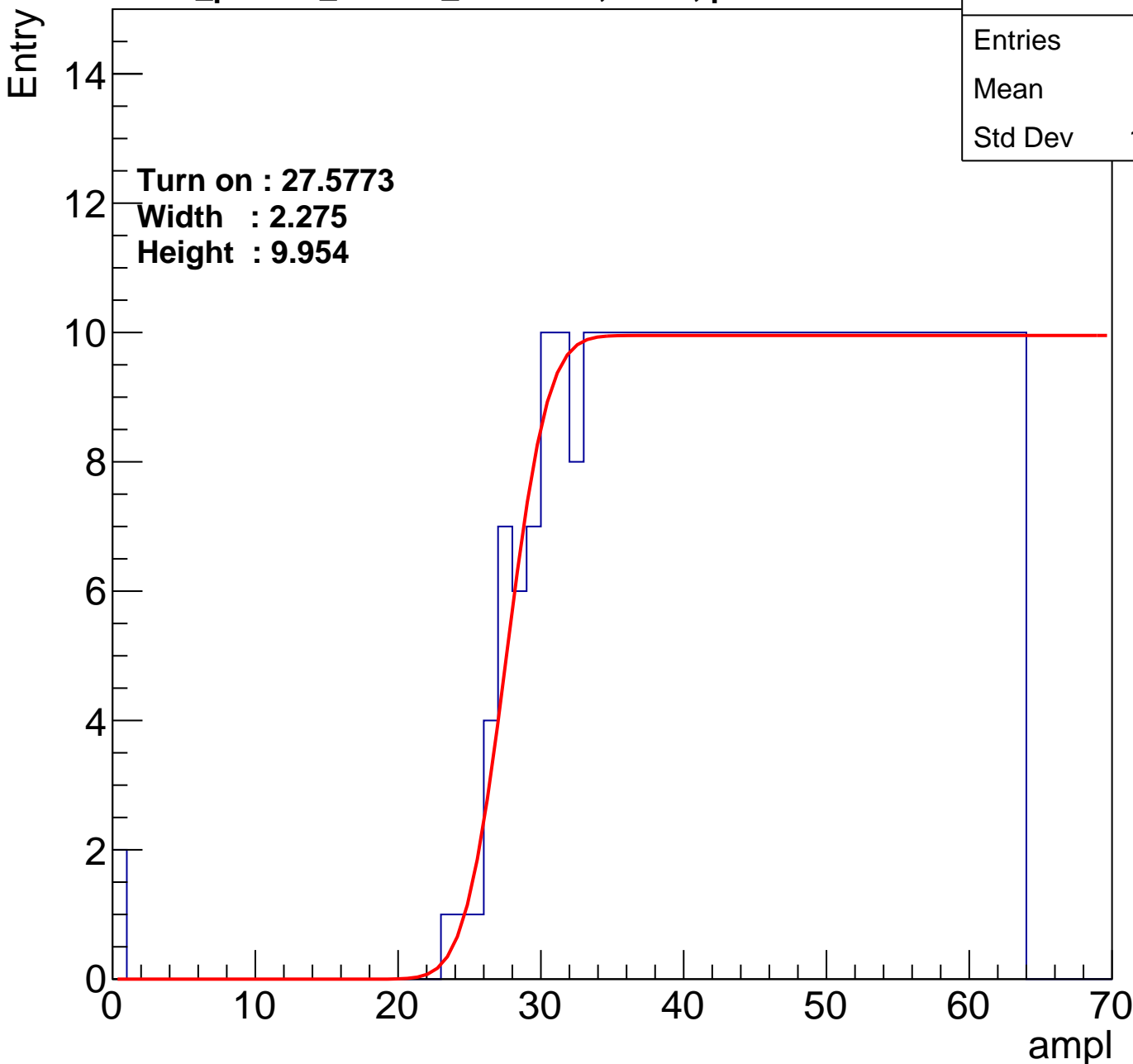
calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.5773

Width : 2.275

Height : 9.954



B0L000S, U16-ch34

calib_packv5_042523_0143.root, FC#5, port B1

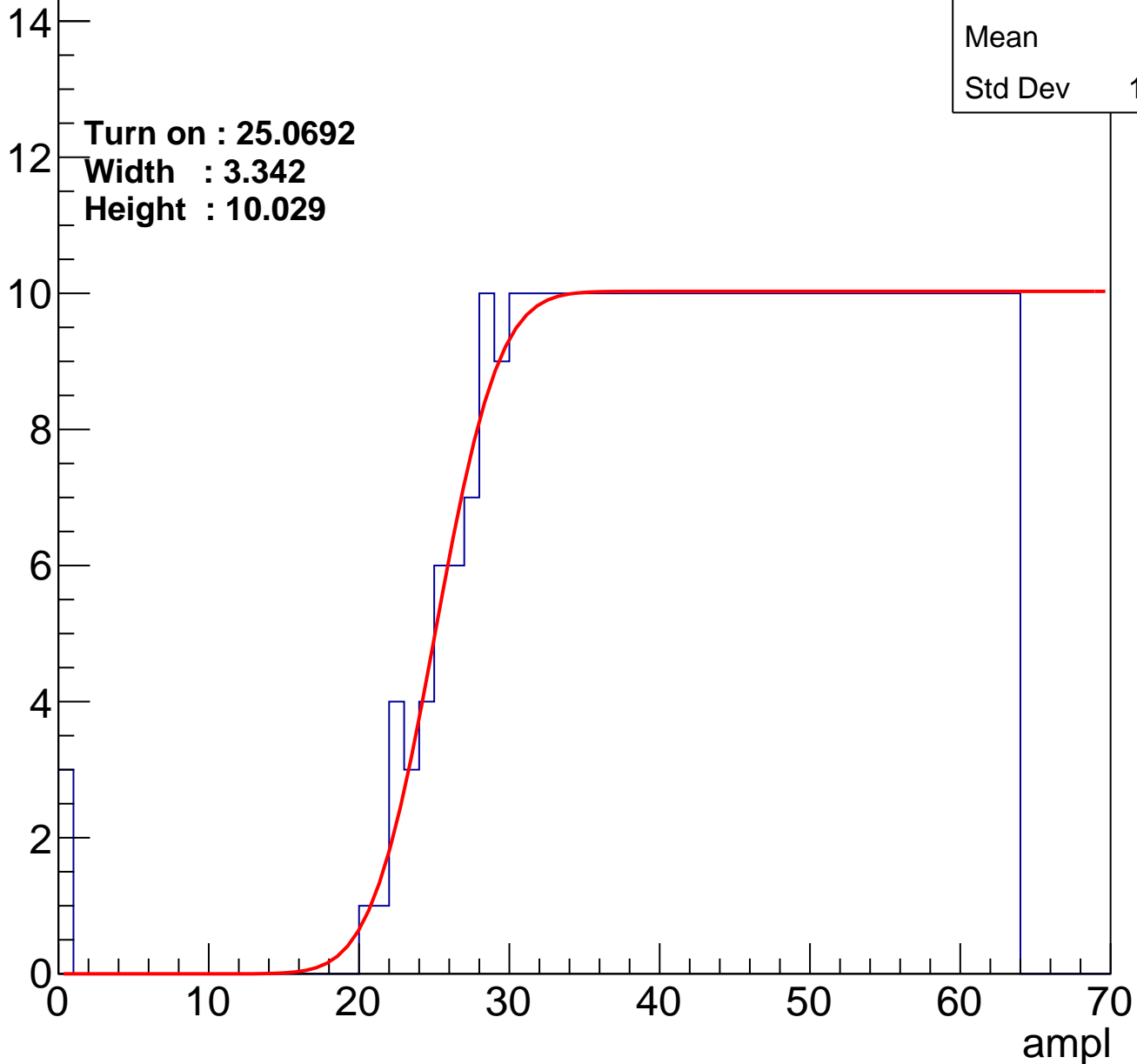
Entries	394
Mean	43.5
Std Dev	12.05

Turn on : 25.0692

Width : 3.342

Height : 10.029

Entry



calib_packv5_042523_0143.root, FC#5, port B1

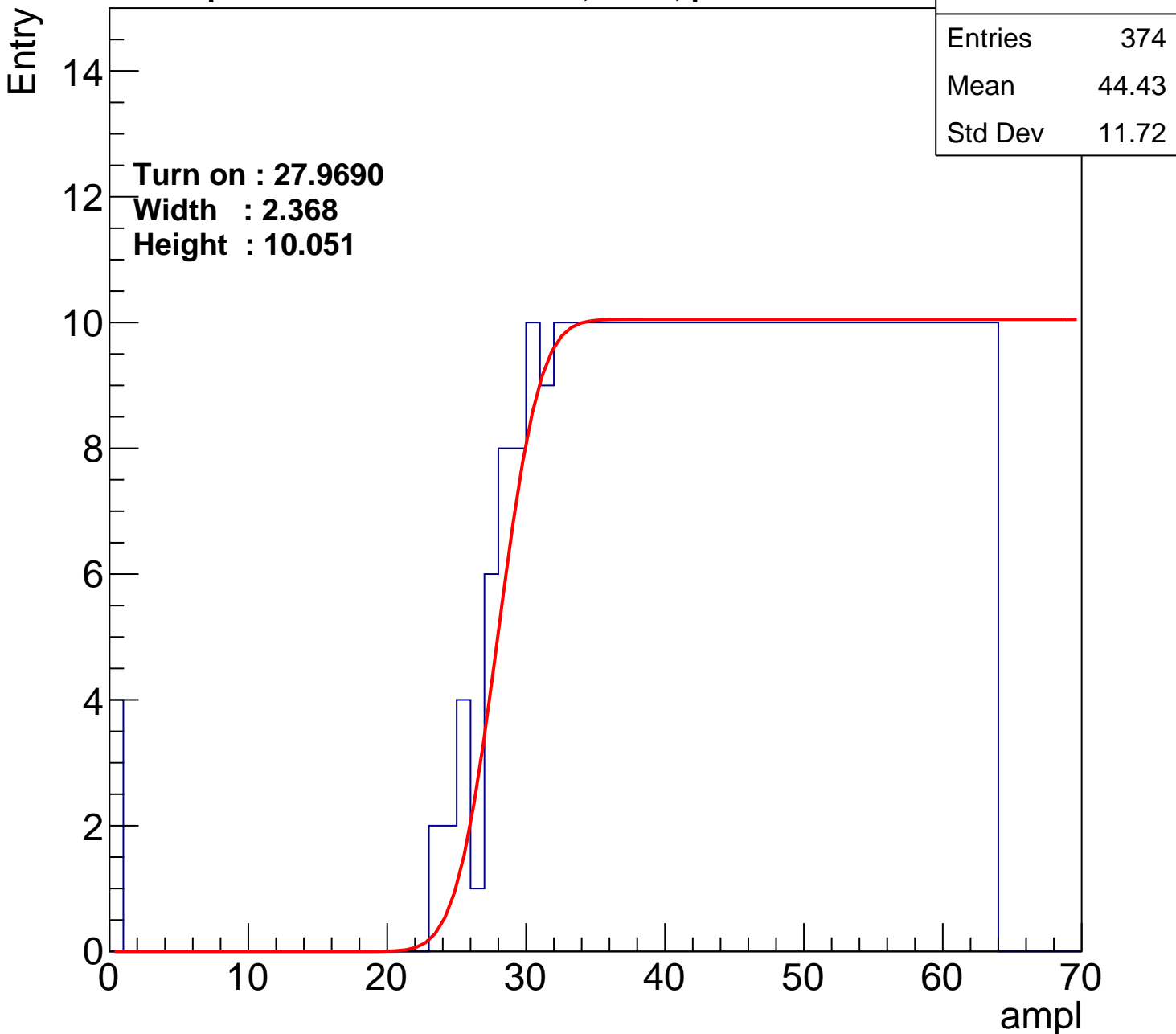
Entries	374
Mean	44.43
Std Dev	11.72

Mean	44.43
------	-------

Std Dev	11.72
---------	-------

Width : 2.368

Height : 10.051



B0L000S, U16-ch36

calib_packv5_042523_0143.root, FC#5, port B1

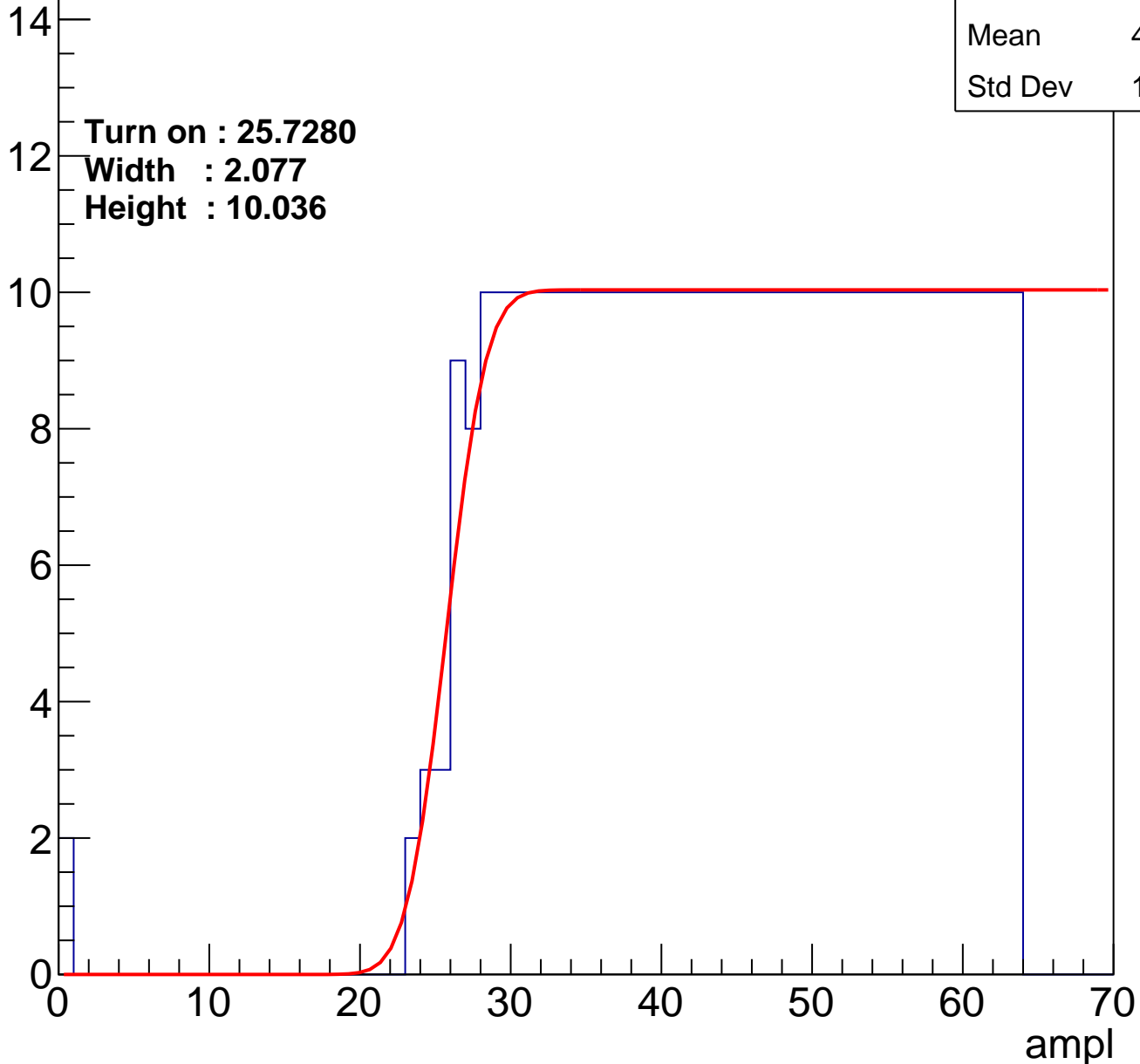
Entries	387
Mean	43.99
Std Dev	11.58

Turn on : 25.7280

Width : 2.077

Height : 10.036

Entry



B0L000S, U16-ch37

calib_packv5_042523_0143.root, FC#5, port B1

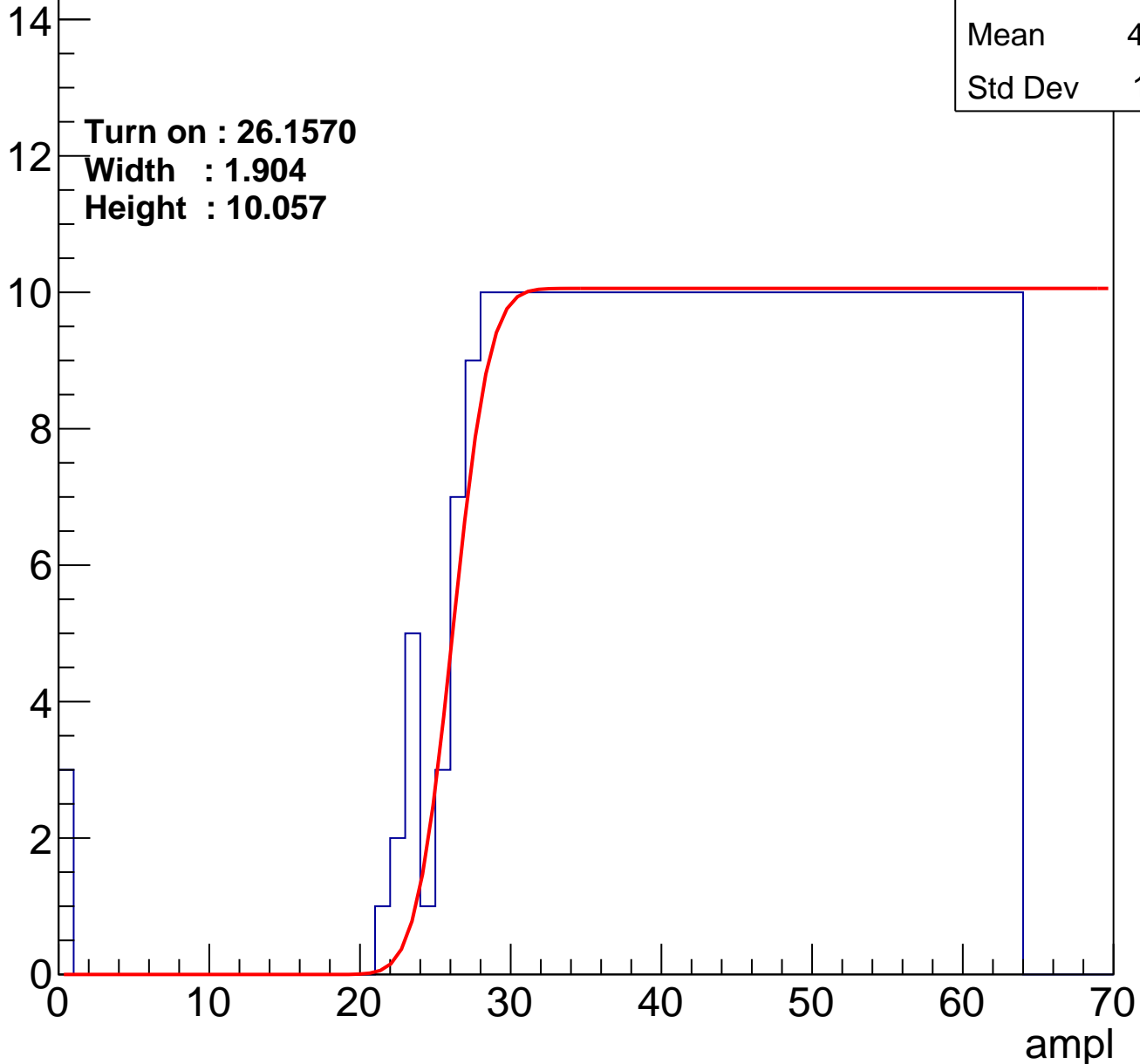
Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 26.1570

Width : 1.904

Height : 10.057

Entry



B0L000S, U16-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.91

Turn on : 26.1932

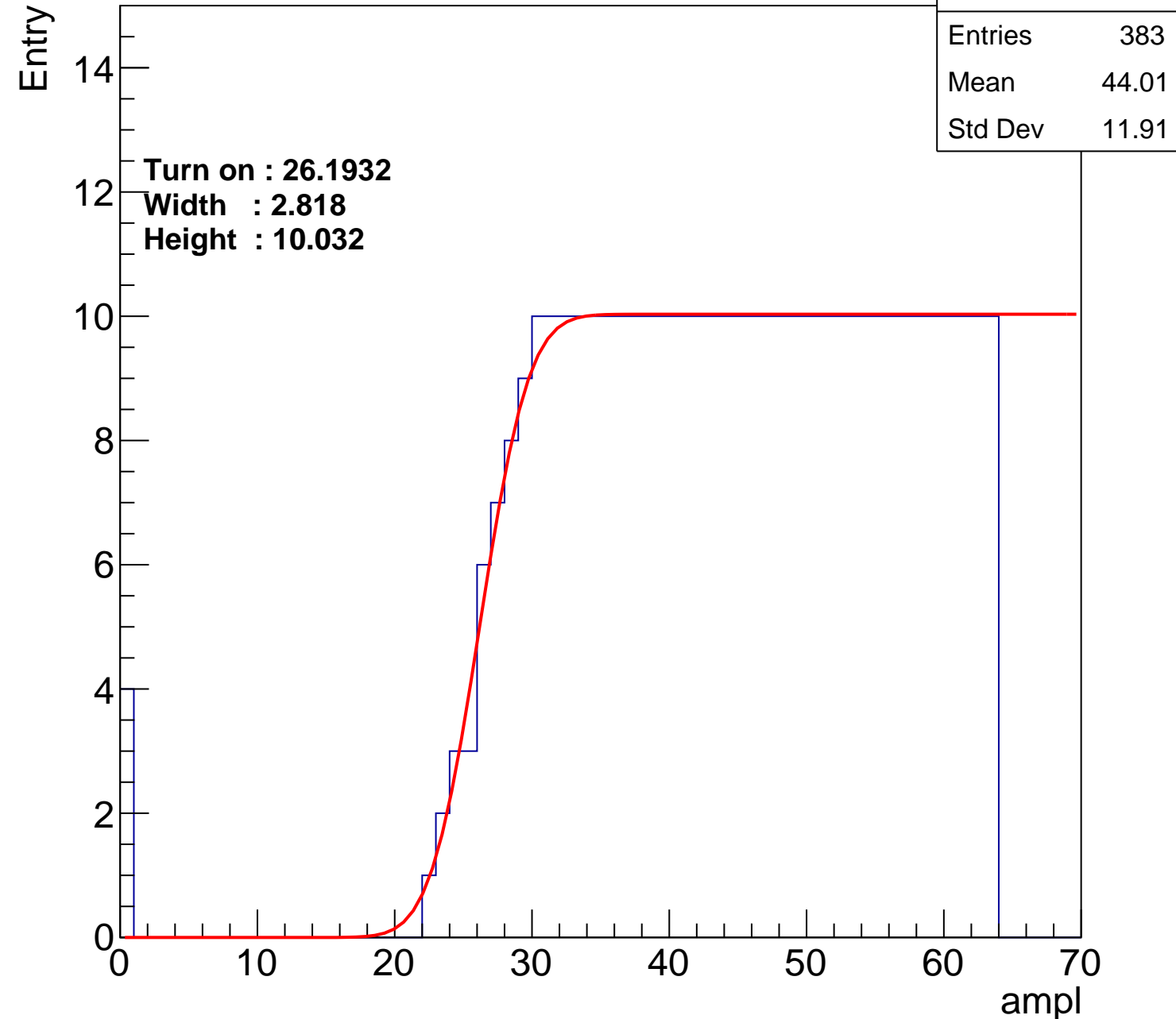
Width : 2.818

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch39

calib_packv5_042523_0143.root, FC#5, port B1

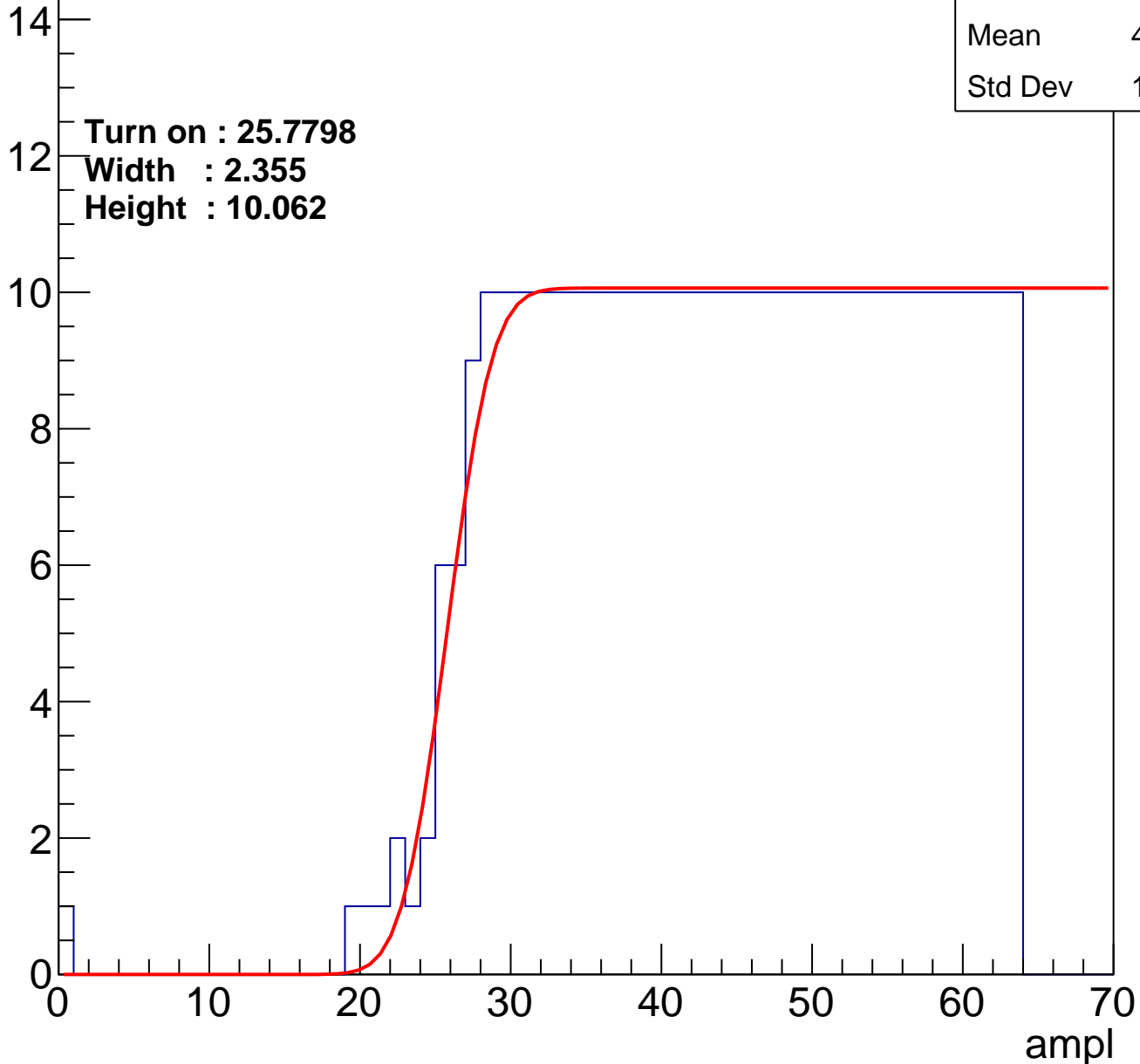
Entries	390
Mean	43.86
Std Dev	11.57

Turn on : 25.7798

Width : 2.355

Height : 10.062

Entry



B0L000S, U16-ch40

calib_packv5_042523_0143.root, FC#5, port B1

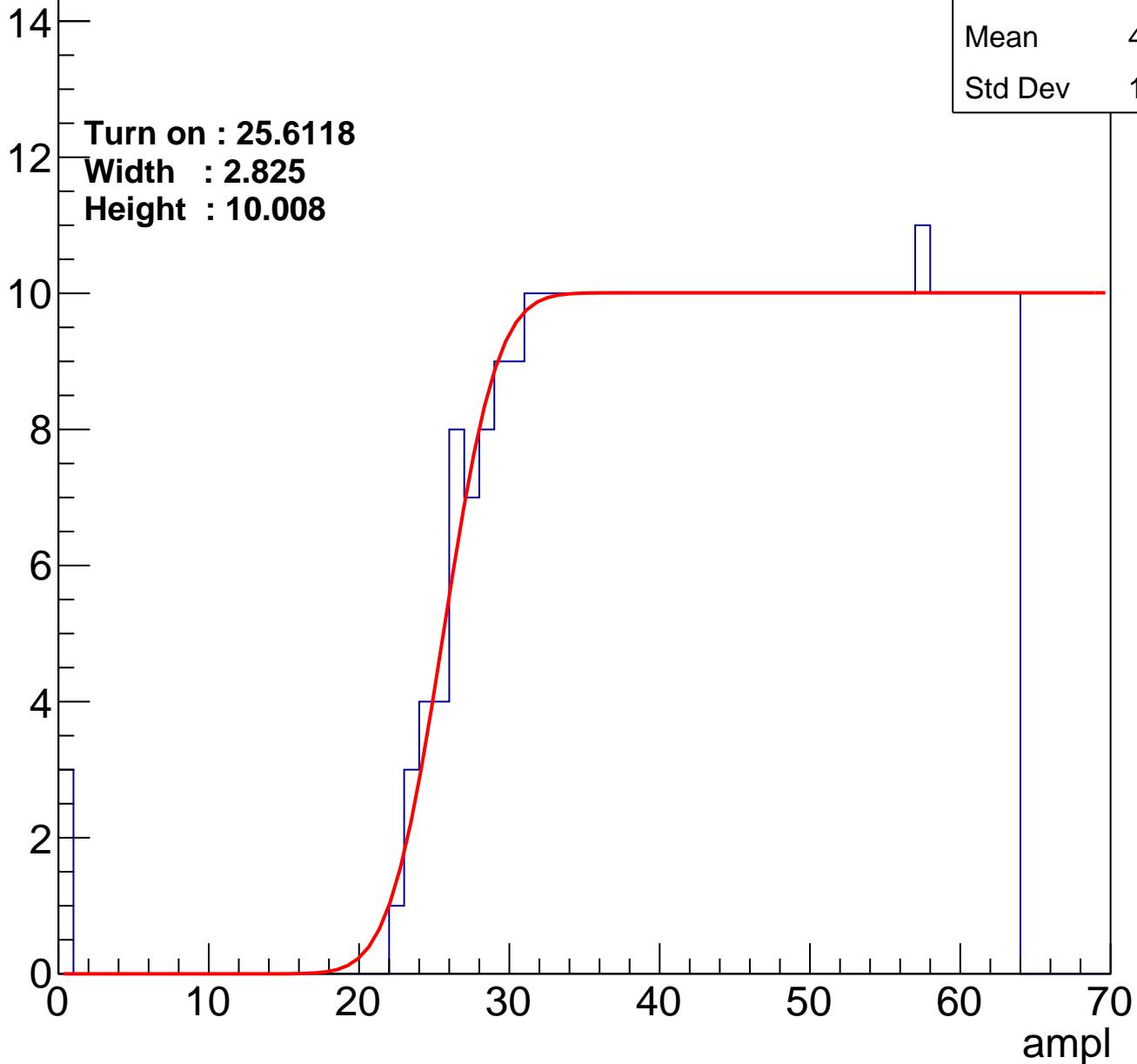
Entries	387
Mean	43.94
Std Dev	11.83

Turn on : 25.6118

Width : 2.825

Height : 10.008

Entry



B0L000S, U16-ch41

calib_packv5_042523_0143.root, FC#5, port B1

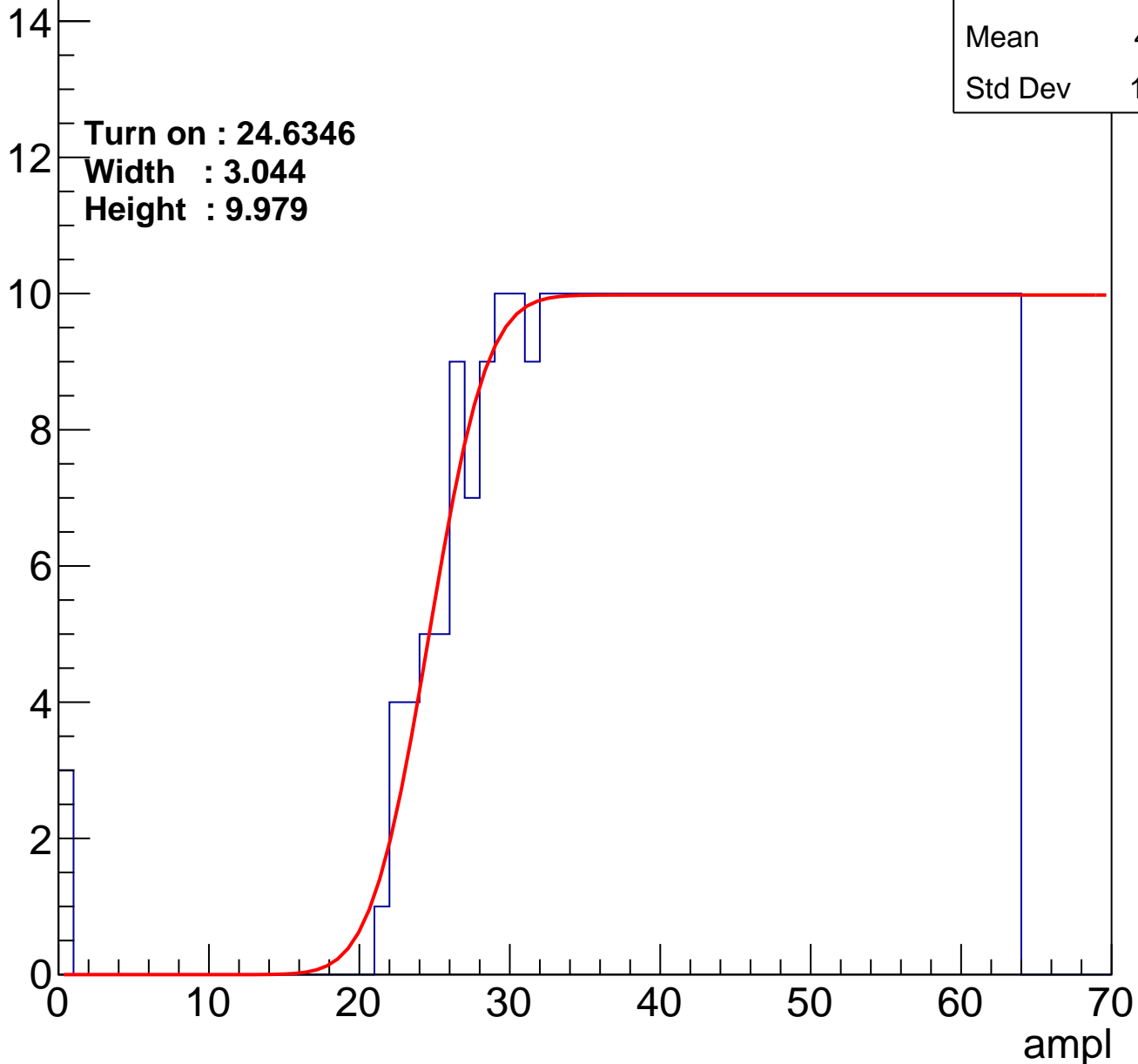
Entries	396
Mean	43.41
Std Dev	12.09

Turn on : 24.6346

Width : 3.044

Height : 9.979

Entry



B0L000S, U16-ch42

calib_packv5_042523_0143.root, FC#5, port B1

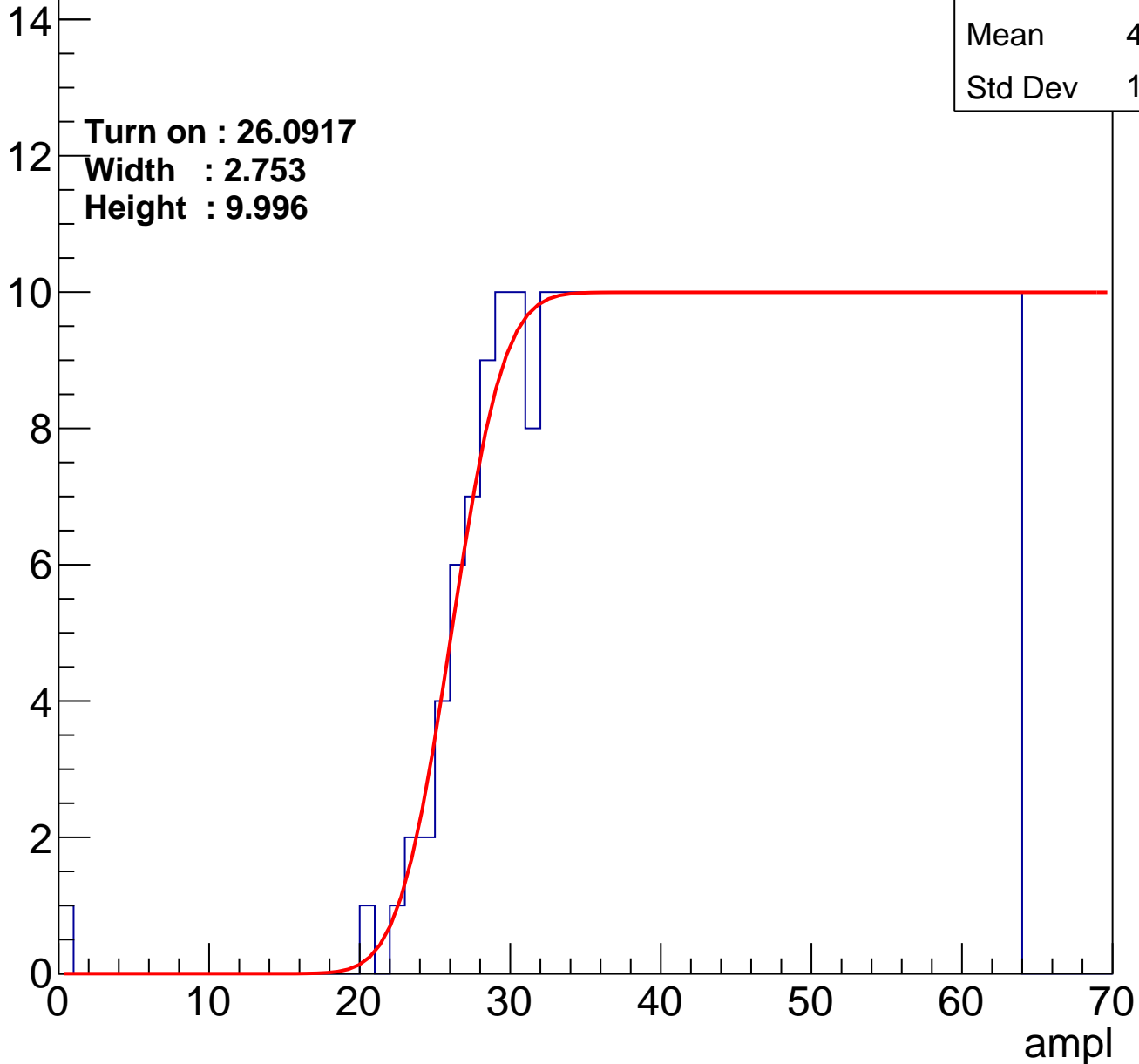
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.0917

Width : 2.753

Height : 9.996

Entry



B0L000S, U16-ch43

calib_packv5_042523_0143.root, FC#5, port B1

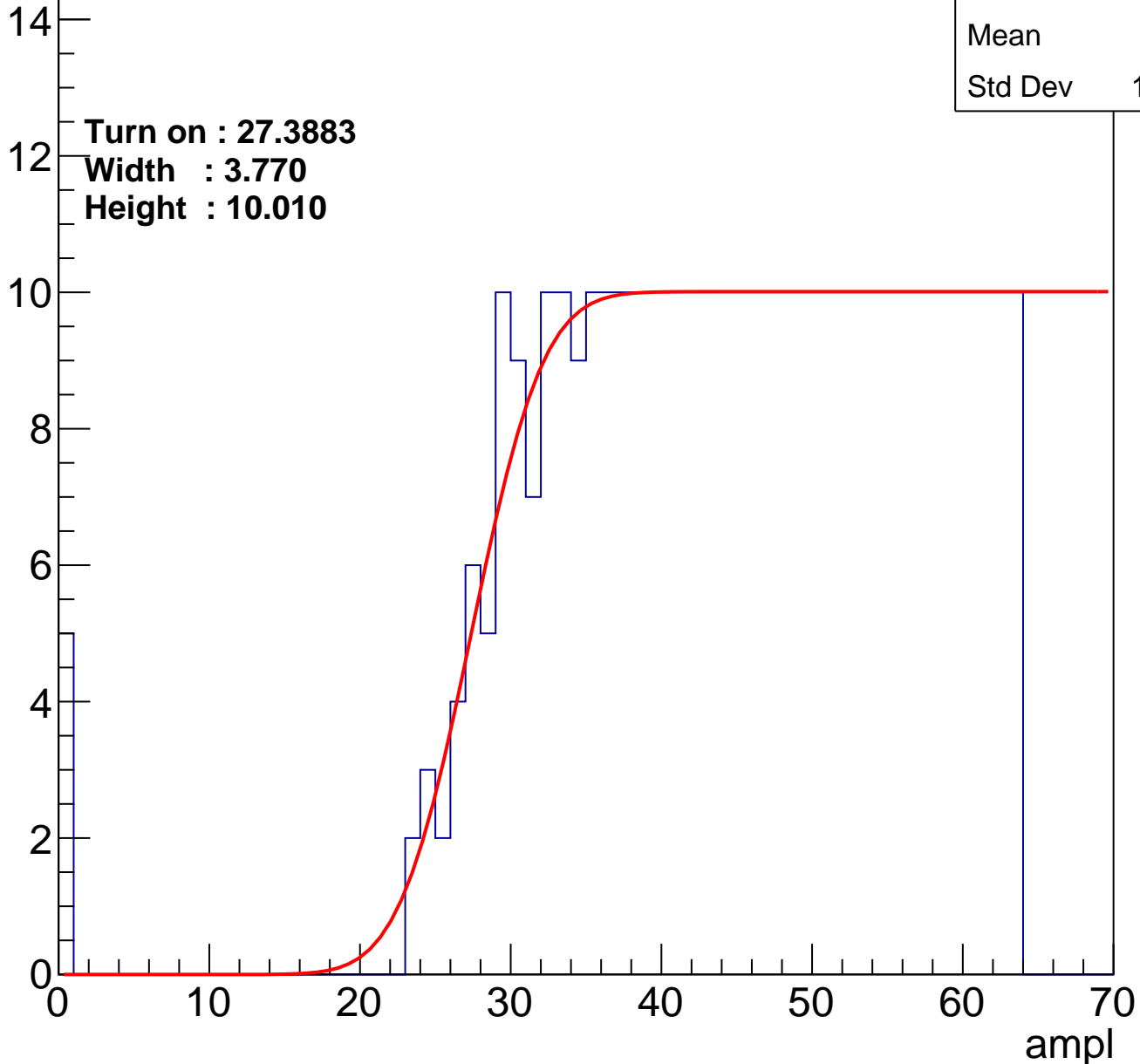
Entries	372
Mean	44.4
Std Dev	11.94

Turn on : 27.3883

Width : 3.770

Height : 10.010

Entry



B0L000S, U16-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.38
Std Dev	11.79

Turn on : 27.1726

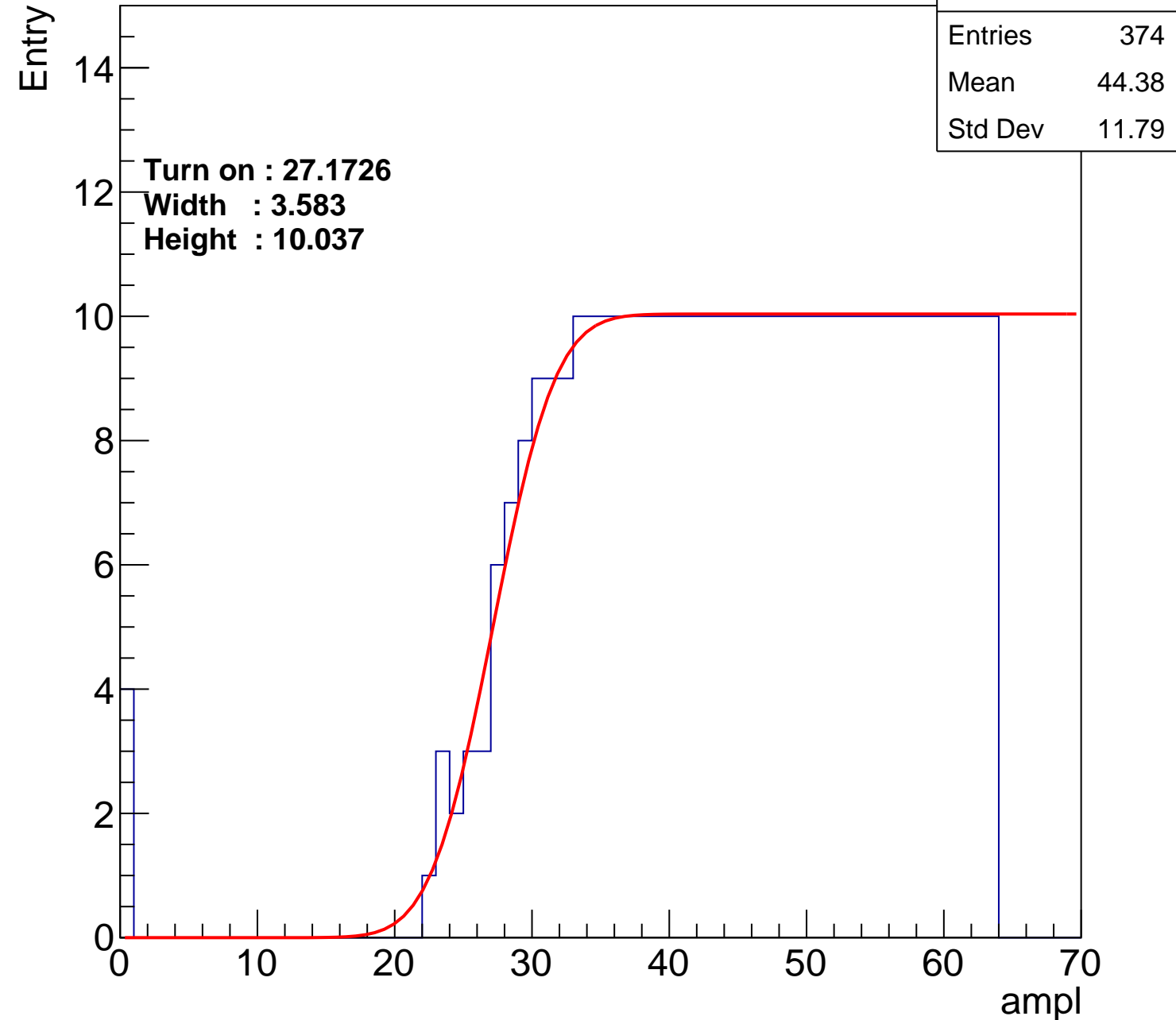
Width : 3.583

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch45

calib_packv5_042523_0143.root, FC#5, port B1

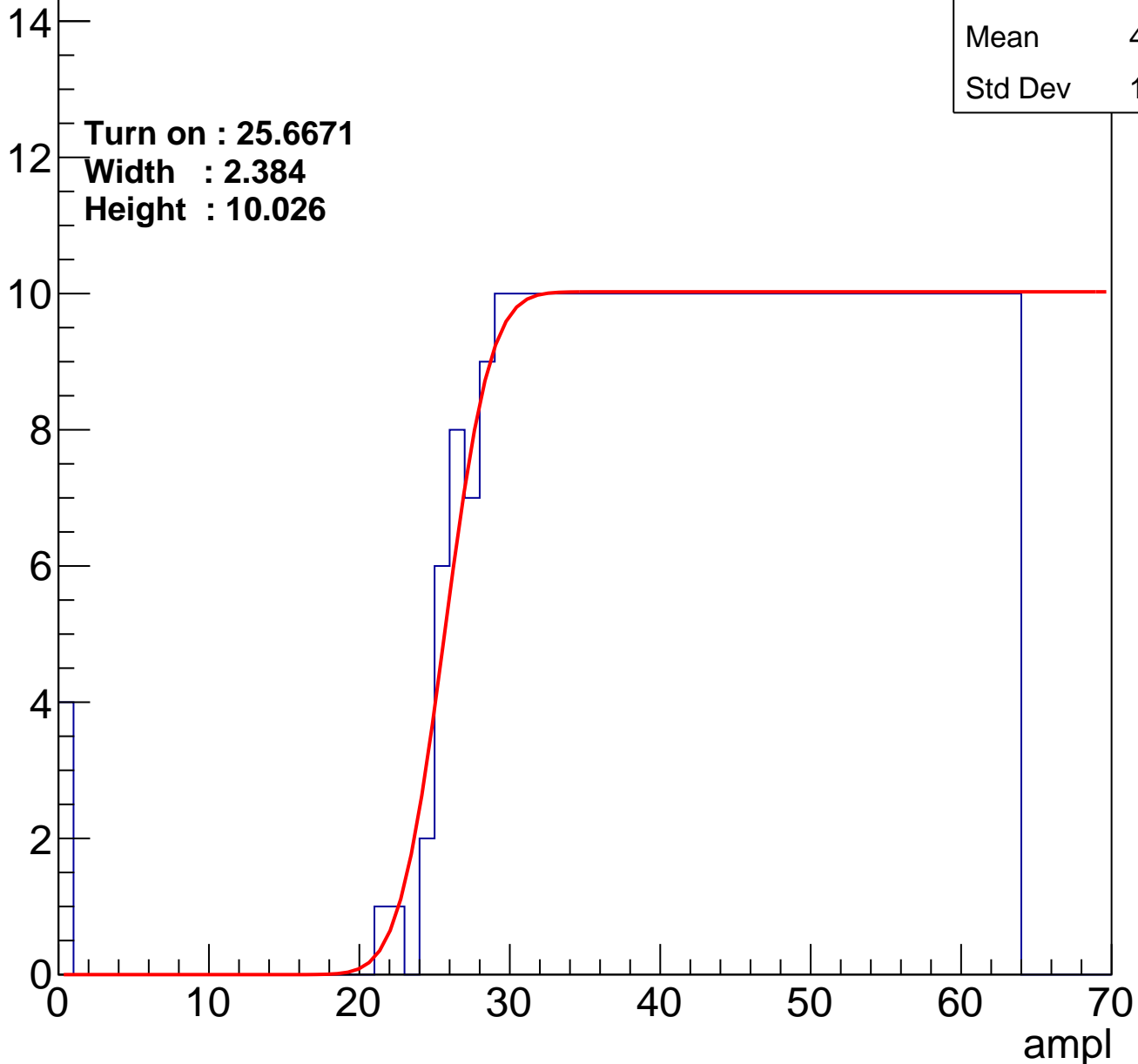
Entries	388
Mean	43.79
Std Dev	11.98

Turn on : 25.6671

Width : 2.384

Height : 10.026

Entry



B0L000S, U16-ch46

calib_packv5_042523_0143.root, FC#5, port B1

Entries	402
Mean	43.12
Std Dev	12.23

Turn on : 24.1113

Width : 2.312

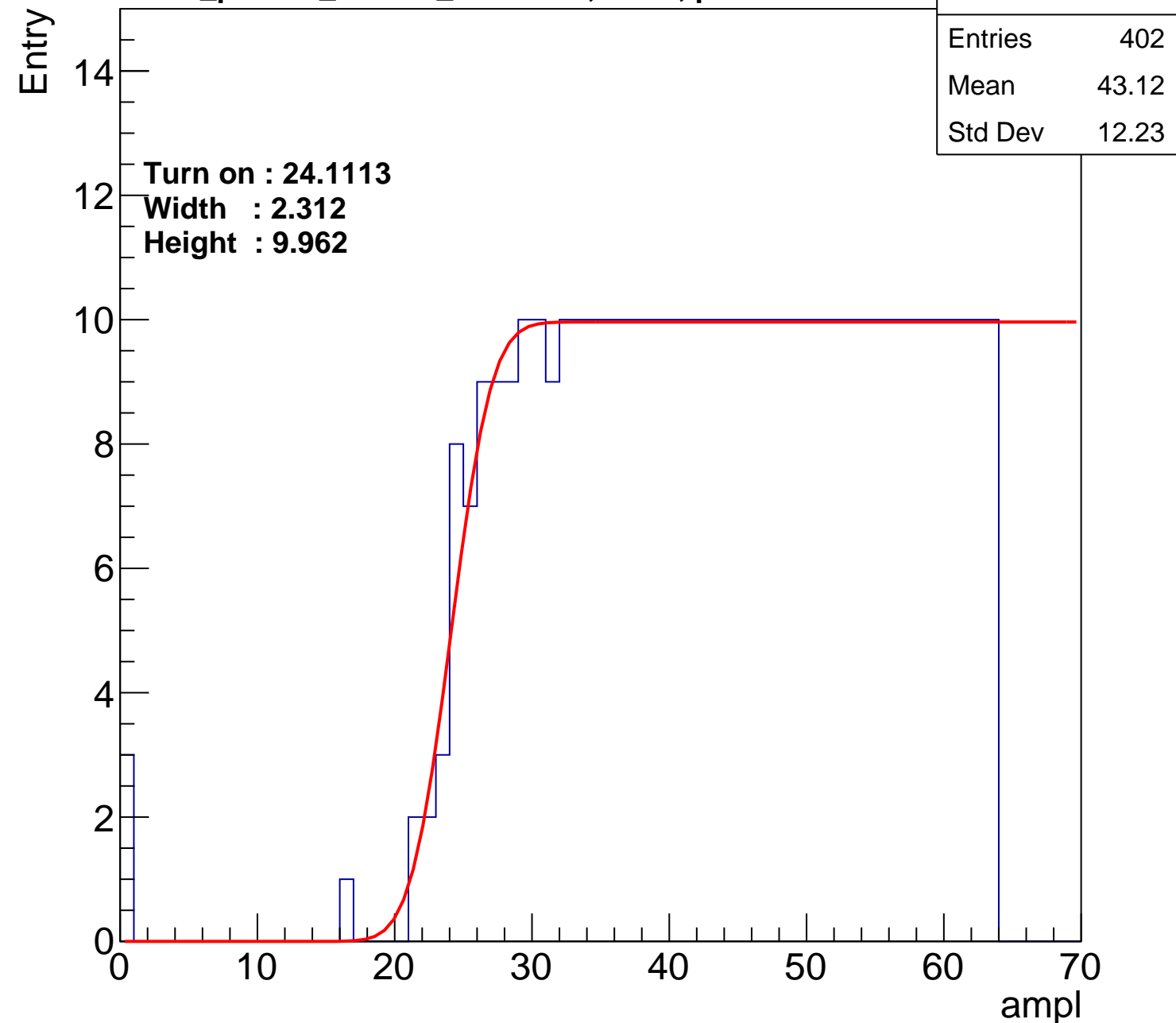
Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U16-ch47

calib_packv5_042523_0143.root, FC#5, port B1

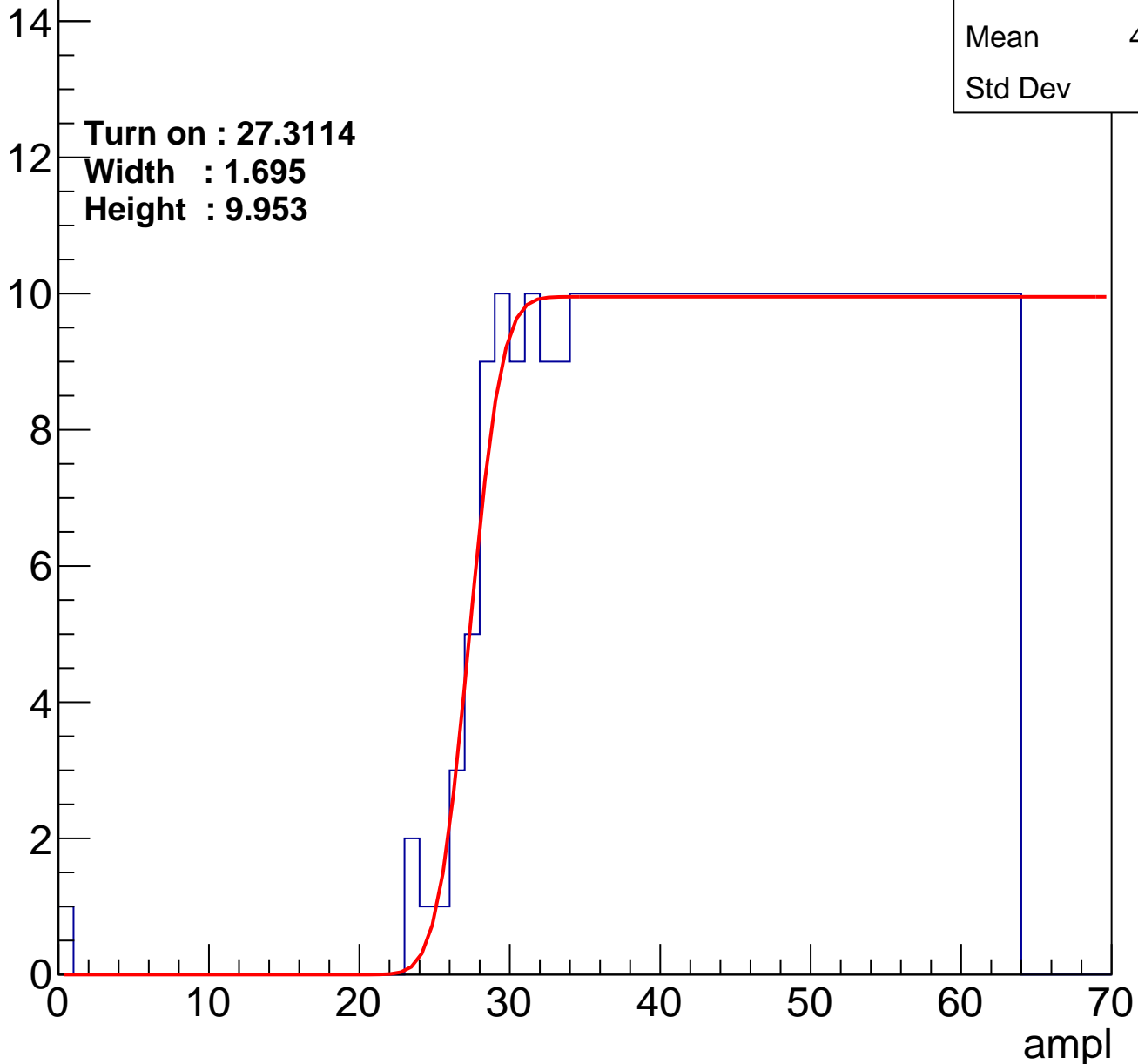
Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.3114

Width : 1.695

Height : 9.953

Entry



B0L000S, U16-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.3221

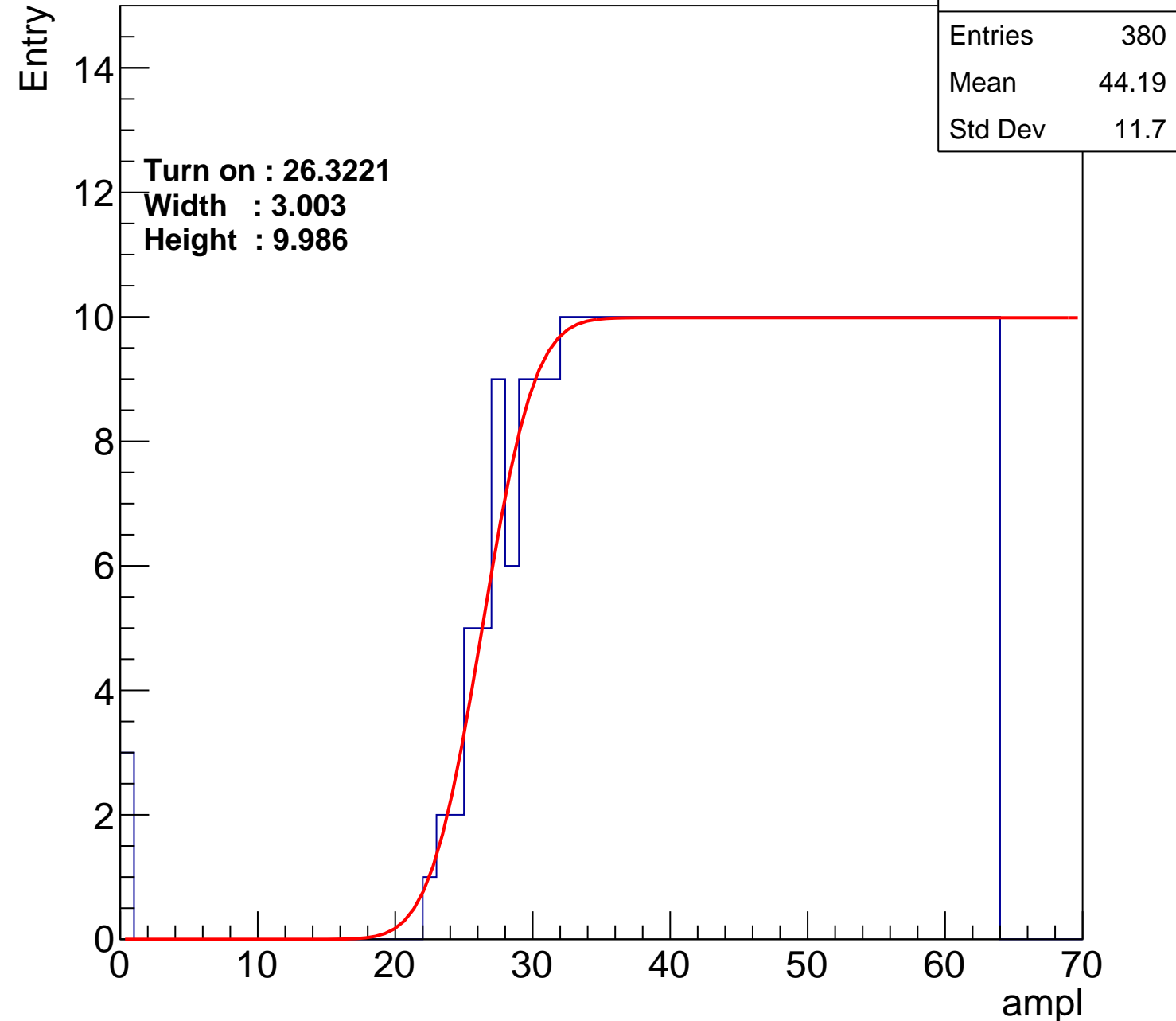
Width : 3.003

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.75
Std Dev	11.41

Turn on : 27.2603

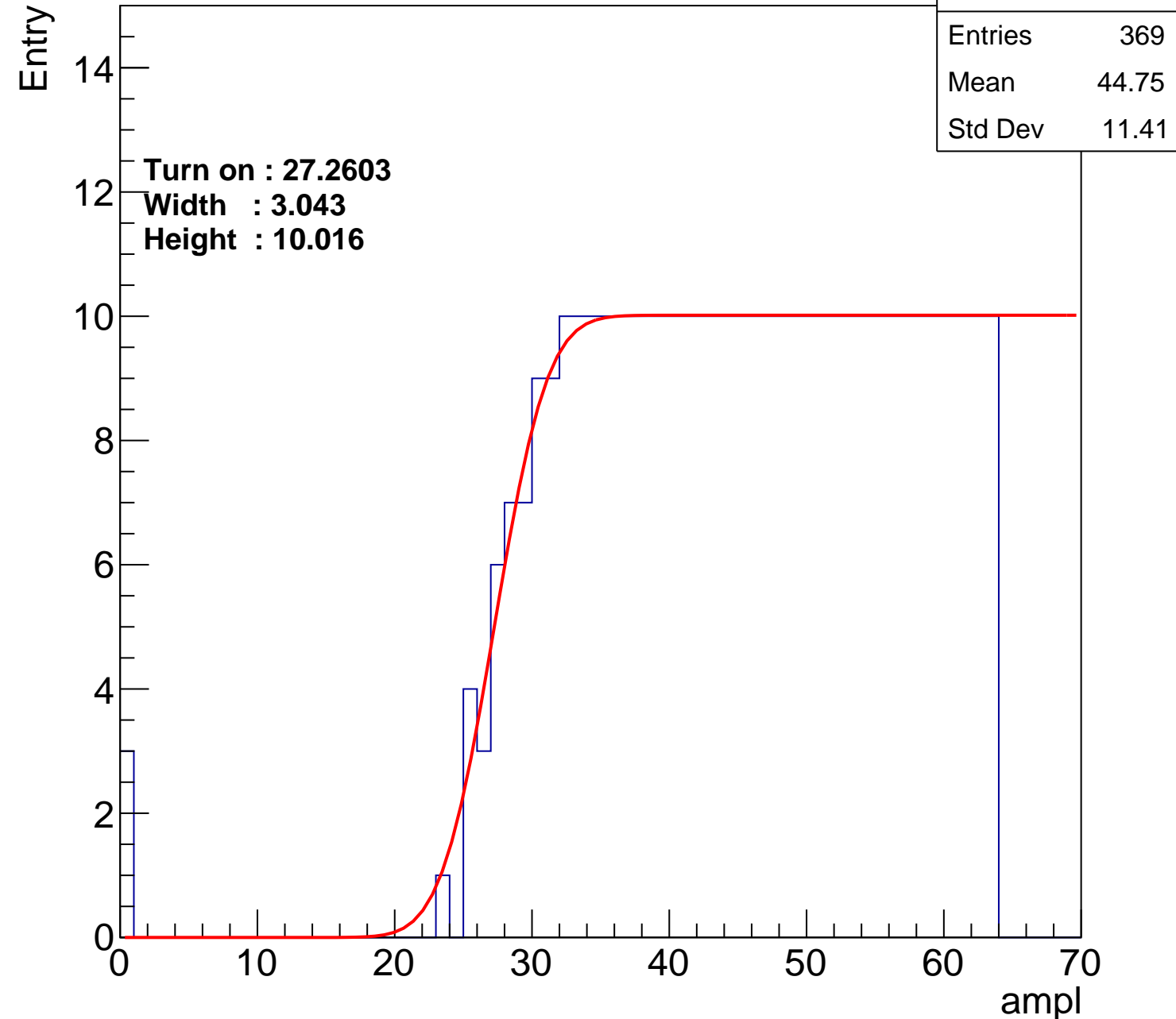
Width : 3.043

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch50

calib_packv5_042523_0143.root, FC#5, port B1

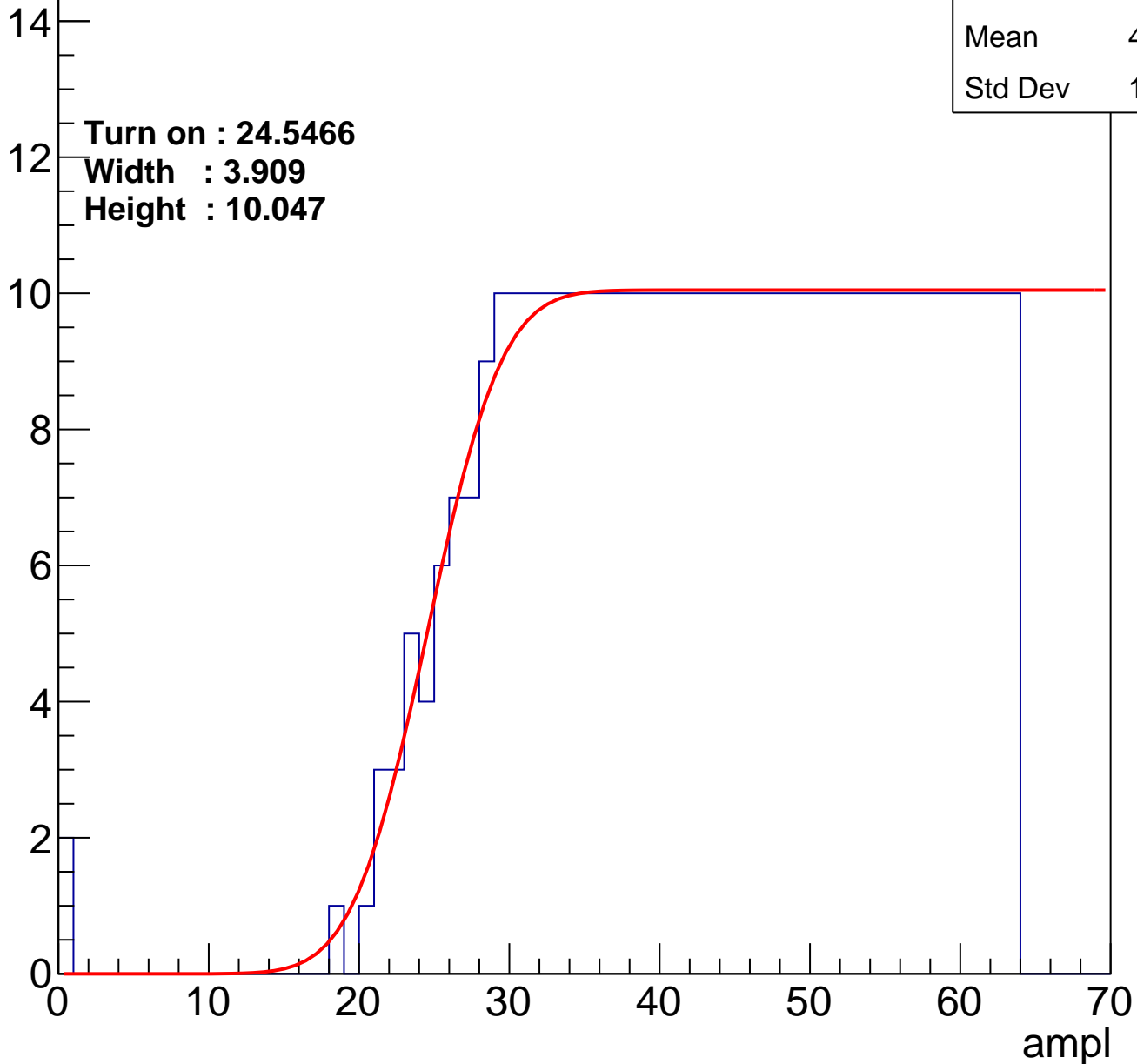
Entries	398
Mean	43.34
Std Dev	12.04

Turn on : 24.5466

Width : 3.909

Height : 10.047

Entry



B0L000S, U16-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.7460

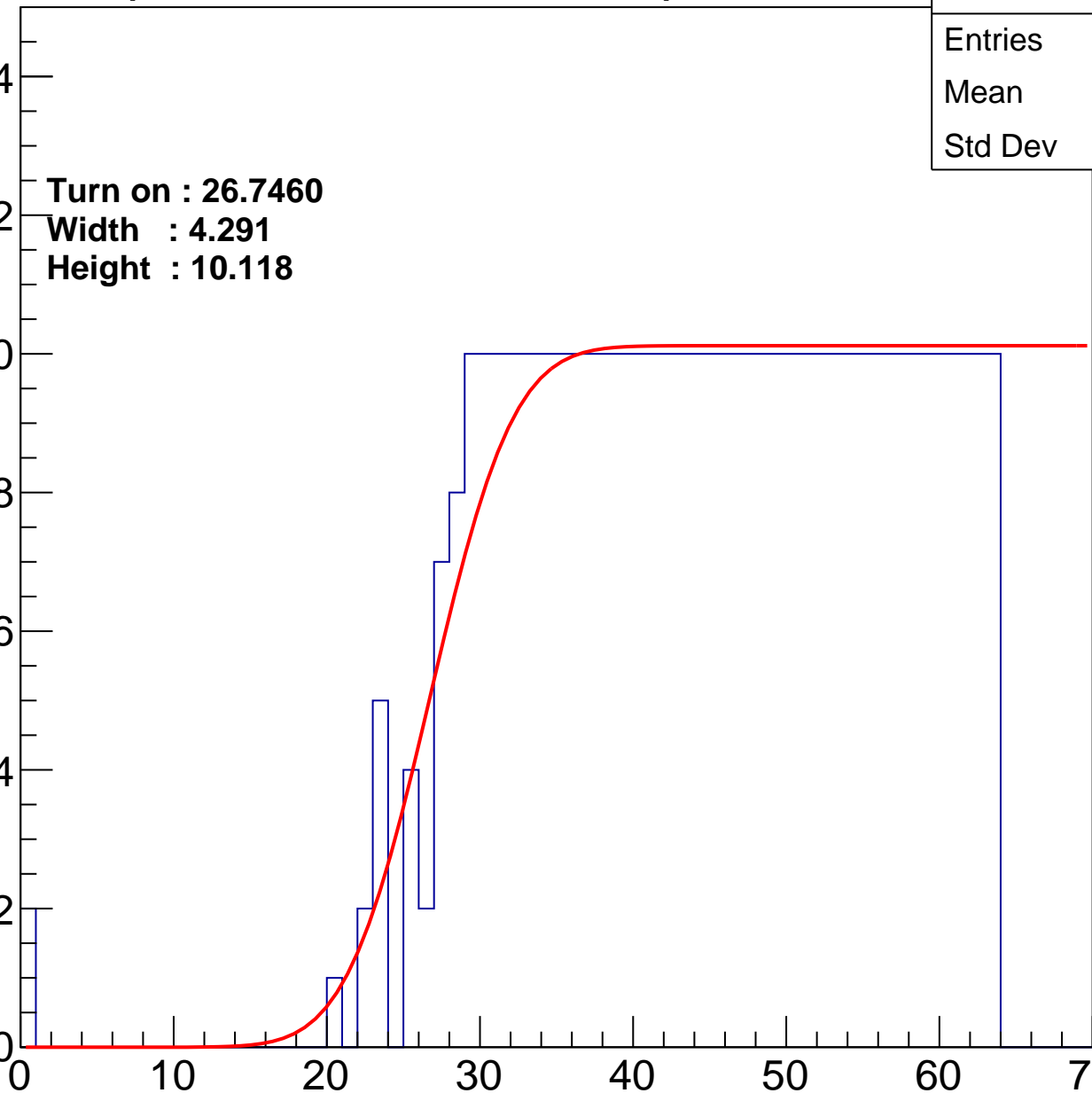
Width : 4.291

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.05
Std Dev	11.64

Turn on : 26.1485

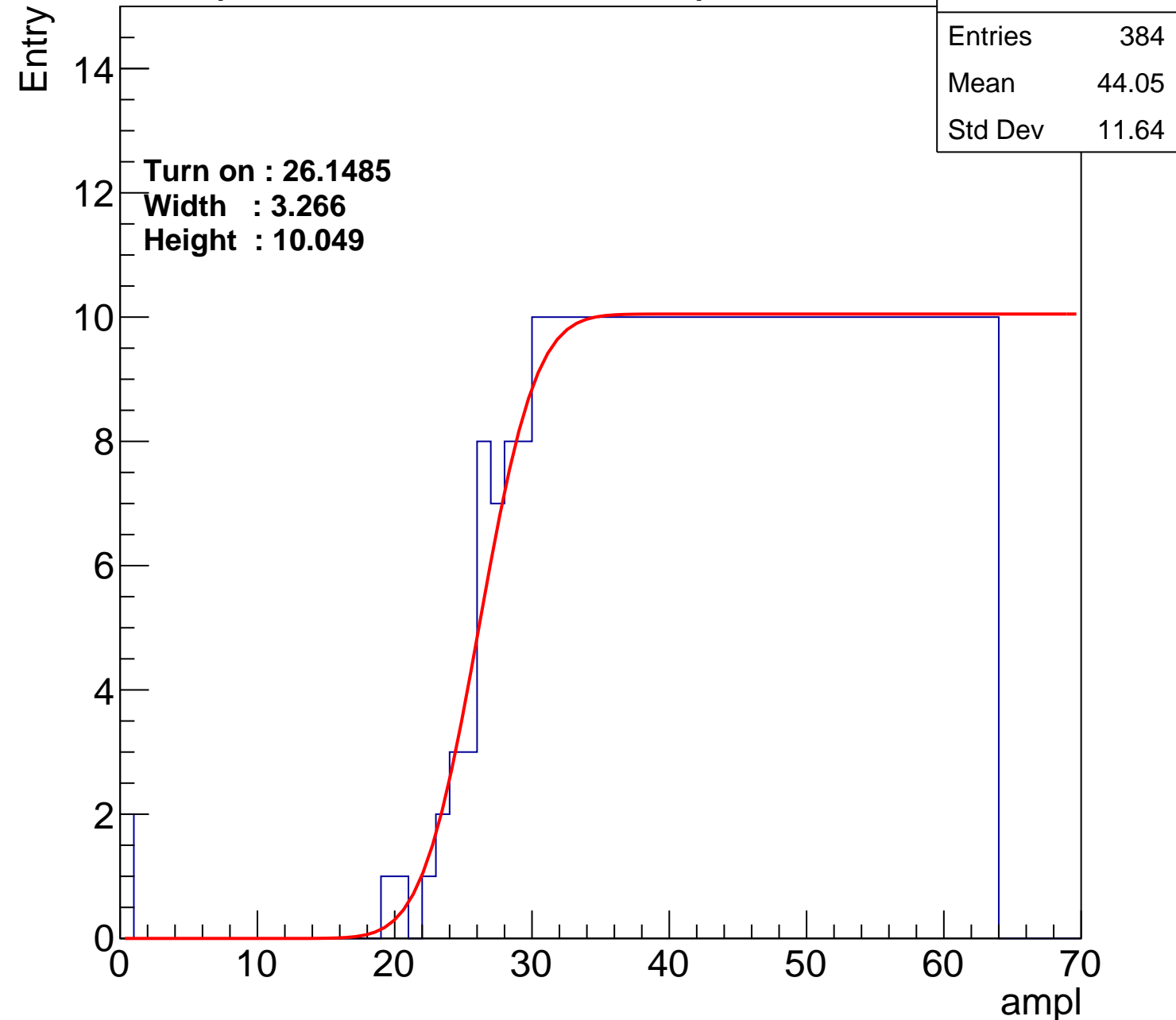
Width : 3.266

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.6285

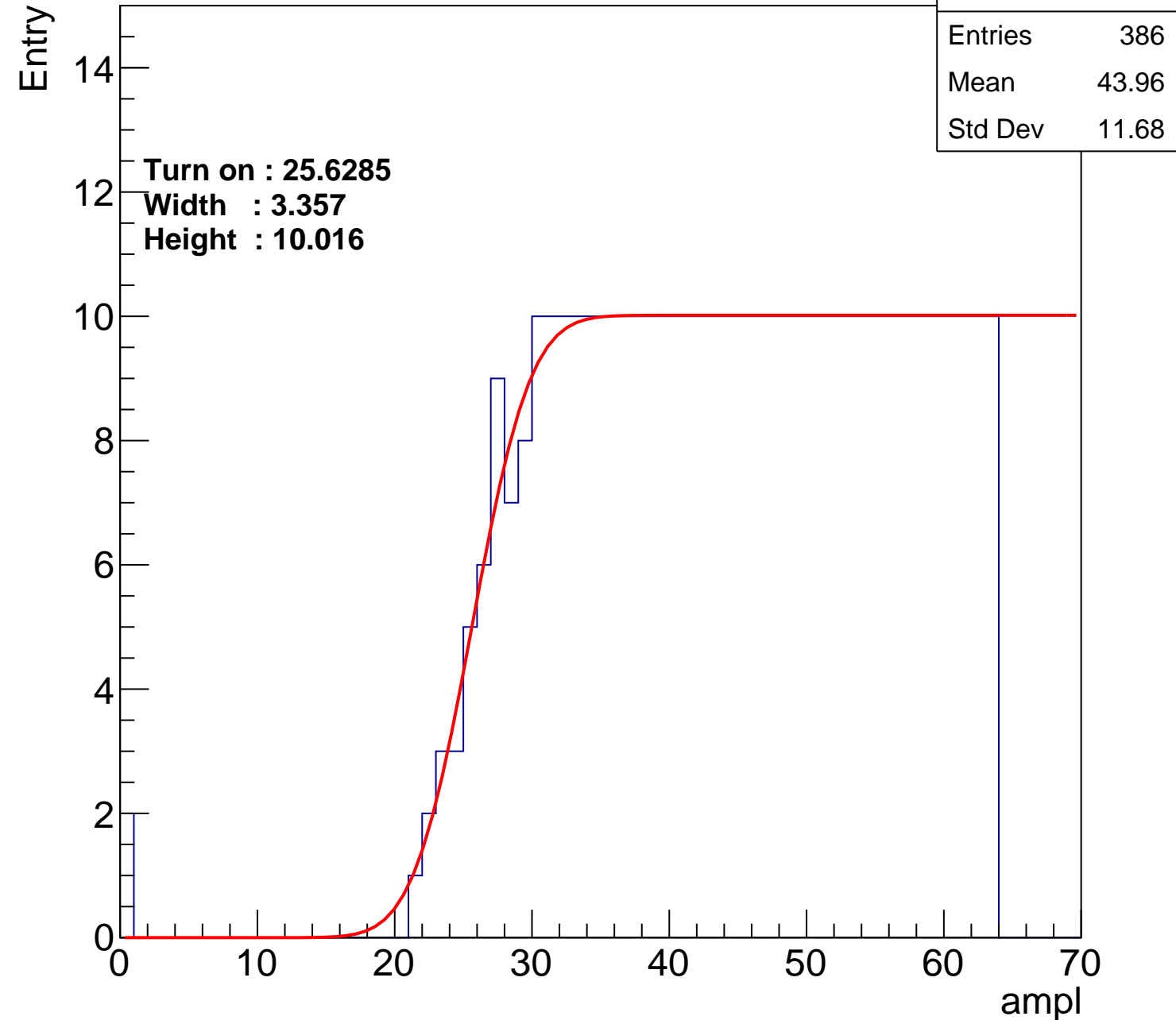
Width : 3.357

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch54

calib_packv5_042523_0143.root, FC#5, port B1

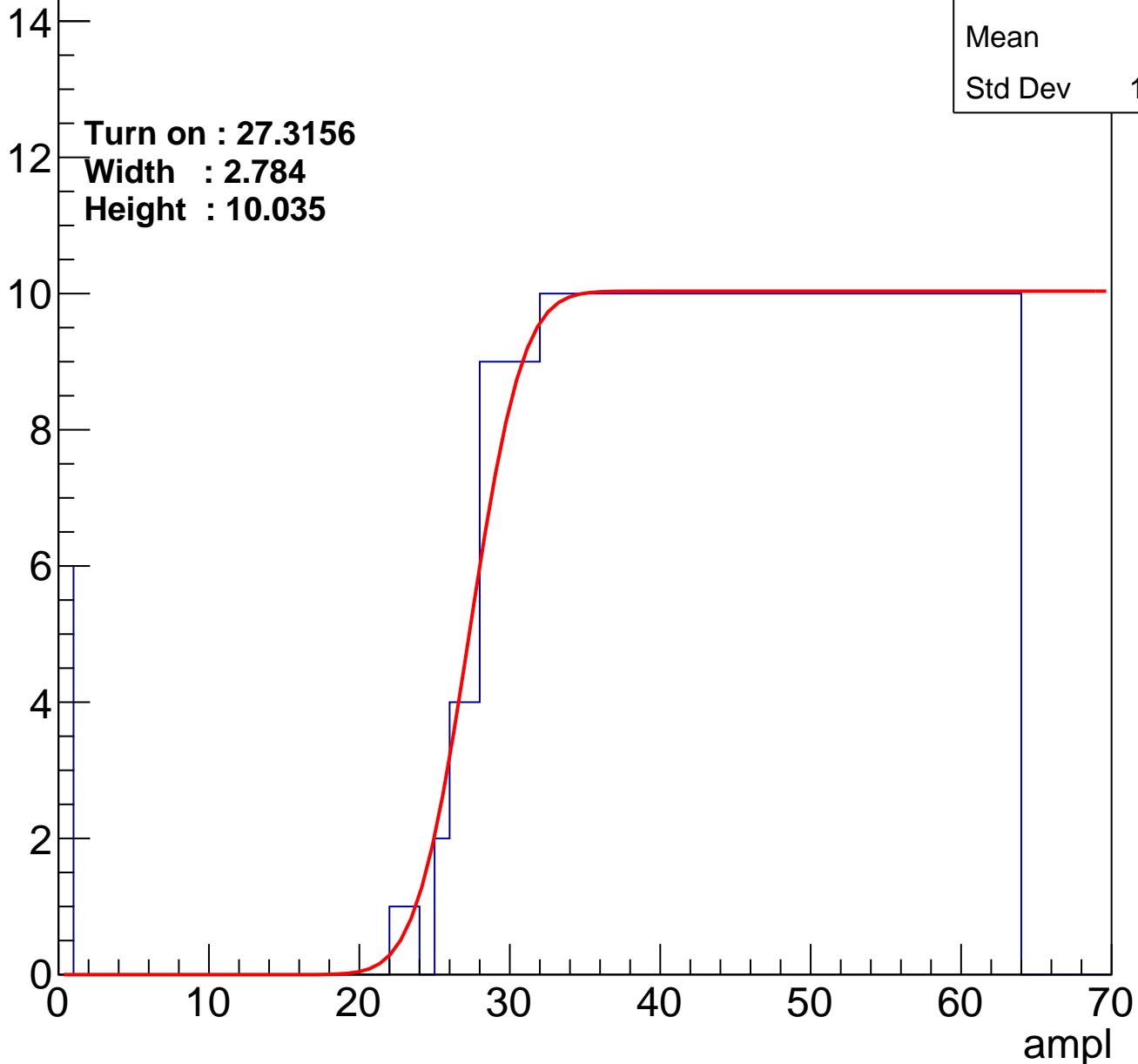
Entries	374
Mean	44.3
Std Dev	12.07

Turn on : 27.3156

Width : 2.784

Height : 10.035

Entry



B0L000S, U16-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 26.2466

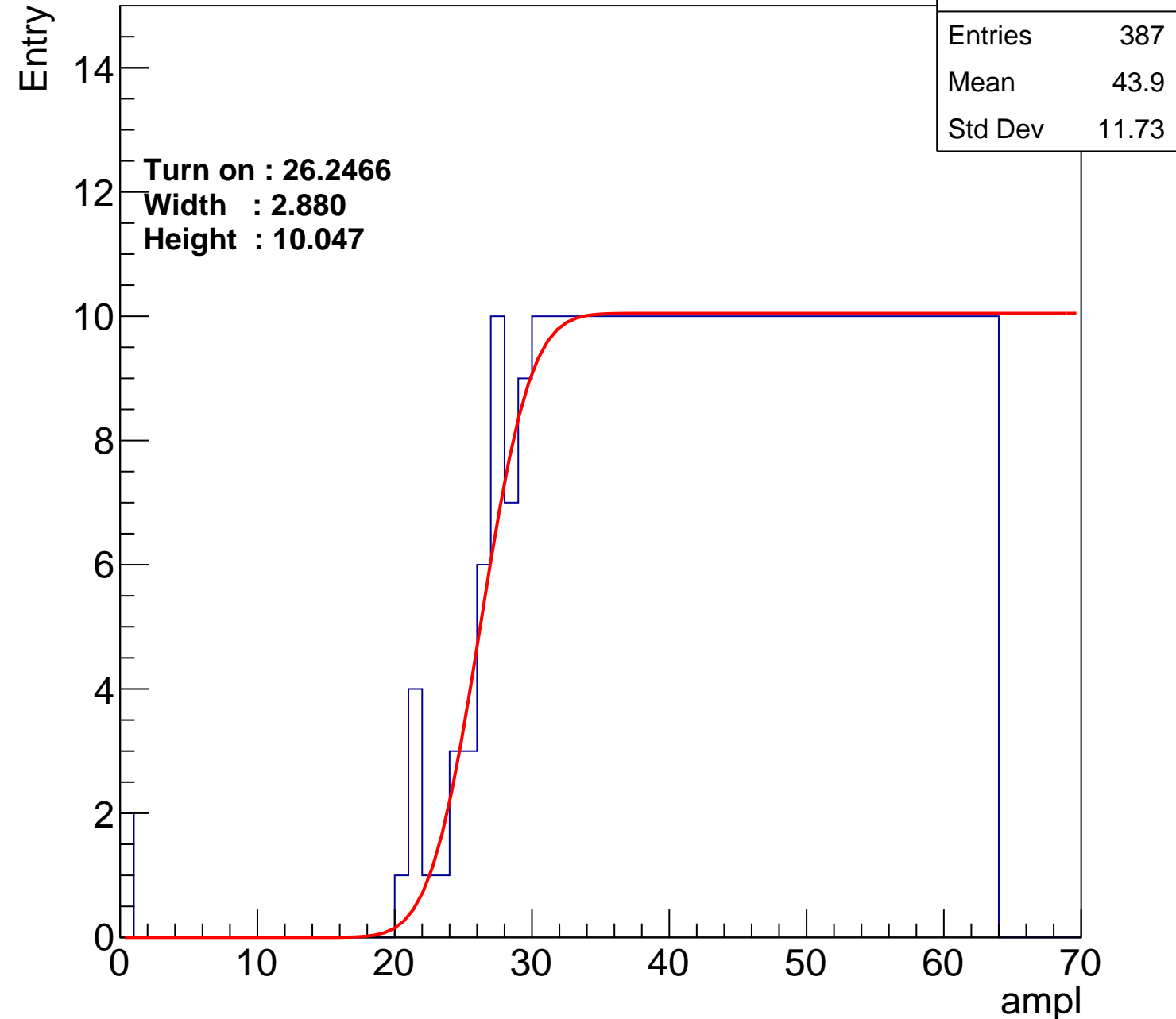
Width : 2.880

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch56

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.7653

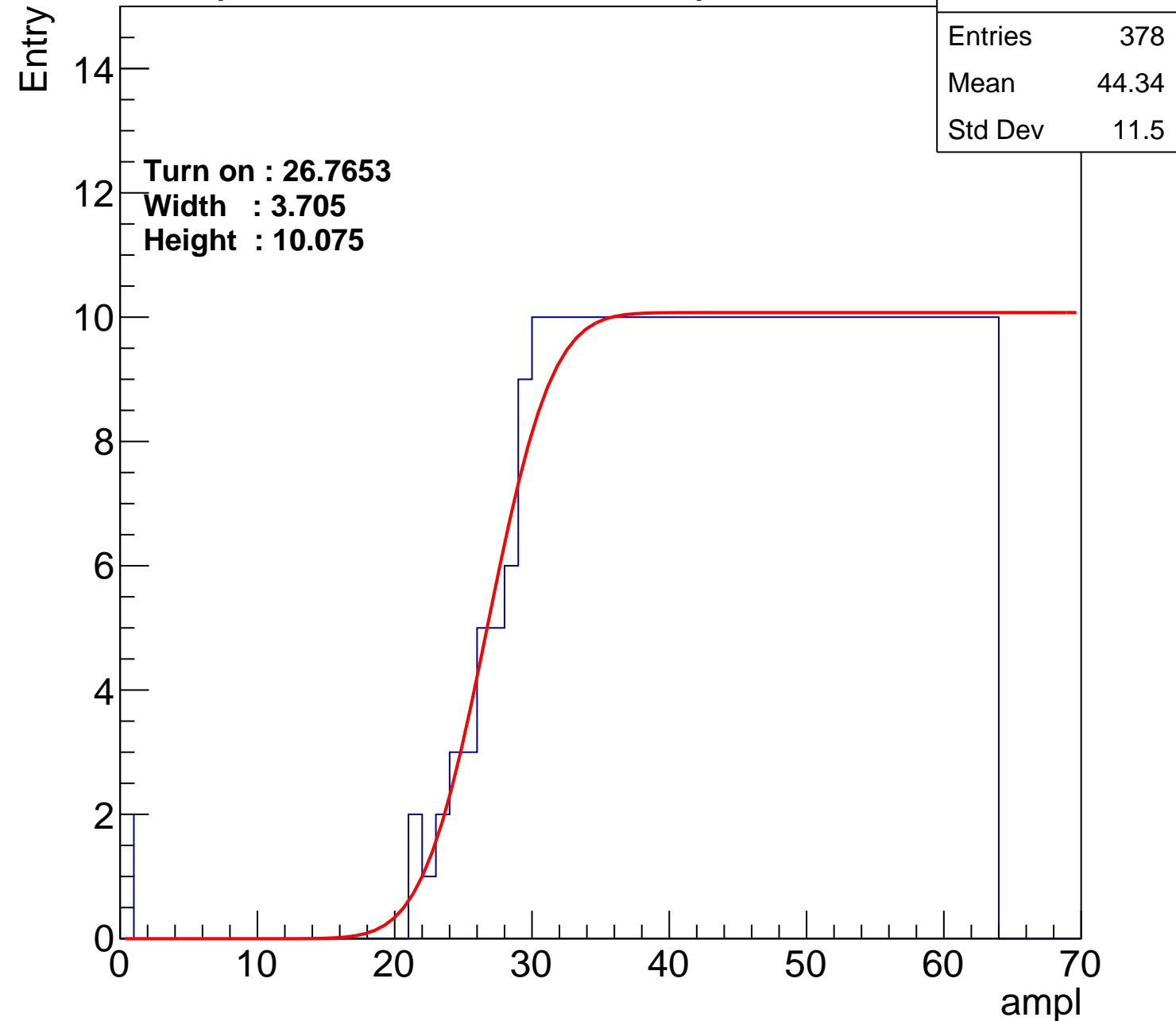
Width : 3.705

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.47
Std Dev	11.41

Turn on : 26.9323

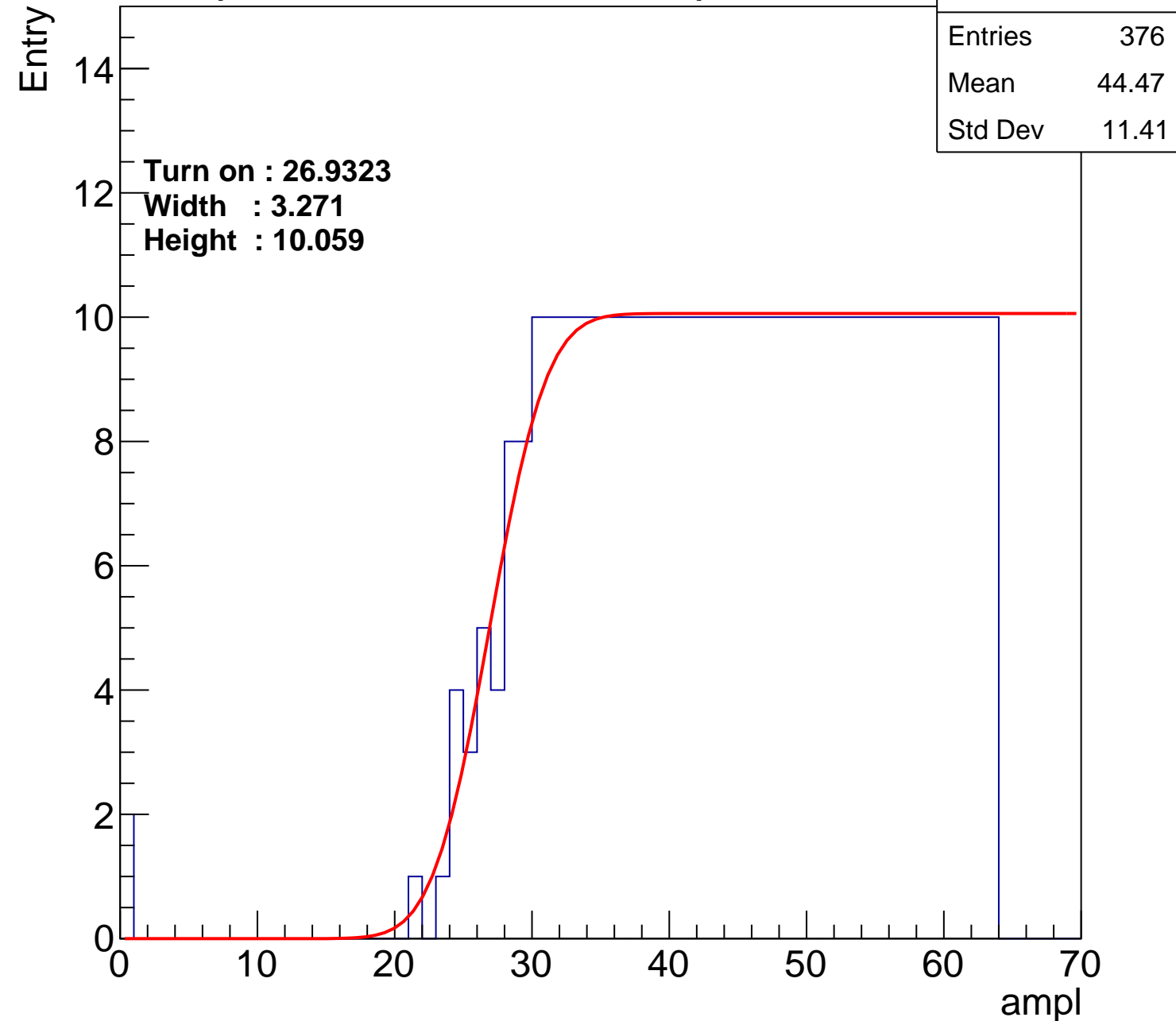
Width : 3.271

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch58

calib_packv5_042523_0143.root, FC#5, port B1

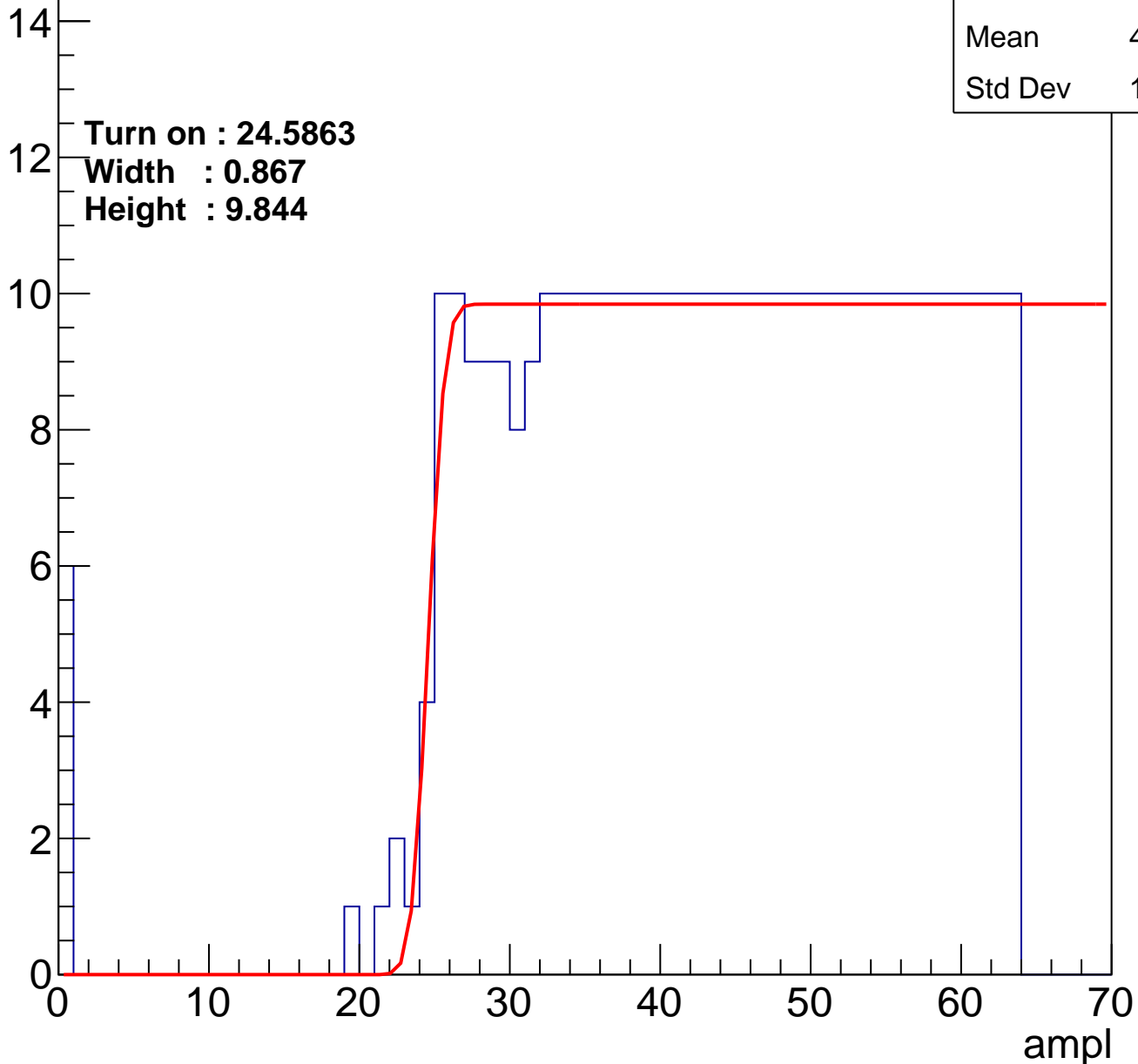
Entries	399
Mean	43.08
Std Dev	12.62

Turn on : 24.5863

Width : 0.867

Height : 9.844

Entry



B0L000S, U16-ch59

calib_packv5_042523_0143.root, FC#5, port B1

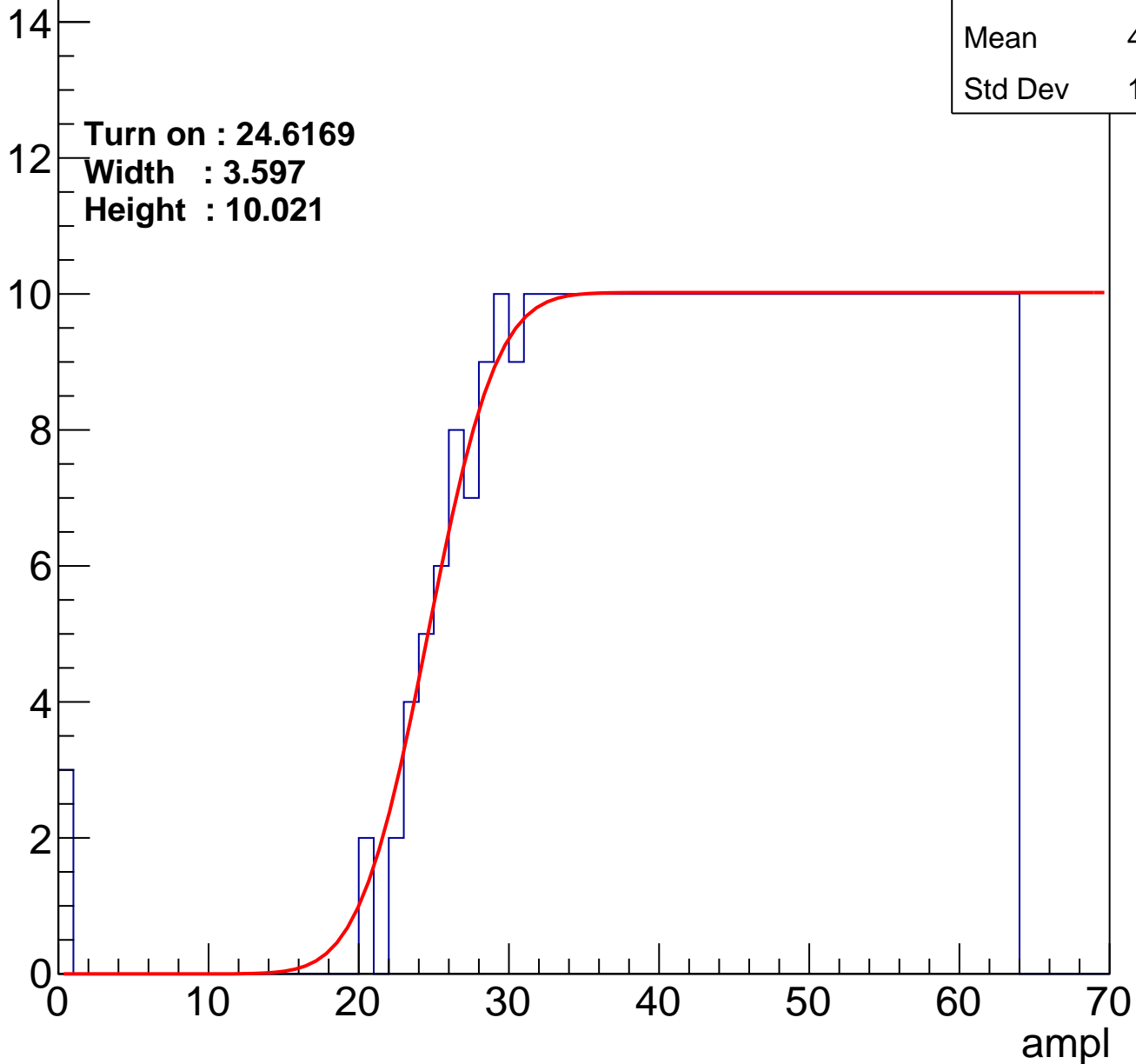
Entries	395
Mean	43.46
Std Dev	12.07

Turn on : 24.6169

Width : 3.597

Height : 10.021

Entry



B0L000S, U16-ch60

calib_packv5_042523_0143.root, FC#5, port B1

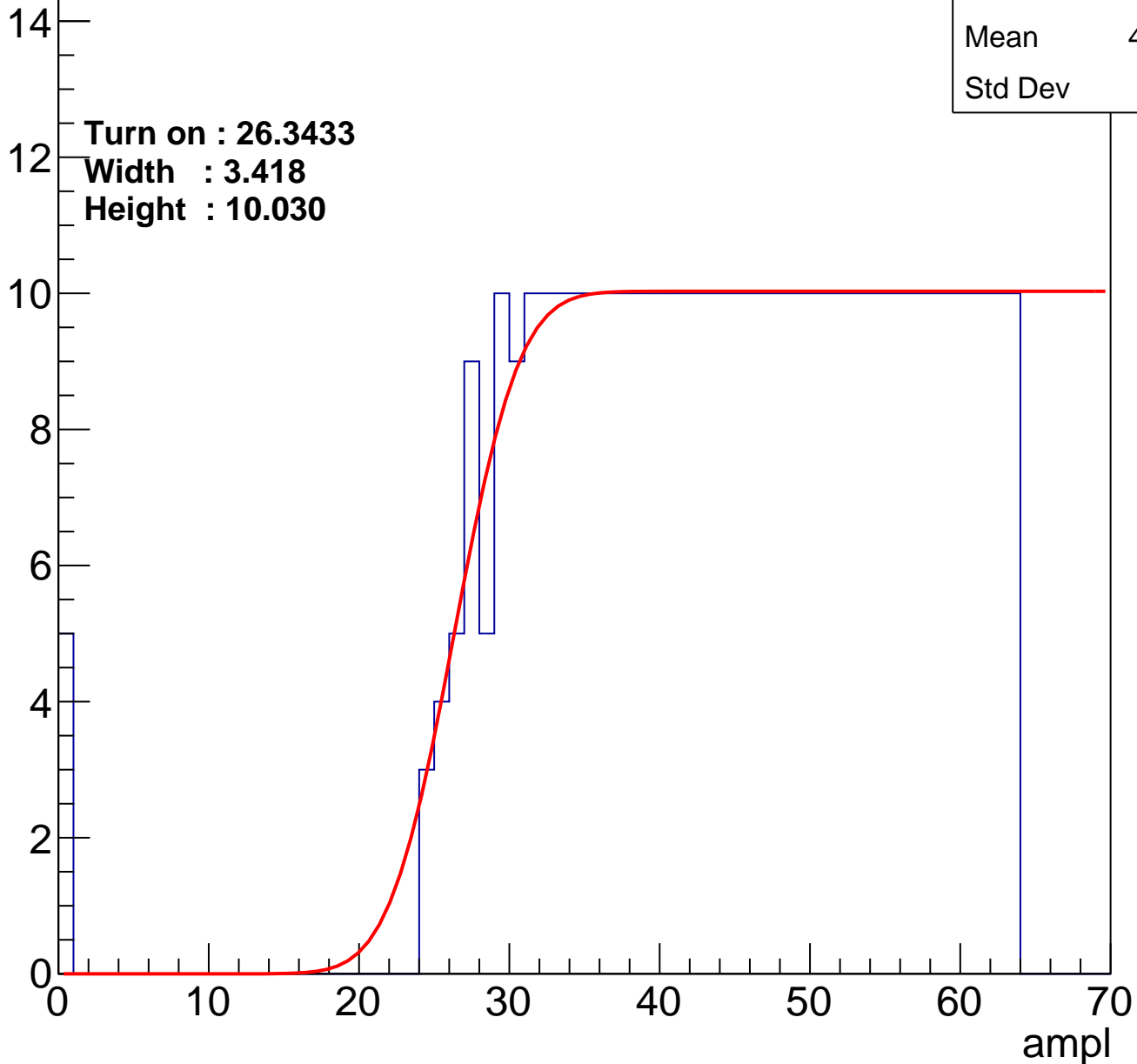
Entries	380
Mean	44.09
Std Dev	12

Turn on : 26.3433

Width : 3.418

Height : 10.030

Entry



B0L000S, U16-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.7295

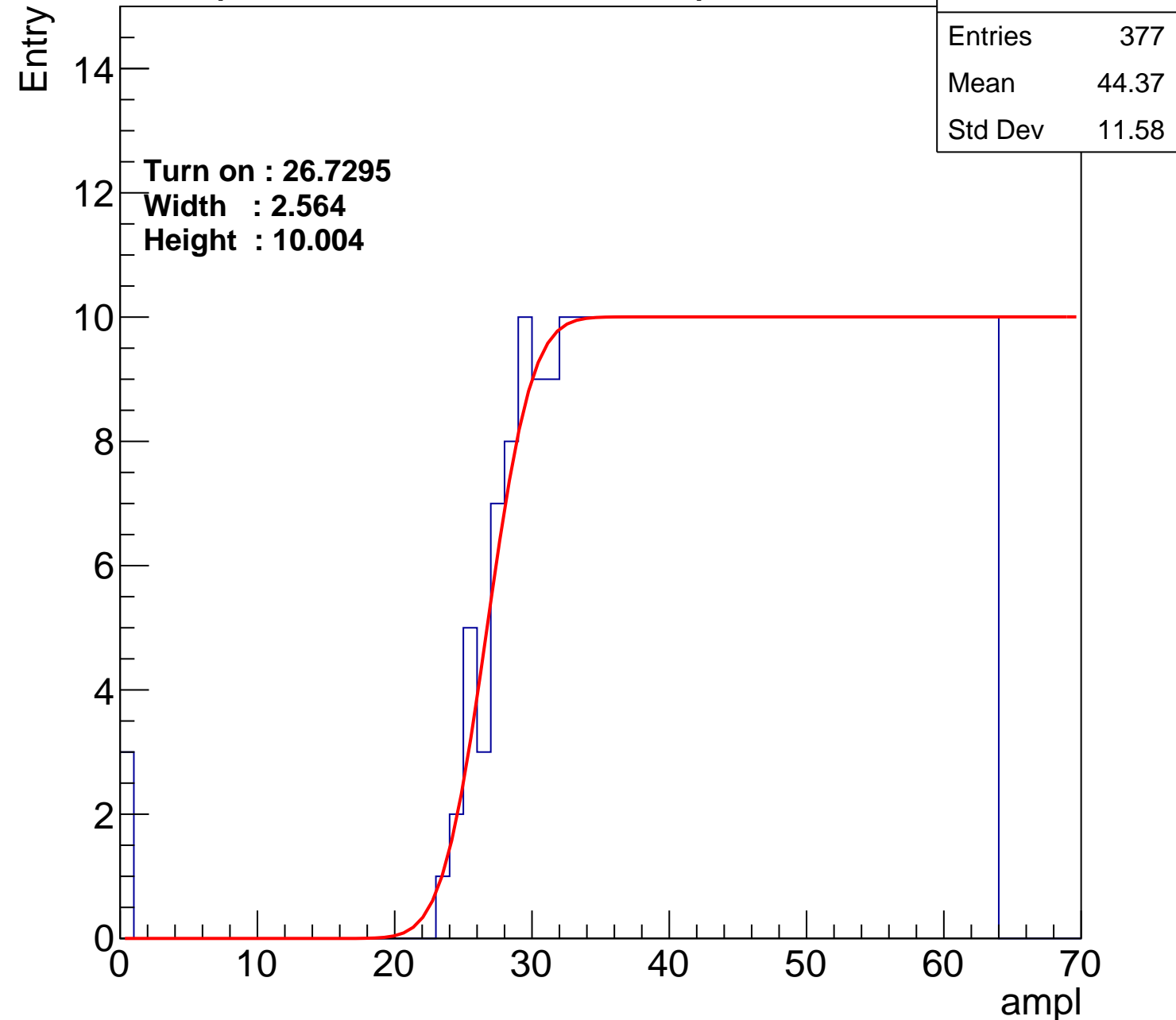
Width : 2.564

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch62

calib_packv5_042523_0143.root, FC#5, port B1

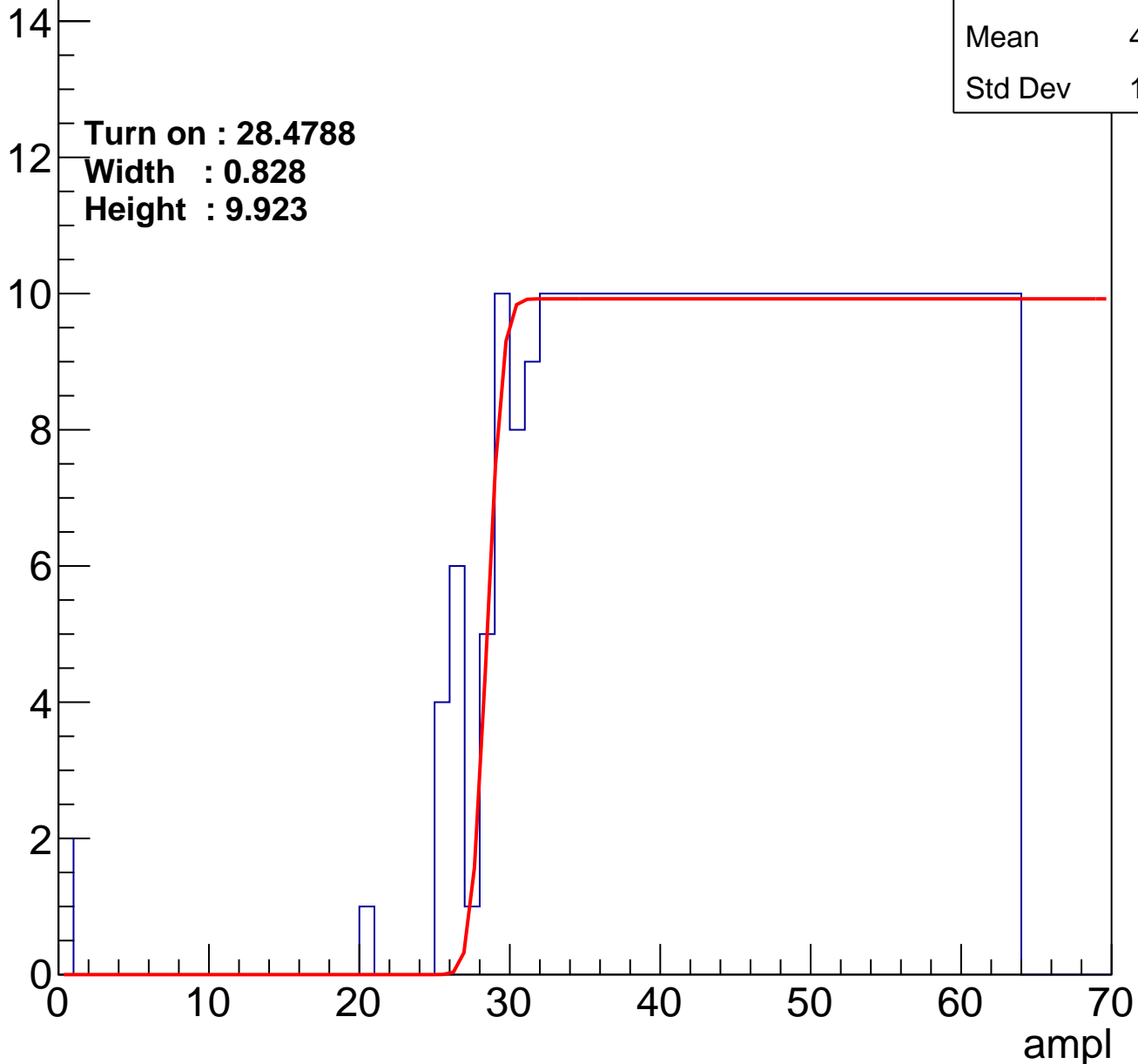
Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 28.4788

Width : 0.828

Height : 9.923

Entry



B0L000S, U16-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.95
Std Dev	11.12

Turn on : 27.8176

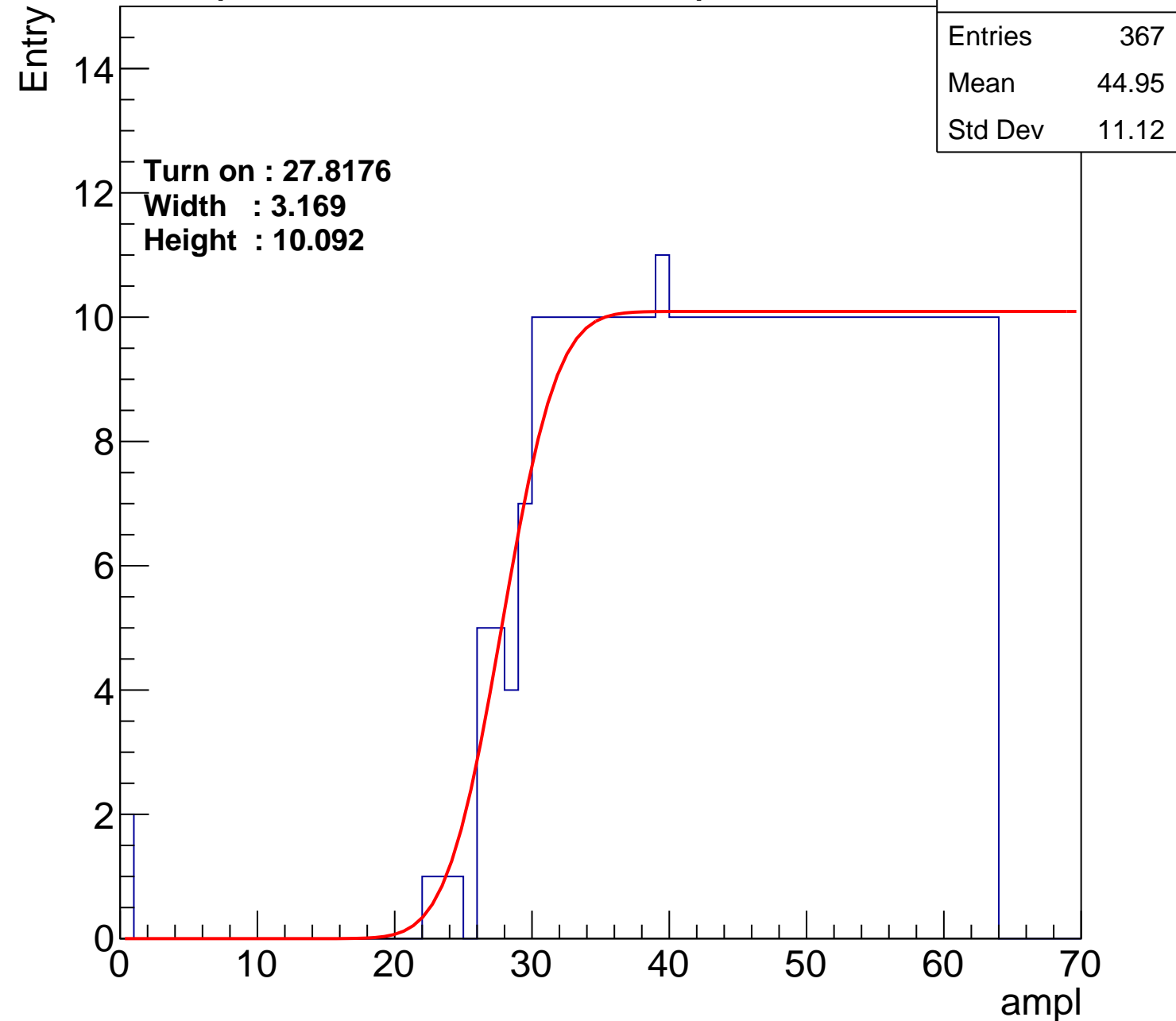
Width : 3.169

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.2
Std Dev	11.99

Turn on : 26.8555

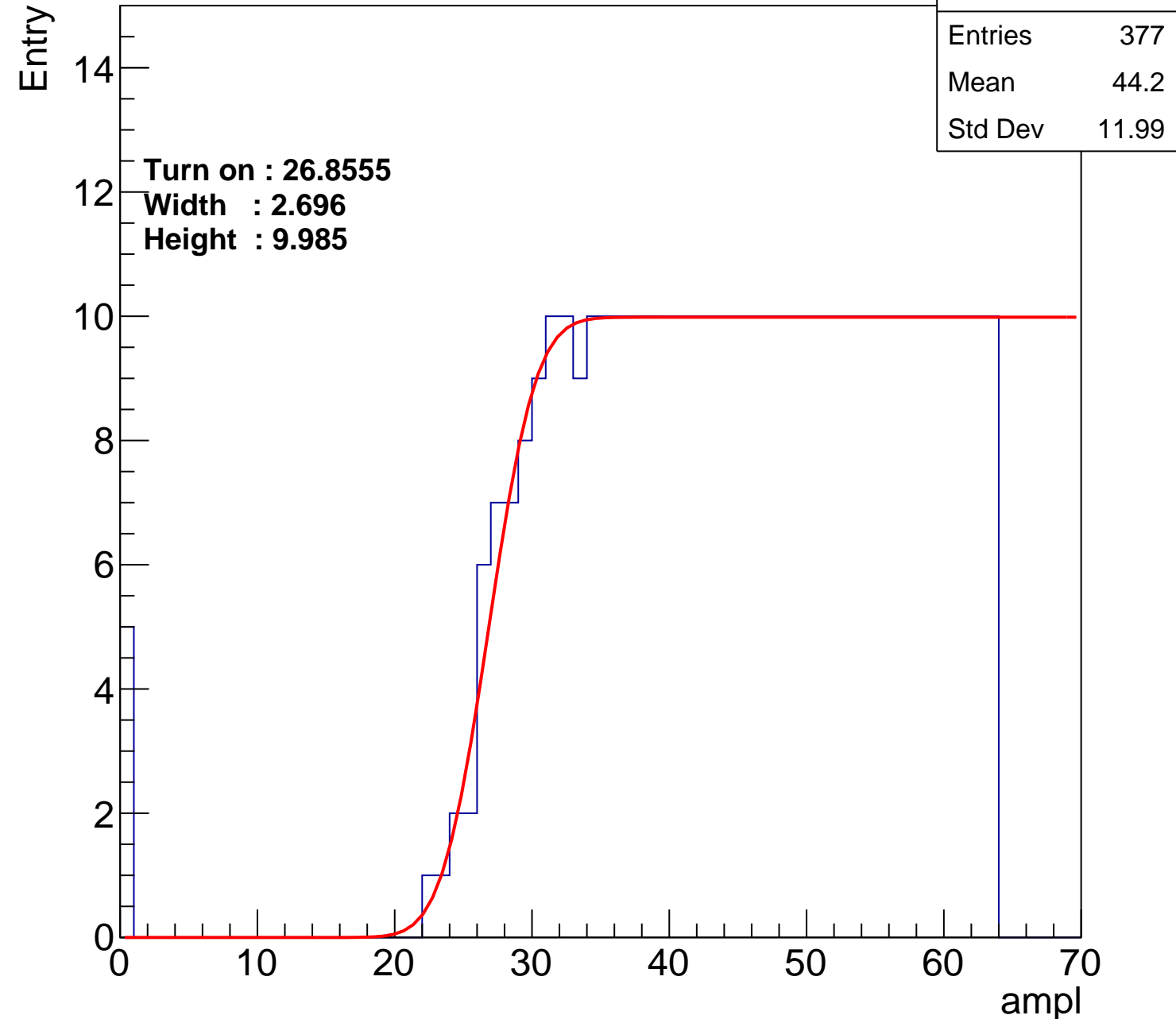
Width : 2.696

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.53
Std Dev	11.62

Turn on : 27.2022

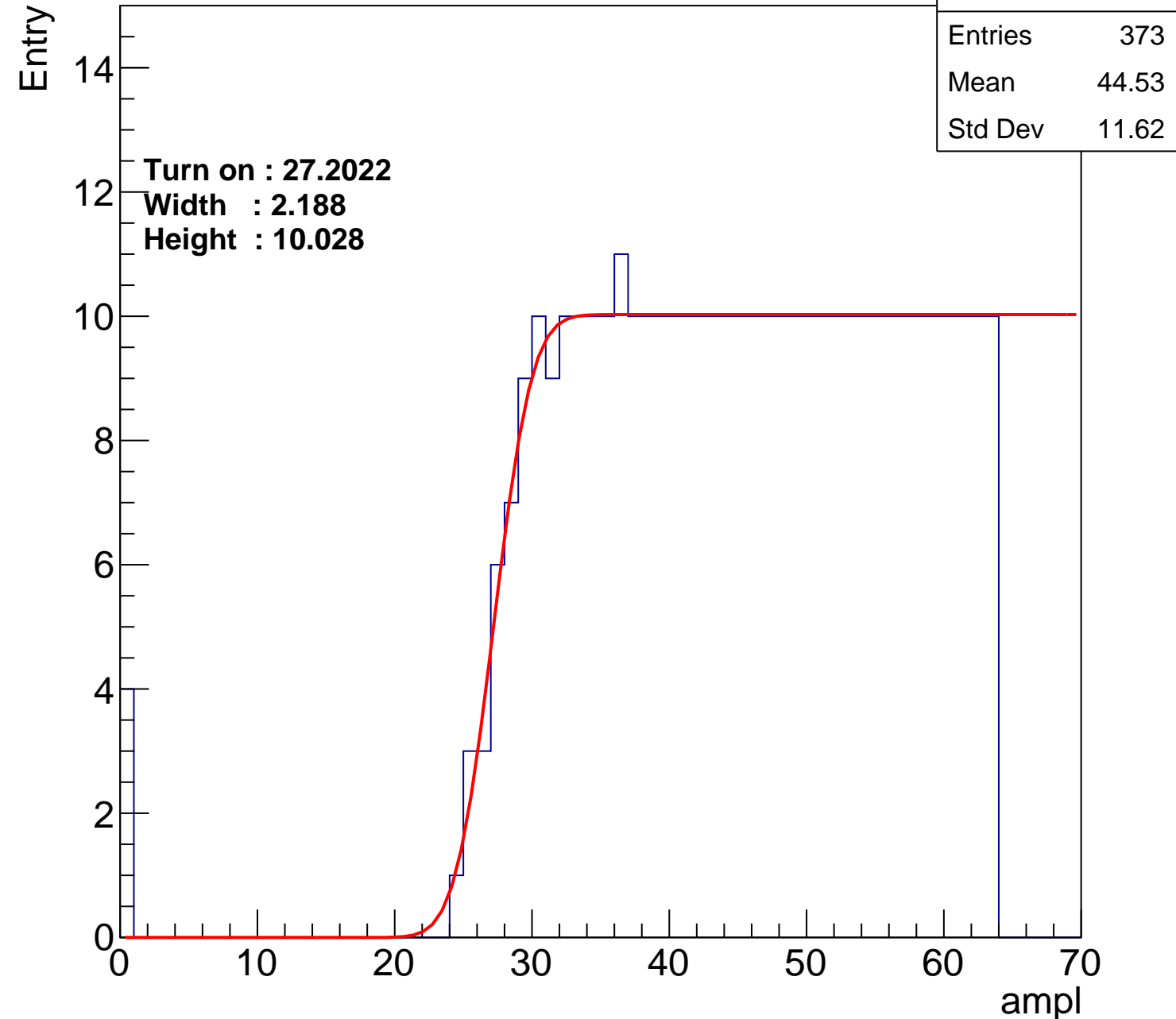
Width : 2.188

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch66

calib_packv5_042523_0143.root, FC#5, port B1

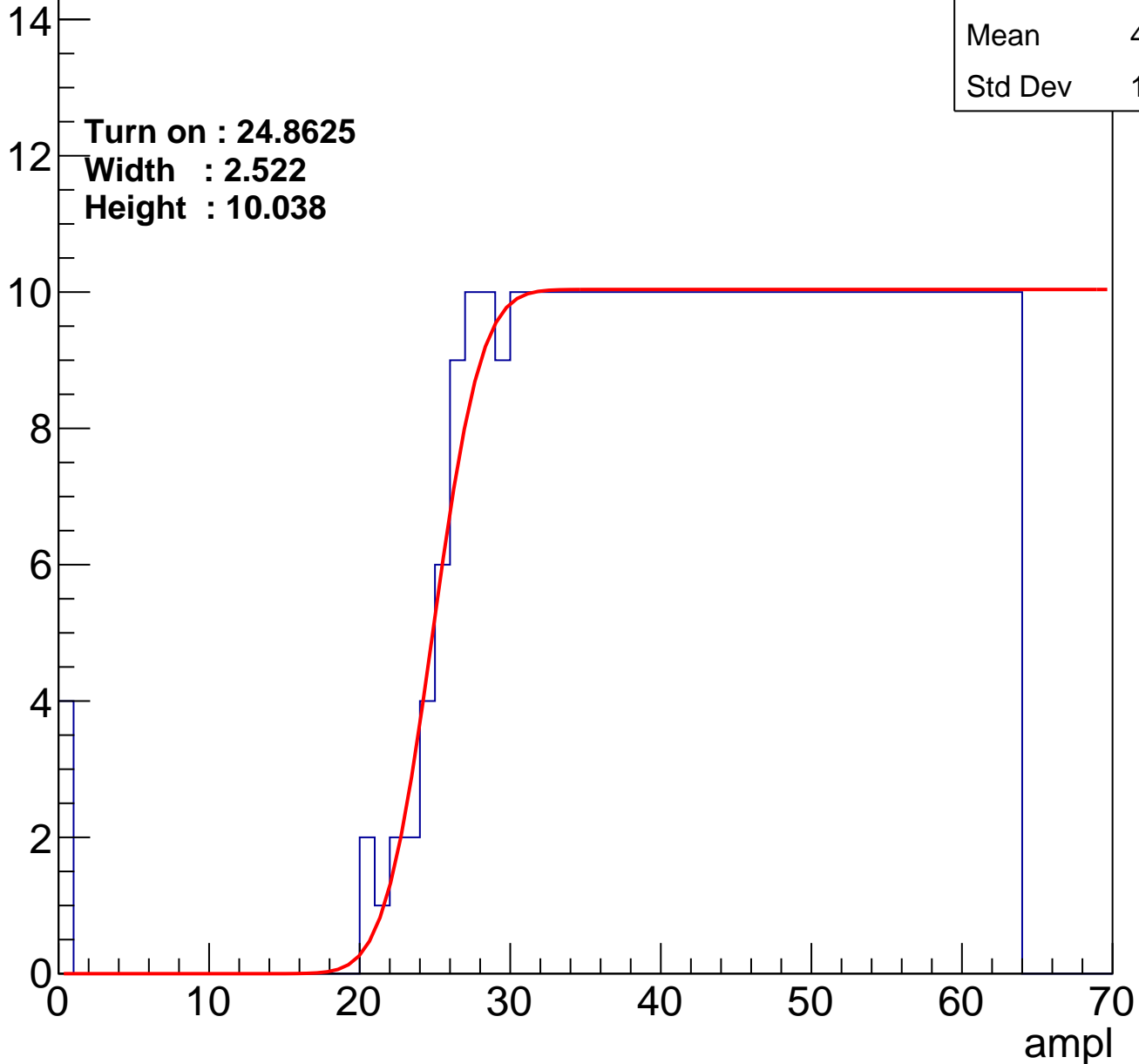
Entries	399
Mean	43.24
Std Dev	12.27

Turn on : 24.8625

Width : 2.522

Height : 10.038

Entry



B0L000S, U16-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.1585

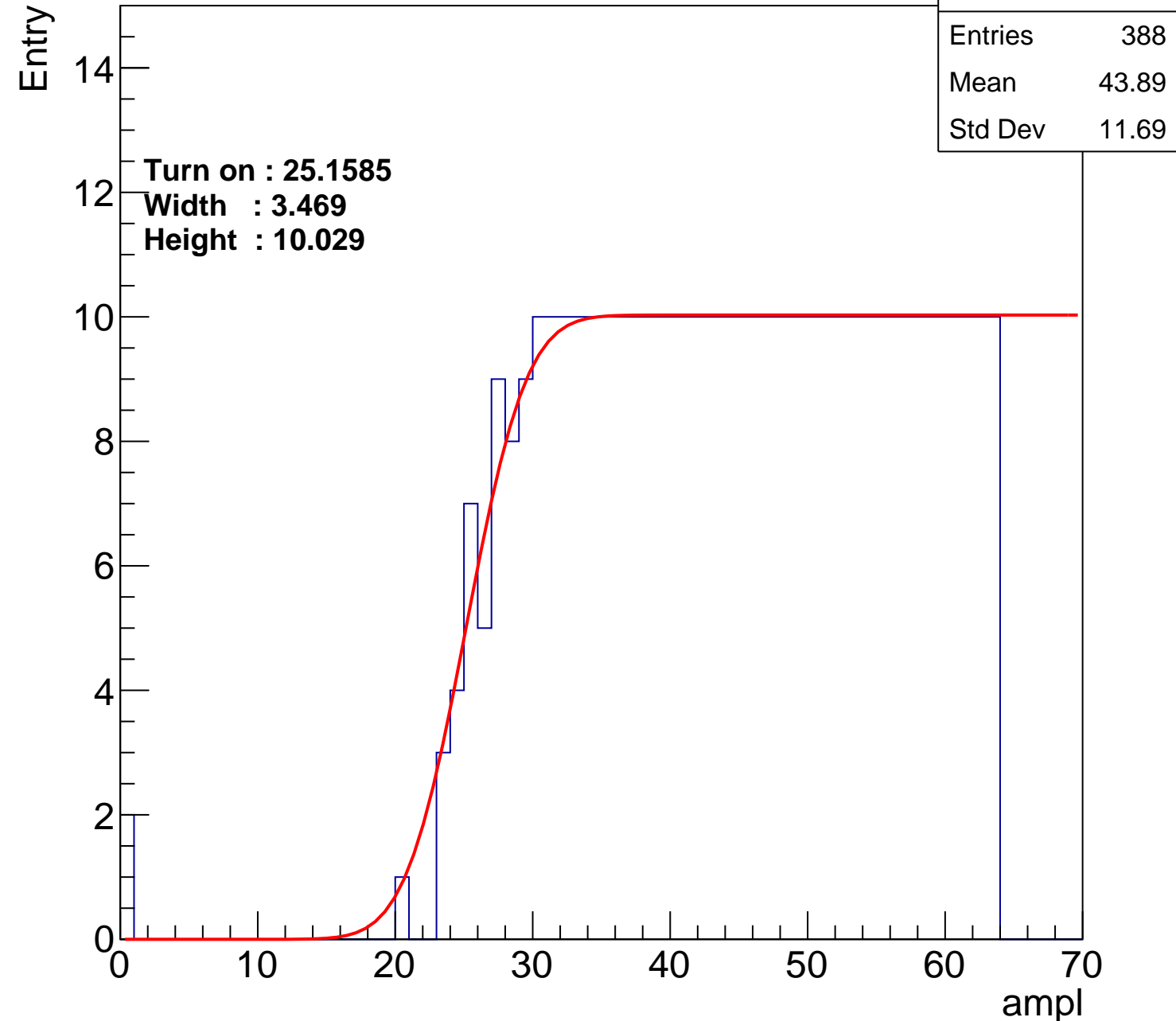
Width : 3.469

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch68

calib_packv5_042523_0143.root, FC#5, port B1

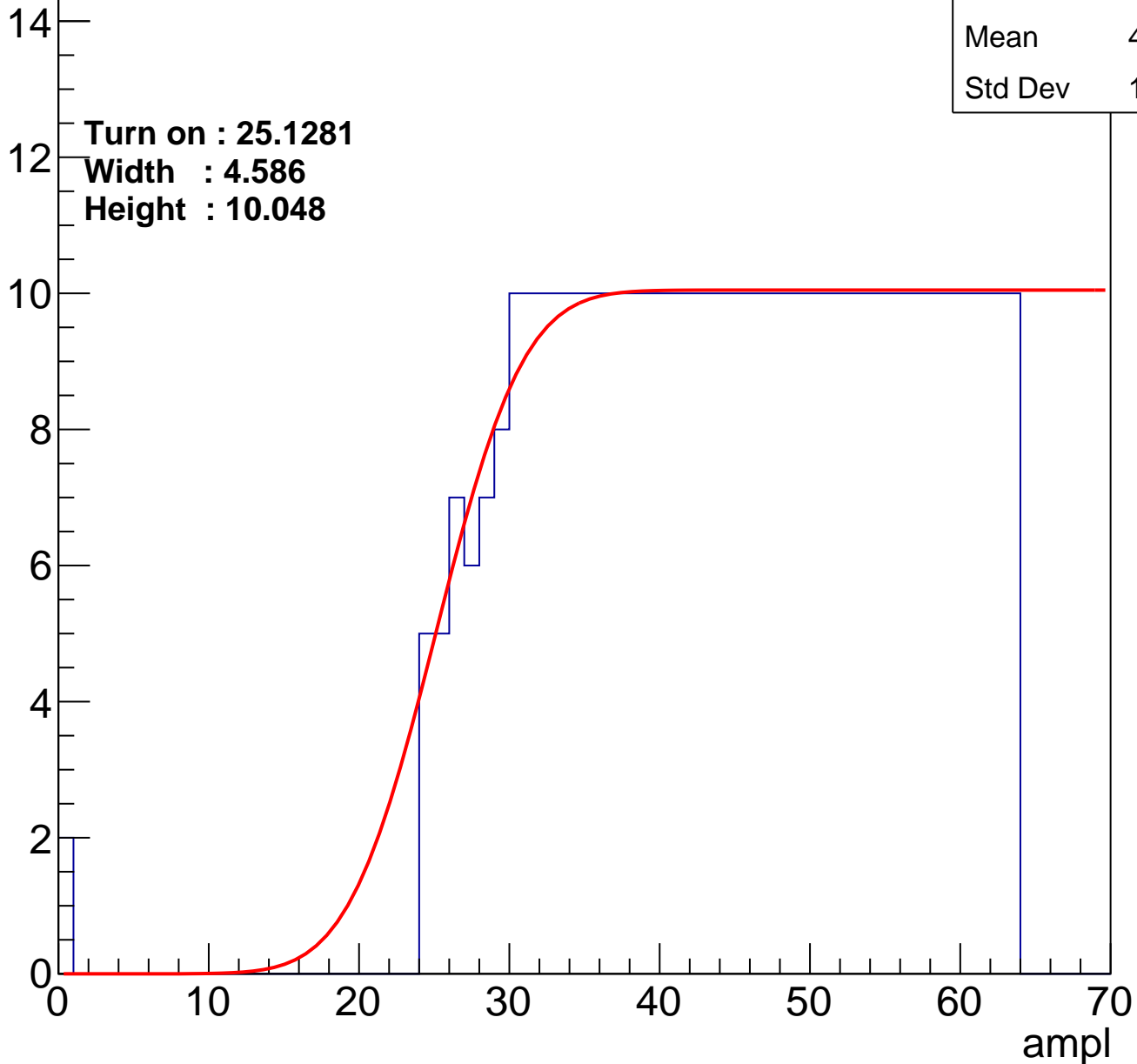
Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 25.1281

Width : 4.586

Height : 10.048

Entry



B0L000S, U16-ch69

calib_packv5_042523_0143.root, FC#5, port B1

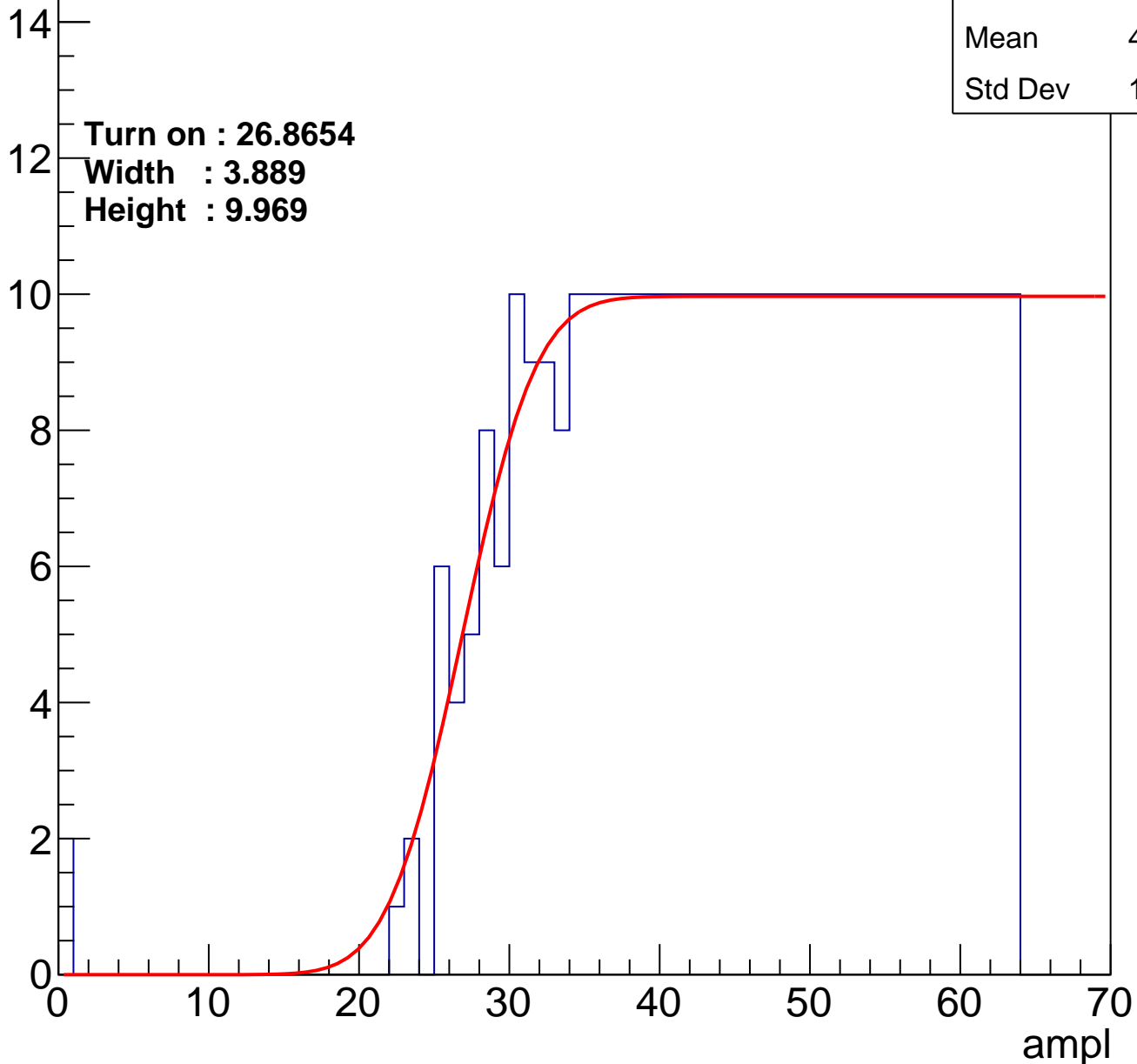
Entries	370
Mean	44.69
Std Dev	11.35

Turn on : 26.8654

Width : 3.889

Height : 9.969

Entry



B0L000S, U16-ch70

calib_packv5_042523_0143.root, FC#5, port B1

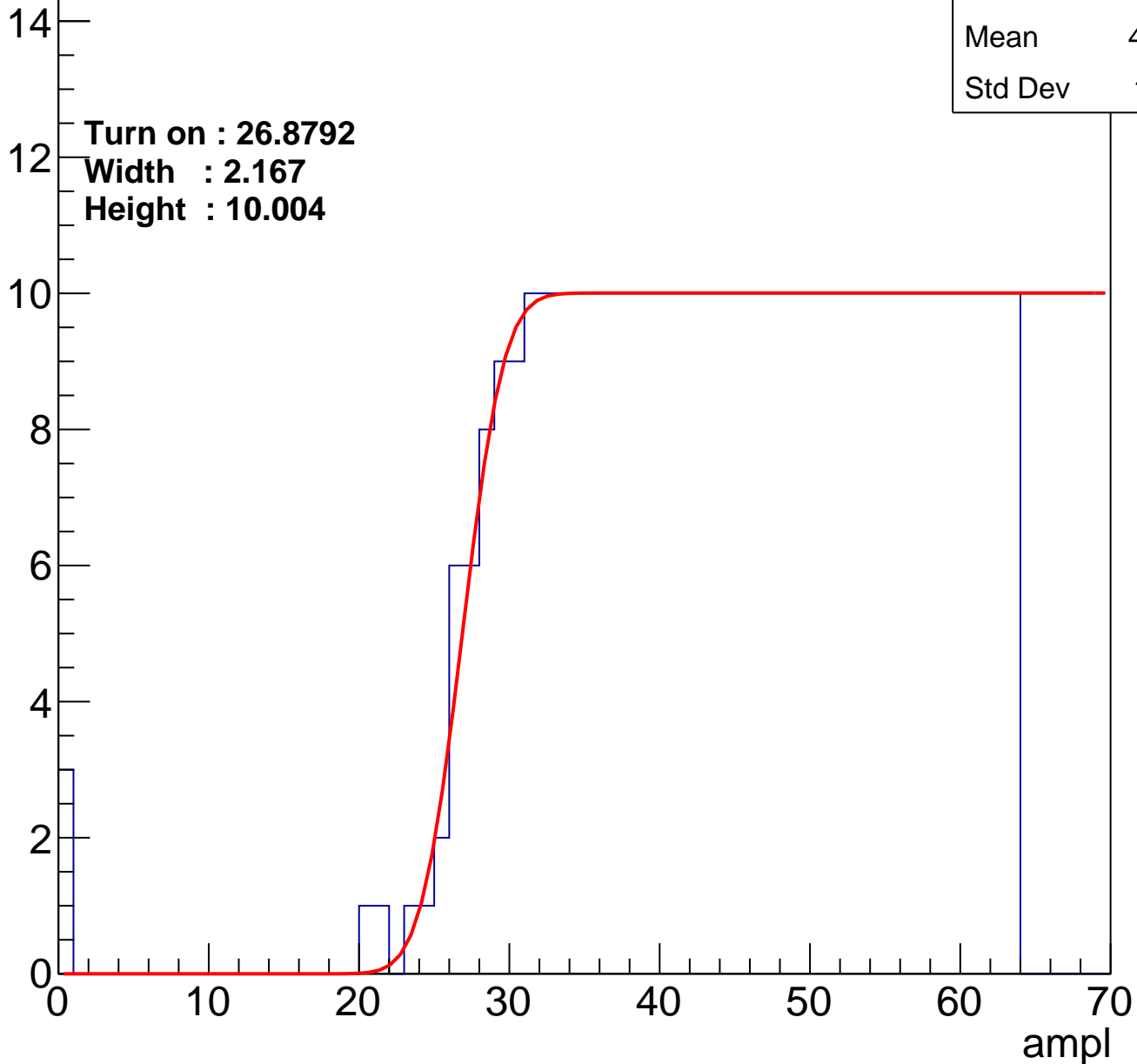
Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 26.8792

Width : 2.167

Height : 10.004

Entry



B0L000S, U16-ch71

calib_packv5_042523_0143.root, FC#5, port B1

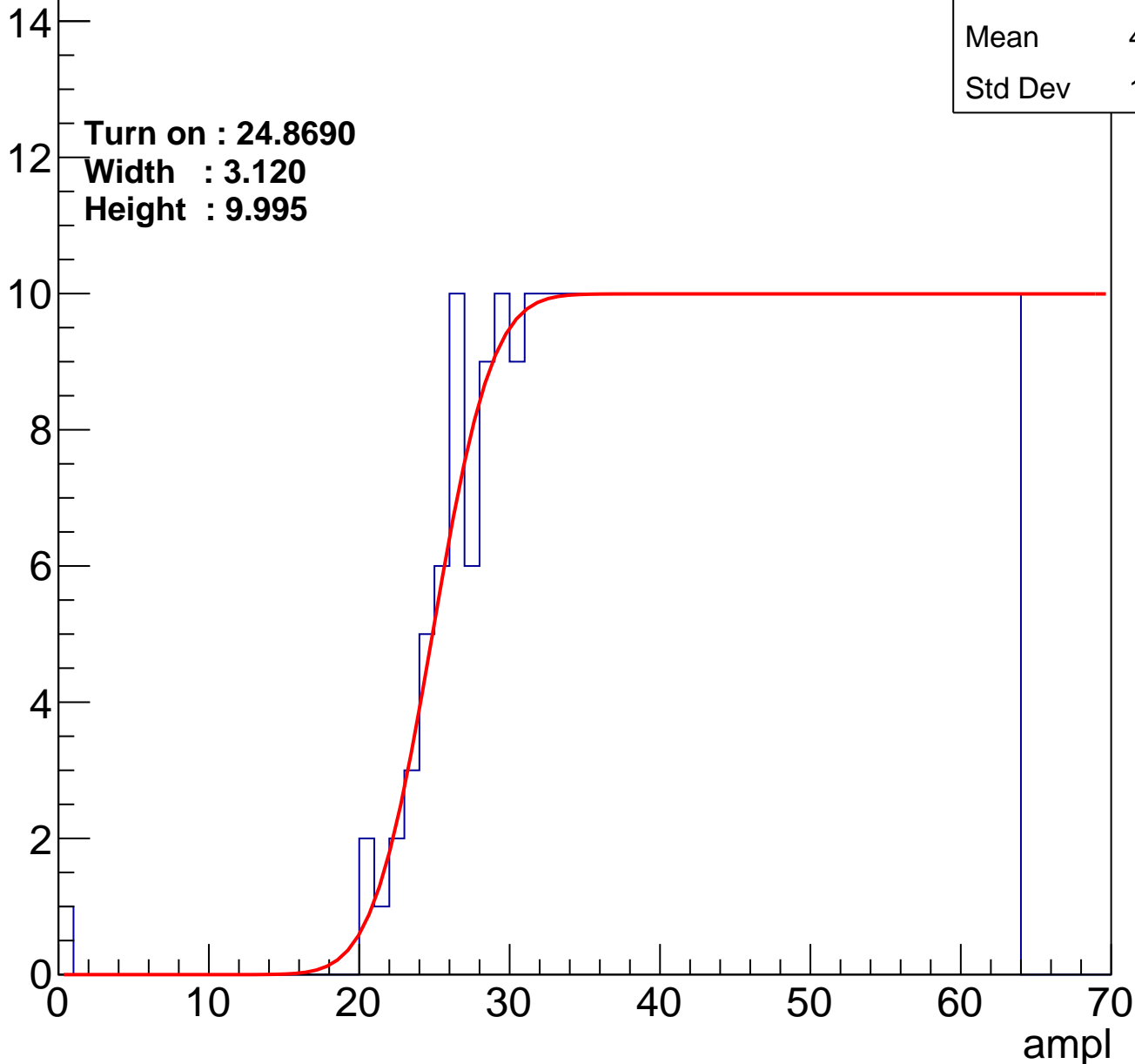
Entries	394
Mean	43.62
Std Dev	11.73

Turn on : 24.8690

Width : 3.120

Height : 9.995

Entry



B0L000S, U16-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.95
Std Dev	11.69

Turn on : 25.8641

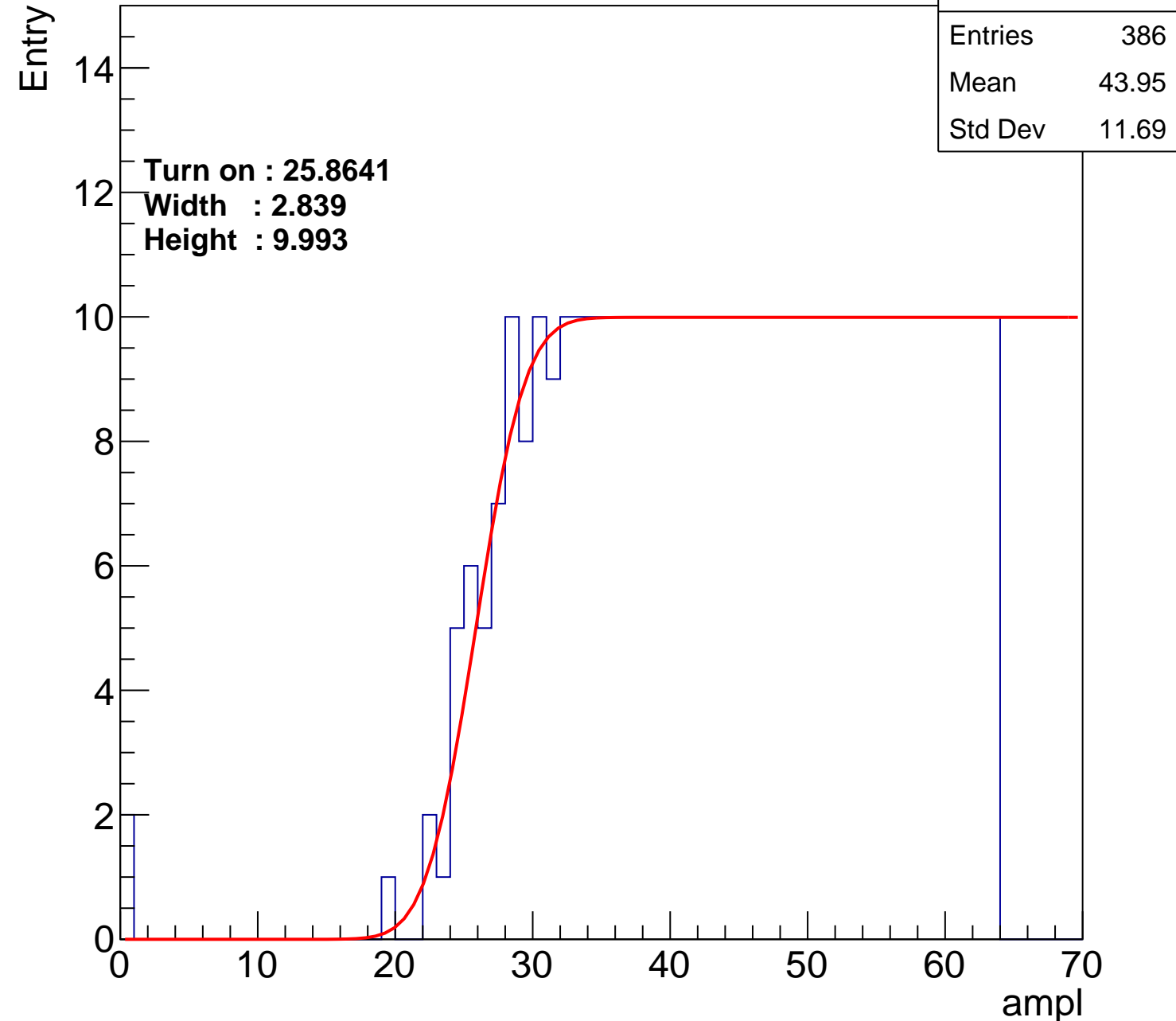
Width : 2.839

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch73

calib_packv5_042523_0143.root, FC#5, port B1

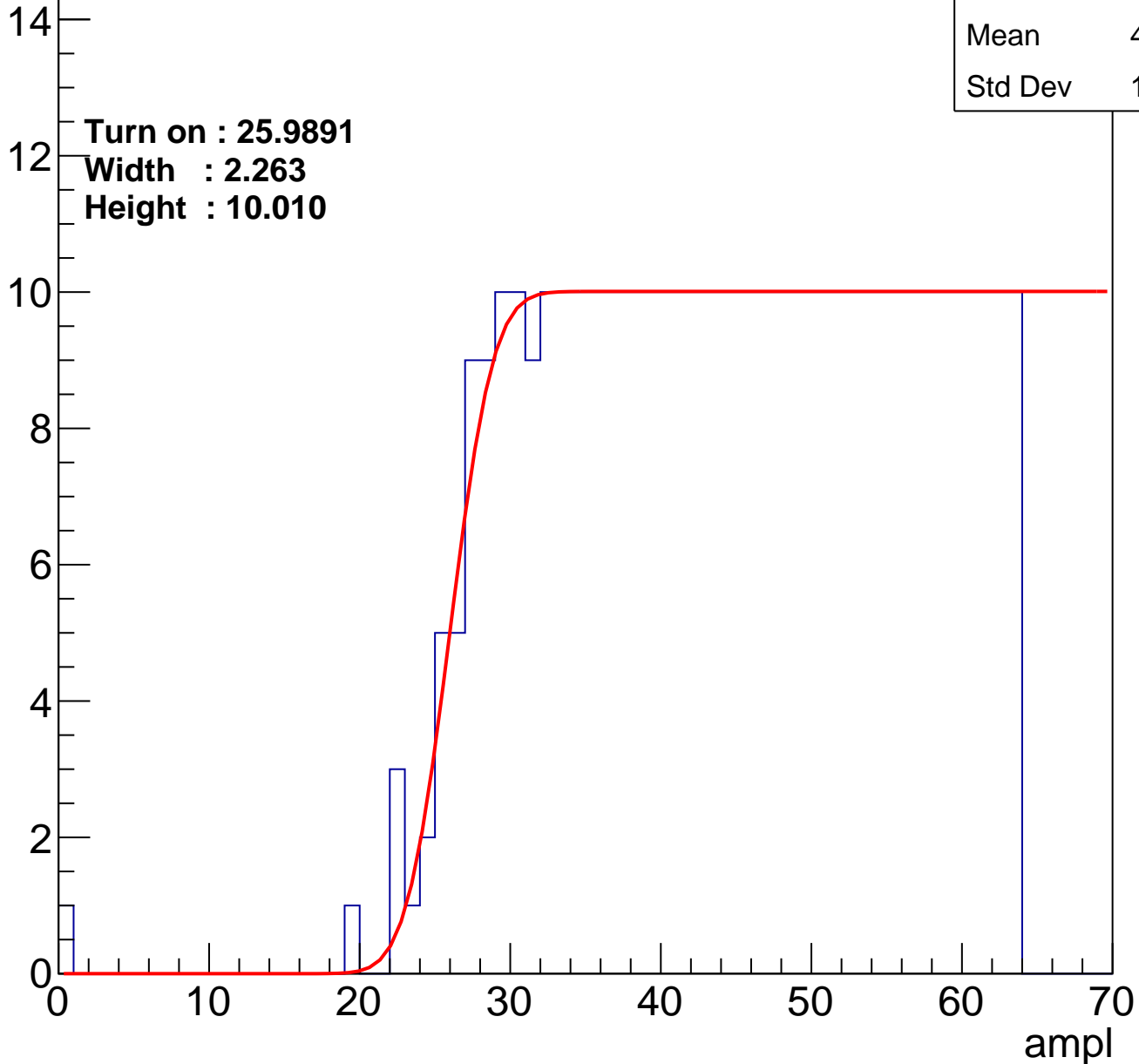
Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.9891

Width : 2.263

Height : 10.010

Entry



B0L000S, U16-ch74

calib_packv5_042523_0143.root, FC#5, port B1

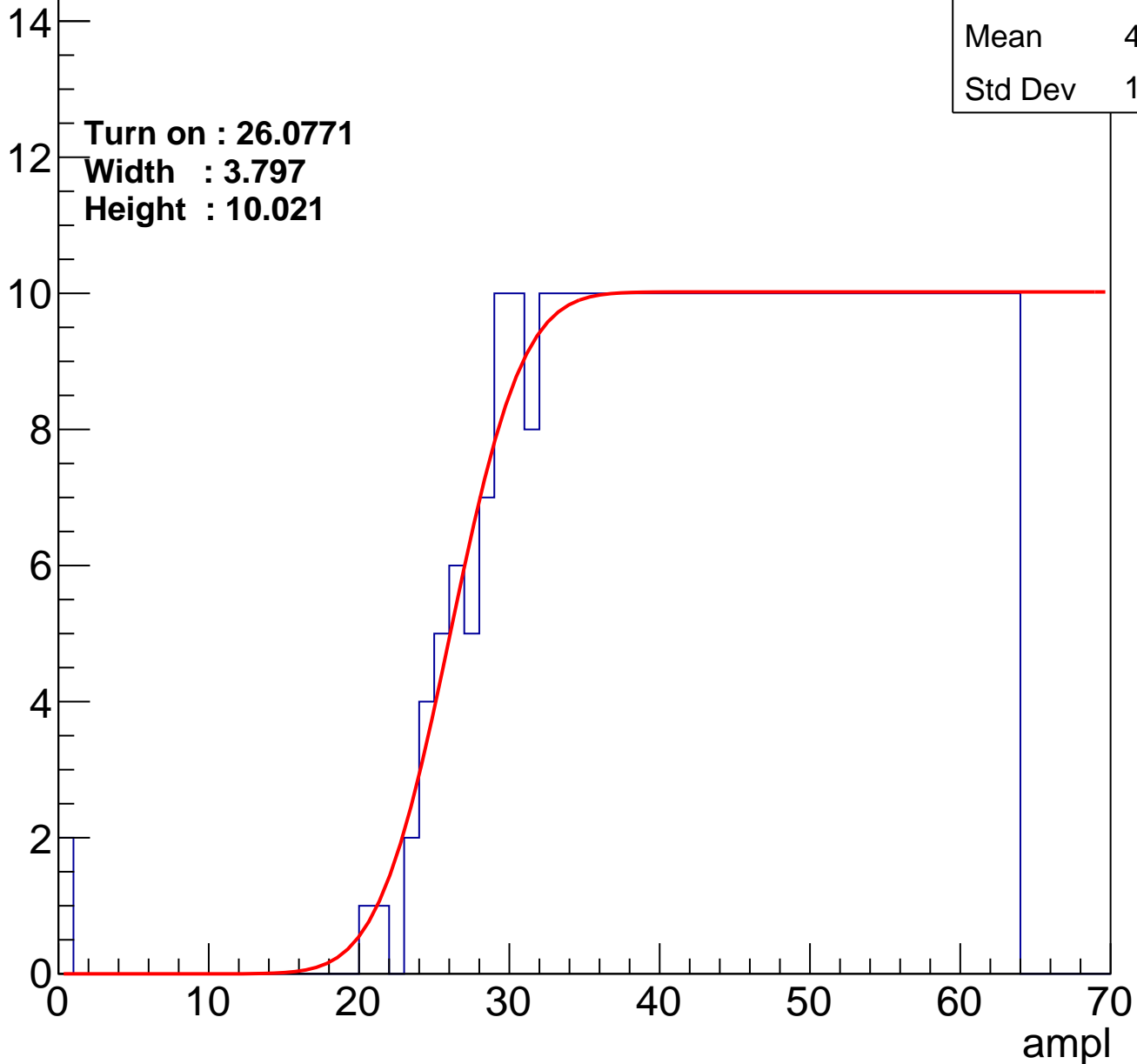
Entries	381
Mean	44.18
Std Dev	11.59

Turn on : 26.0771

Width : 3.797

Height : 10.021

Entry



B0L000S, U16-ch75

calib_packv5_042523_0143.root, FC#5, port B1

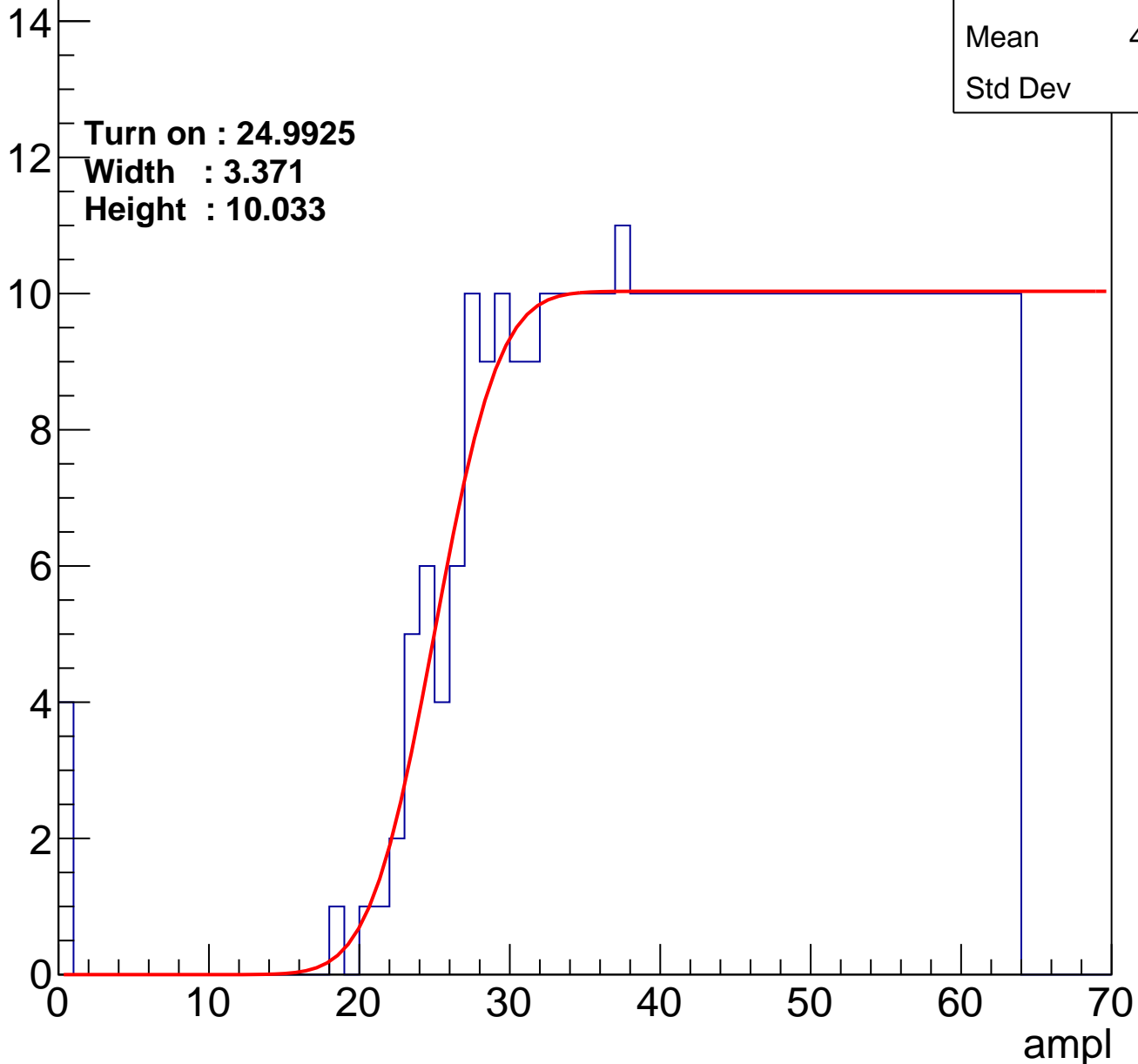
Entries	398
Mean	43.26
Std Dev	12.3

Turn on : 24.9925

Width : 3.371

Height : 10.033

Entry



B0L000S, U16-ch76

calib_packv5_042523_0143.root, FC#5, port B1

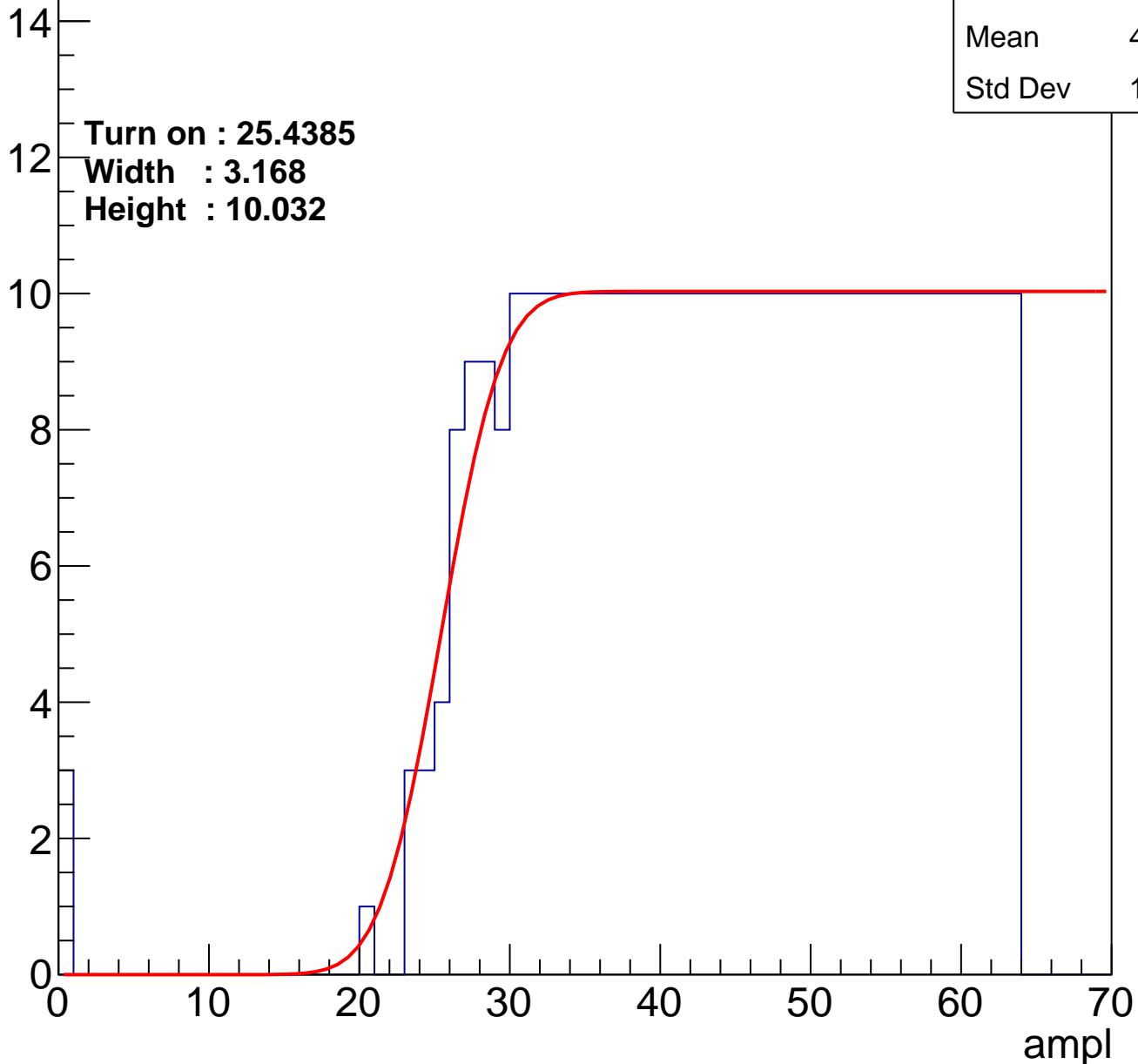
Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.4385

Width : 3.168

Height : 10.032

Entry



B0L000S, U16-ch77

calib_packv5_042523_0143.root, FC#5, port B1

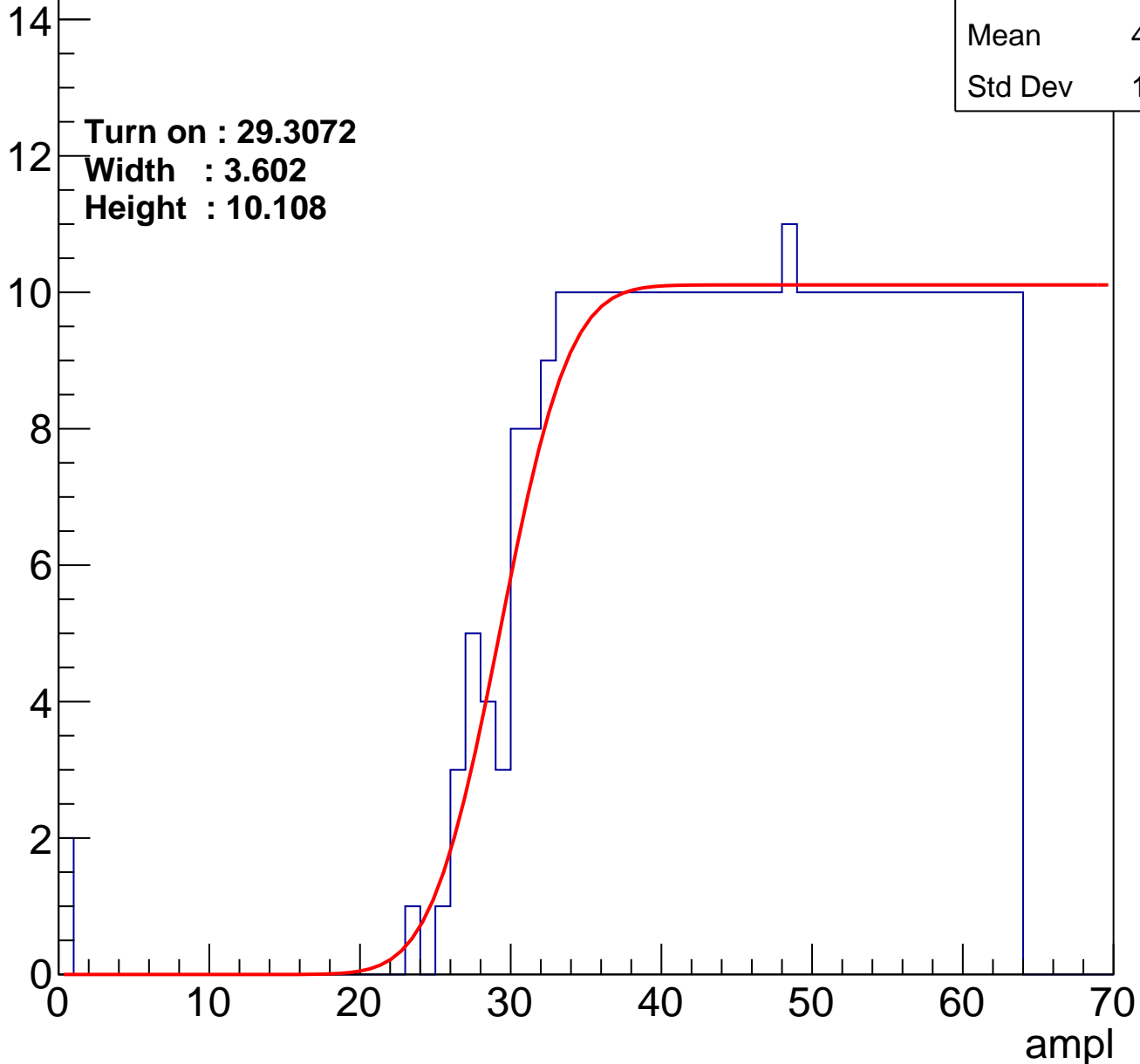
Entries	355
Mean	45.53
Std Dev	10.87

Turn on : 29.3072

Width : 3.602

Height : 10.108

Entry



B0L000S, U16-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 27.4103

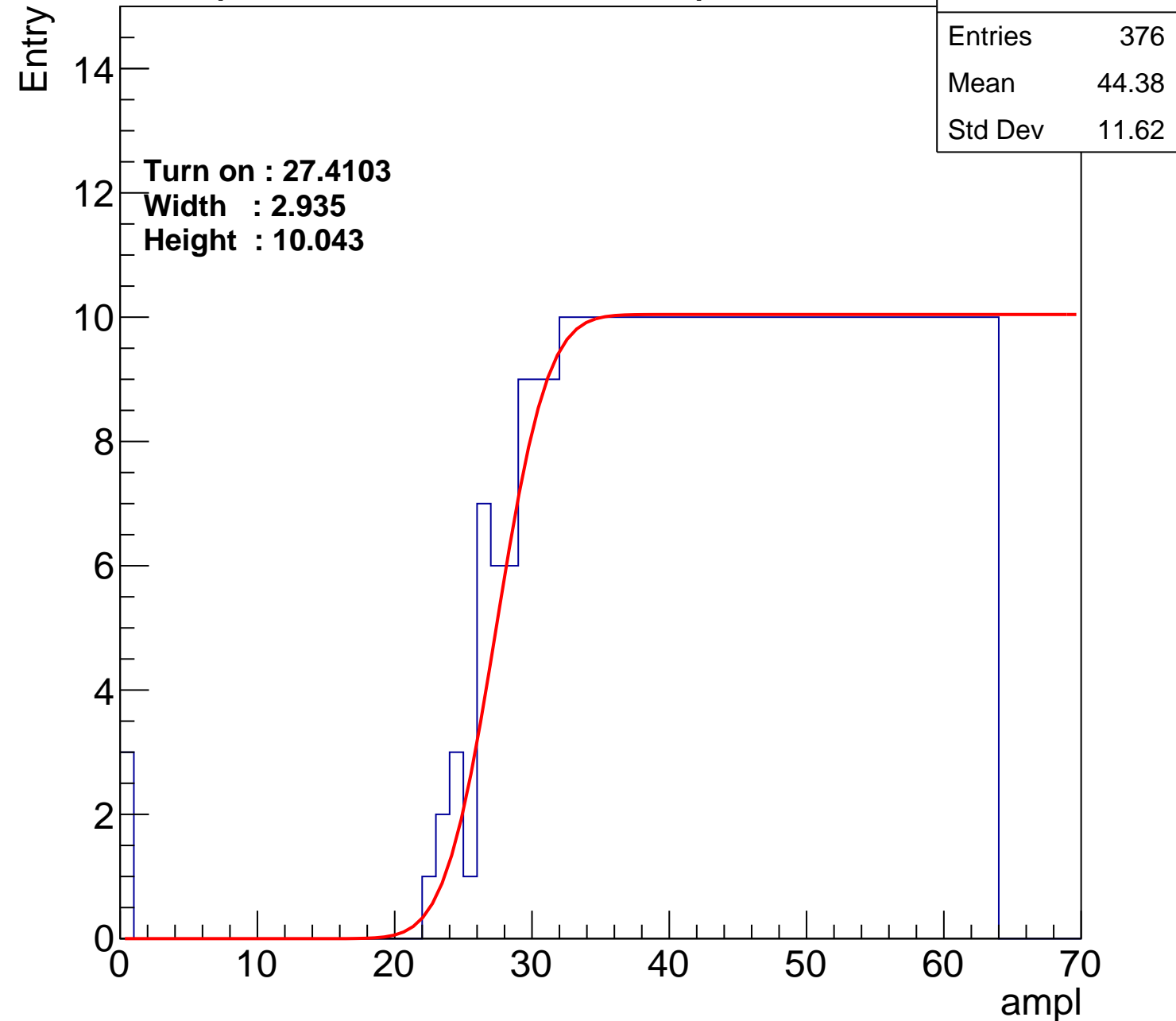
Width : 2.935

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.21
Std Dev	11.73

Turn on : 27.5921

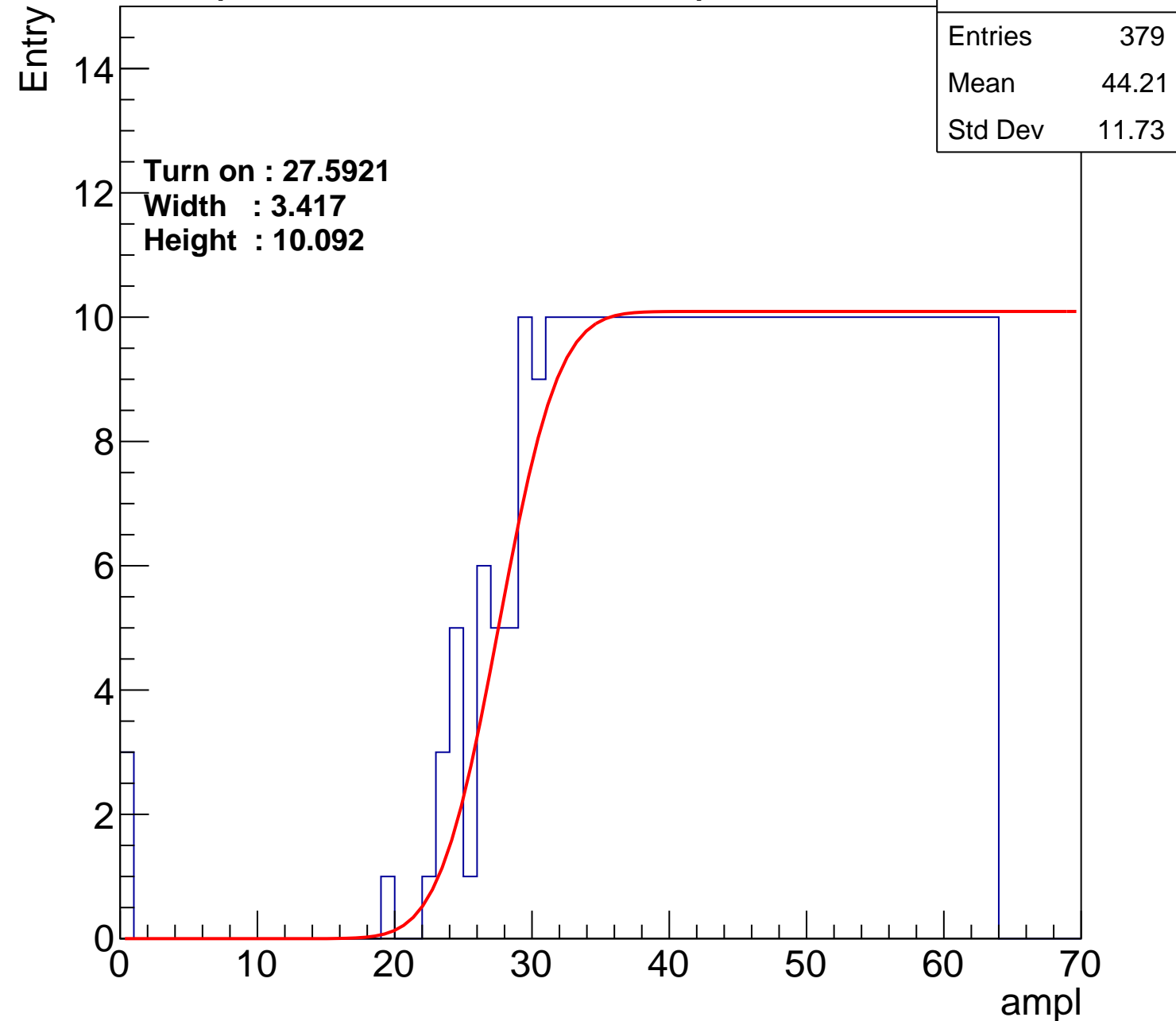
Width : 3.417

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.8901

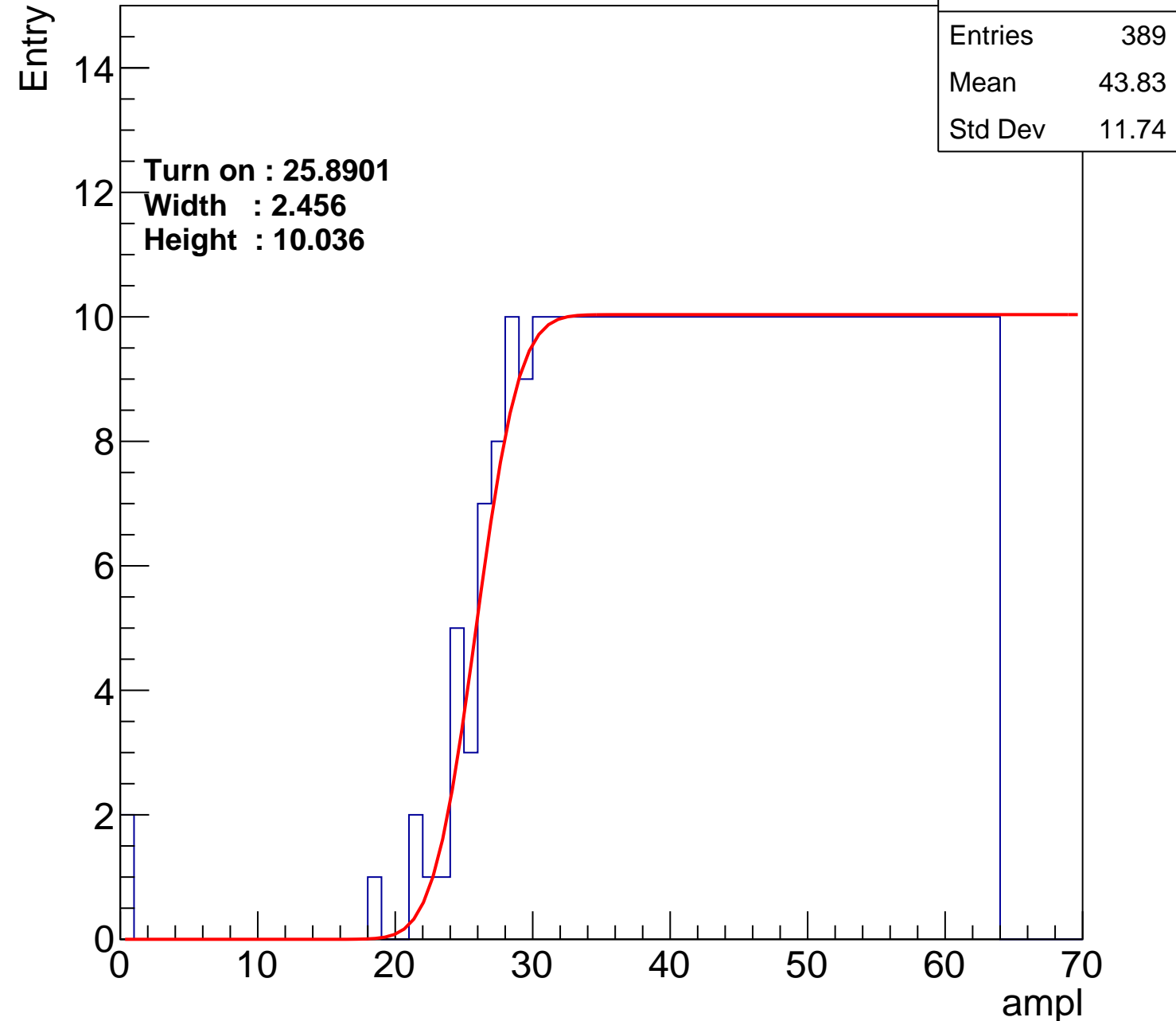
Width : 2.456

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch81

calib_packv5_042523_0143.root, FC#5, port B1

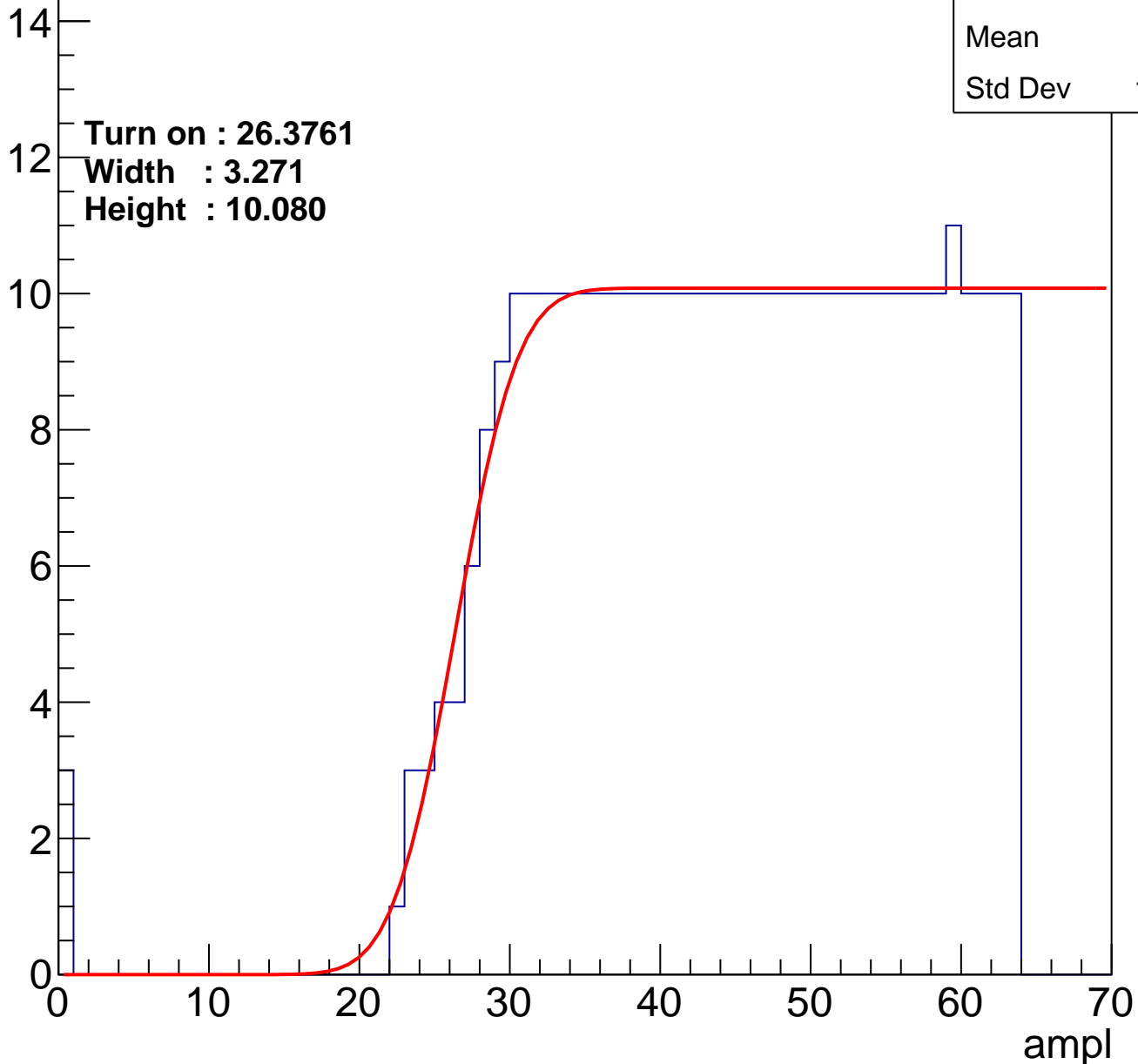
Entries	382
Mean	44.2
Std Dev	11.71

Turn on : 26.3761

Width : 3.271

Height : 10.080

Entry



B0L000S, U16-ch82

calib_packv5_042523_0143.root, FC#5, port B1

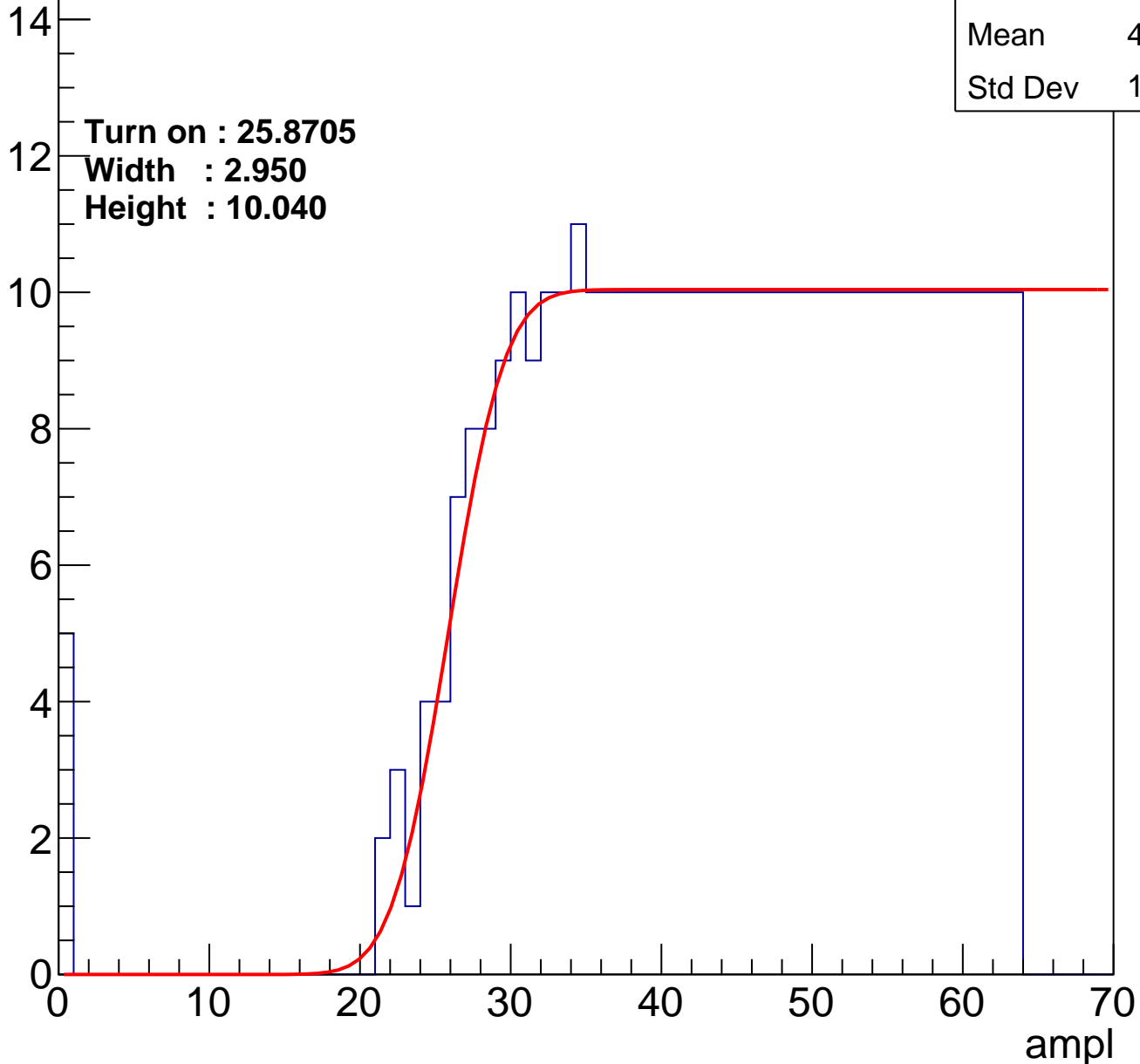
Entries	391
Mean	43.54
Std Dev	12.29

Turn on : 25.8705

Width : 2.950

Height : 10.040

Entry



B0L000S, U16-ch83

calib_packv5_042523_0143.root, FC#5, port B1

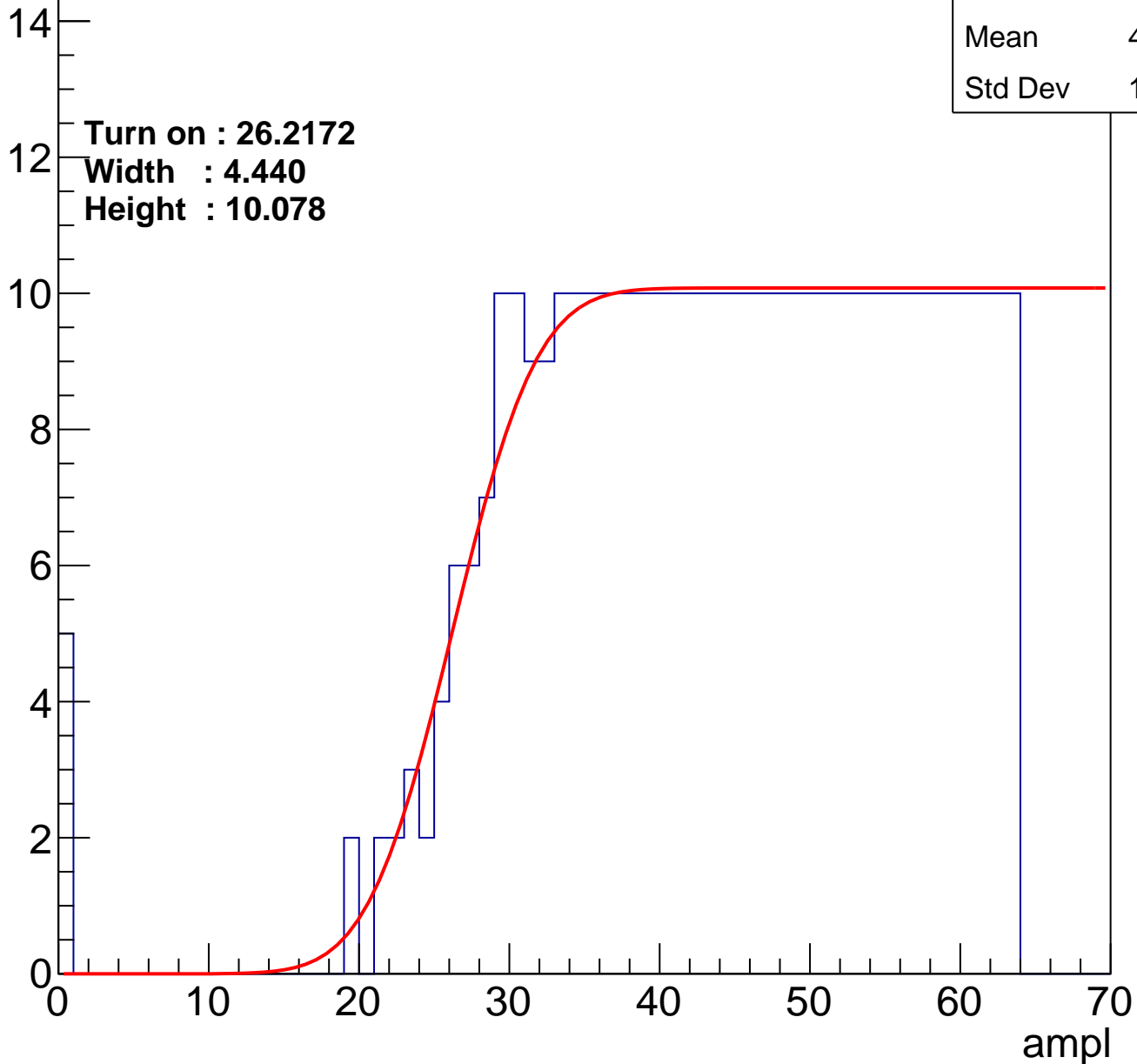
Entries	387
Mean	43.65
Std Dev	12.32

Turn on : 26.2172

Width : 4.440

Height : 10.078

Entry



B0L000S, U16-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.56
Std Dev	12.17

Turn on : 25.6490

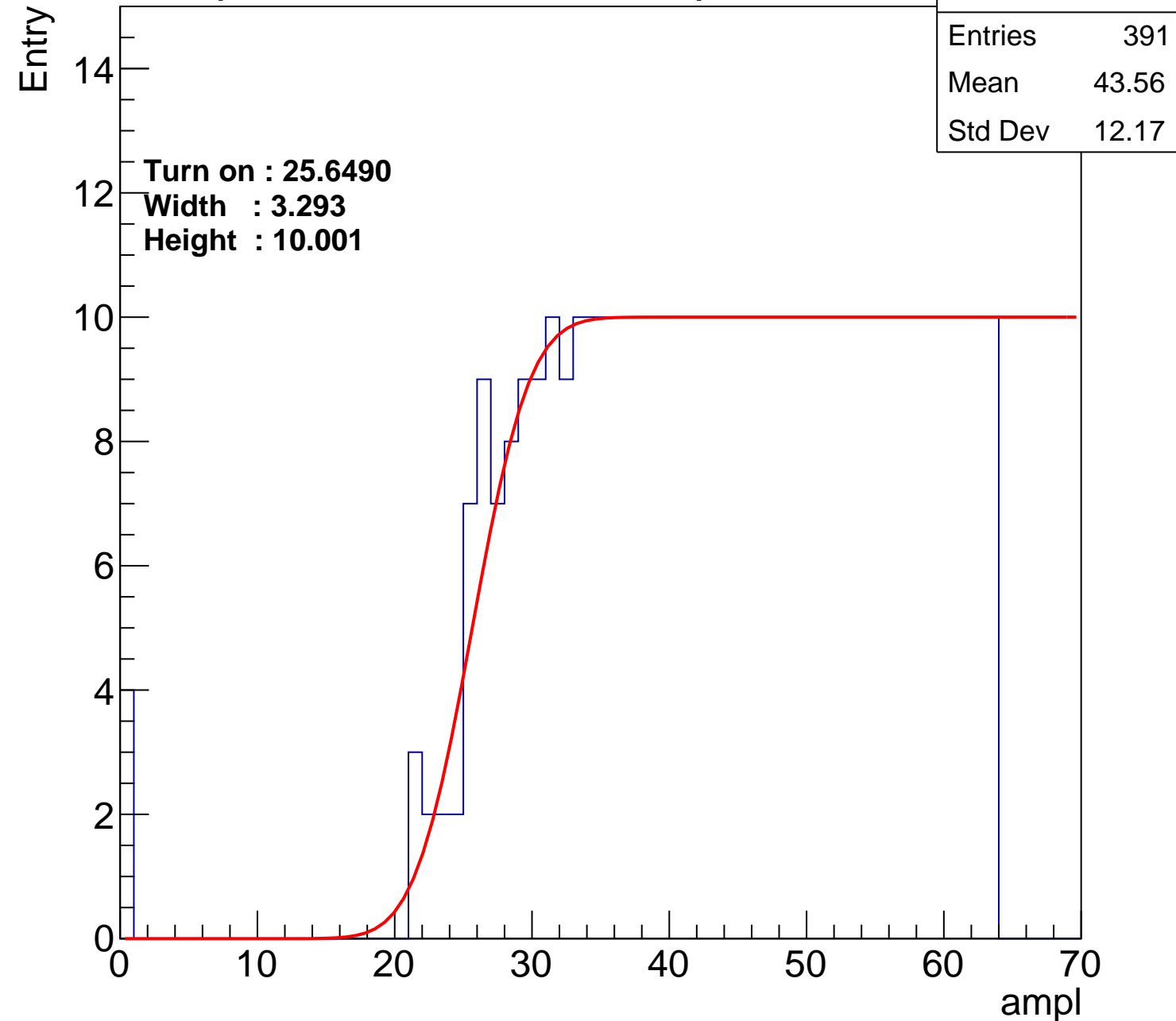
Width : 3.293

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.95
Std Dev	11.76

Turn on : 26.3032

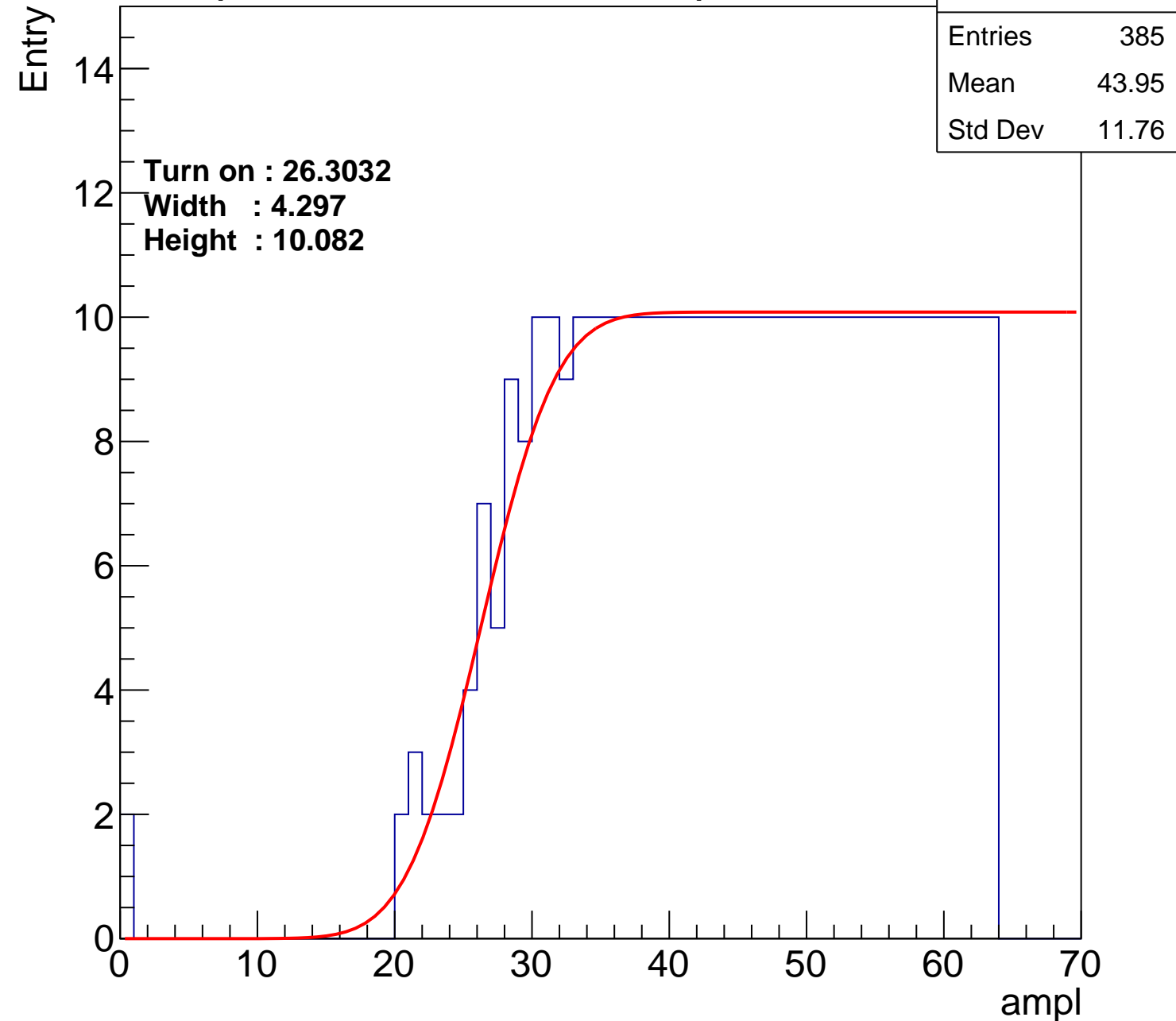
Width : 4.297

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch86

calib_packv5_042523_0143.root, FC#5, port B1

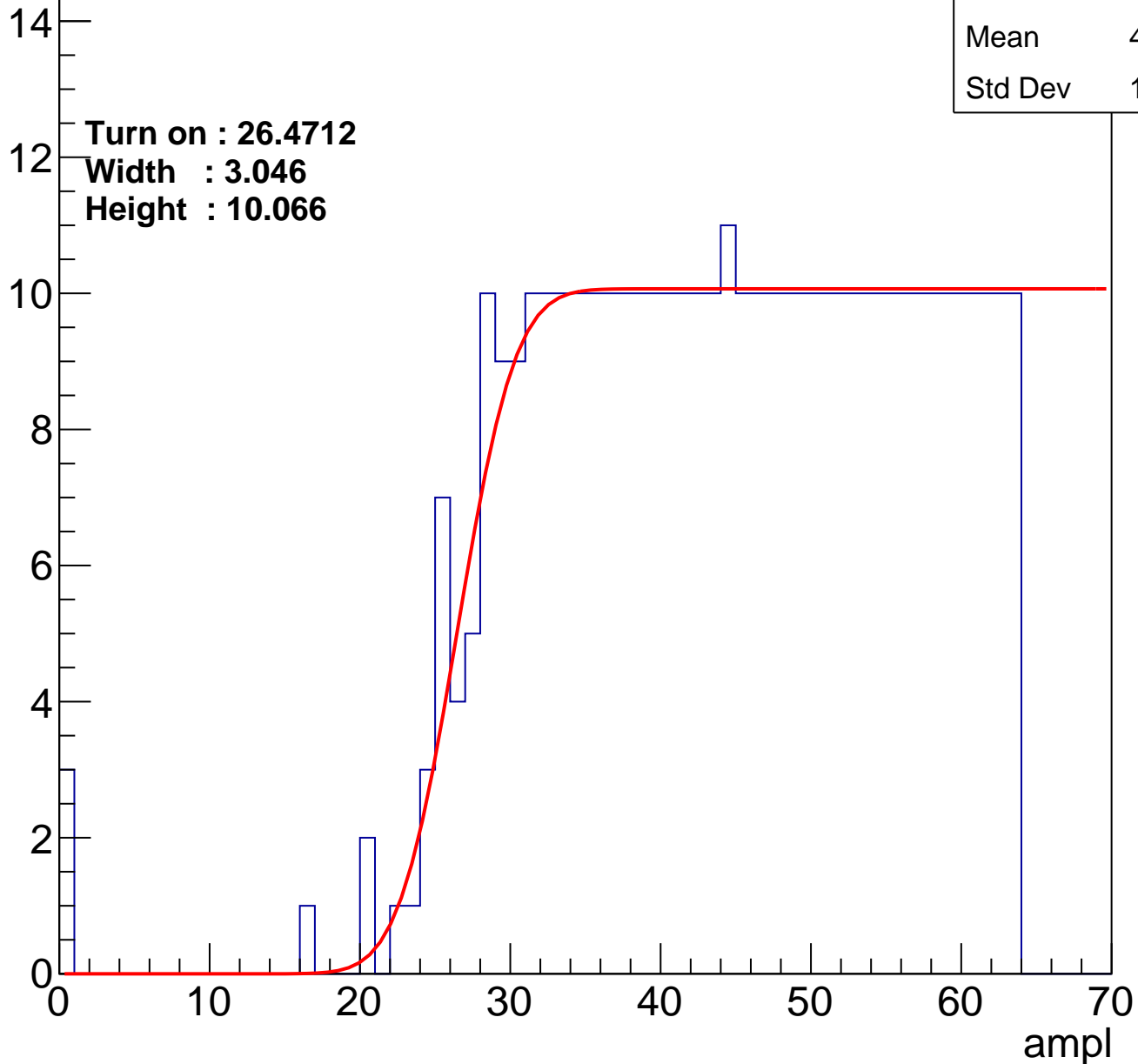
Entries	386
Mean	43.92
Std Dev	11.87

Turn on : 26.4712

Width : 3.046

Height : 10.066

Entry



B0L000S, U16-ch87

calib_packv5_042523_0143.root, FC#5, port B1

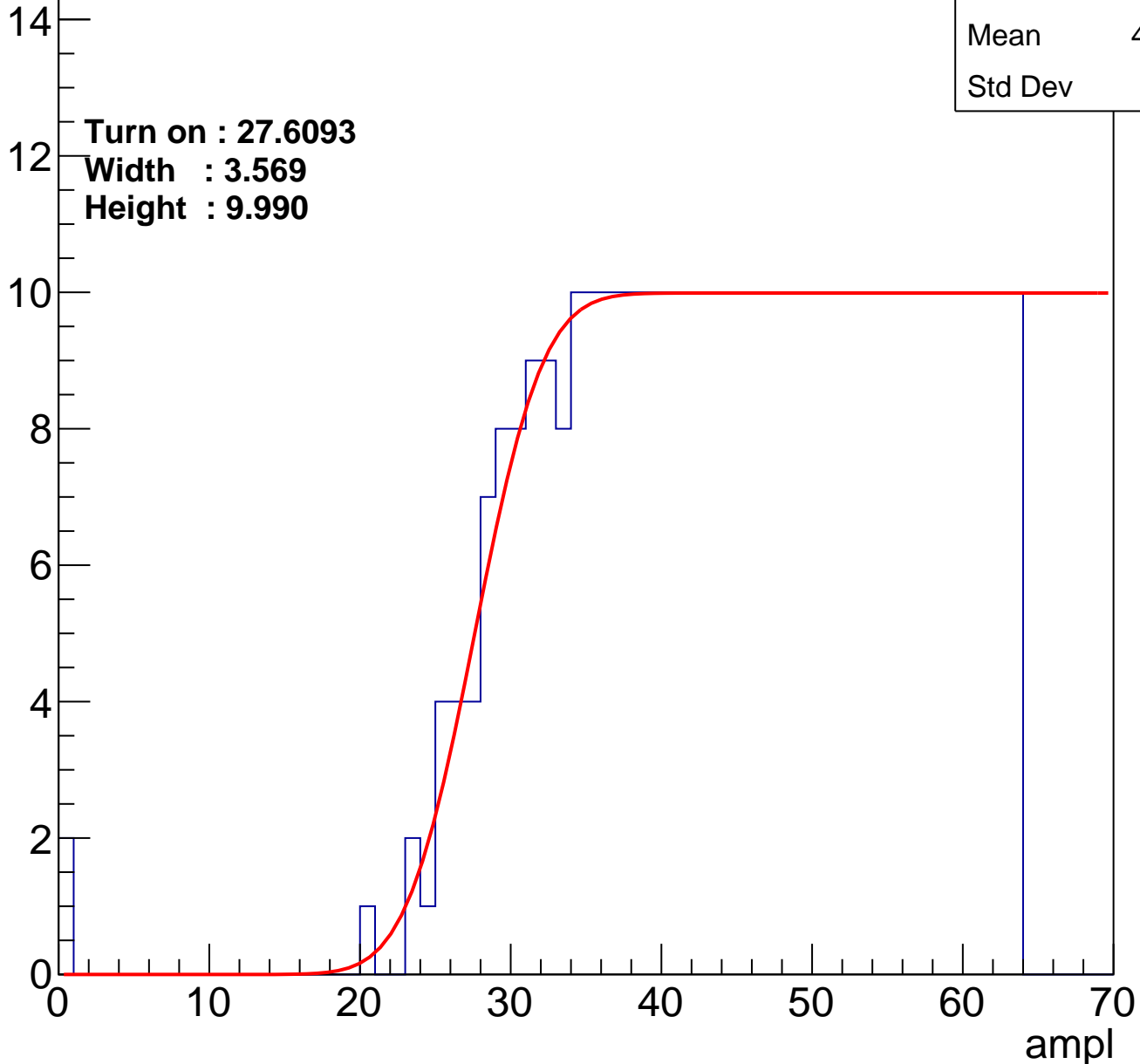
Entries	367
Mean	44.83
Std Dev	11.3

Turn on : 27.6093

Width : 3.569

Height : 9.990

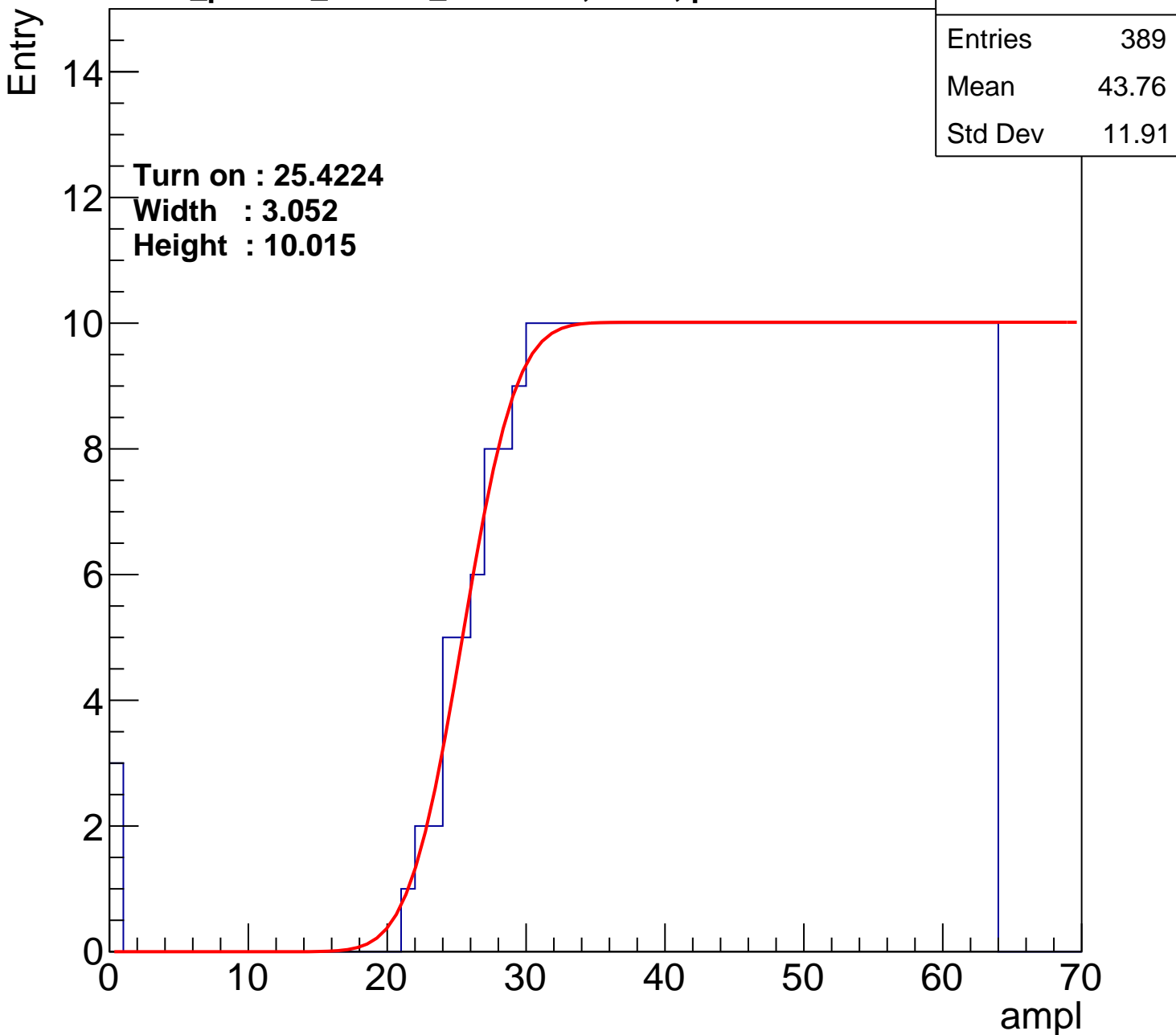
Entry



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 25.4224
Width : 3.052
Height : 10.015



B0L000S, U16-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.7025

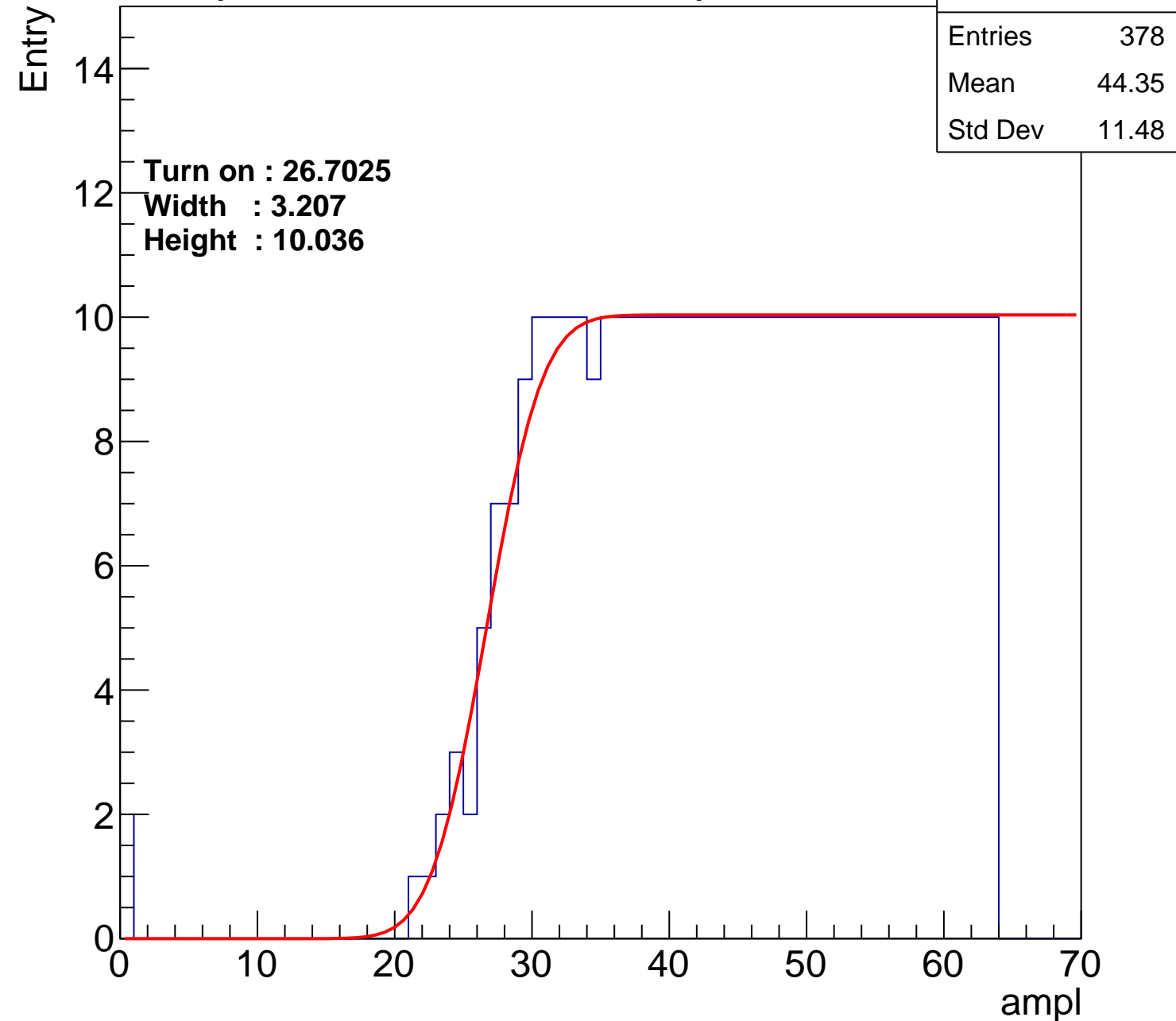
Width : 3.207

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch90

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.41
Std Dev	12.37

Turn on : 25.2873

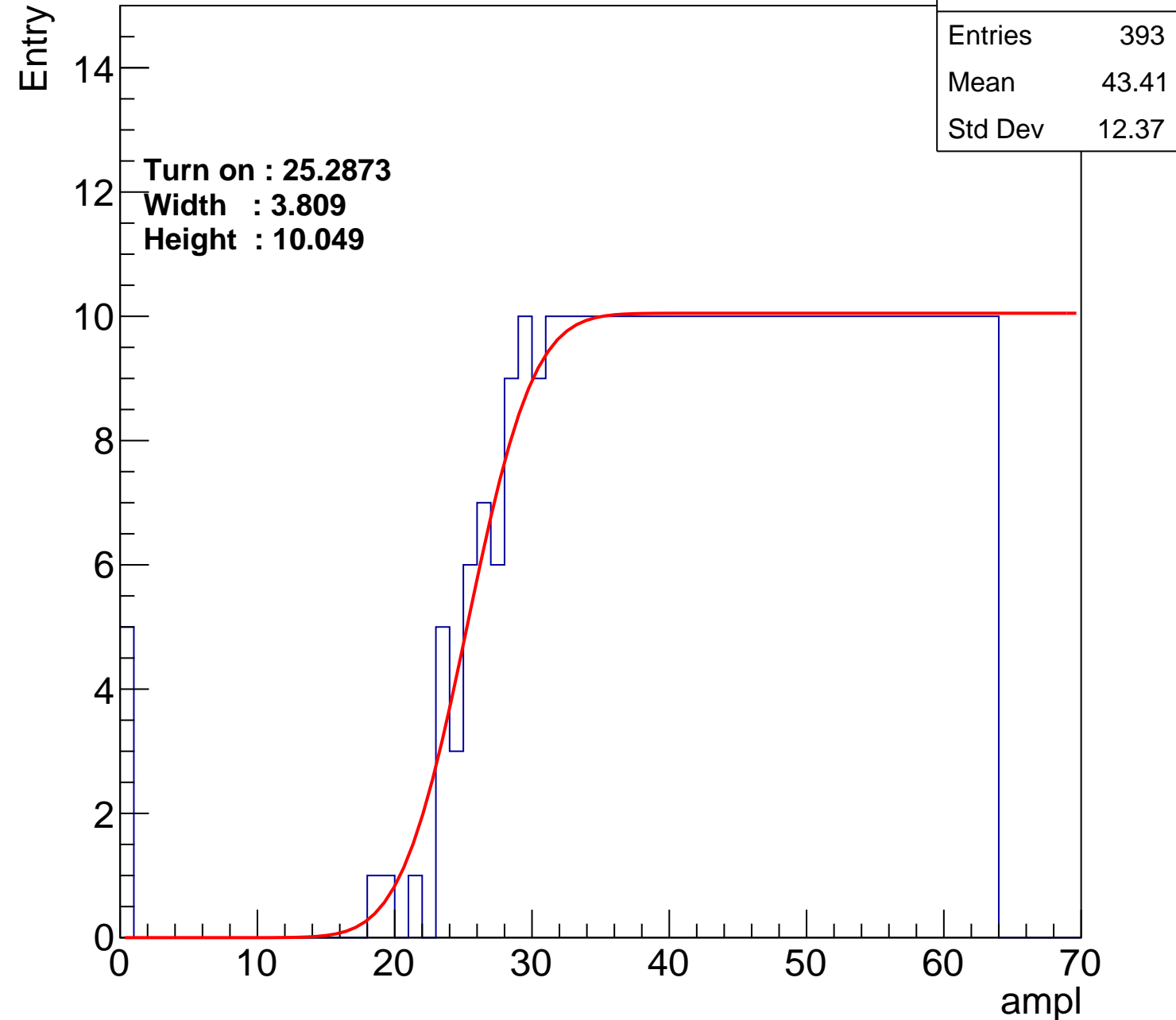
Width : 3.809

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch91

calib_packv5_042523_0143.root, FC#5, port B1

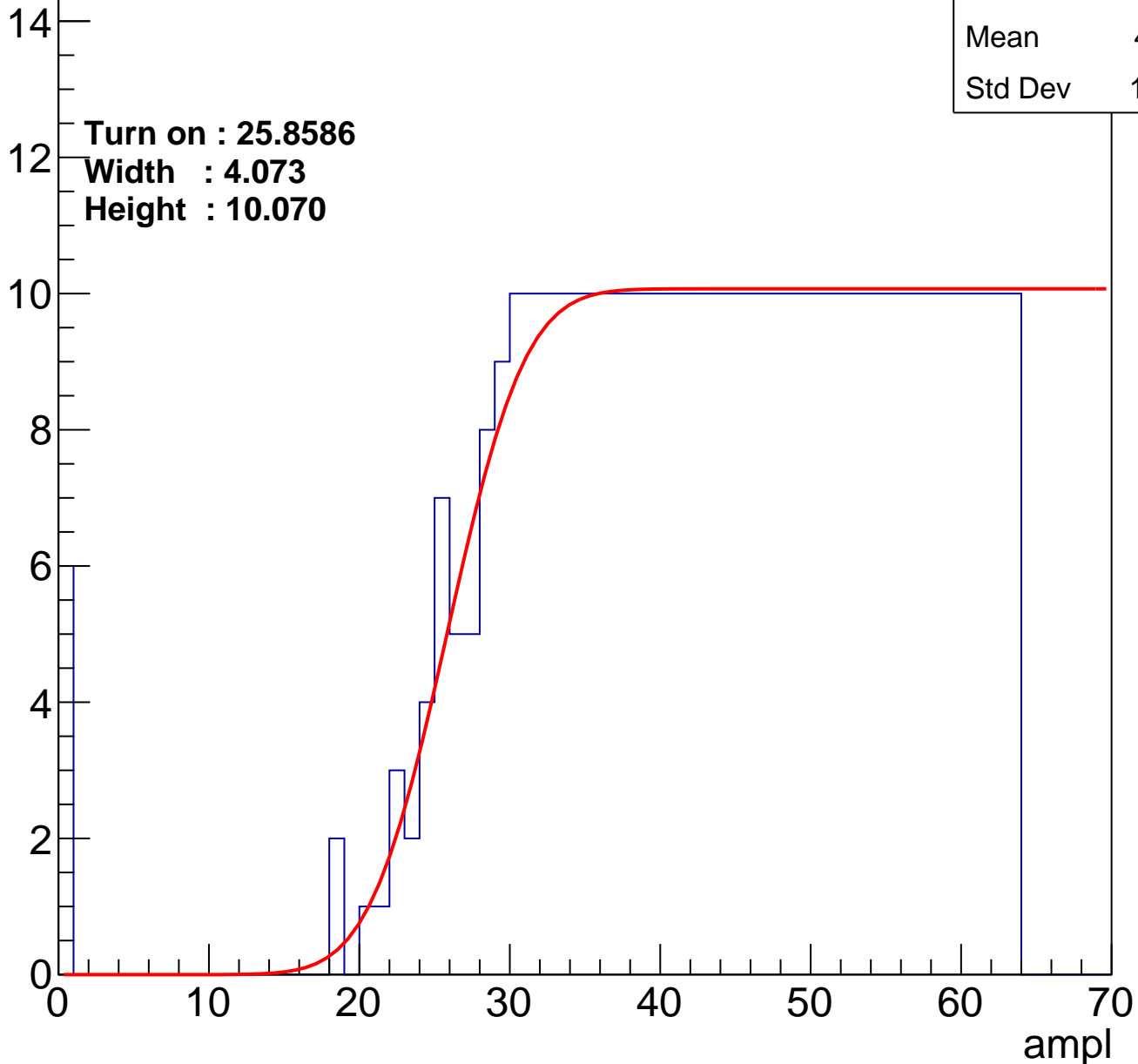
Entries	393
Mean	43.31
Std Dev	12.59

Turn on : 25.8586

Width : 4.073

Height : 10.070

Entry



B0L000S, U16-ch92

calib_packv5_042523_0143.root, FC#5, port B1

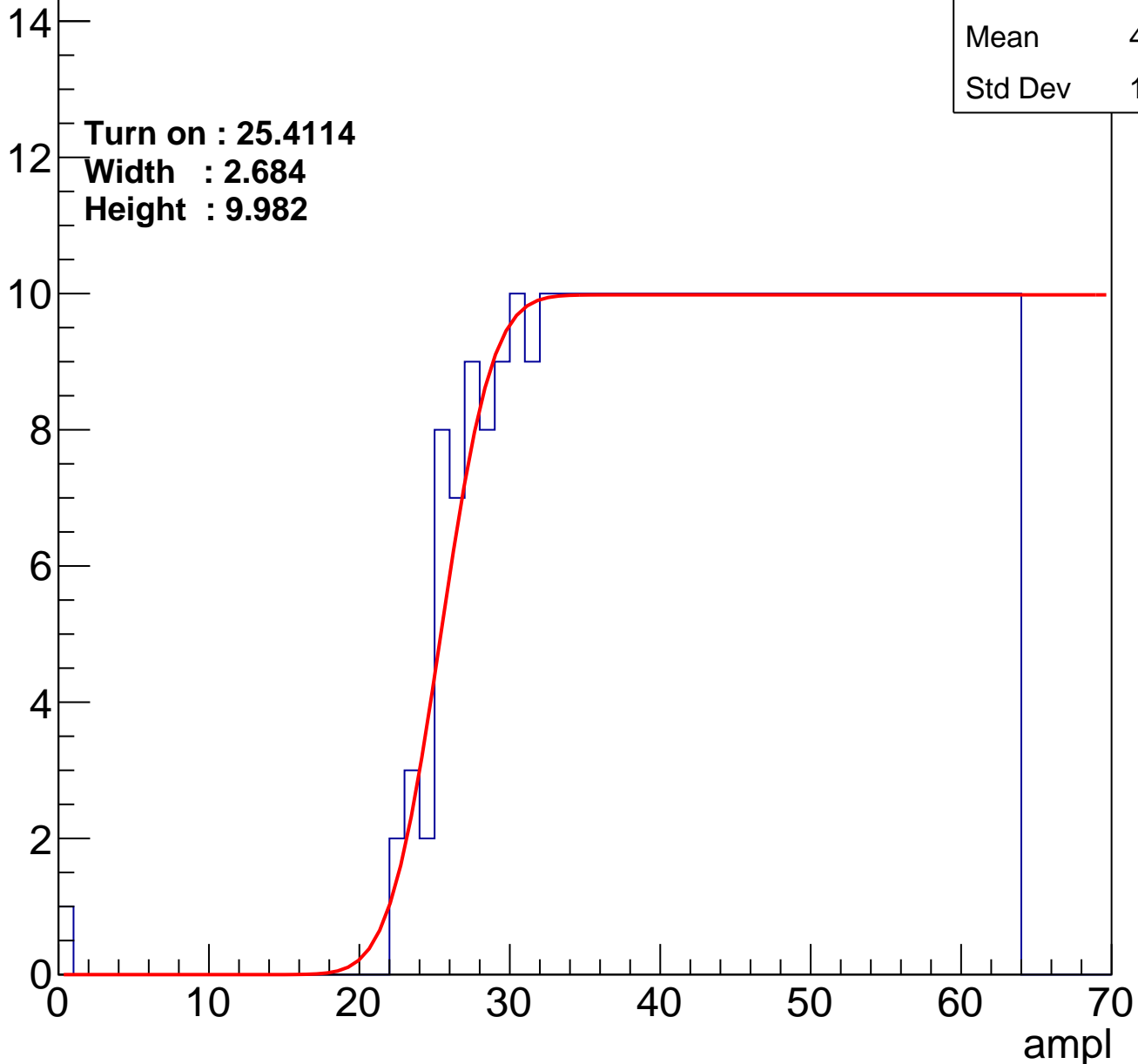
Entries	388
Mean	43.94
Std Dev	11.53

Turn on : 25.4114

Width : 2.684

Height : 9.982

Entry



B0L000S, U16-ch93

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.05
Std Dev	12.36

Turn on : 27.1675

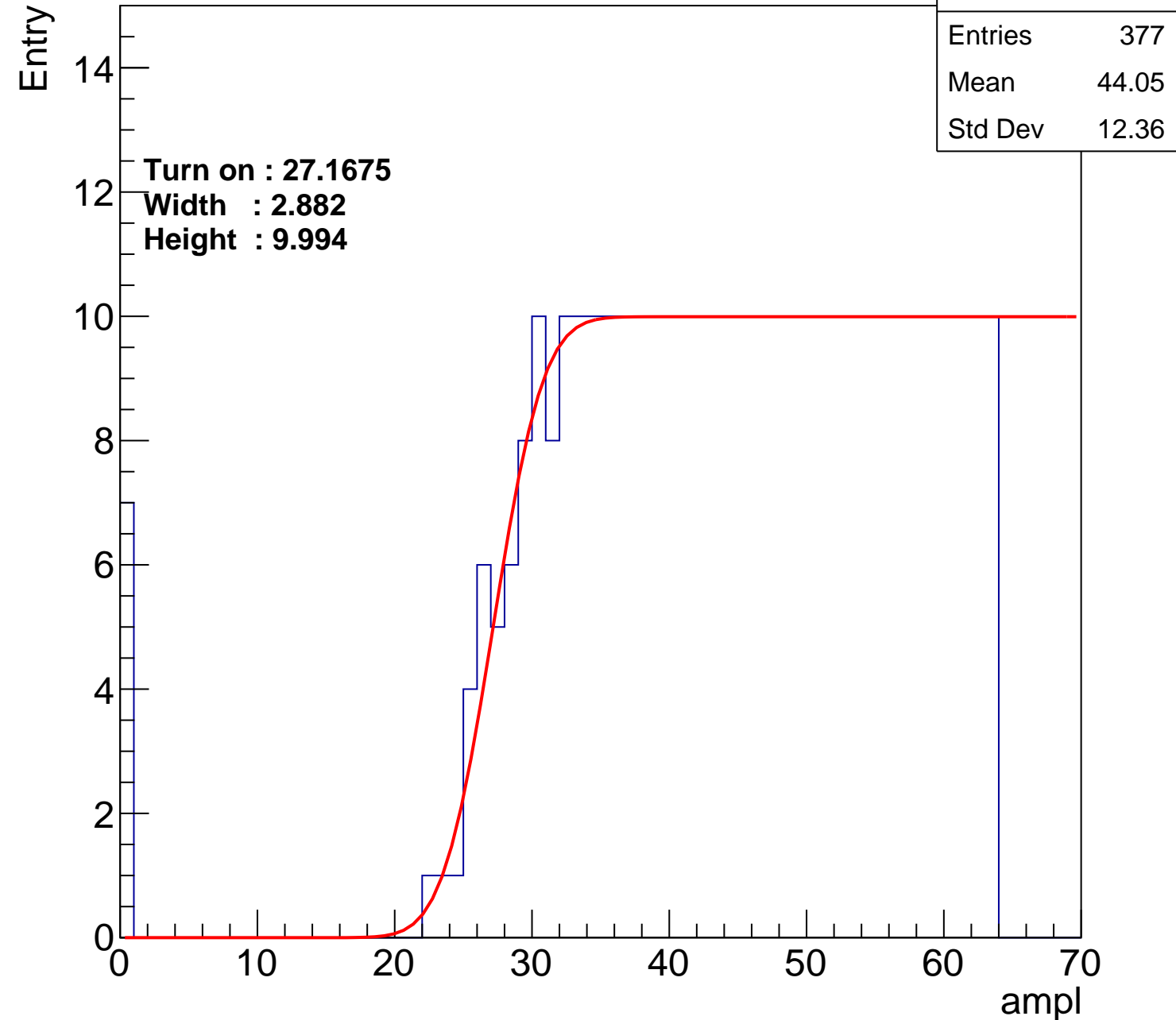
Width : 2.882

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch94

calib_packv5_042523_0143.root, FC#5, port B1

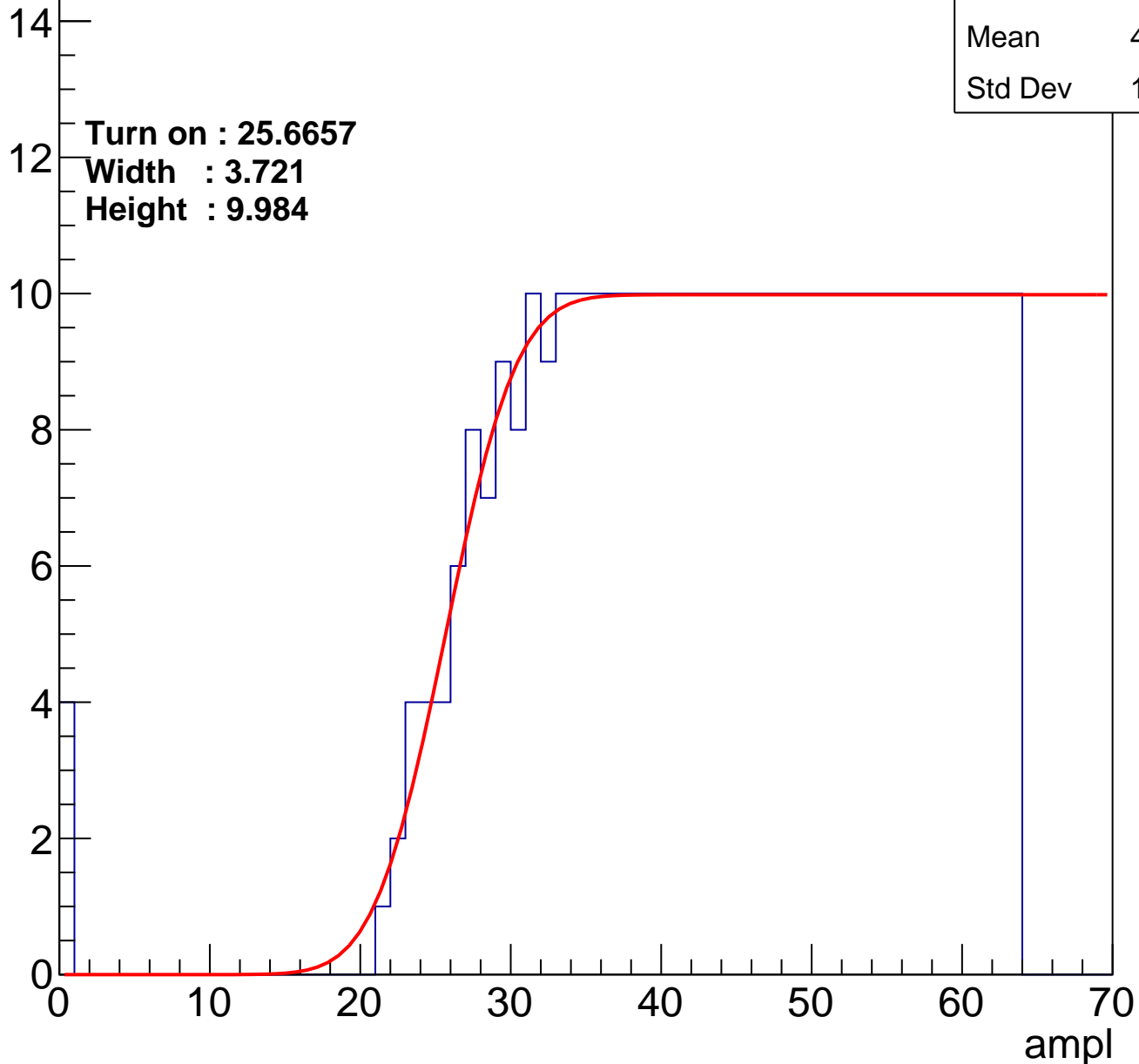
Entries	386
Mean	43.78
Std Dev	12.09

Turn on : 25.6657

Width : 3.721

Height : 9.984

Entry



B0L000S, U16-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.72
Std Dev	11.44

Turn on : 27.2138

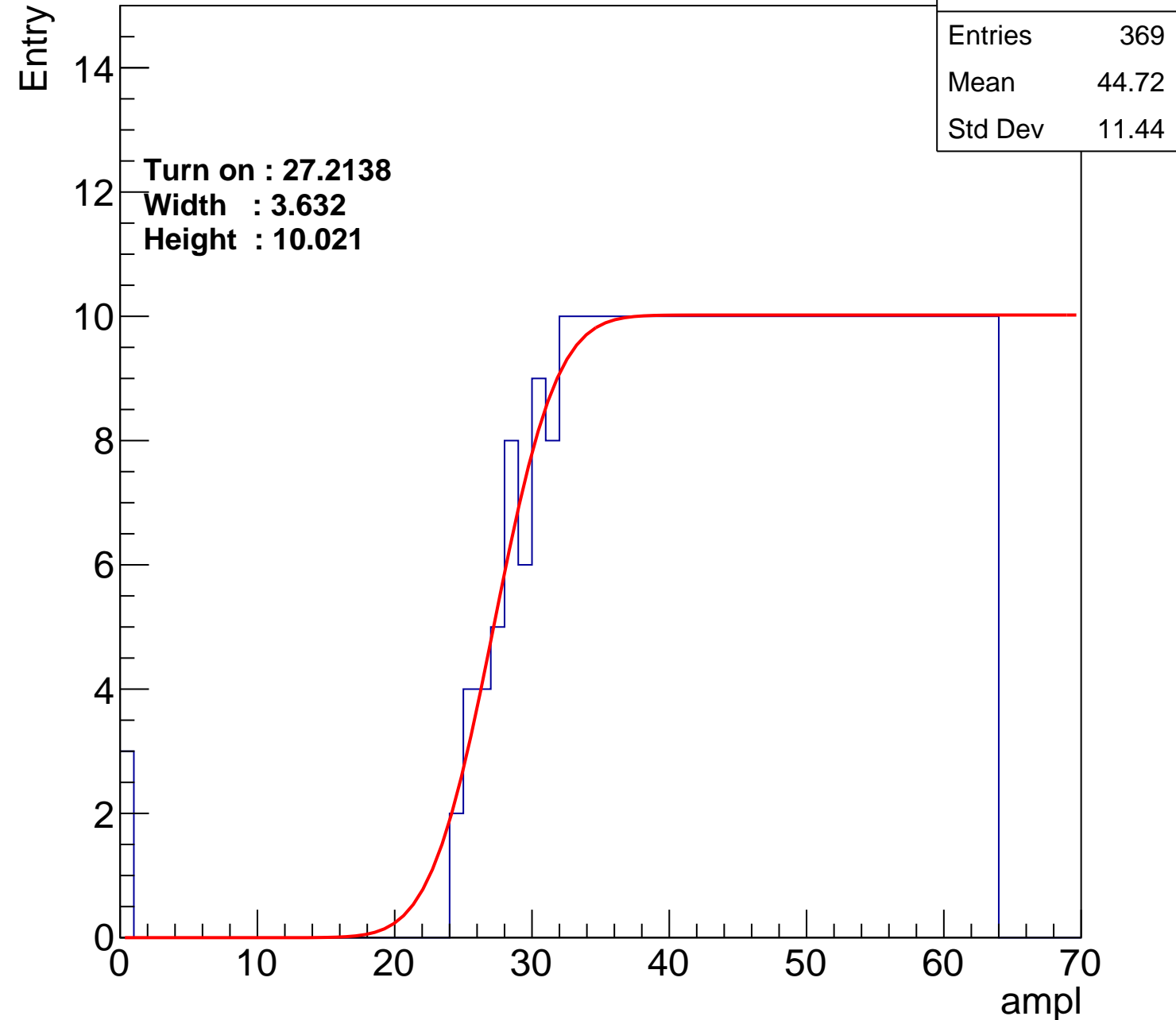
Width : 3.632

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl

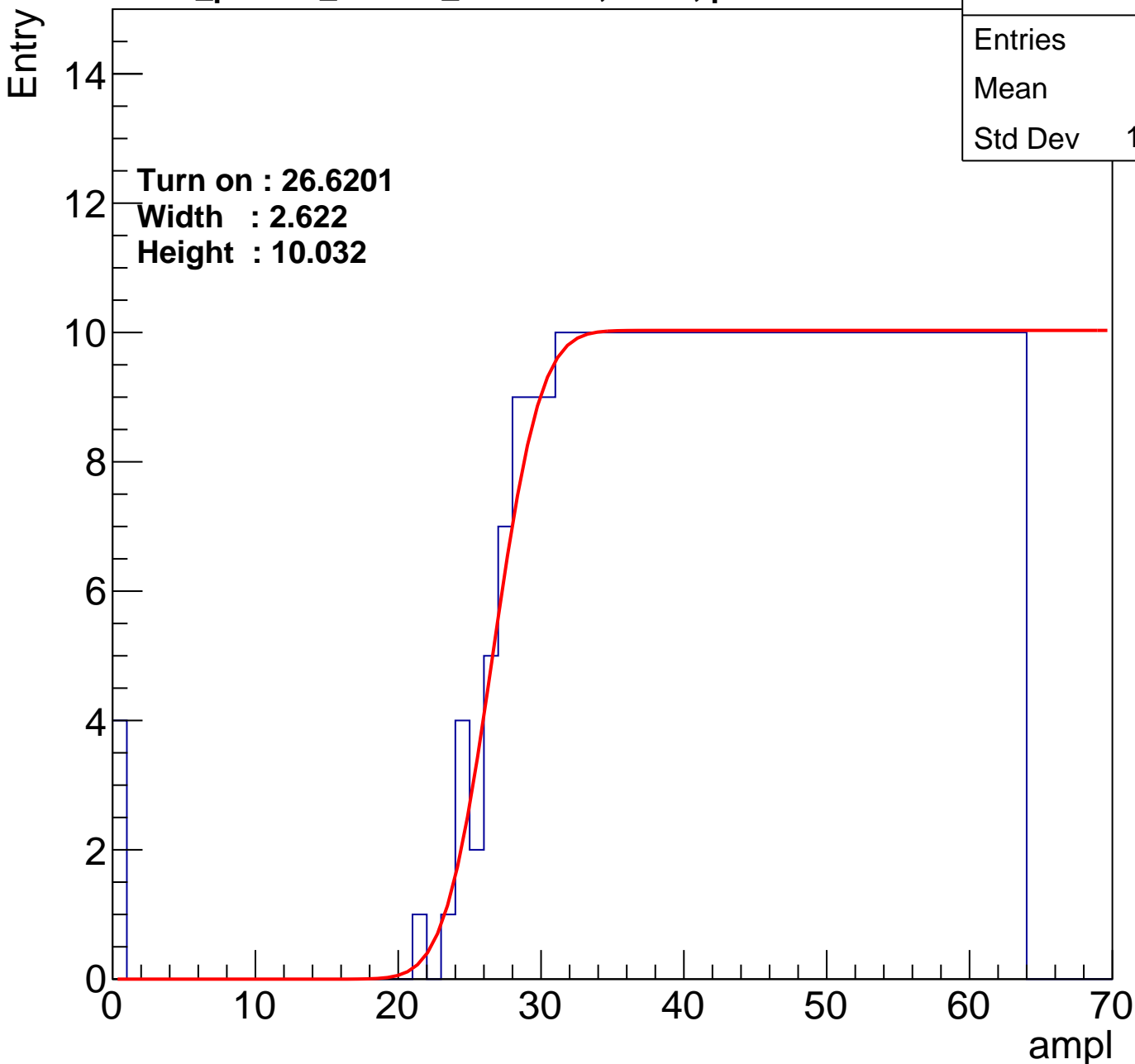


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.1
Std Dev	11.87

Height : 10.032



B0L000S, U16-ch97

calib_packv5_042523_0143.root, FC#5, port B1

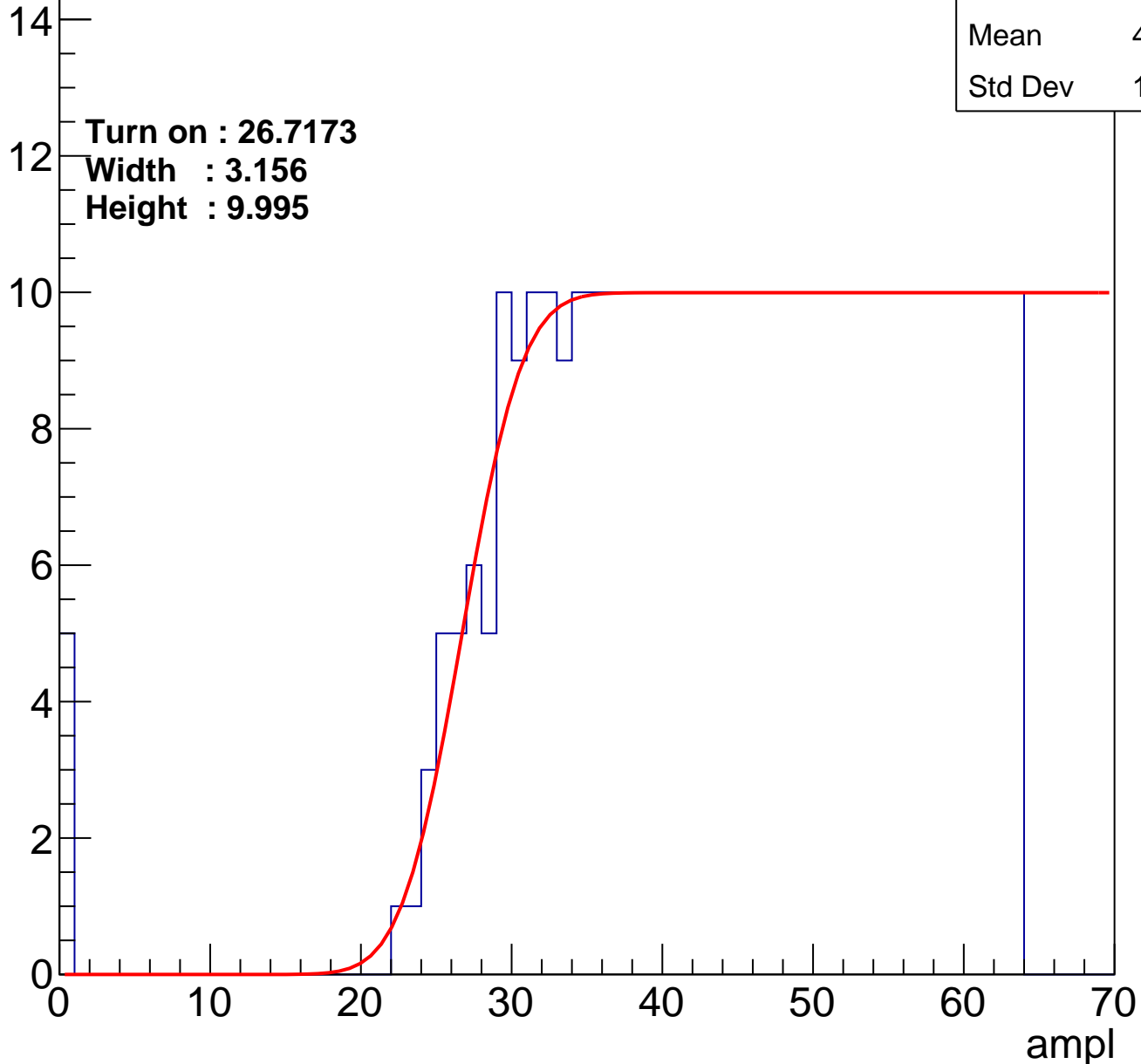
Entries	379
Mean	44.09
Std Dev	12.05

Turn on : 26.7173

Width : 3.156

Height : 9.995

Entry



B0L000S, U16-ch98

calib_packv5_042523_0143.root, FC#5, port B1

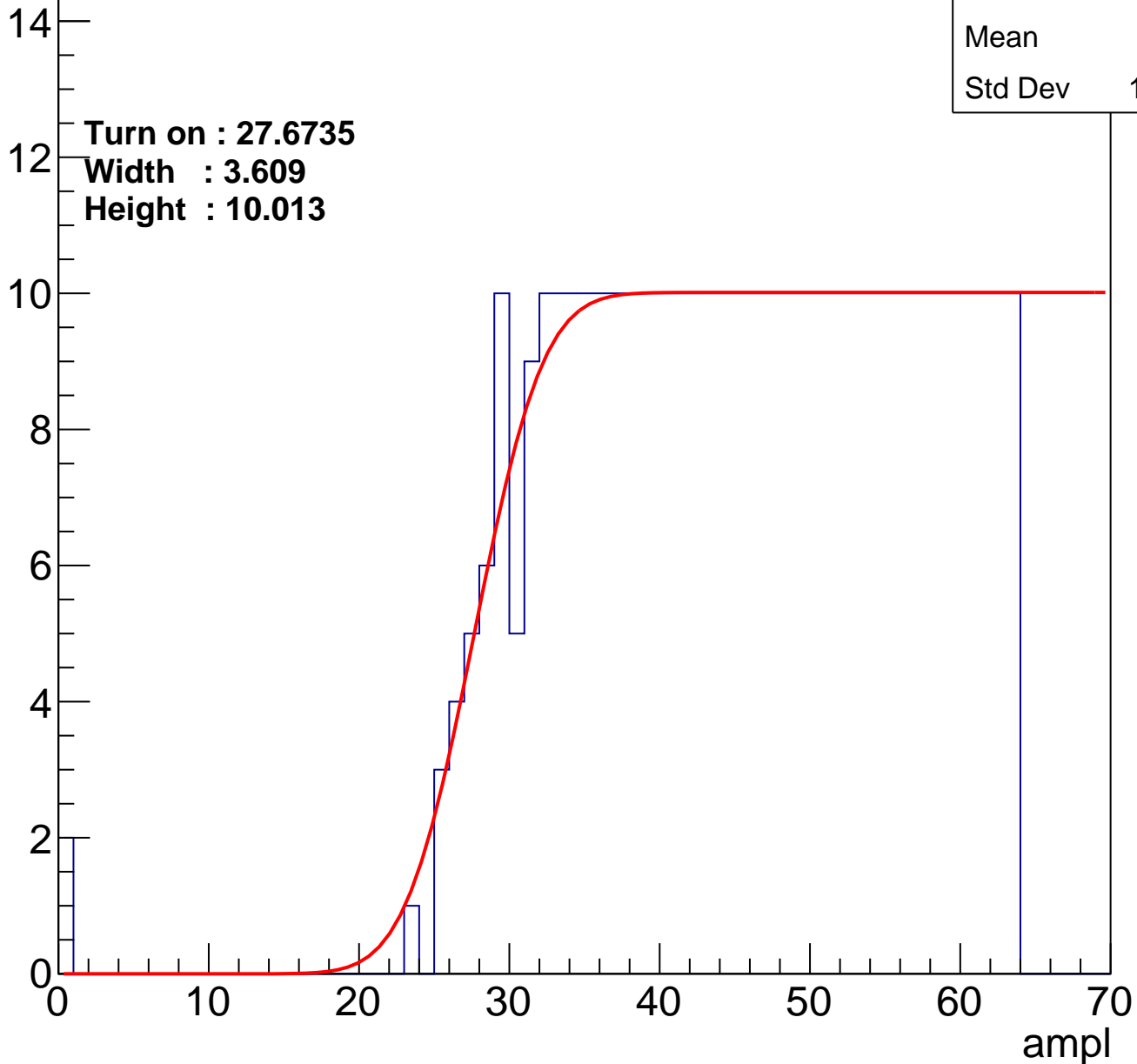
Entries	365
Mean	45
Std Dev	11.14

Turn on : 27.6735

Width : 3.609

Height : 10.013

Entry



B0L000S, U16-ch99

calib_packv5_042523_0143.root, FC#5, port B1

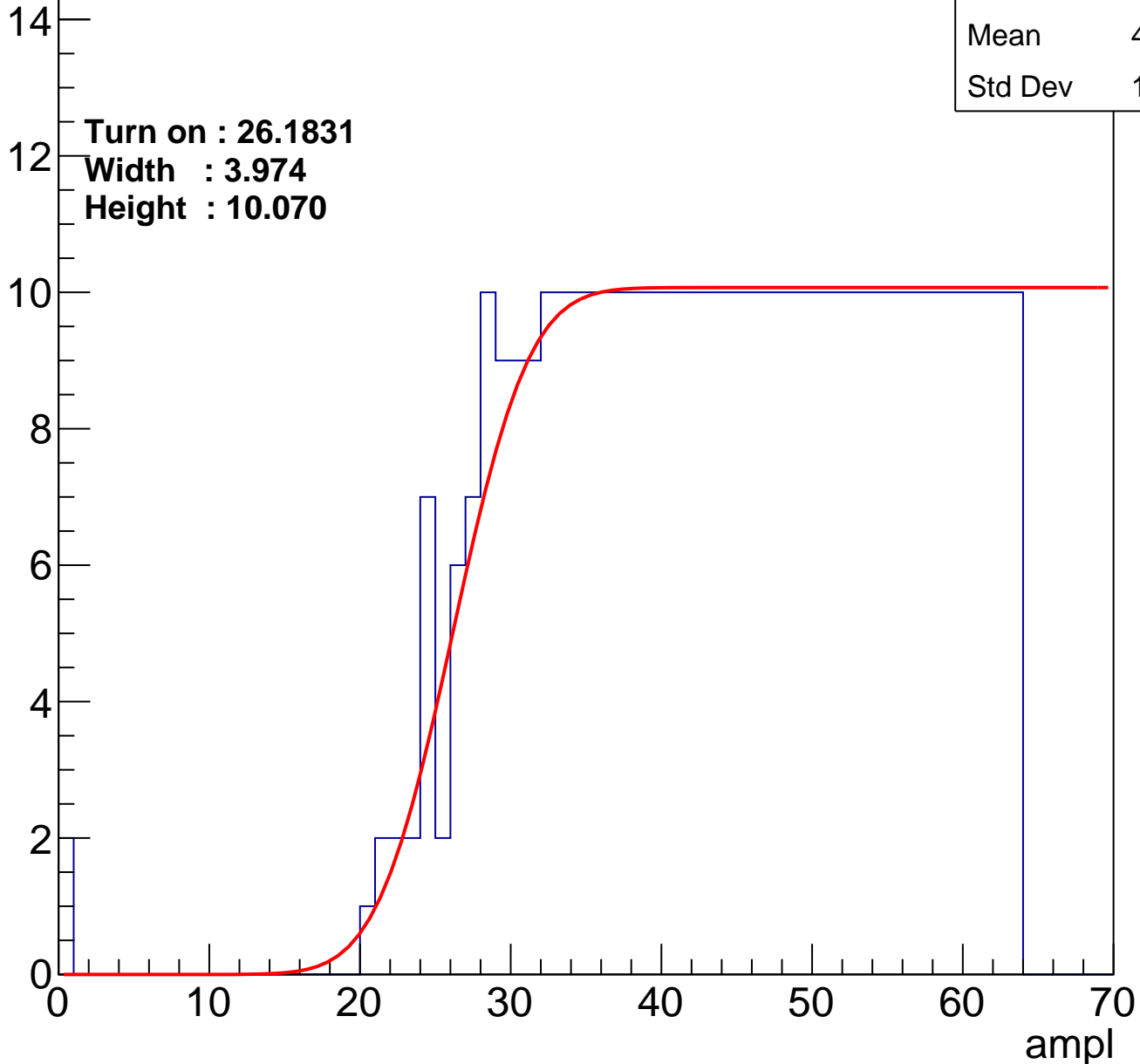
Entries	388
Mean	43.83
Std Dev	11.79

Turn on : 26.1831

Width : 3.974

Height : 10.070

Entry



B0L000S, U16-ch100

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.68
Std Dev	12.22

Turn on : 26.2340

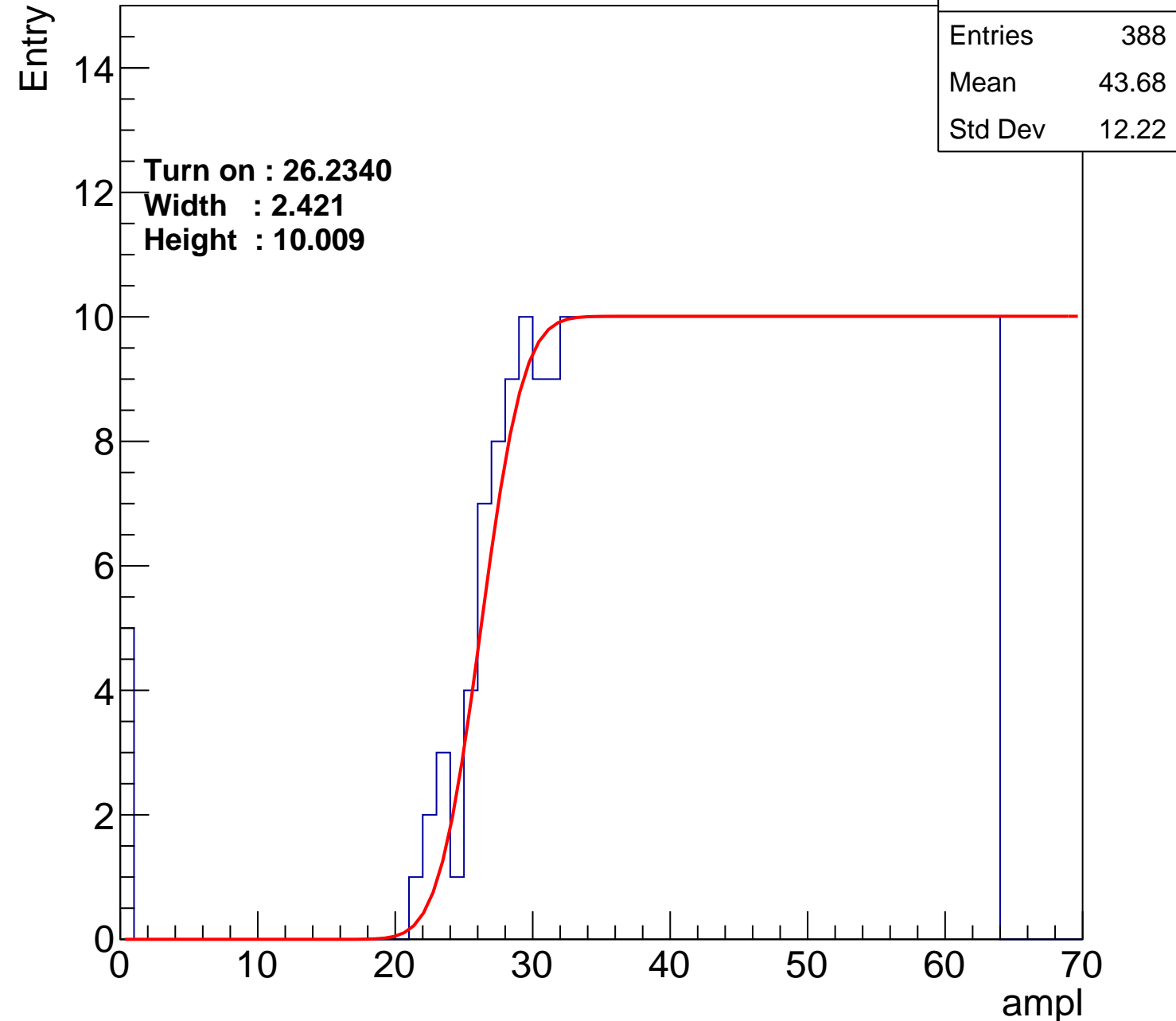
Width : 2.421

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	396
Mean	43.1
Std Dev	12.87

Turn on : 25.6147

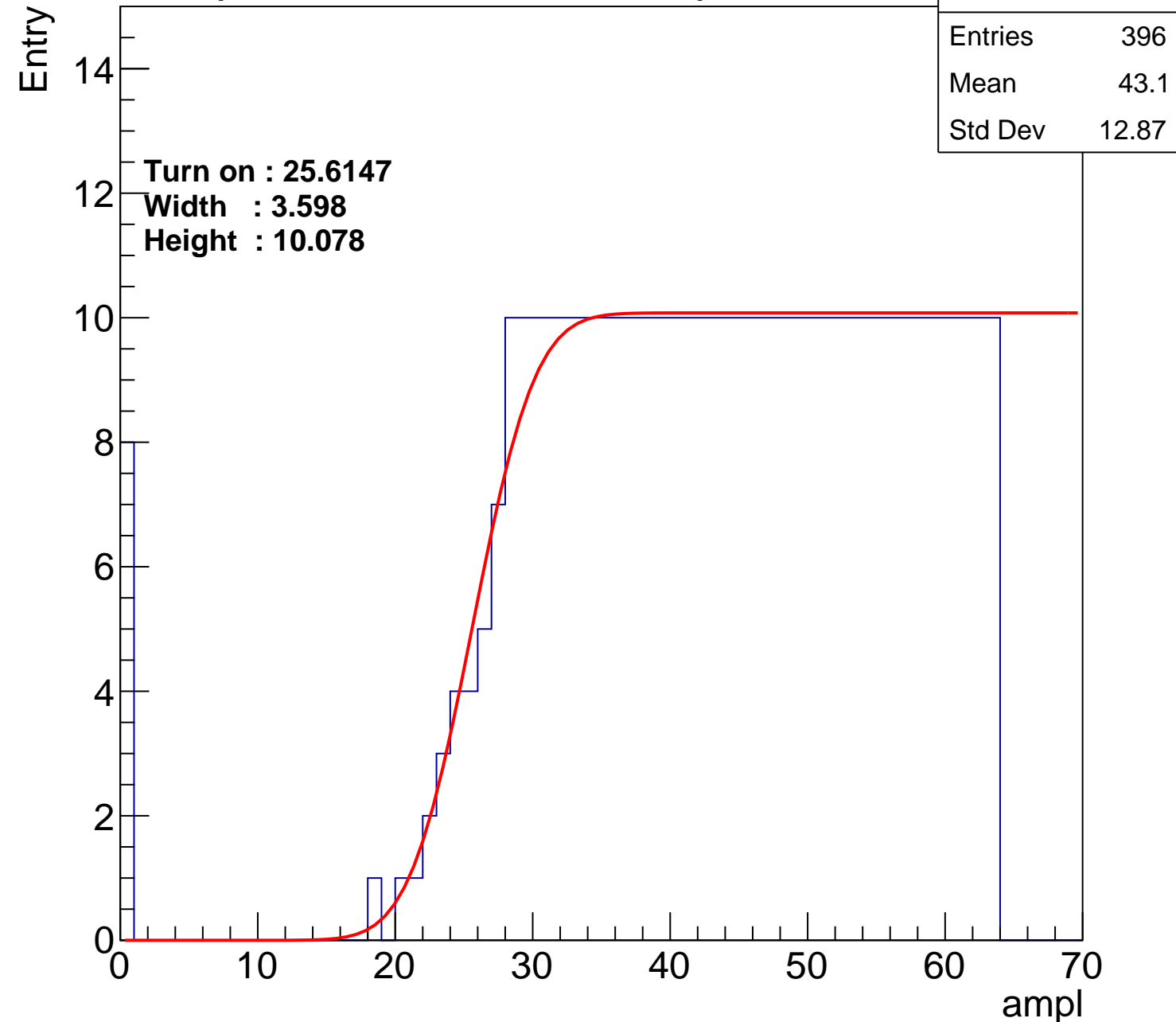
Width : 3.598

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch102

calib_packv5_042523_0143.root, FC#5, port B1

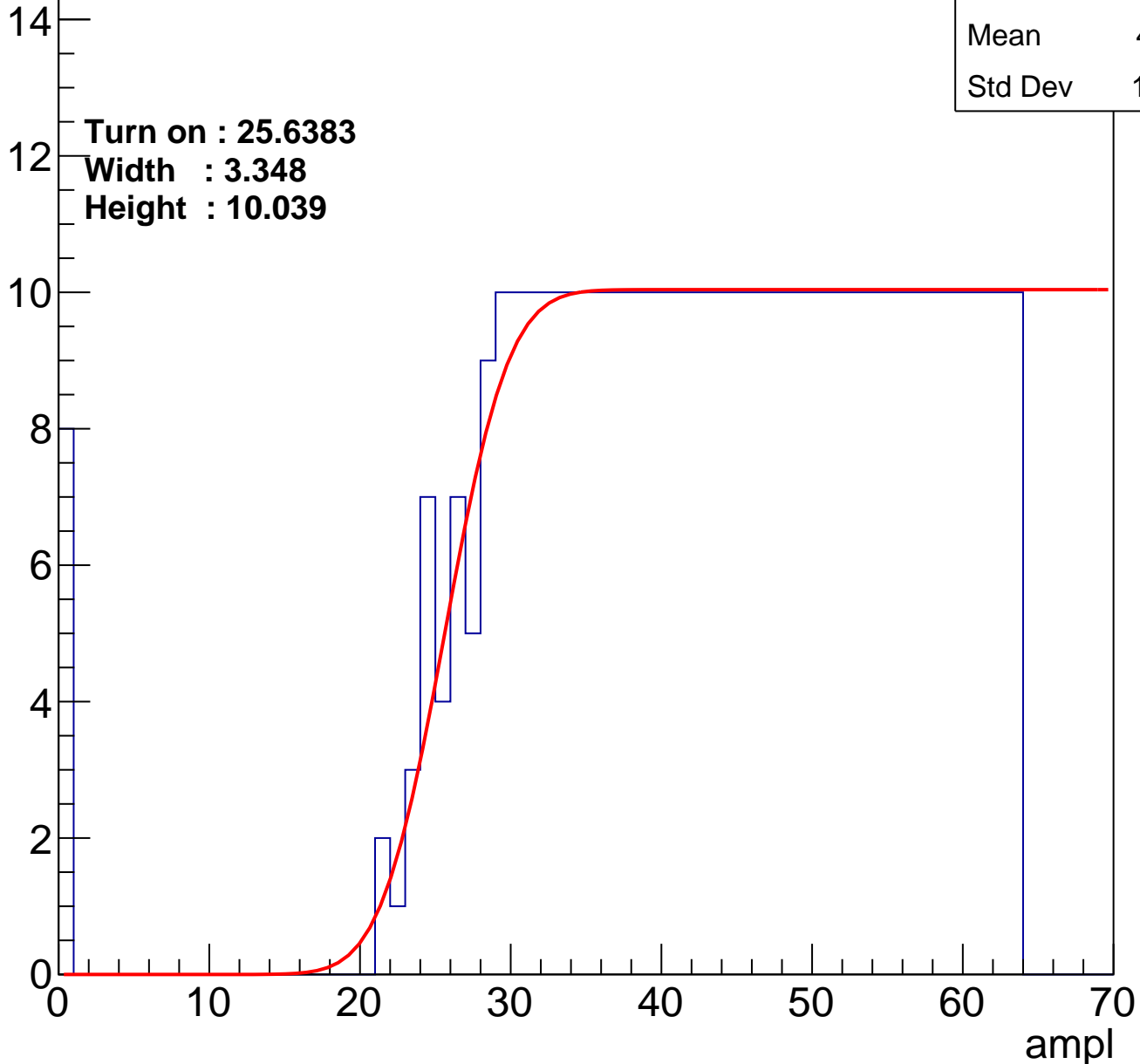
Entries	396
Mean	43.11
Std Dev	12.85

Turn on : 25.6383

Width : 3.348

Height : 10.039

Entry



B0L000S, U16-ch103

calib_packv5_042523_0143.root, FC#5, port B1

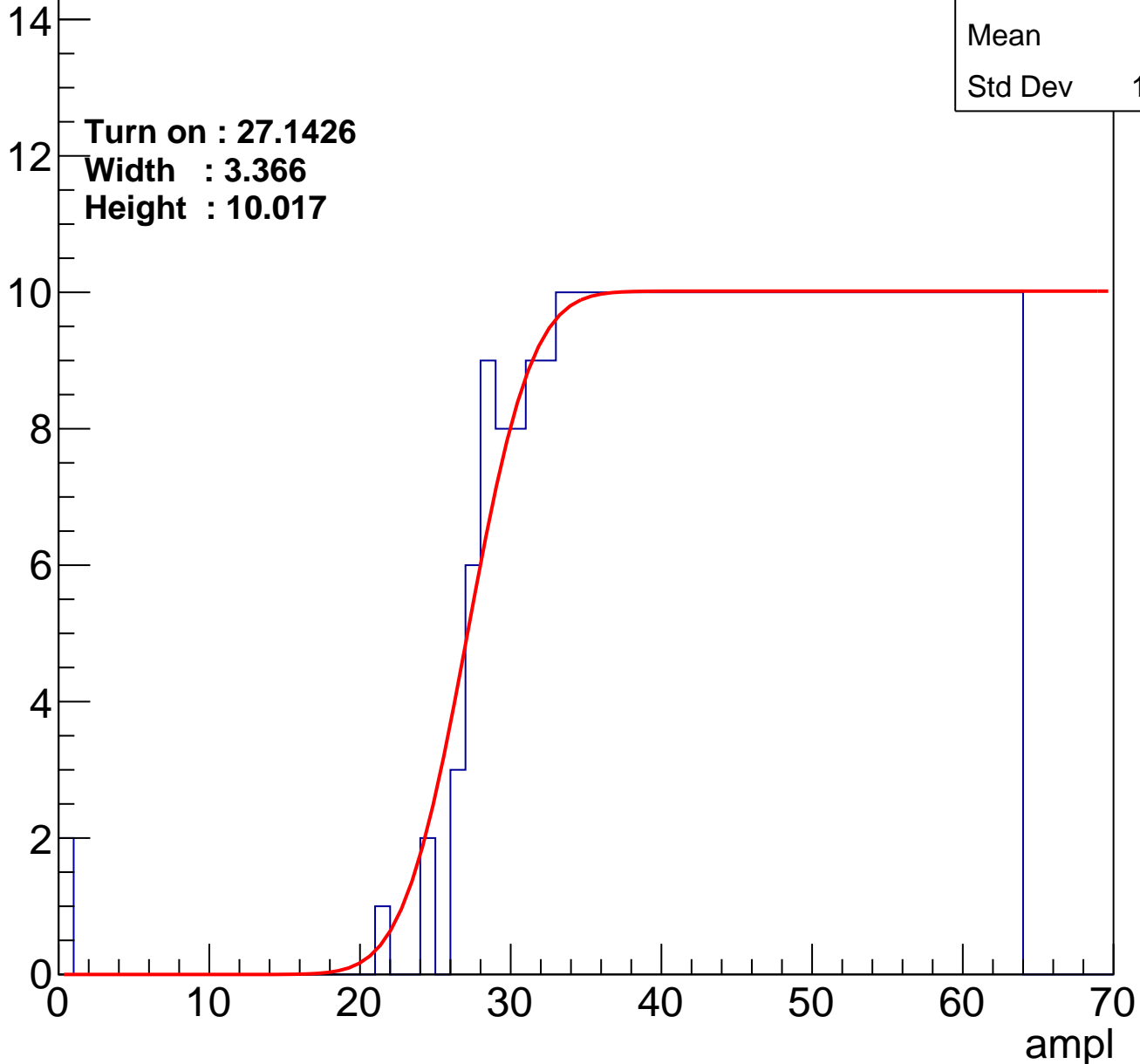
Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.1426

Width : 3.366

Height : 10.017

Entry



B0L000S, U16-ch104

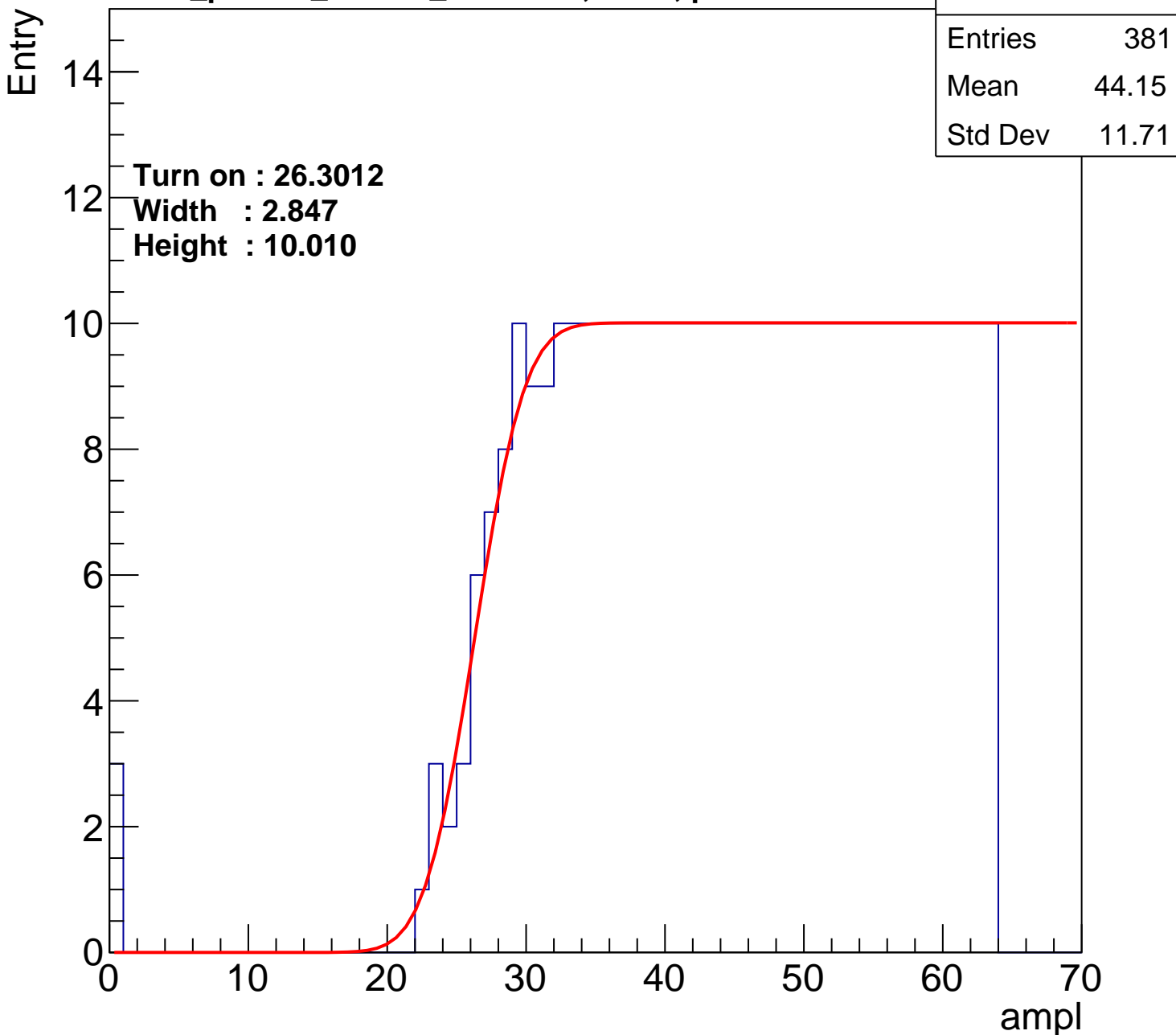
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 26.3012

Width : 2.847

Height : 10.010

Entries	381
Mean	44.15
Std Dev	11.71



B0L000S, U16-ch105

calib_packv5_042523_0143.root, FC#5, port B1

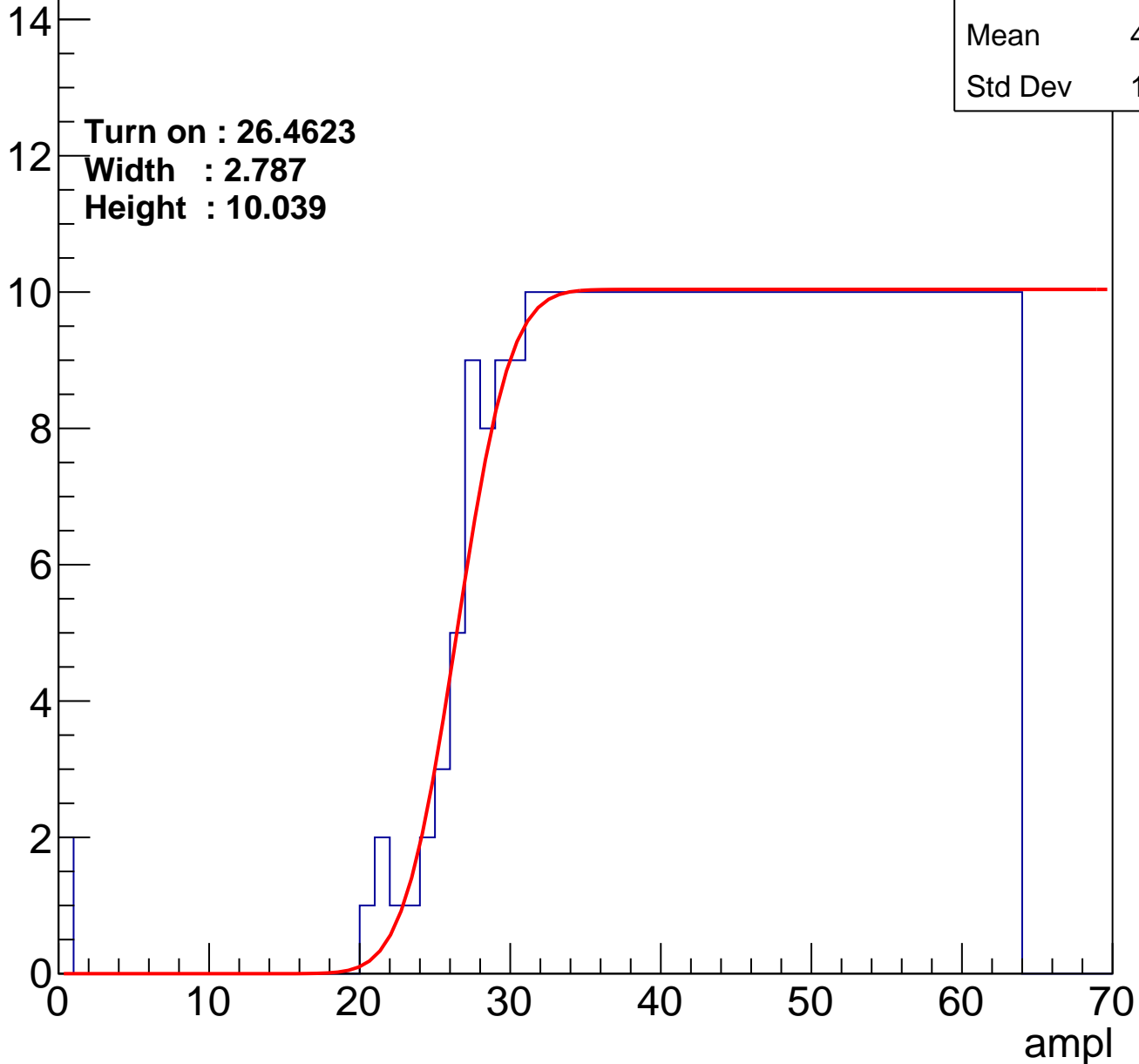
Entries	382
Mean	44.16
Std Dev	11.58

Turn on : 26.4623

Width : 2.787

Height : 10.039

Entry



B0L000S, U16-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	402
Mean	43.07
Std Dev	12.37

Turn on : 24.4562

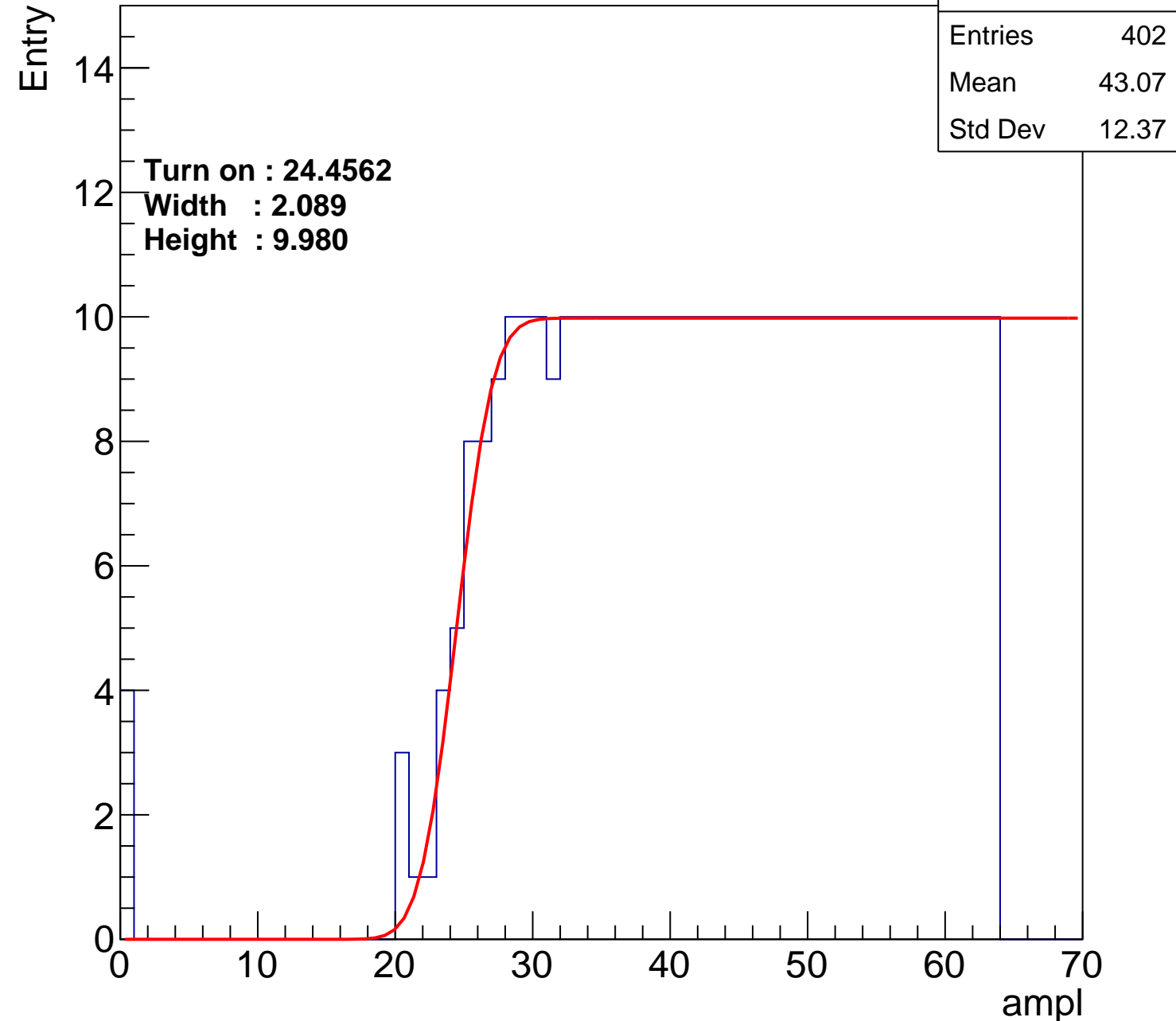
Width : 2.089

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch107

calib_packv5_042523_0143.root, FC#5, port B1

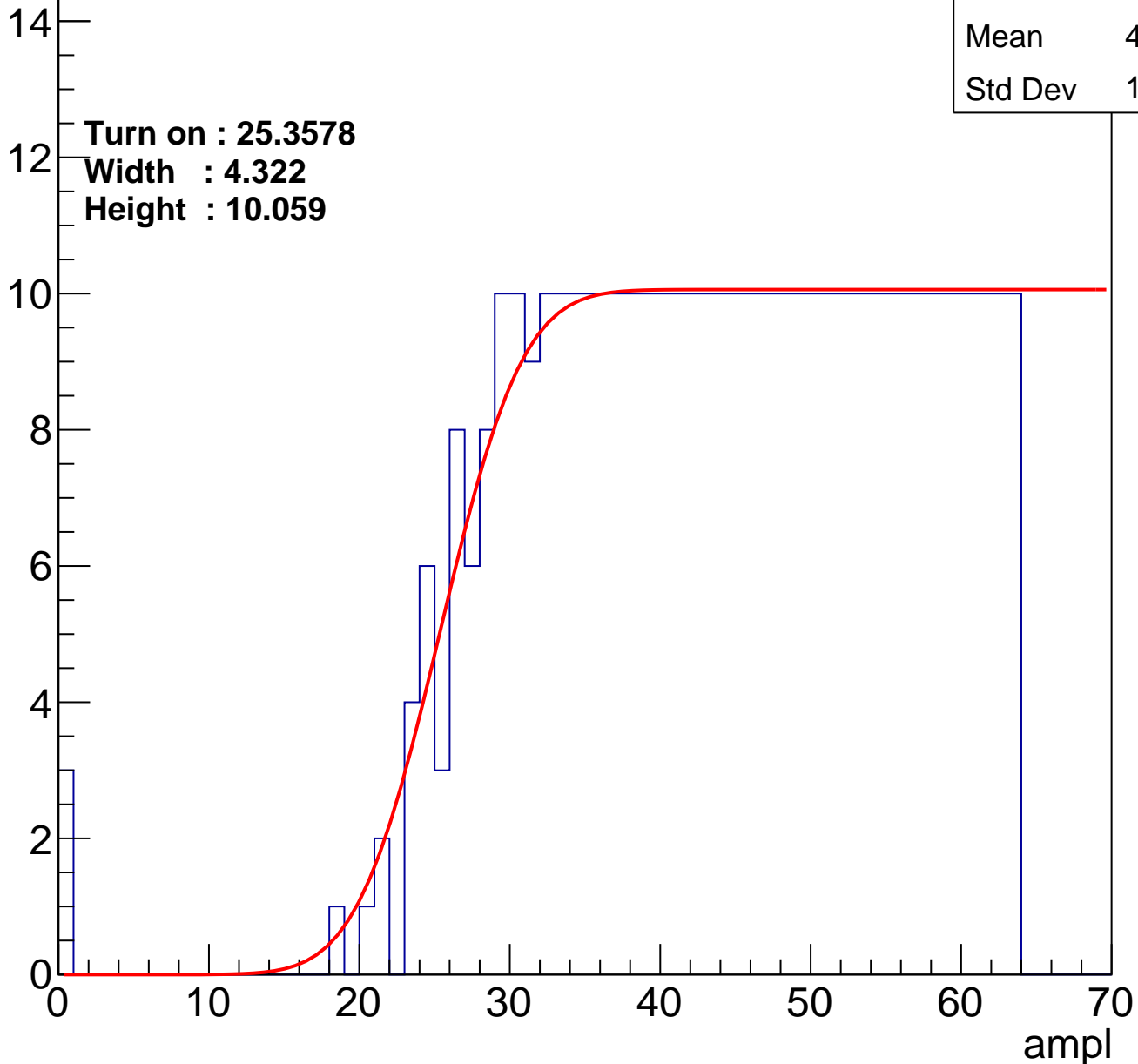
Entries	391
Mean	43.62
Std Dev	12.03

Turn on : 25.3578

Width : 4.322

Height : 10.059

Entry



B0L000S, U16-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.17
Std Dev	11.27

Turn on : 28.5040

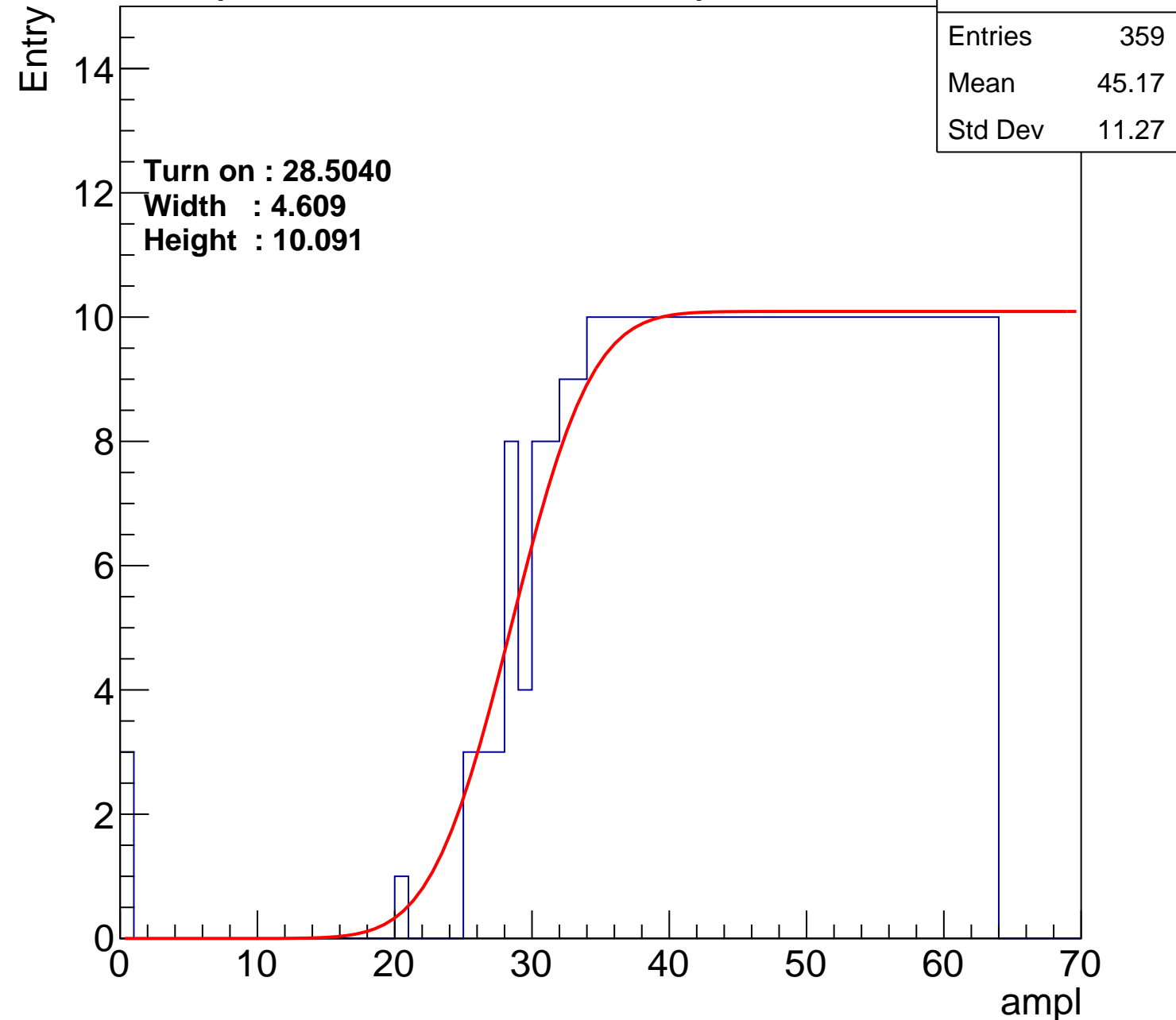
Width : 4.609

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 27.1498

Width : 3.662

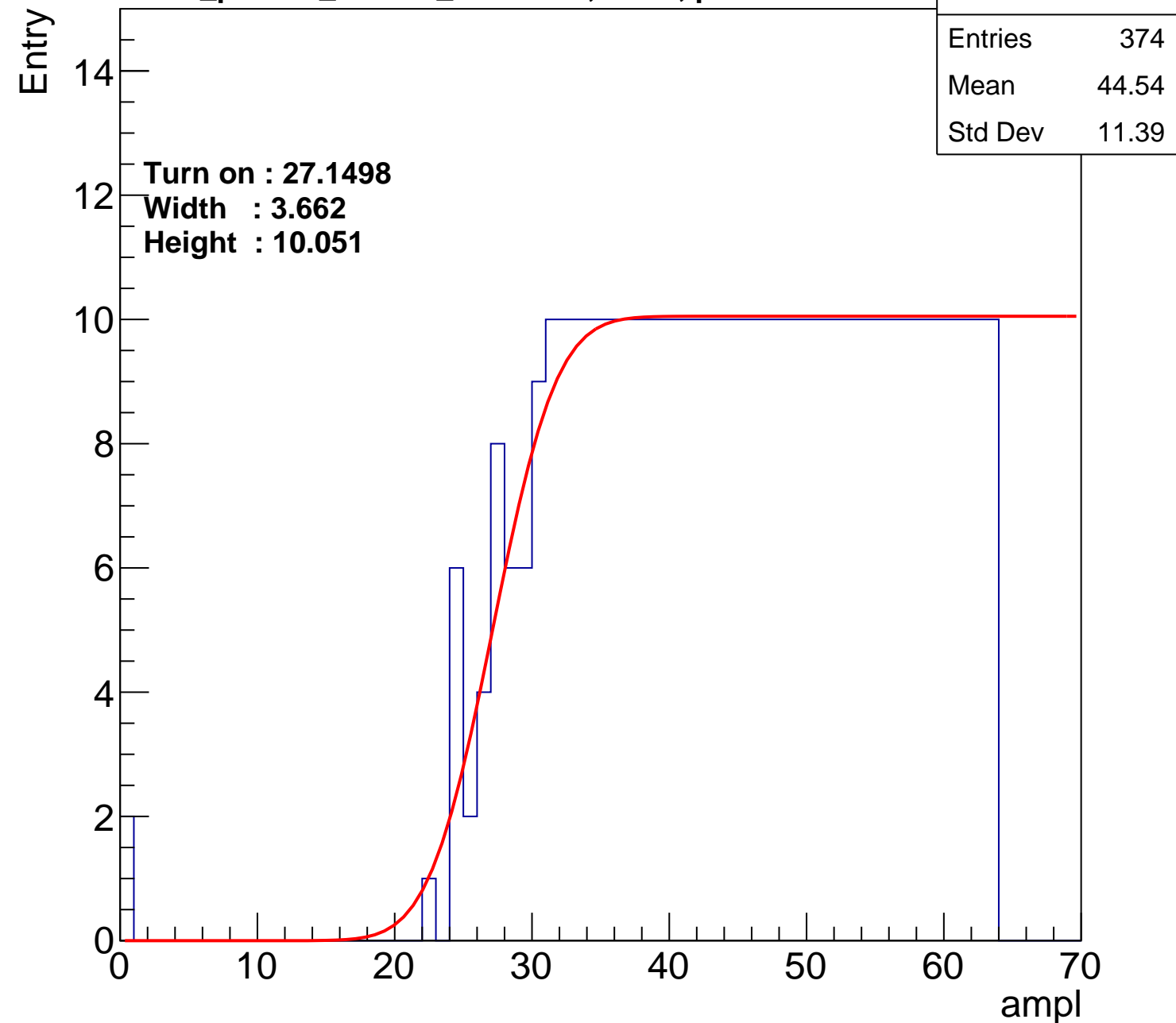
Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U16-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.54
Std Dev	12.45

Turn on : 25.7129

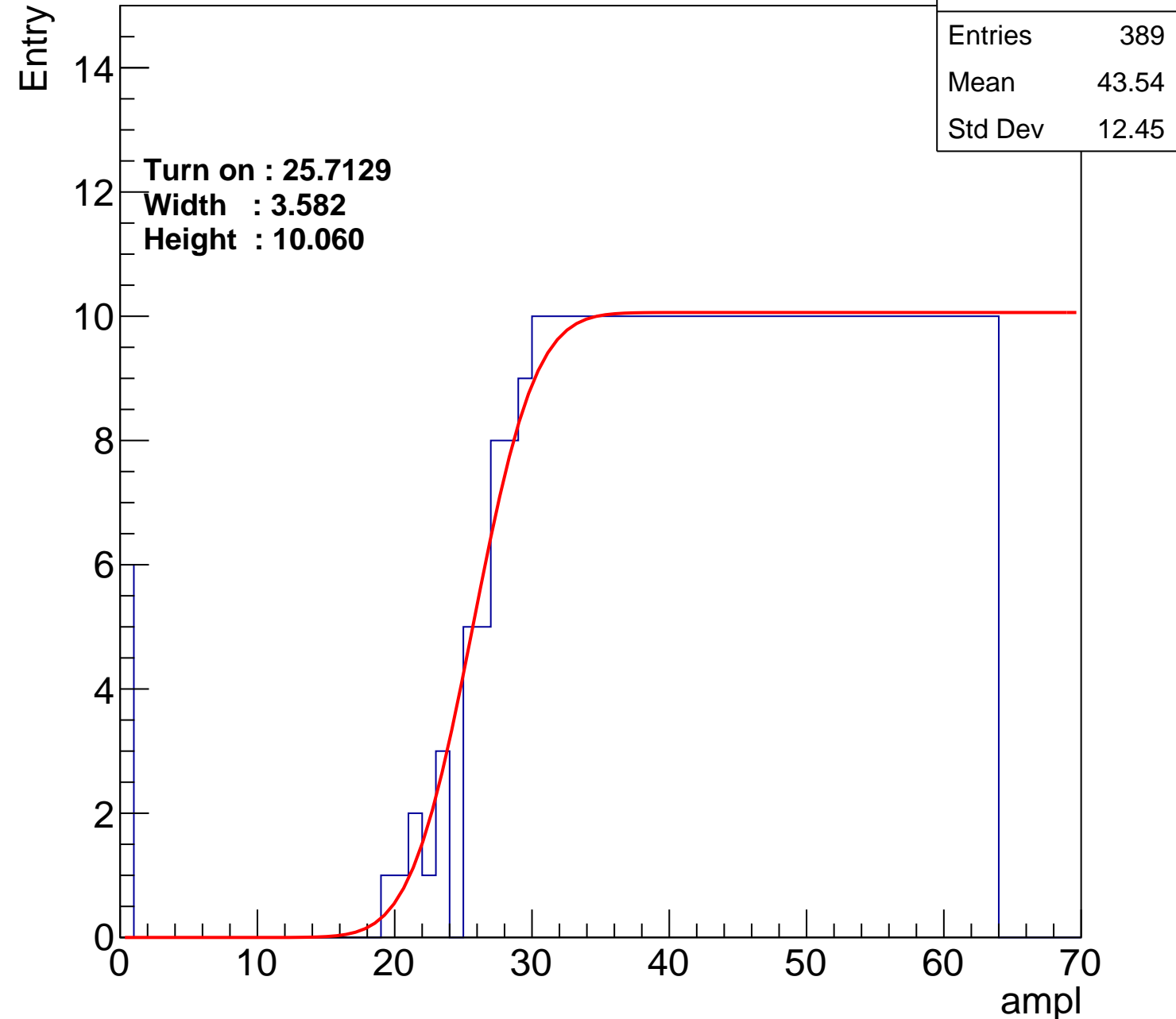
Width : 3.582

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.49
Std Dev	10.88

Turn on : 28.6352

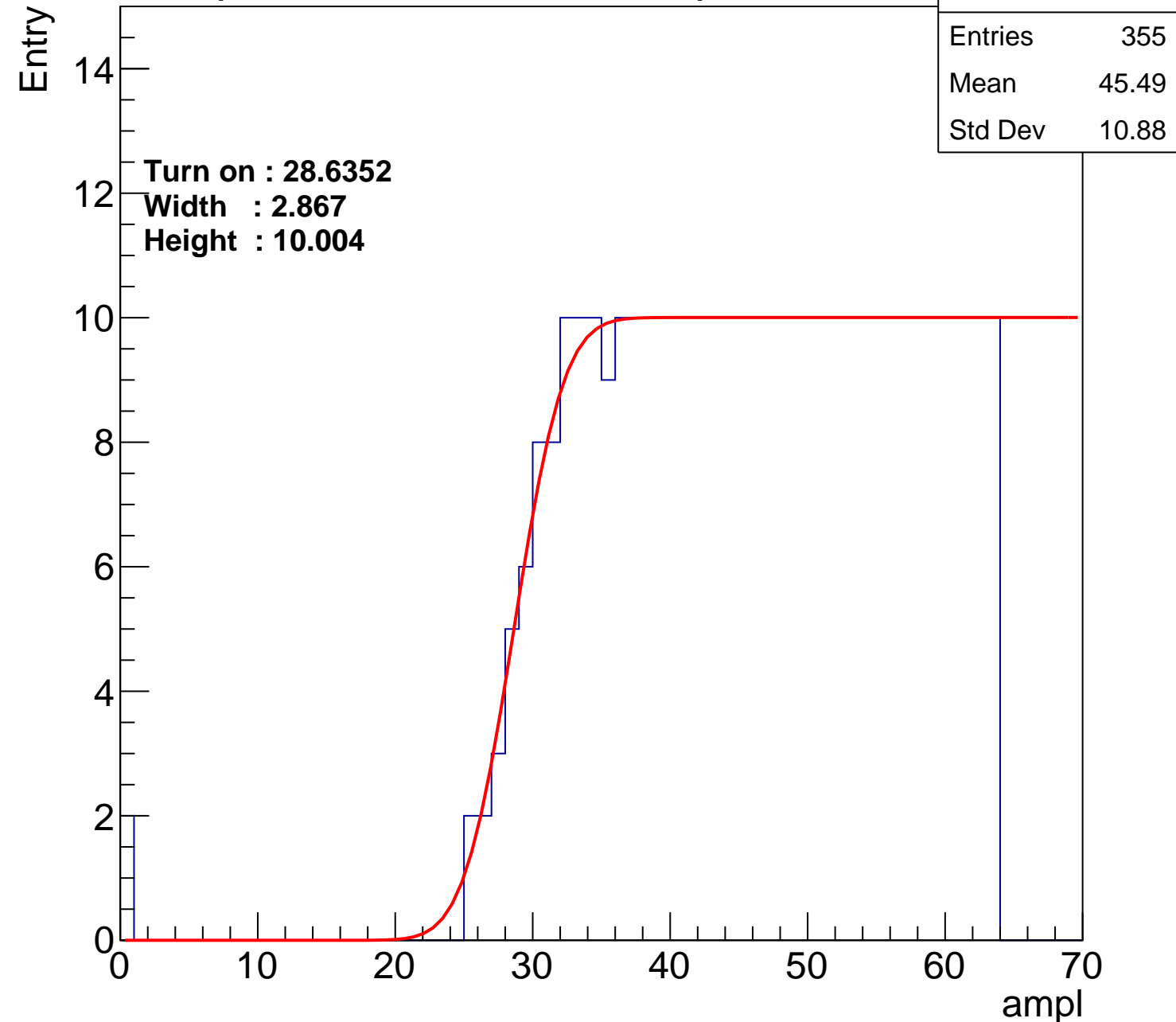
Width : 2.867

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.6057

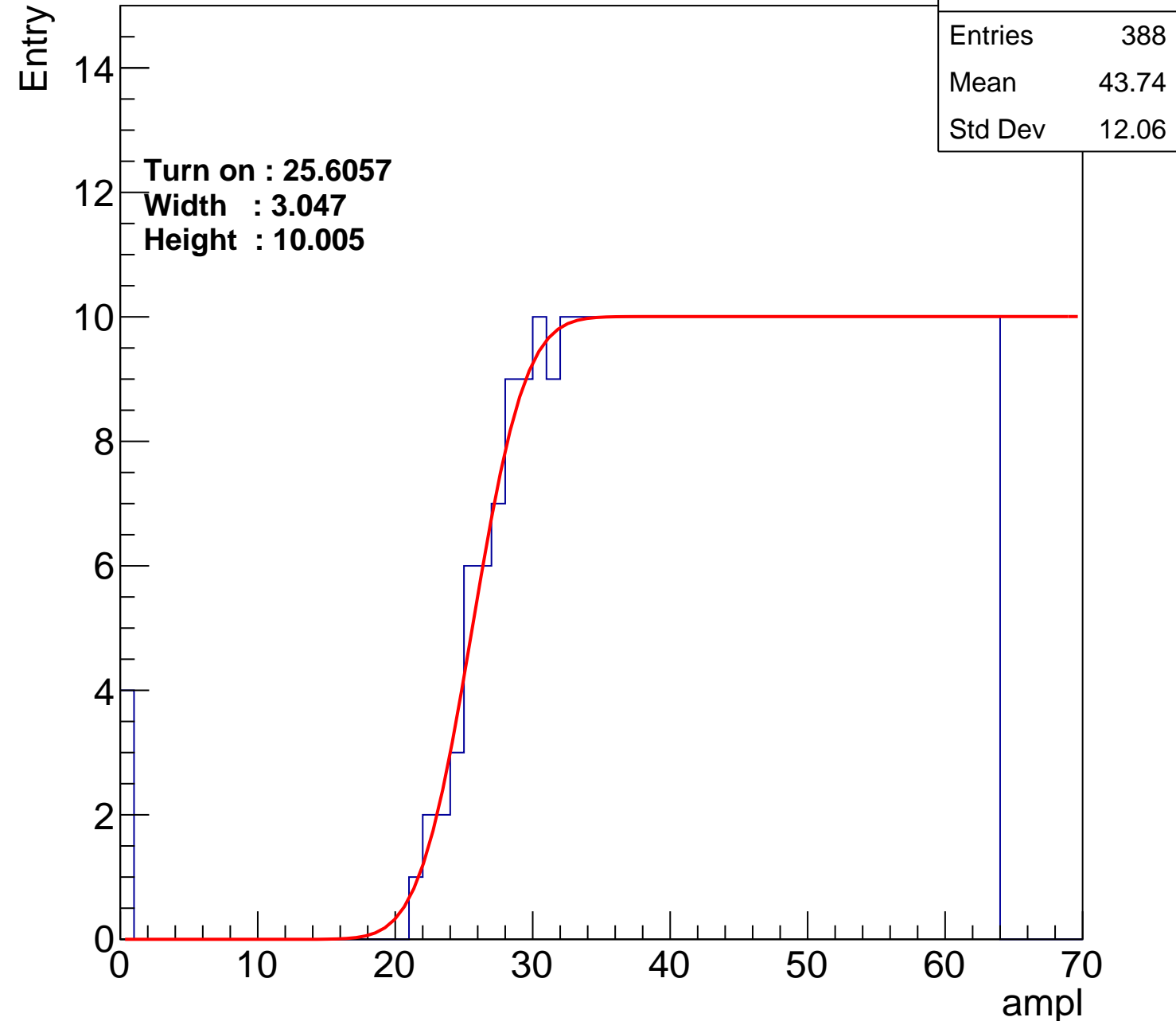
Width : 3.047

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.4159

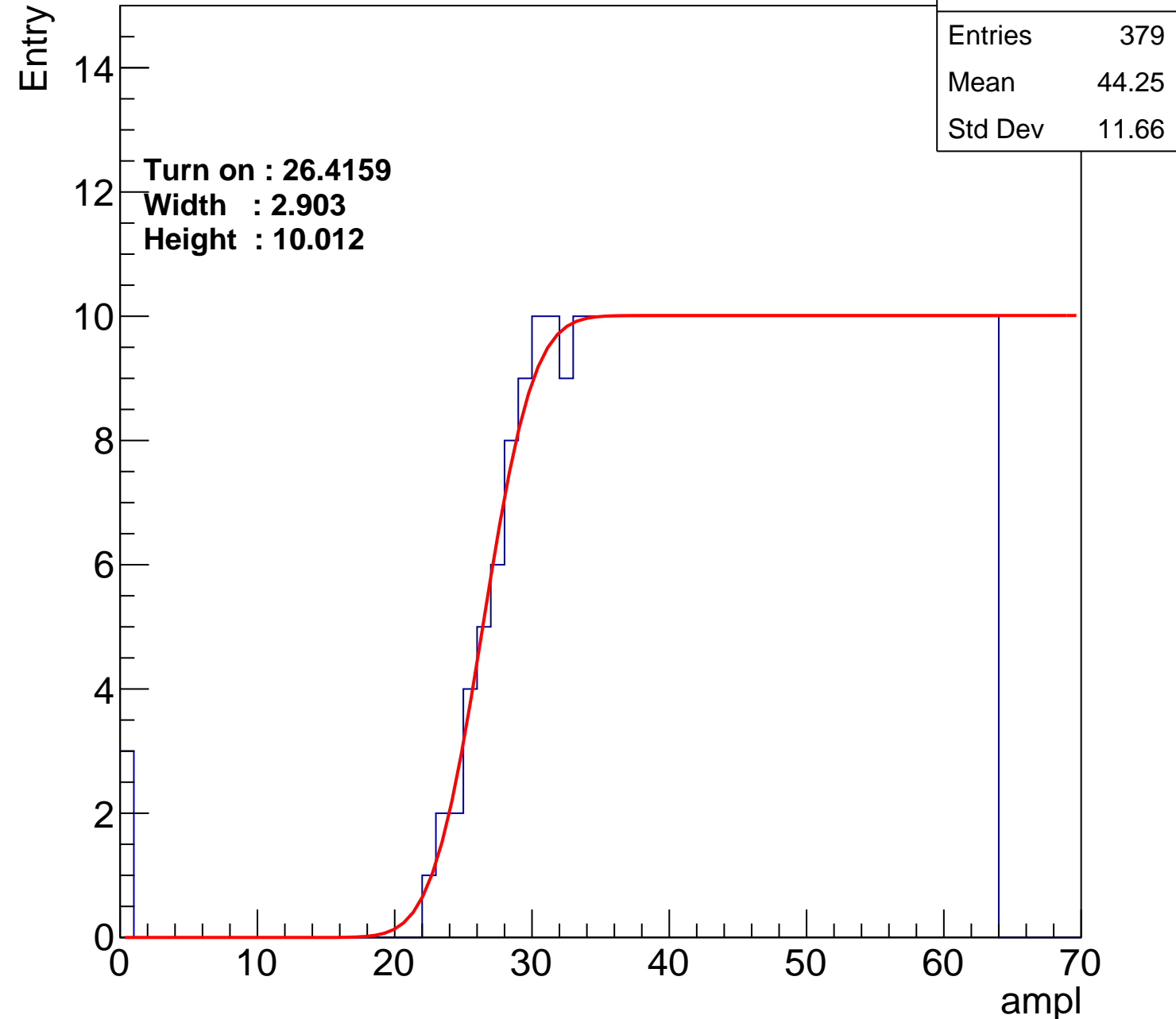
Width : 2.903

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	397
Mean	43.47
Std Dev	12.02

Turn on : 24.0345

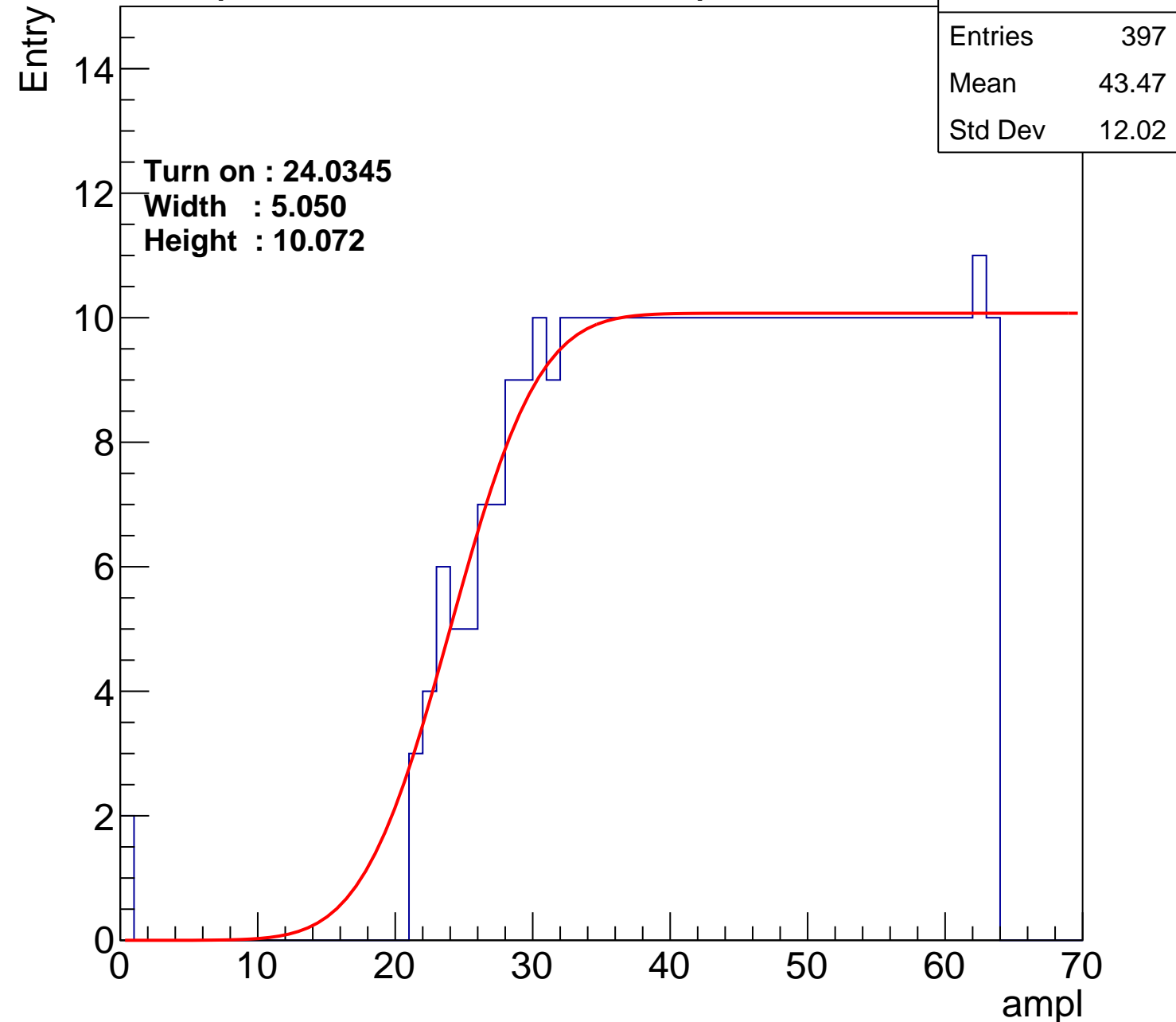
Width : 5.050

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch115

calib_packv5_042523_0143.root, FC#5, port B1

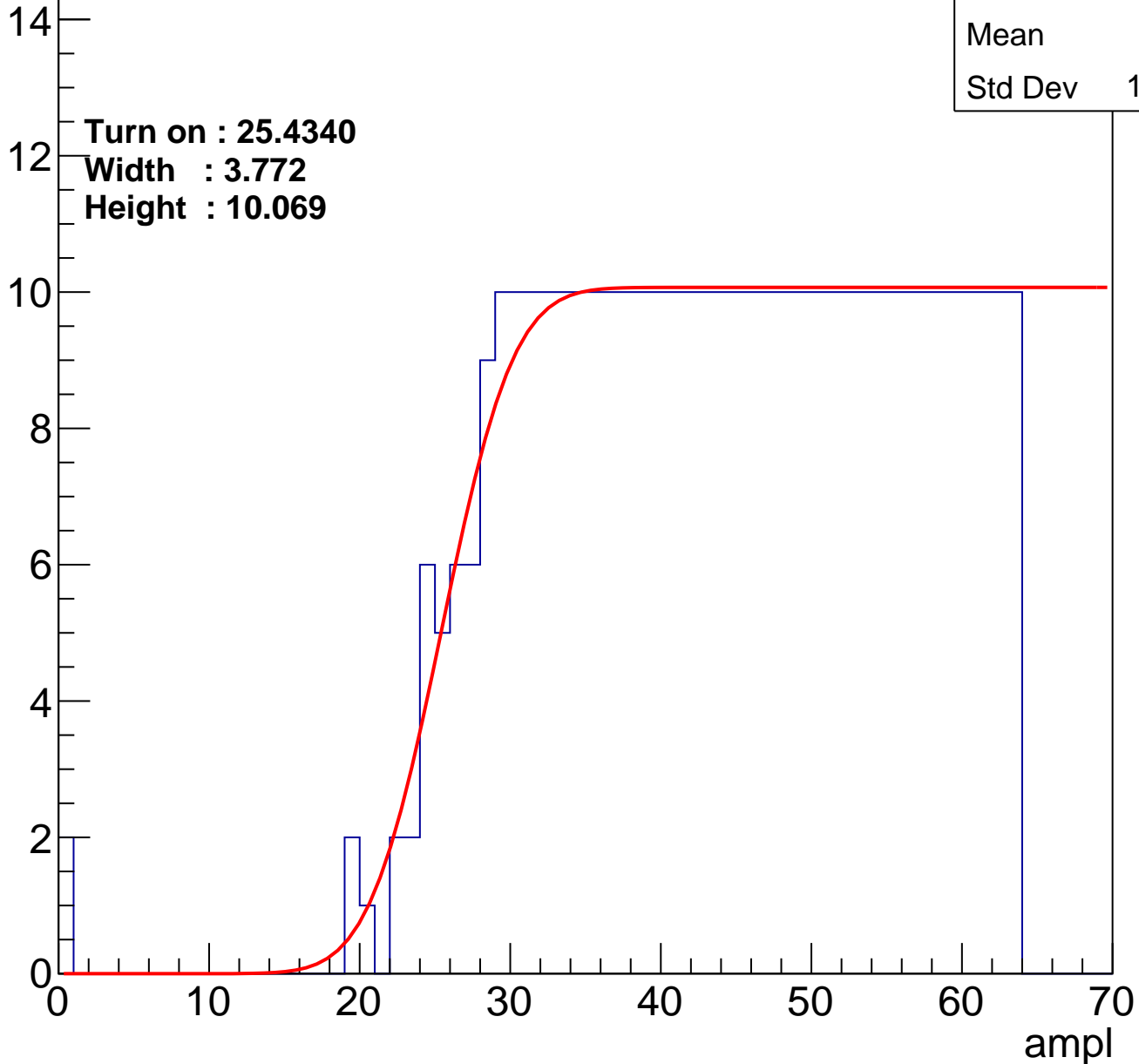
Entries	391
Mean	43.7
Std Dev	11.84

Turn on : 25.4340

Width : 3.772

Height : 10.069

Entry



B0L000S, U16-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.22
Std Dev	12.36

Turn on : 28.0791

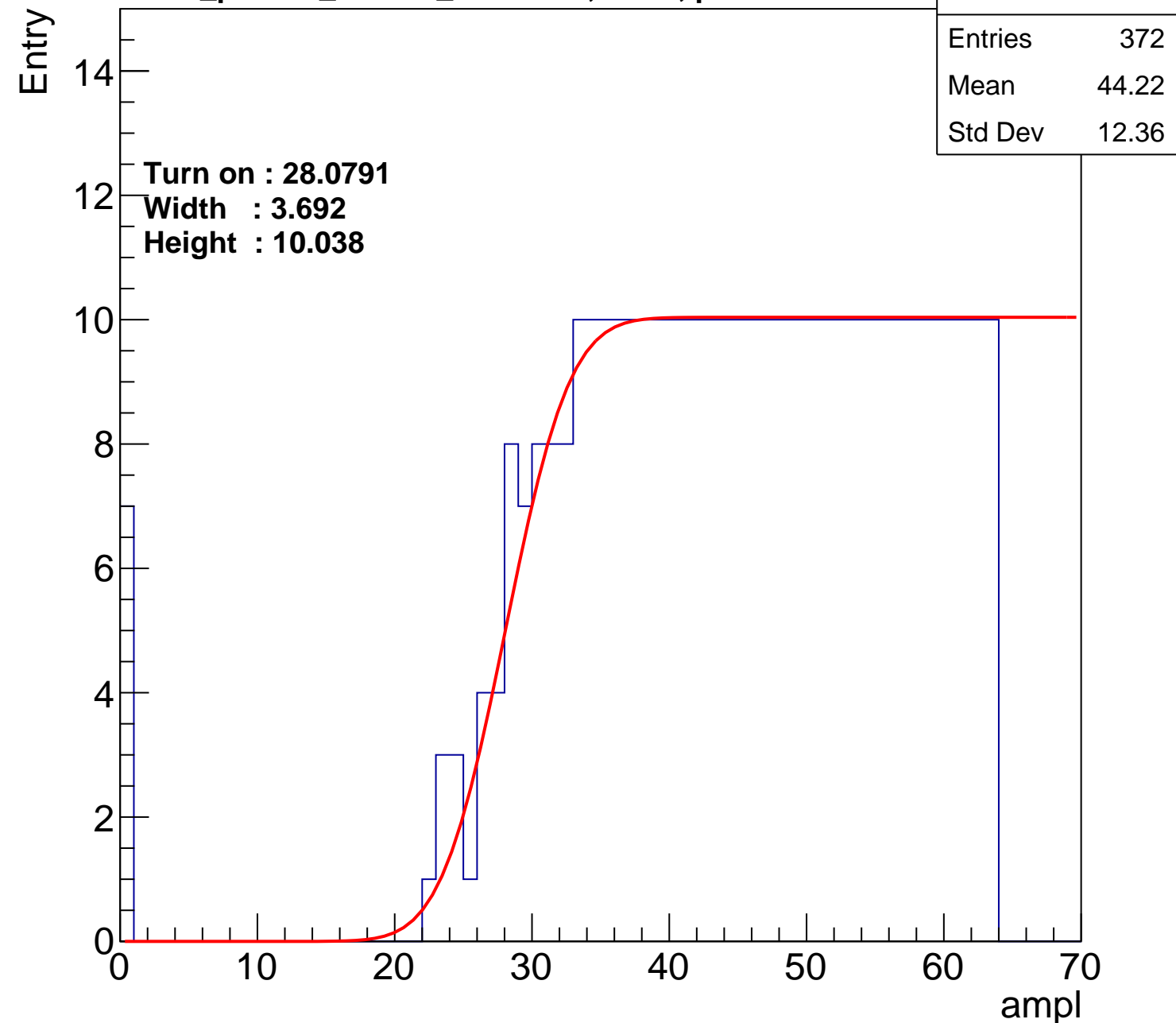
Width : 3.692

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.89
Std Dev	11.75

Turn on : 25.5835

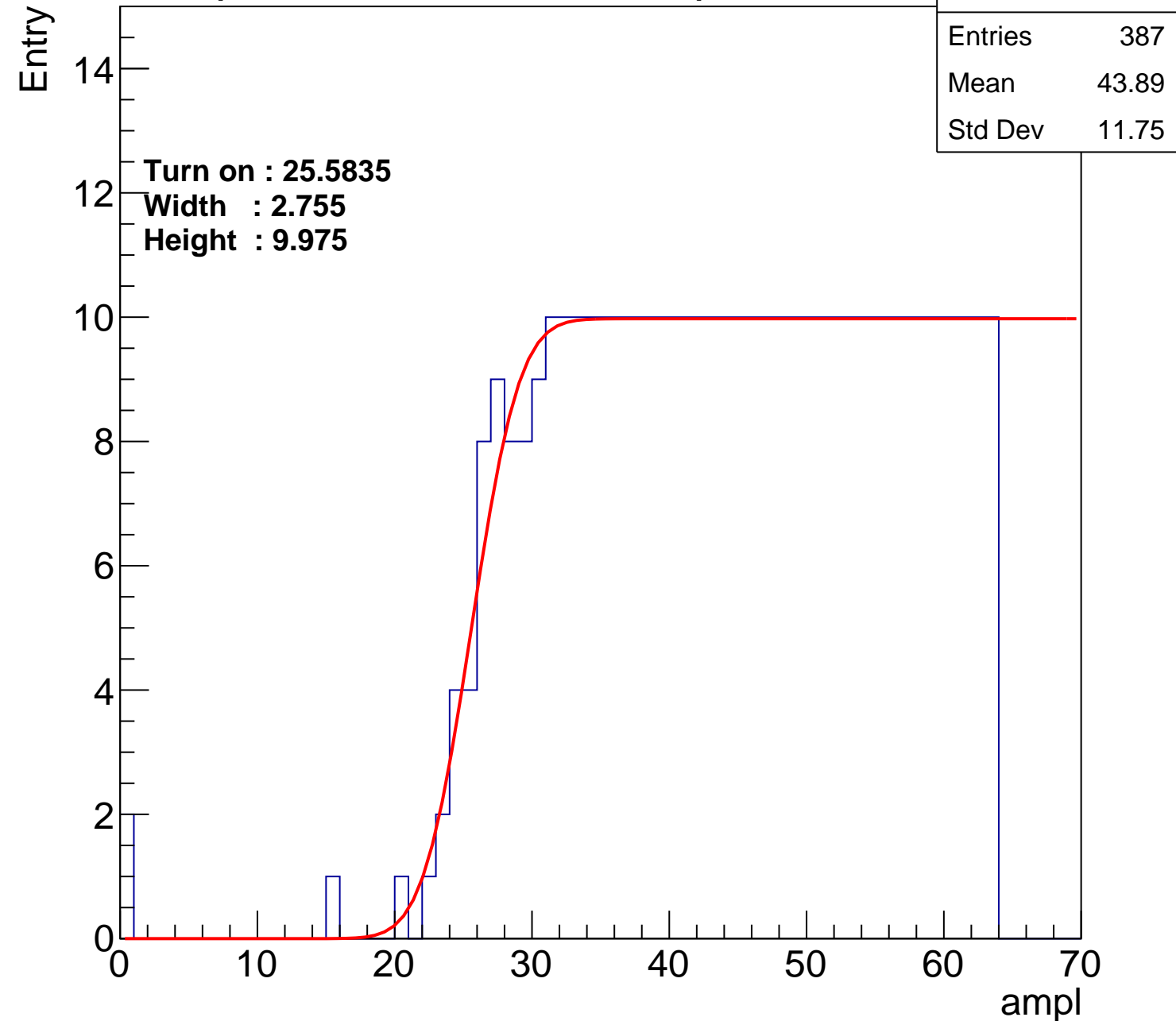
Width : 2.755

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.83
Std Dev	12

Turn on : 26.3312

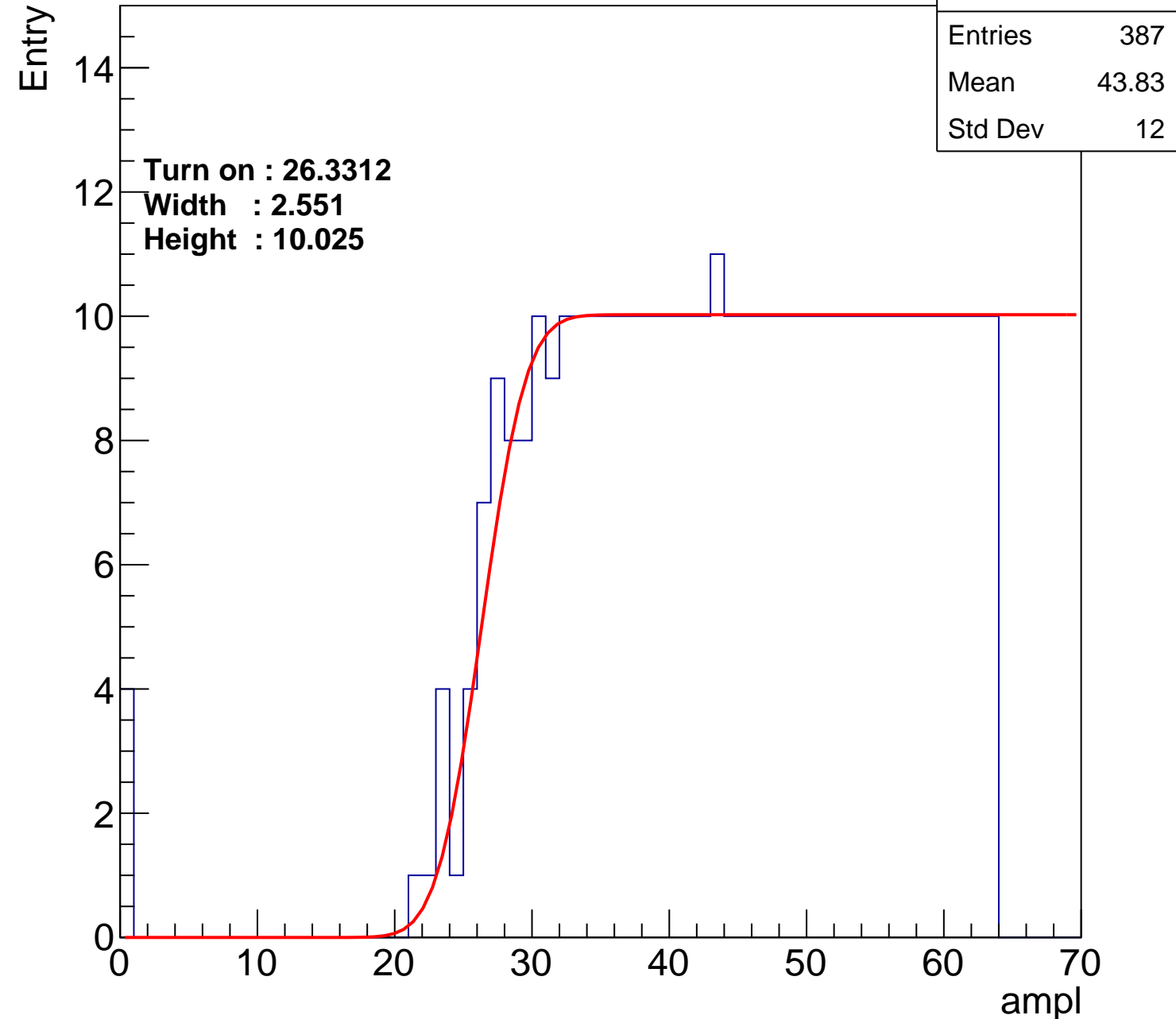
Width : 2.551

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 27.2125

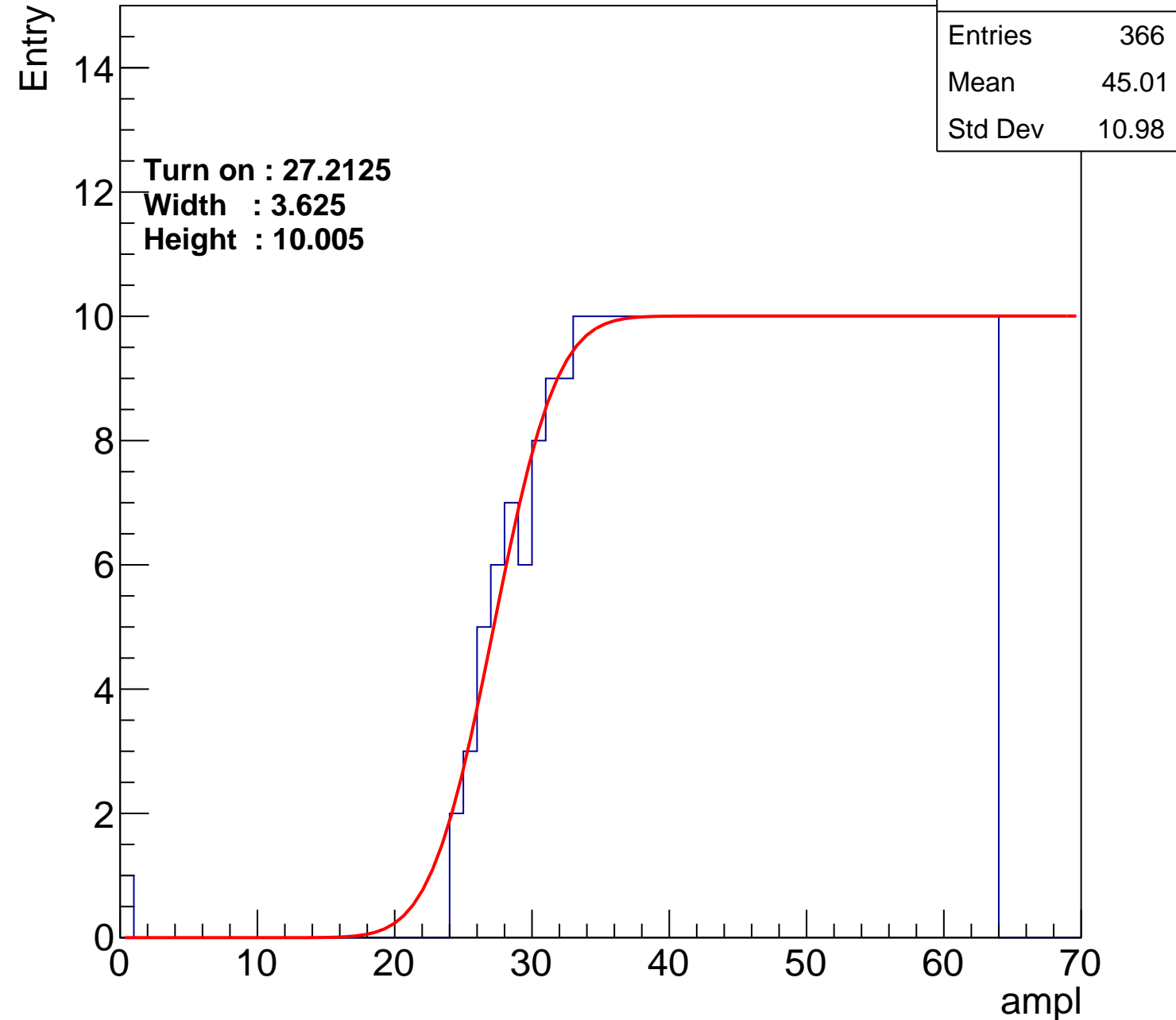
Width : 3.625

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch120

calib_packv5_042523_0143.root, FC#5, port B1

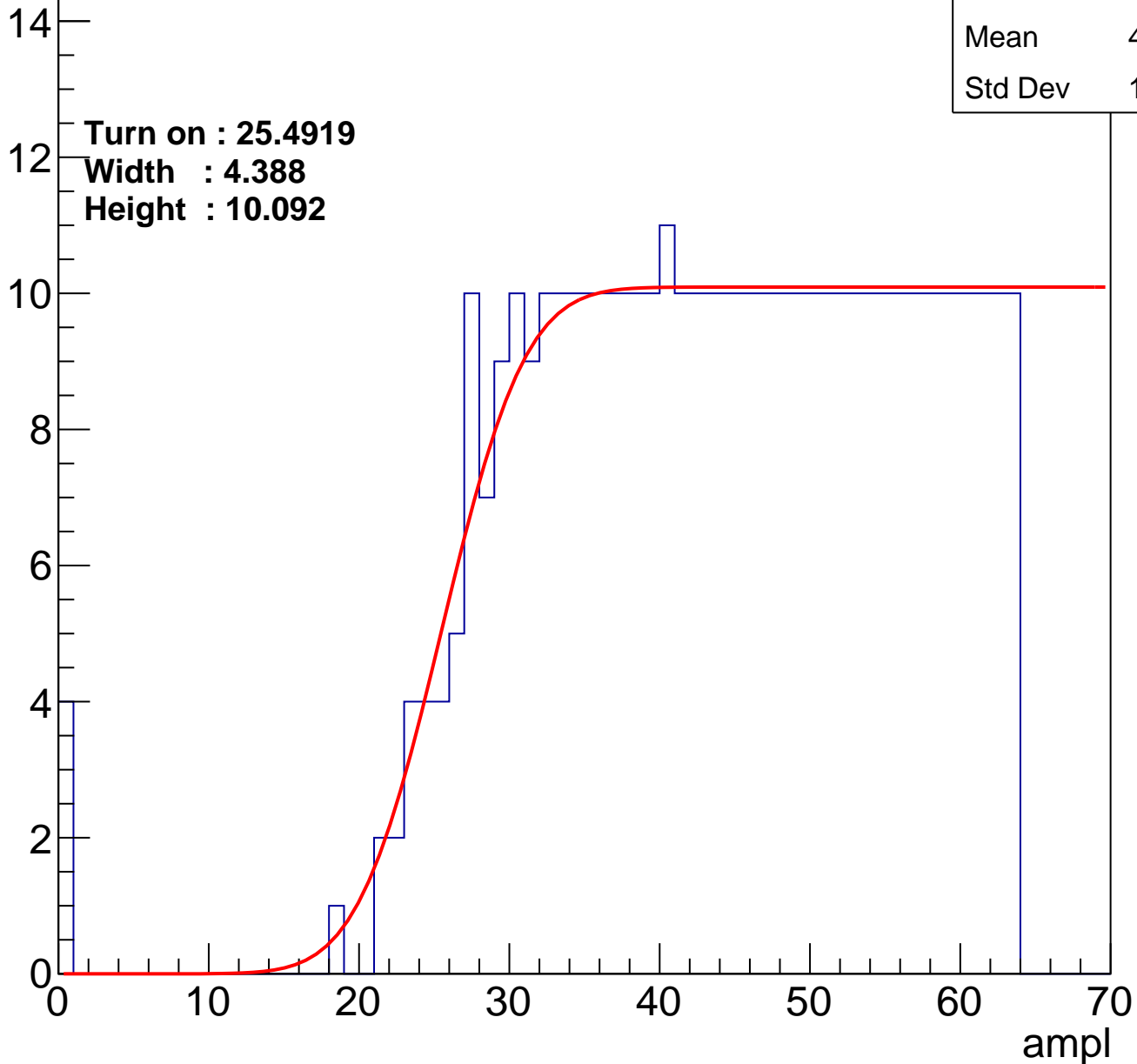
Entries	392
Mean	43.54
Std Dev	12.18

Turn on : 25.4919

Width : 4.388

Height : 10.092

Entry



B0L000S, U16-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.49

Turn on : 26.1134

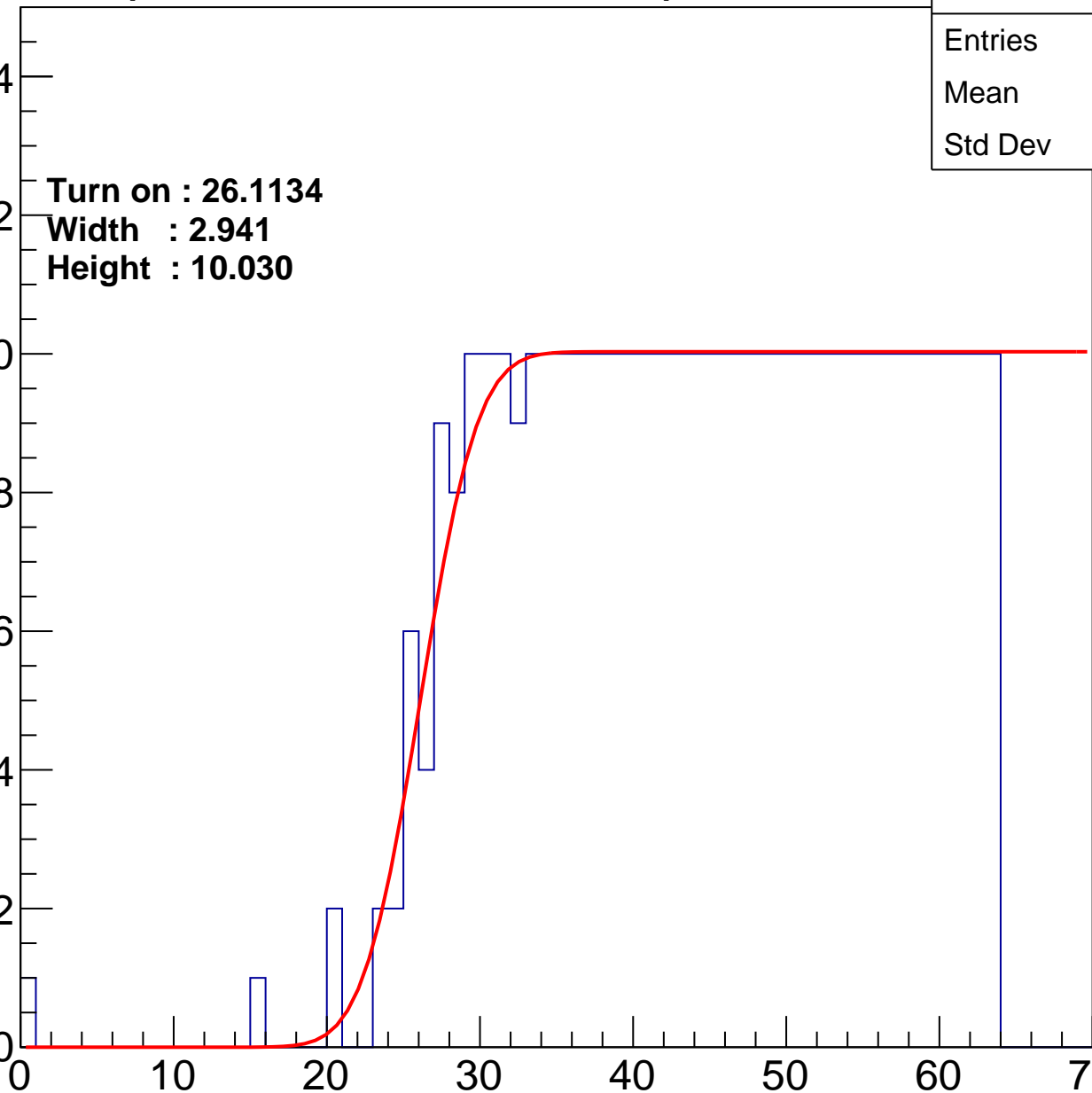
Width : 2.941

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch122

calib_packv5_042523_0143.root, FC#5, port B1

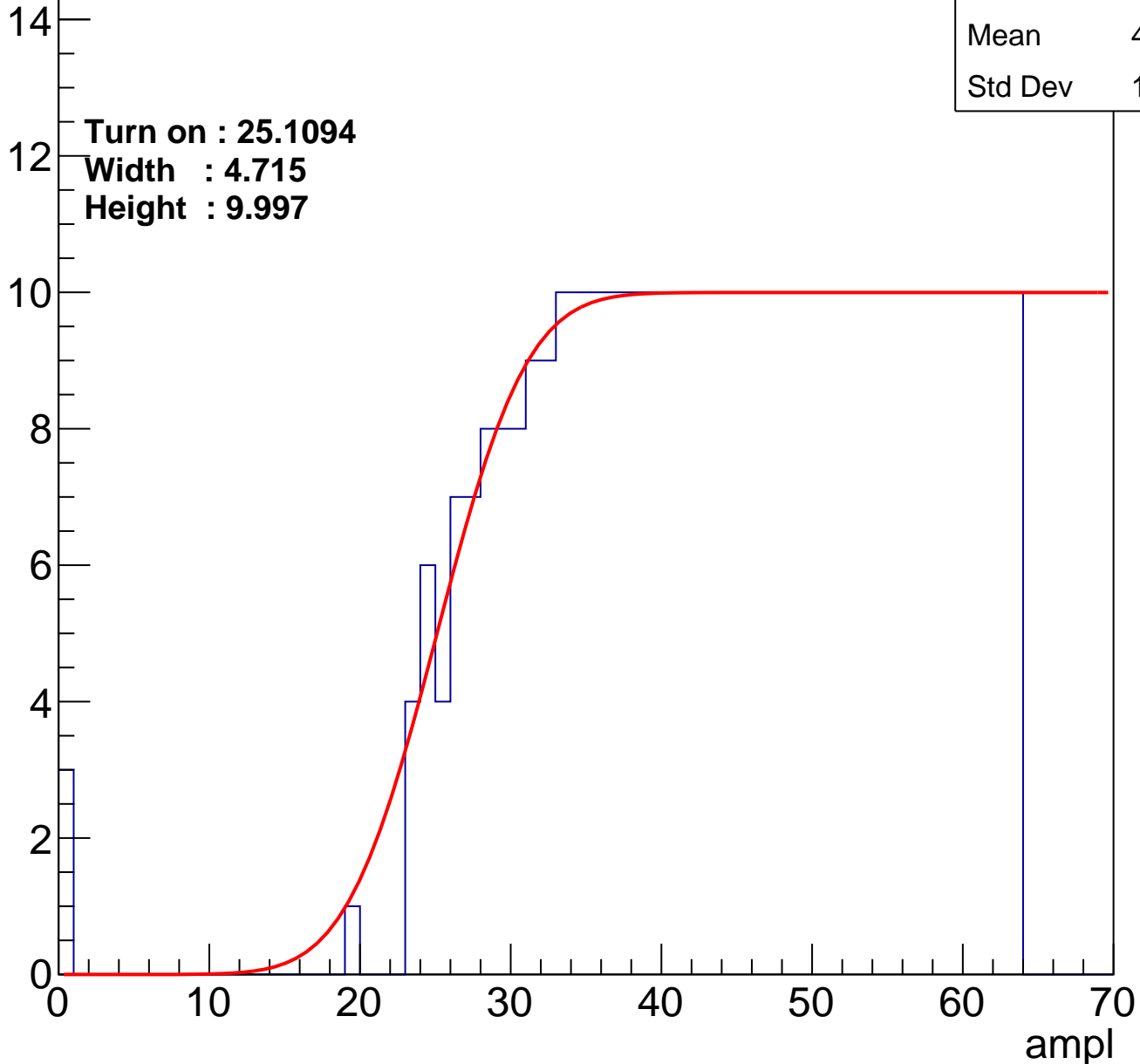
Entries	384
Mean	43.93
Std Dev	11.89

Turn on : 25.1094

Width : 4.715

Height : 9.997

Entry



B0L000S, U16-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	395
Mean	43.56
Std Dev	11.77

Turn on : 24.4367

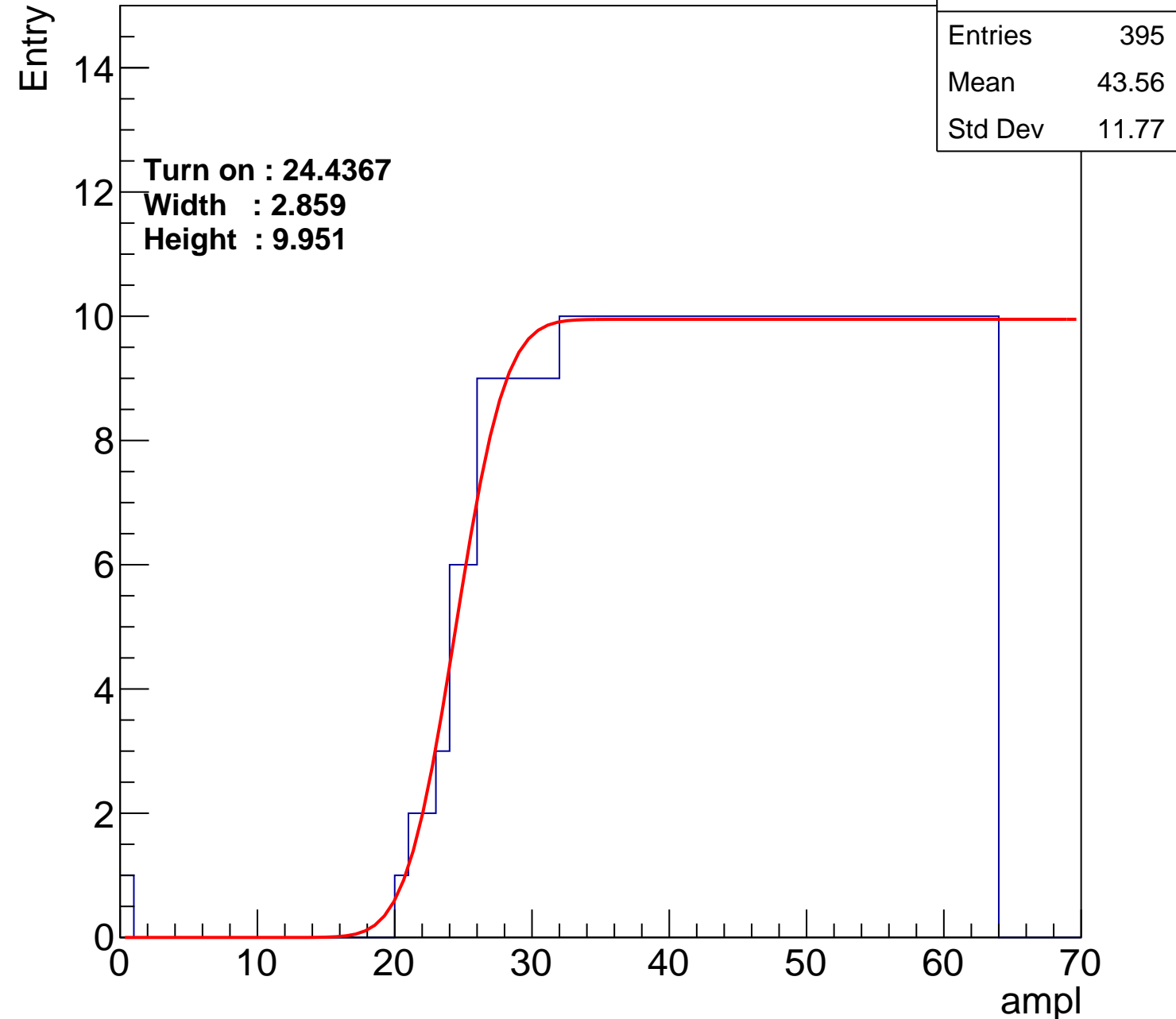
Width : 2.859

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch124

calib_packv5_042523_0143.root, FC#5, port B1

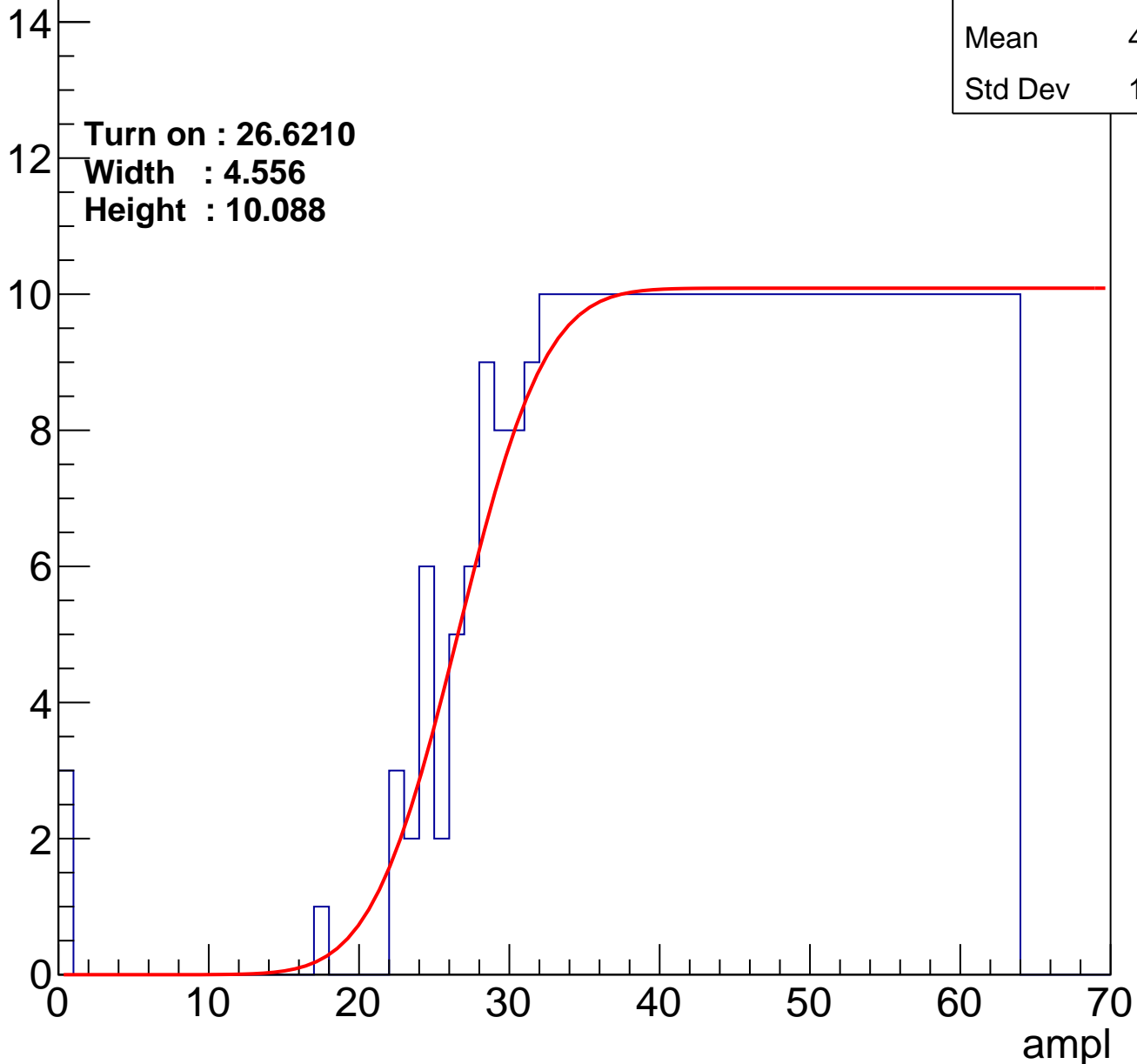
Entries	382
Mean	44.03
Std Dev	11.86

Turn on : 26.6210

Width : 4.556

Height : 10.088

Entry



B0L000S, U16-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.21
Std Dev	11.73

Turn on : 27.1139

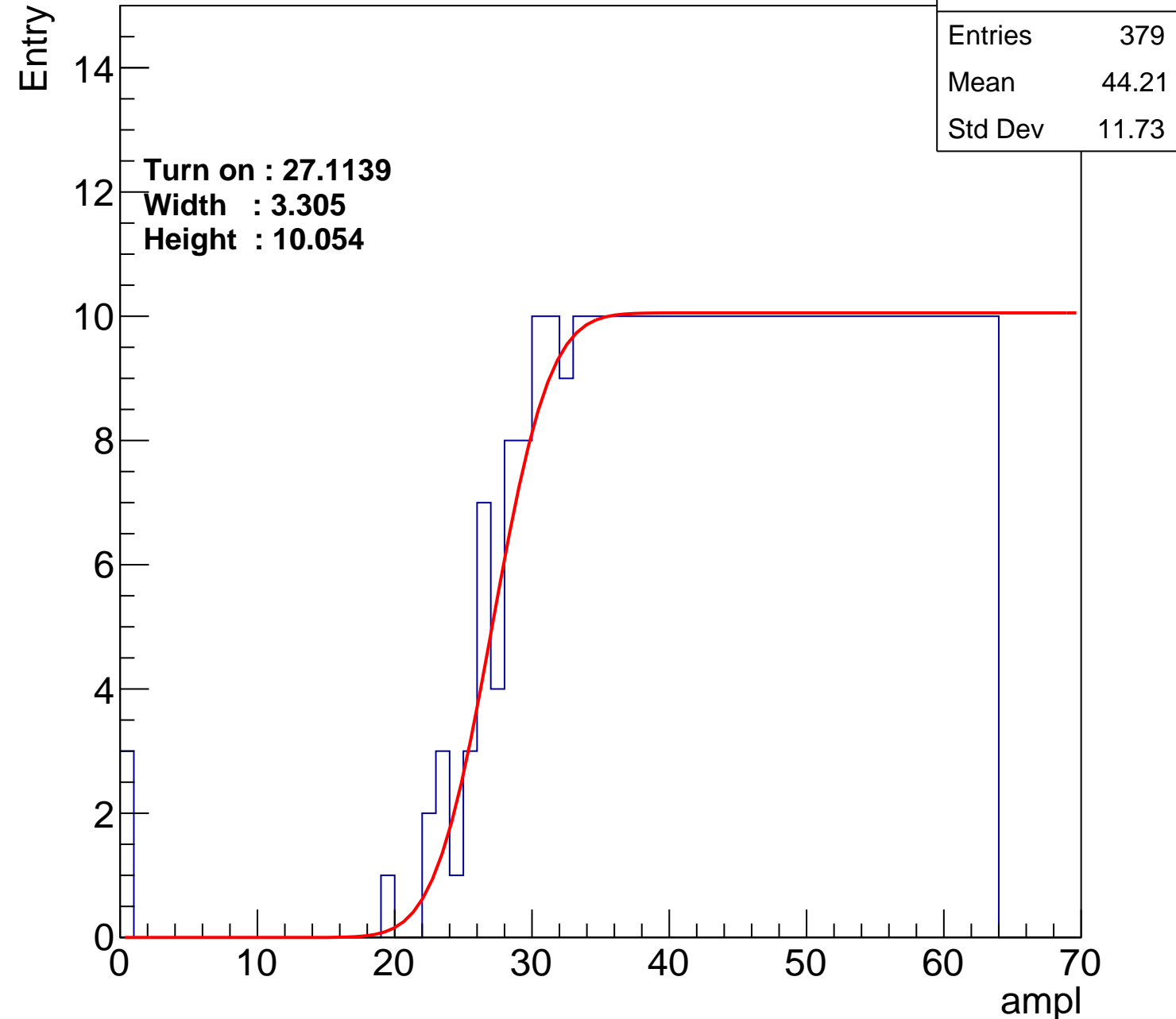
Width : 3.305

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.07
Std Dev	11.79

Turn on : 26.7825

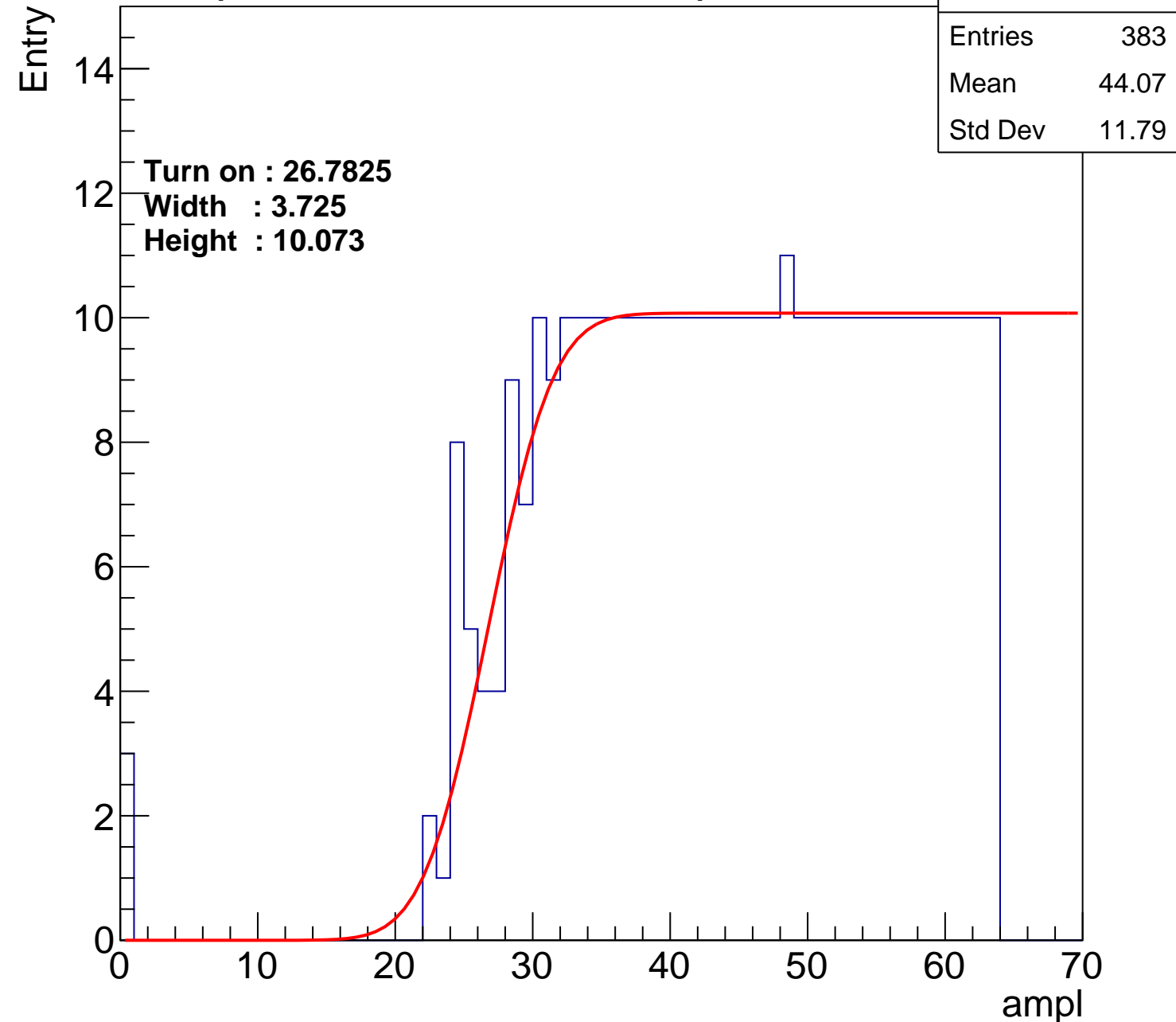
Width : 3.725

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	399
Mean	42.87
Std Dev	13.09

Turn on : 24.9951

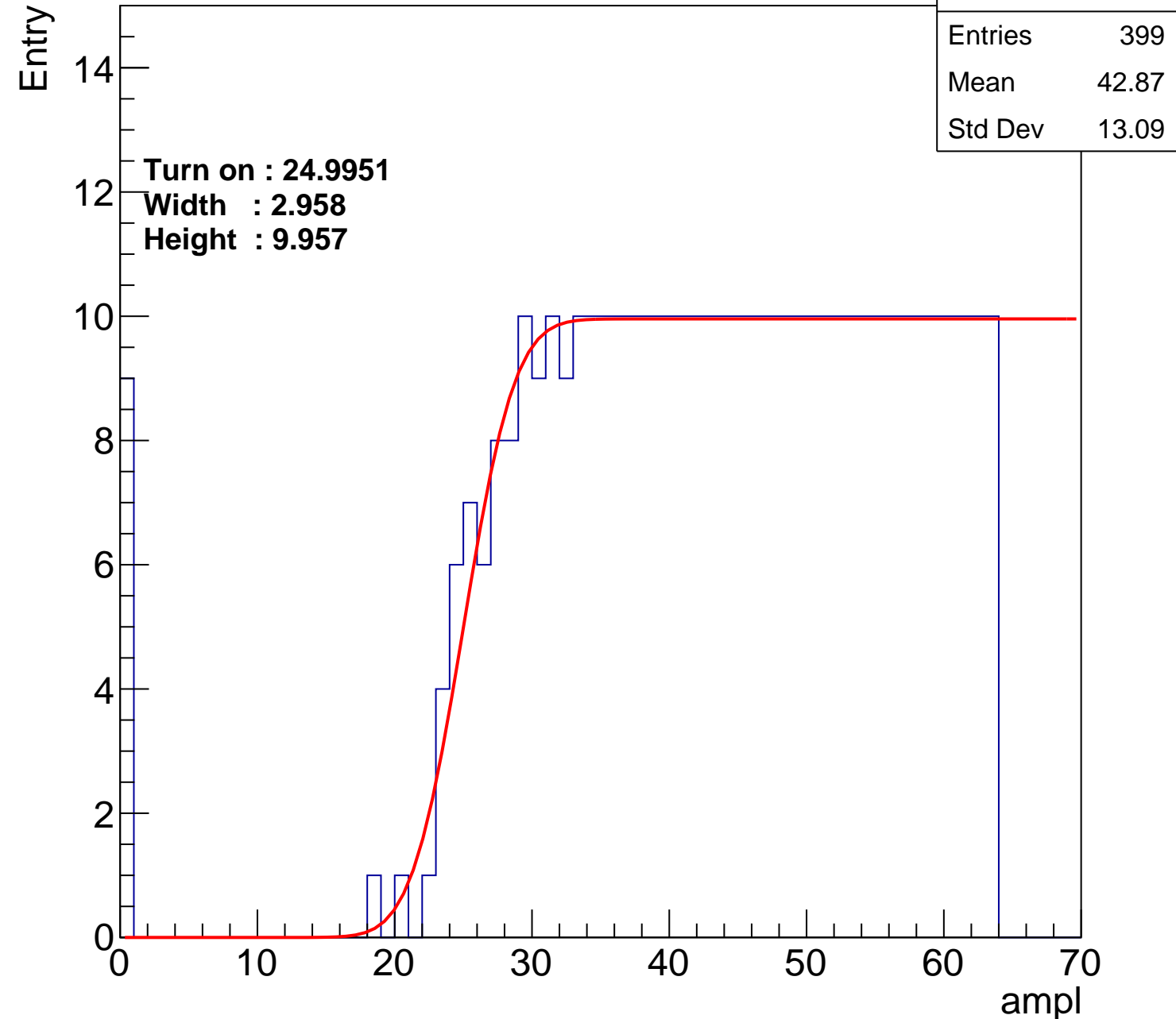
Width : 2.958

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U16-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	399
Mean	42.87
Std Dev	13.09

Turn on : 24.9951

Width : 2.958

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl

