



# B0L000S, U19-ch0

calib\_packv5\_042523\_0143.root, FC#5, port B1

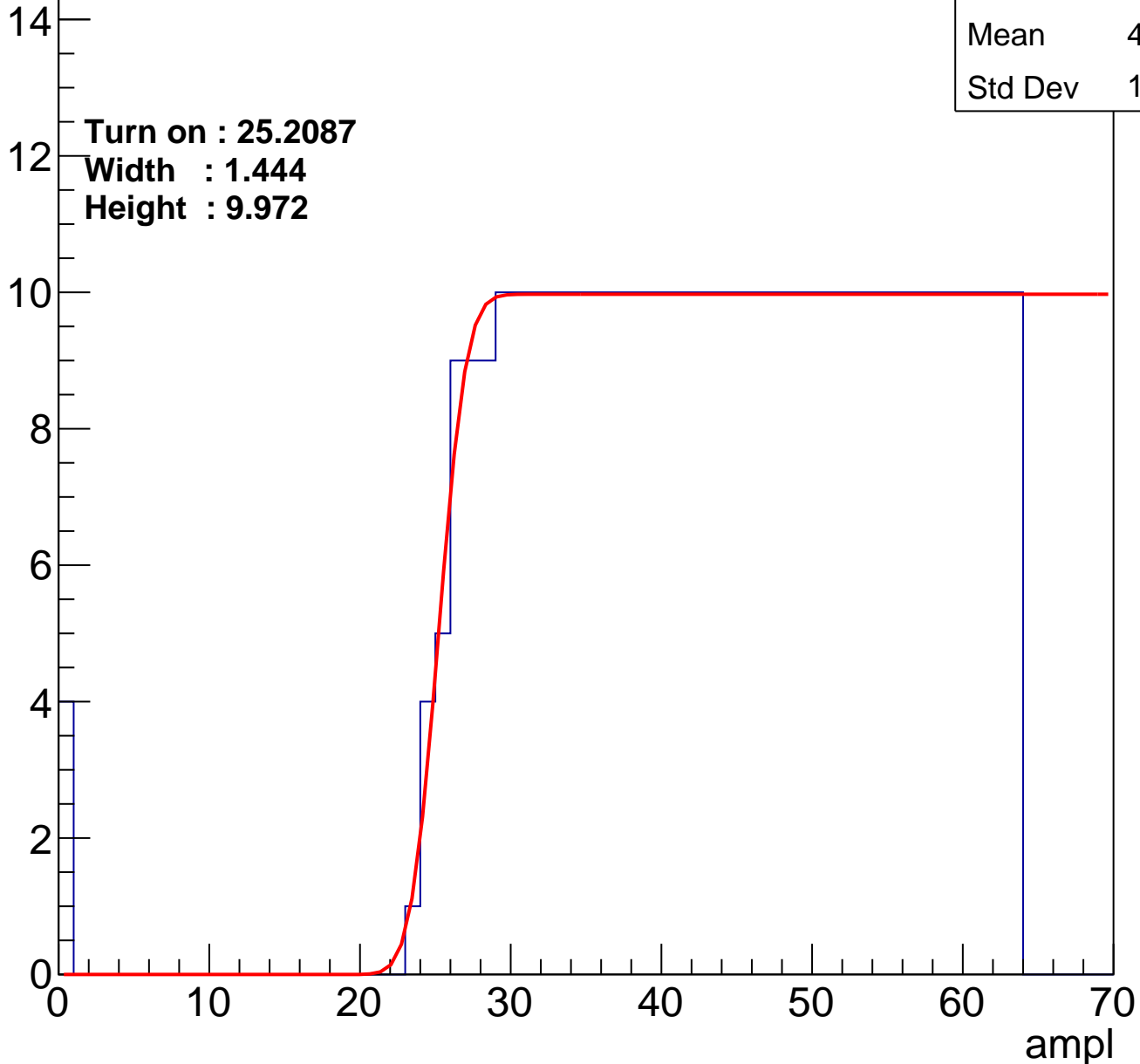
Entries	391
Mean	43.66
Std Dev	12.02

**Turn on : 25.2087**

**Width : 1.444**

**Height : 9.972**

Entry



# B0L000S, U19-ch1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.56
Std Dev	11.31

Turn on : 26.6937

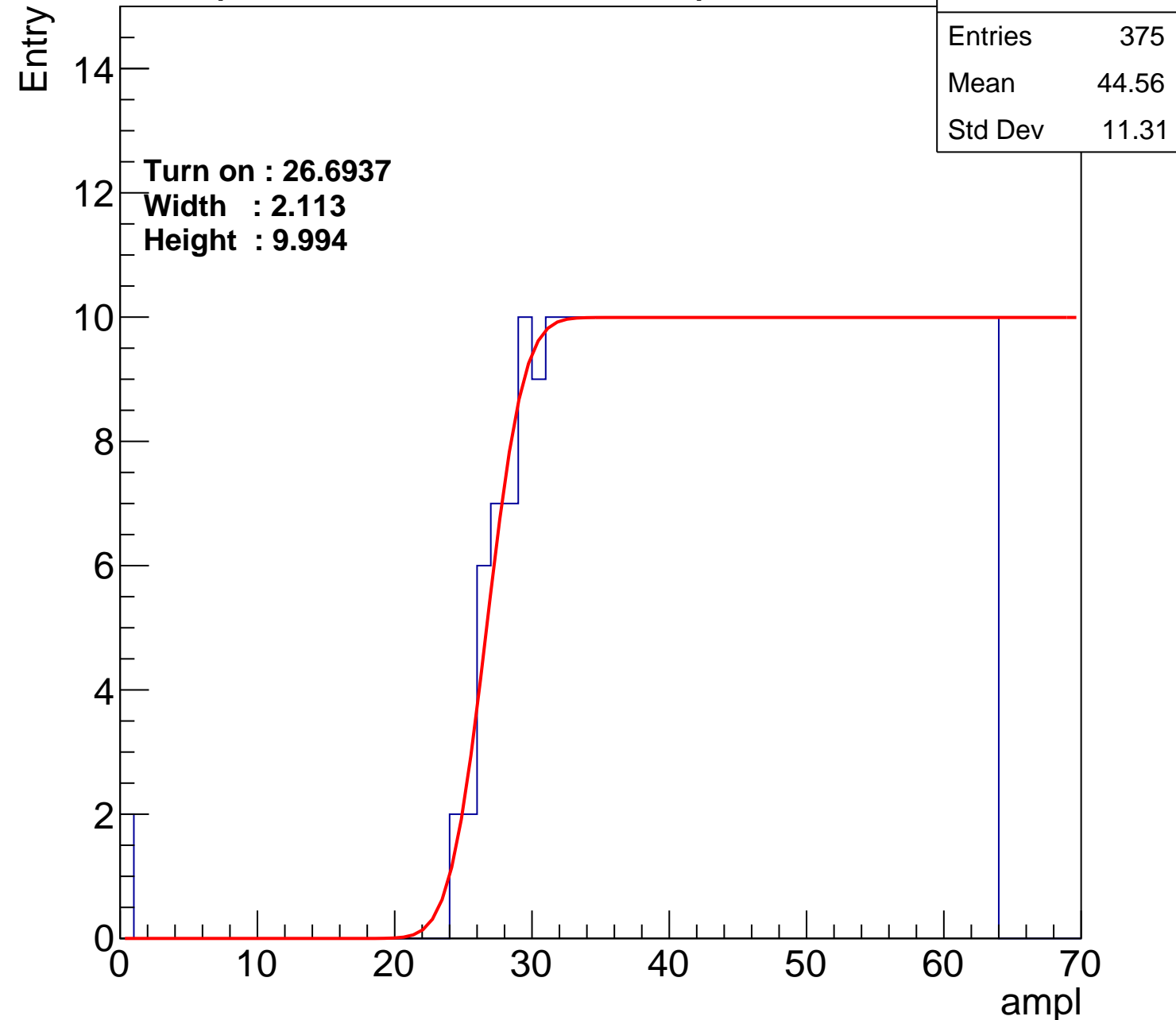
Width : 2.113

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch2

calib\_packv5\_042523\_0143.root, FC#5, port B1

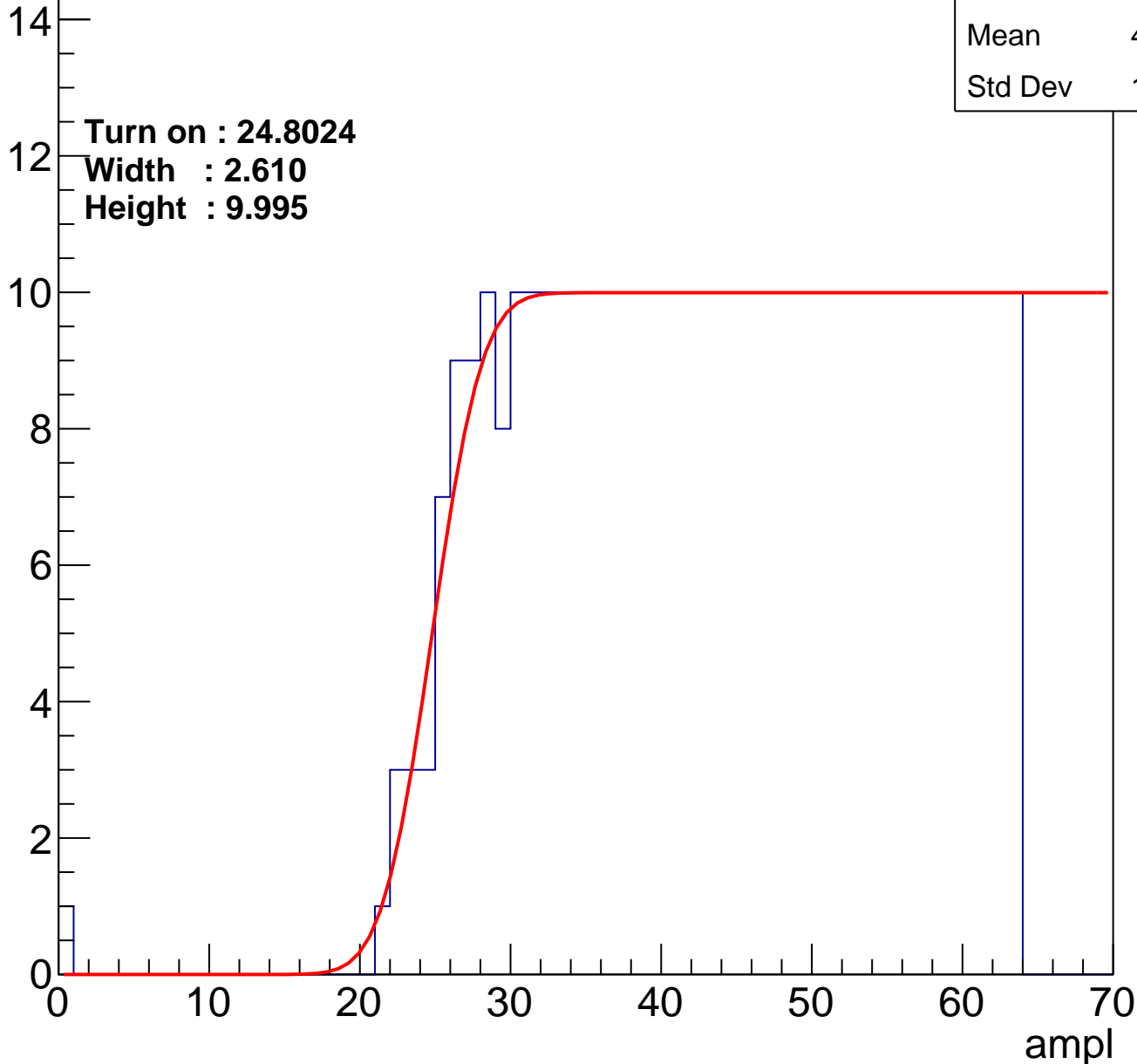
Entry

Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.8024

Width : 2.610

Height : 9.995



# B0L000S, U19-ch3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.77
Std Dev	11.37

Turn on : 27.7276

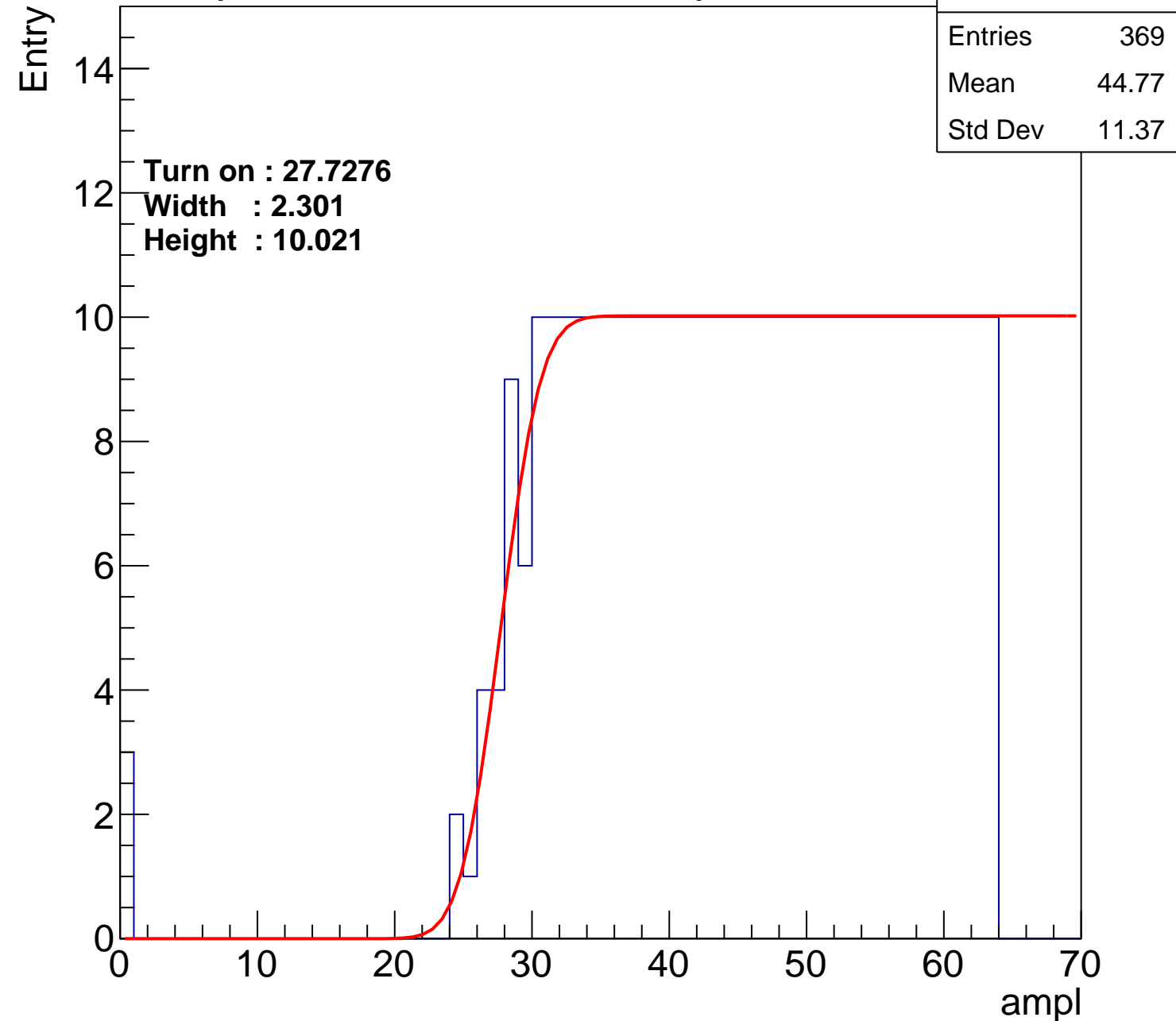
Width : 2.301

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# BOL000S, U19-ch4

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	375
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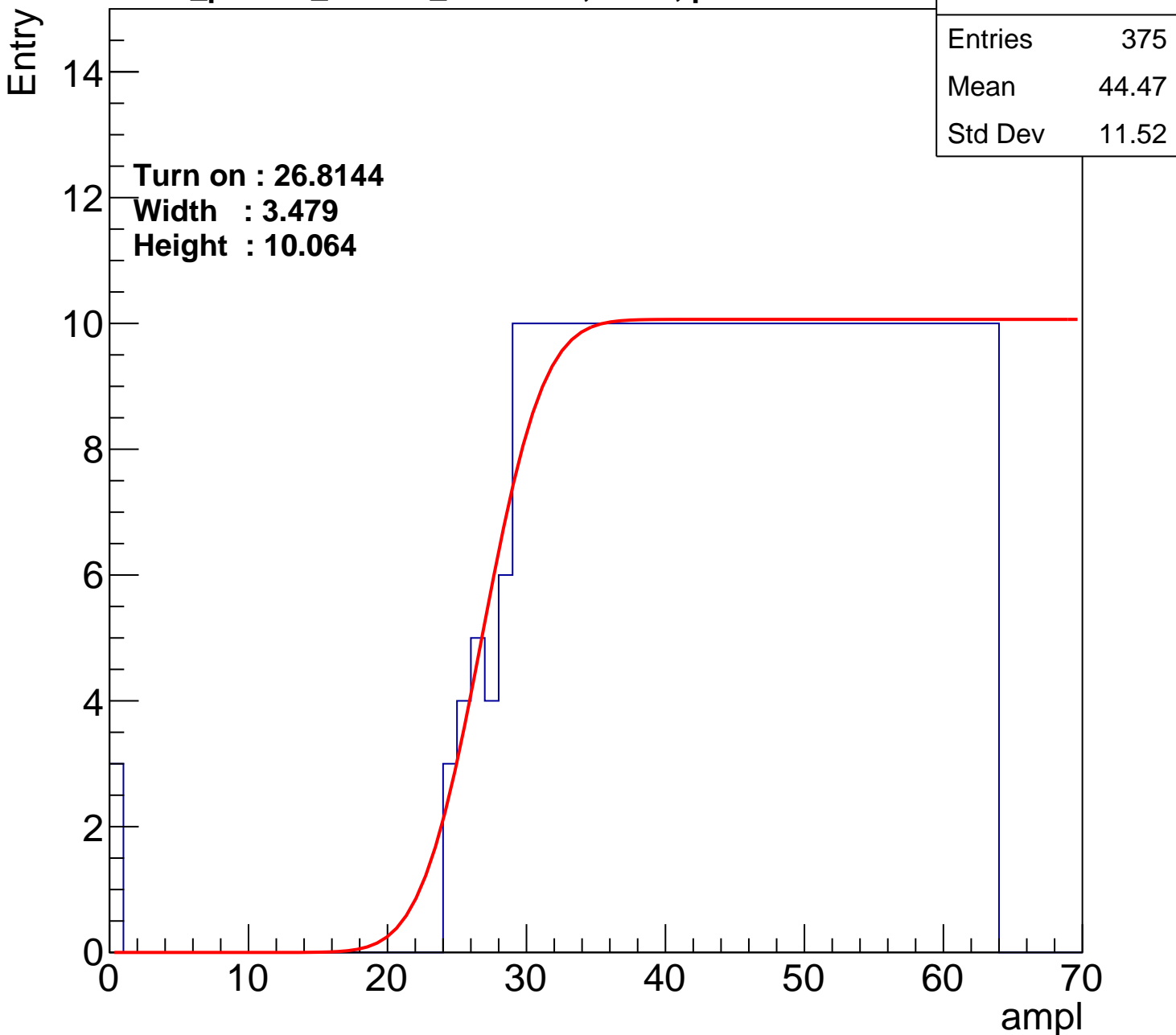
Mean	44.47
------	-------

Std Dev	11.52
---------	-------

**Turn on : 26.8144**

**Width : 3.479**

**Height : 10.064**



# B0L000S, U19-ch5

calib\_packv5\_042523\_0143.root, FC#5, port B1

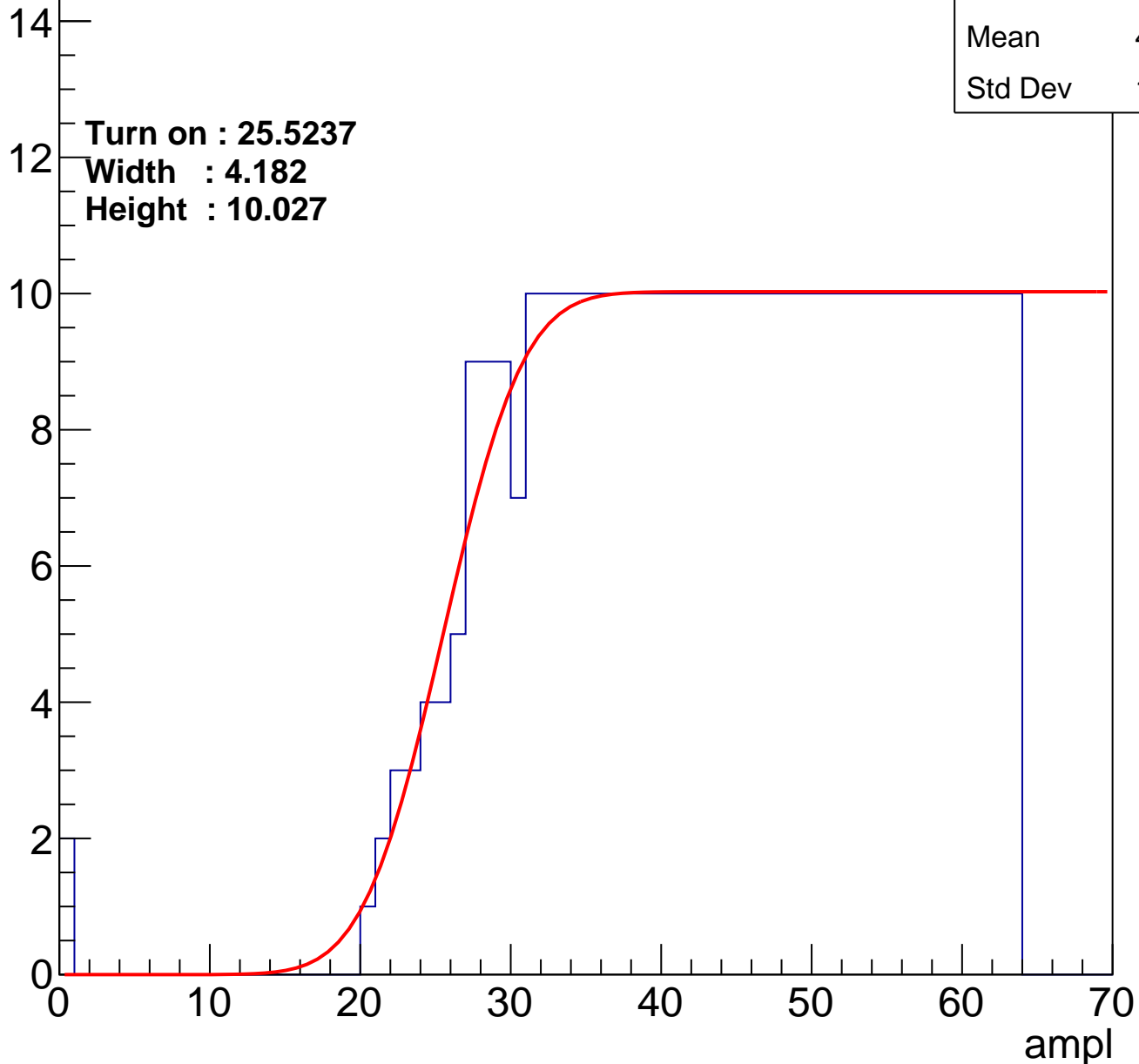
Entries	388
Mean	43.81
Std Dev	11.81

**Turn on : 25.5237**

**Width : 4.182**

**Height : 10.027**

Entry



# B0L000S, U19-ch6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	44.04
Std Dev	11.43

Turn on : 25.7034

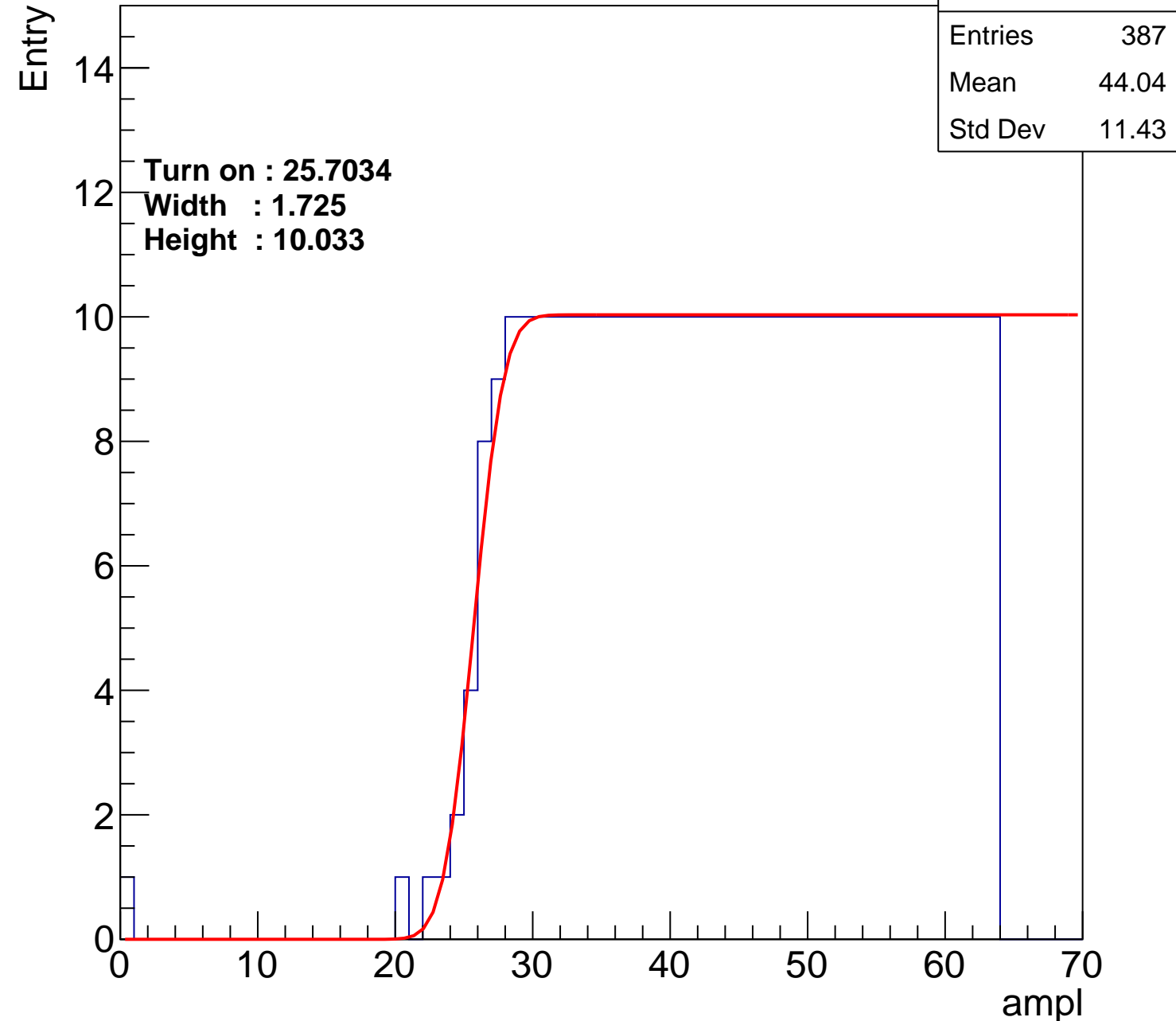
Width : 1.725

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 27.1689

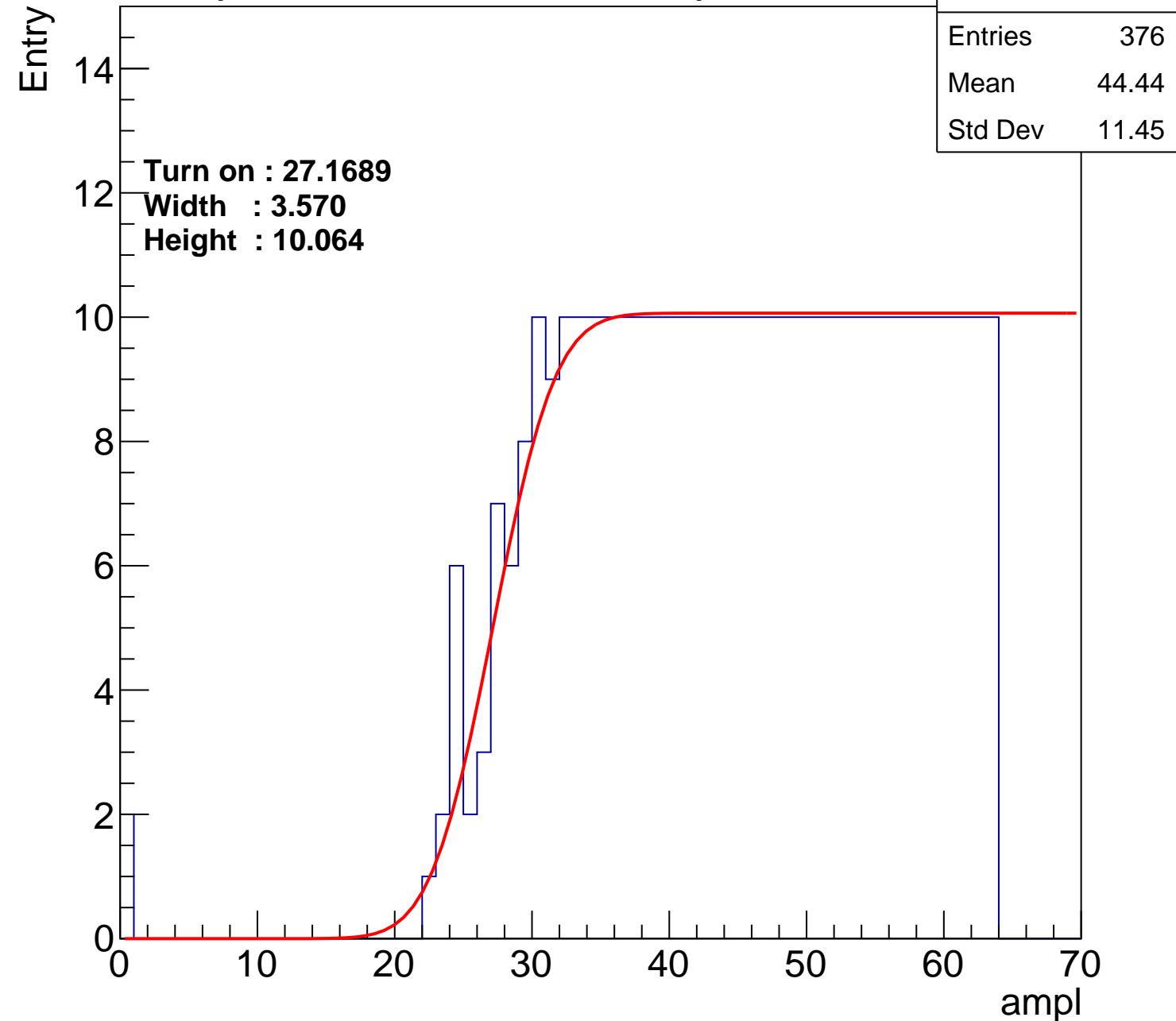
Width : 3.570

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch8

calib\_packv5\_042523\_0143.root, FC#5, port B1

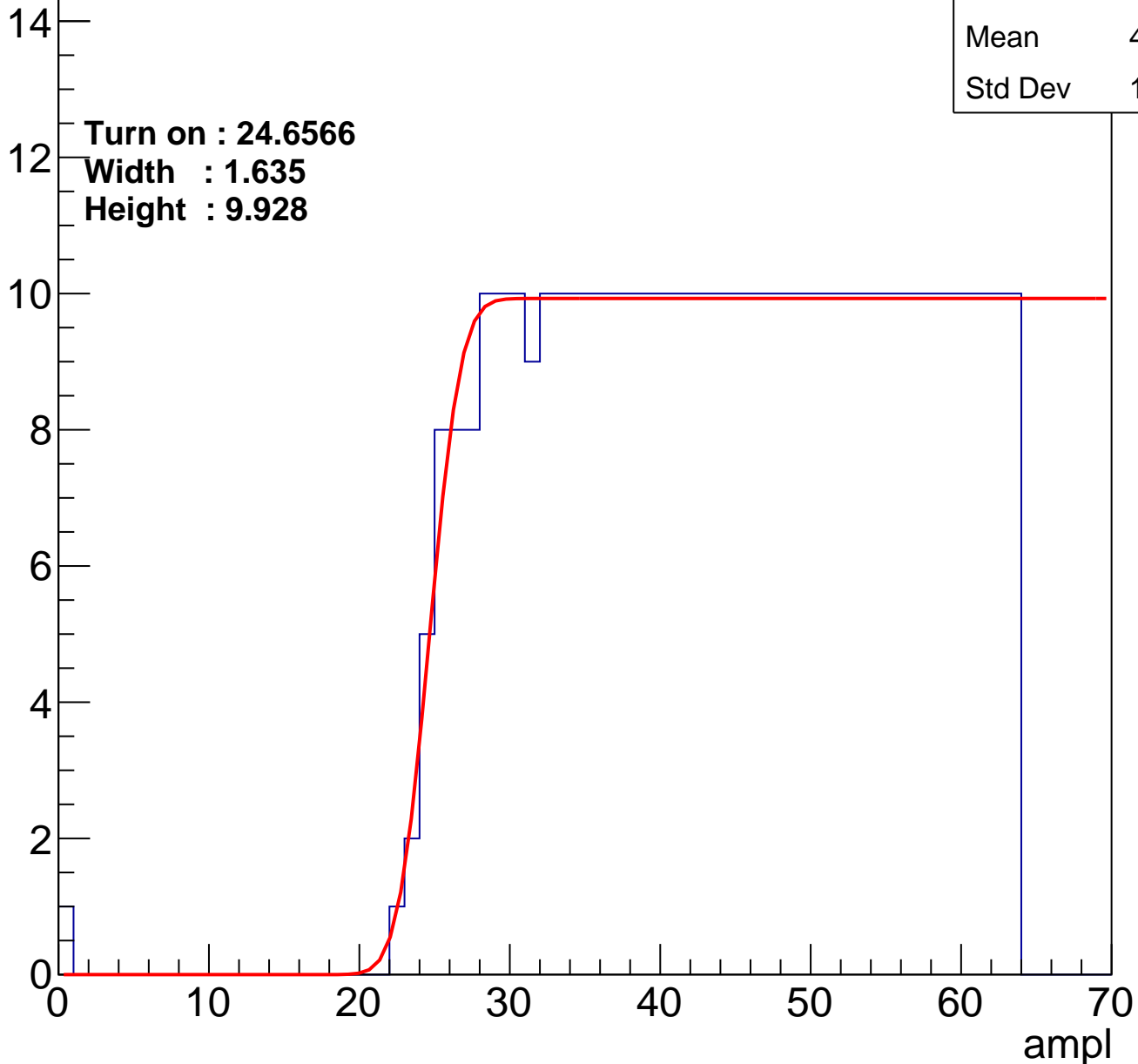
Entries	392
Mean	43.78
Std Dev	11.58

Turn on : 24.6566

Width : 1.635

Height : 9.928

Entry



# B0L000S, U19-ch9

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 25.9412

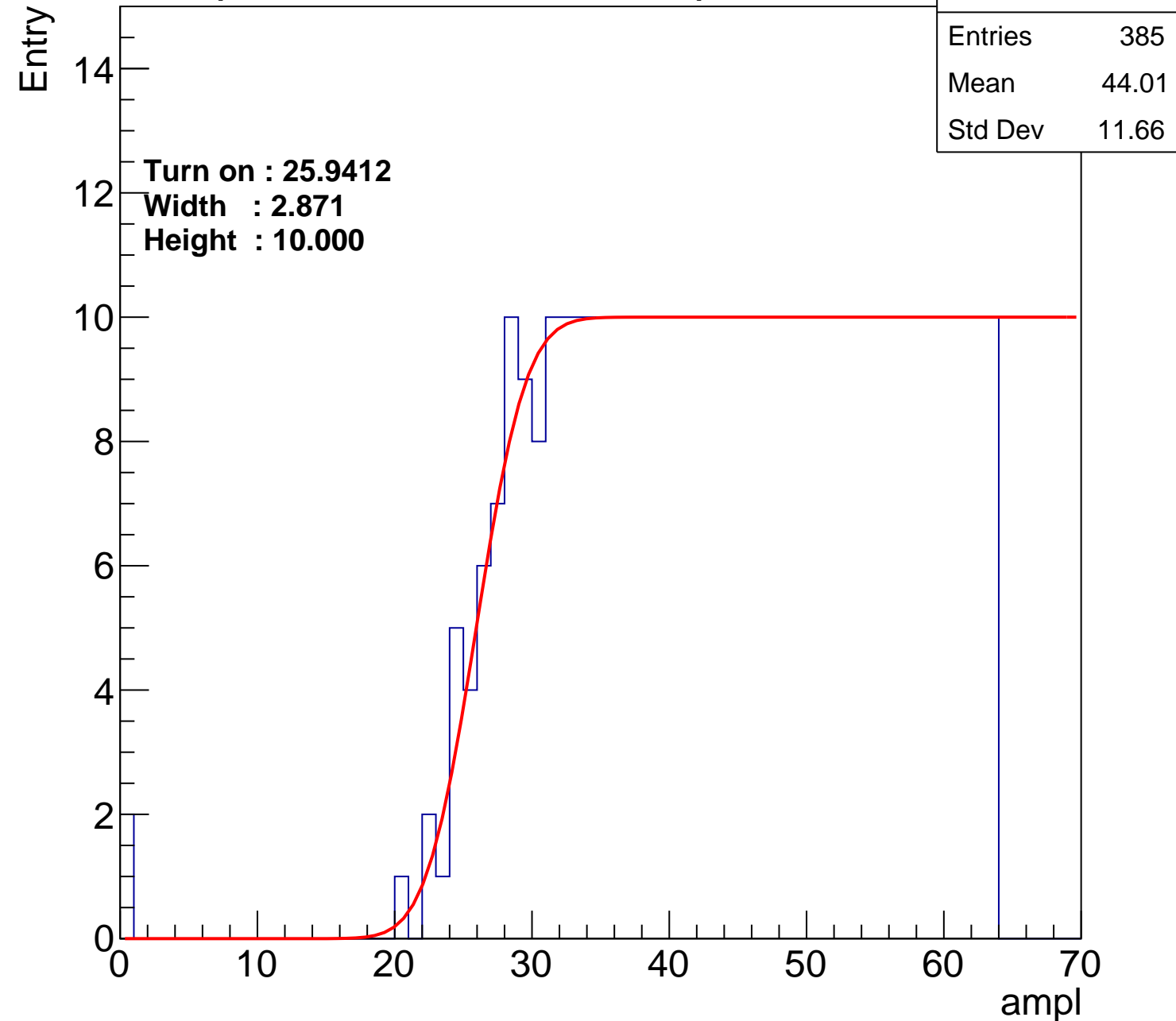
Width : 2.871

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch10

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.44
Std Dev	11.4

**Turn on : 26.6385**

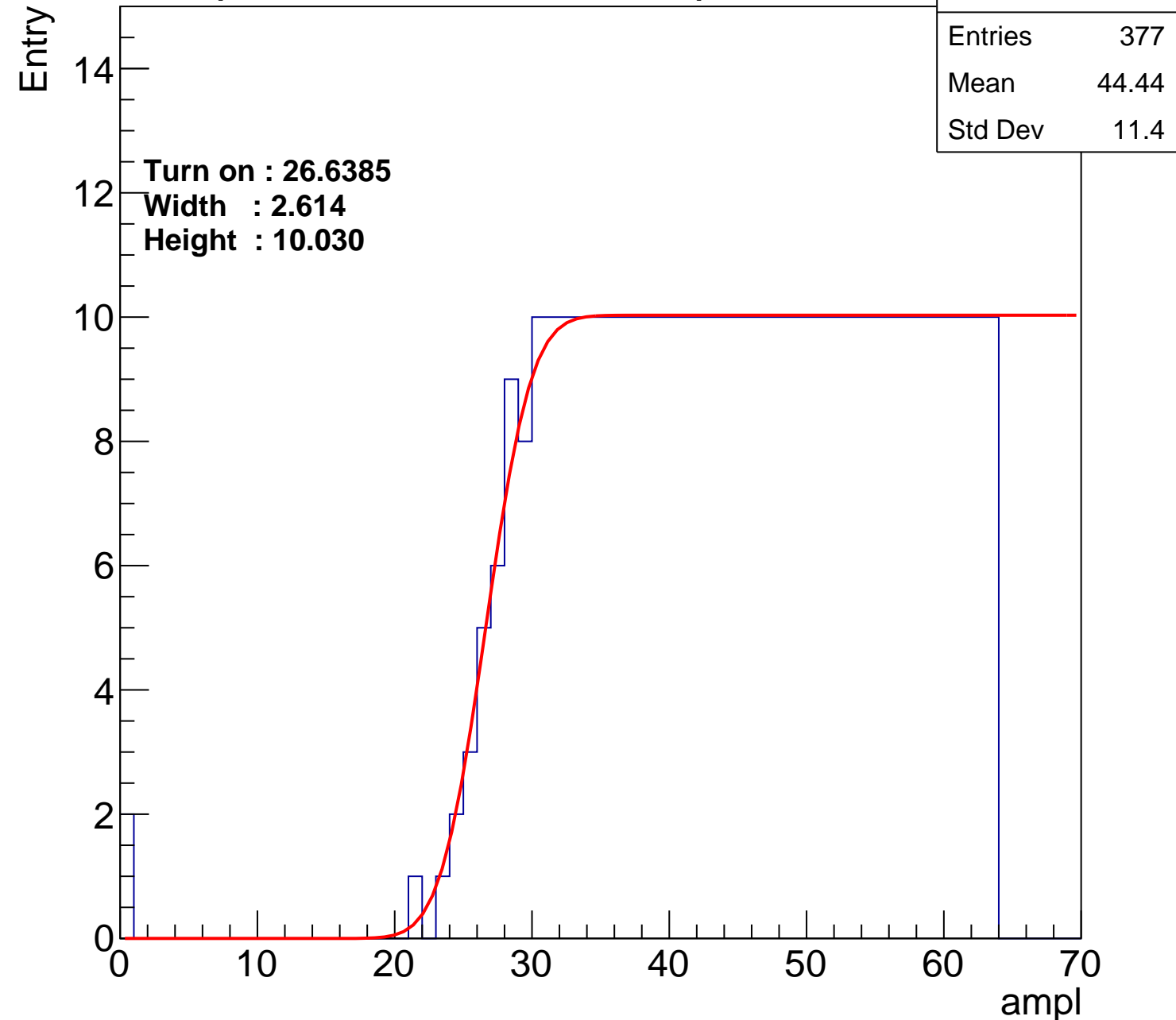
**Width : 2.614**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch11

calib\_packv5\_042523\_0143.root, FC#5, port B1

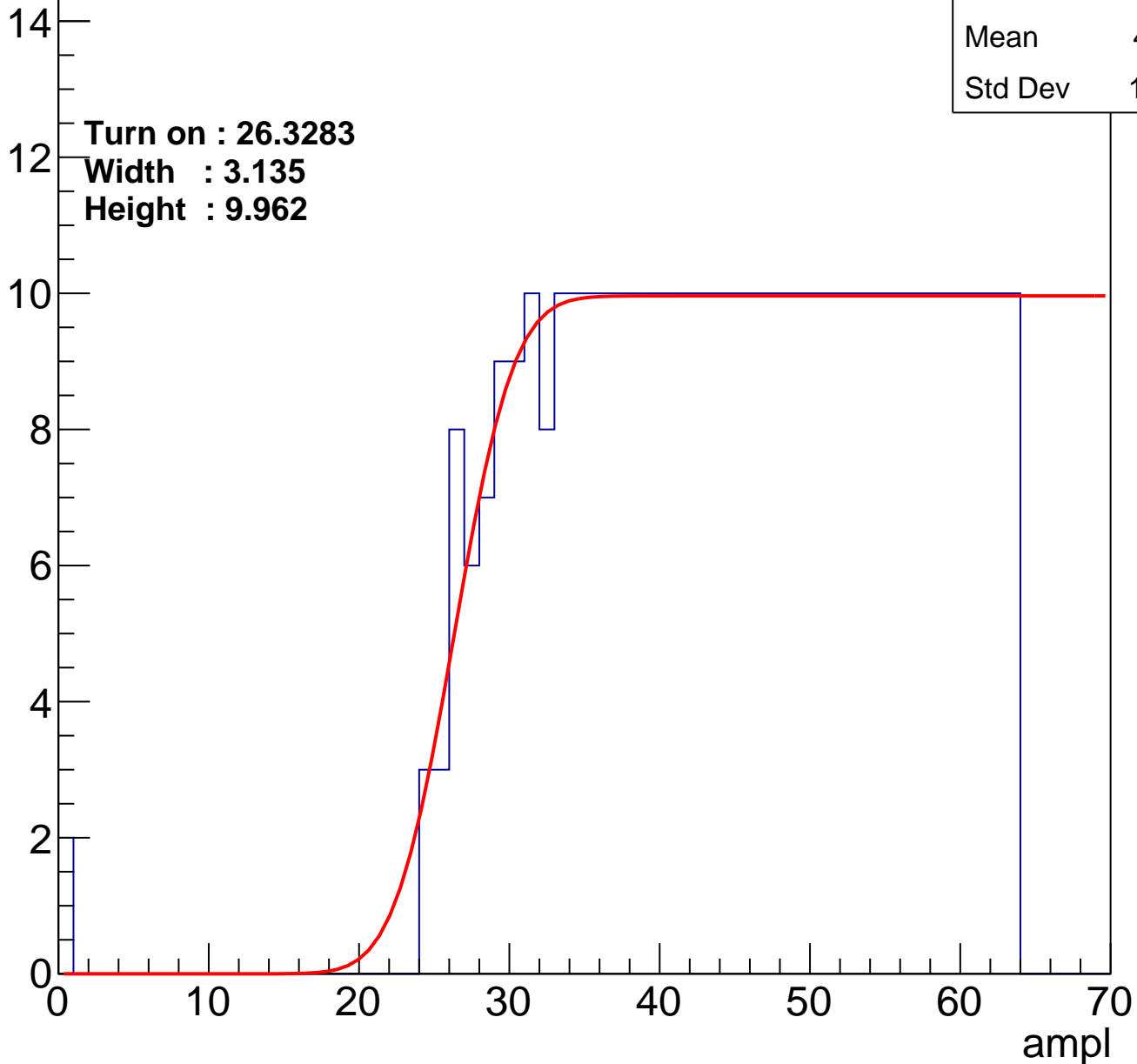
Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.3283

Width : 3.135

Height : 9.962

Entry



# B0L000S, U19-ch12

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 26.8417

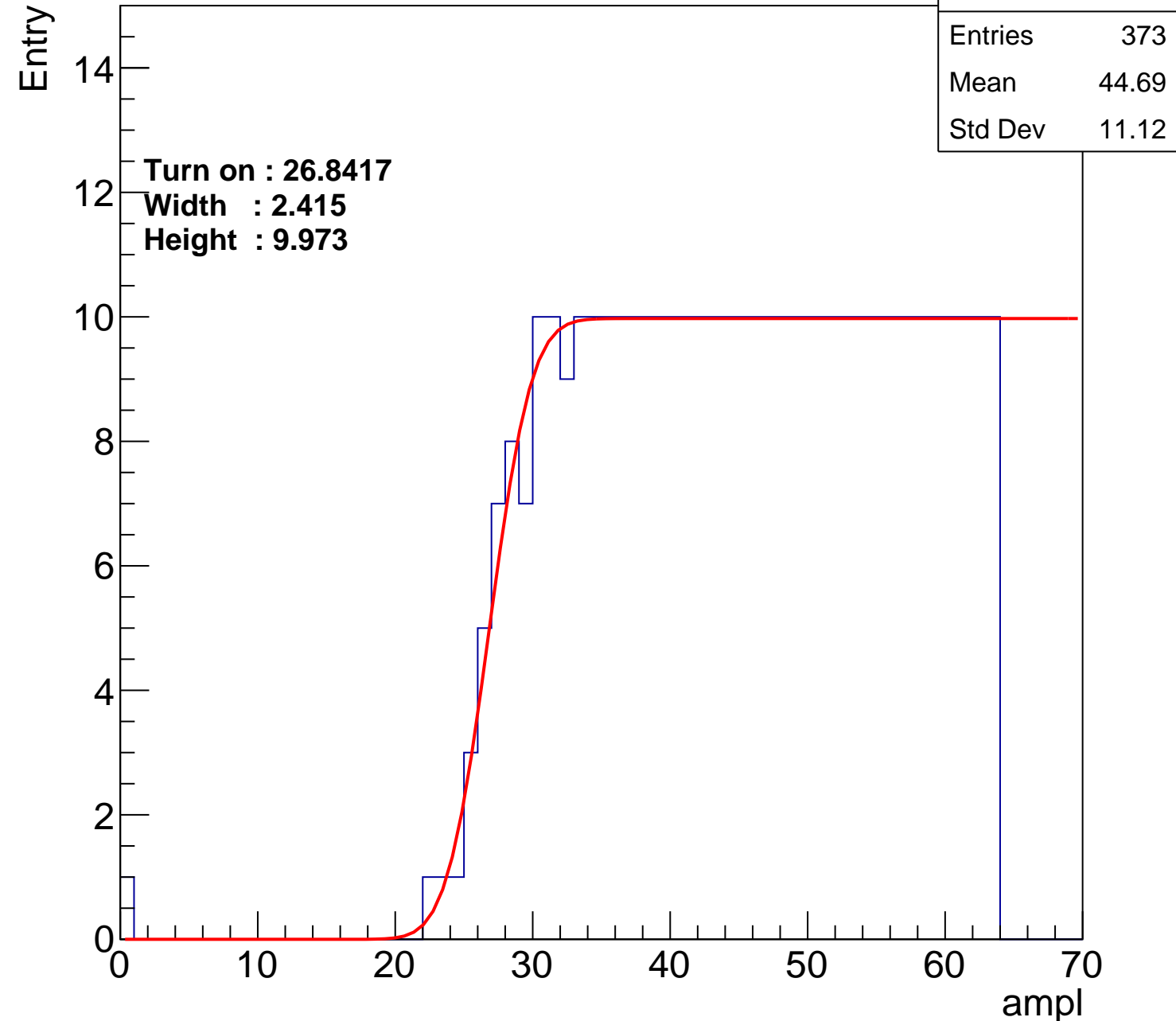
Width : 2.415

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch13

calib\_packv5\_042523\_0143.root, FC#5, port B1

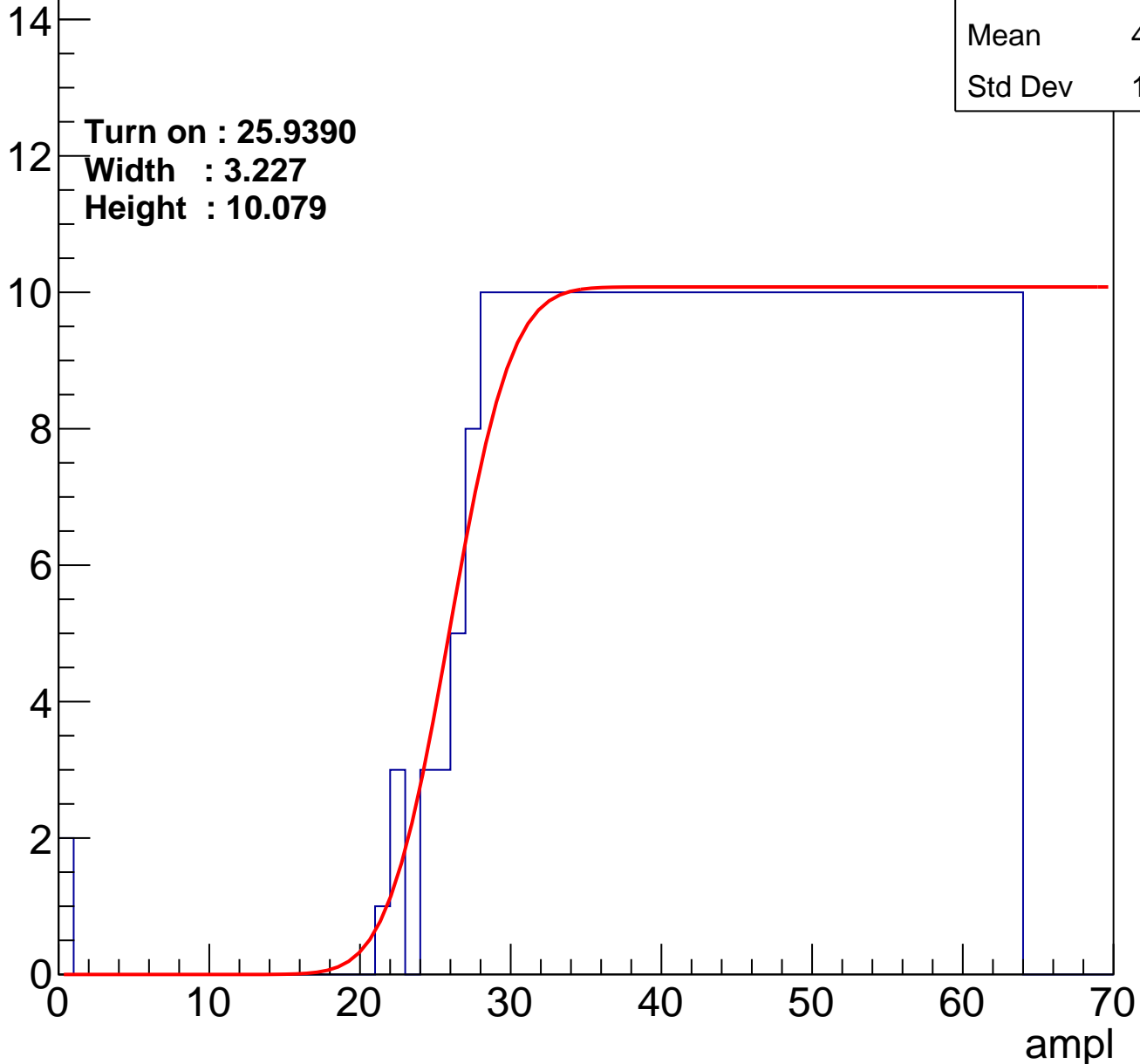
Entries	385
Mean	44.05
Std Dev	11.59

**Turn on : 25.9390**

**Width : 3.227**

**Height : 10.079**

Entry



# B0L000S, U19-ch14

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.1218

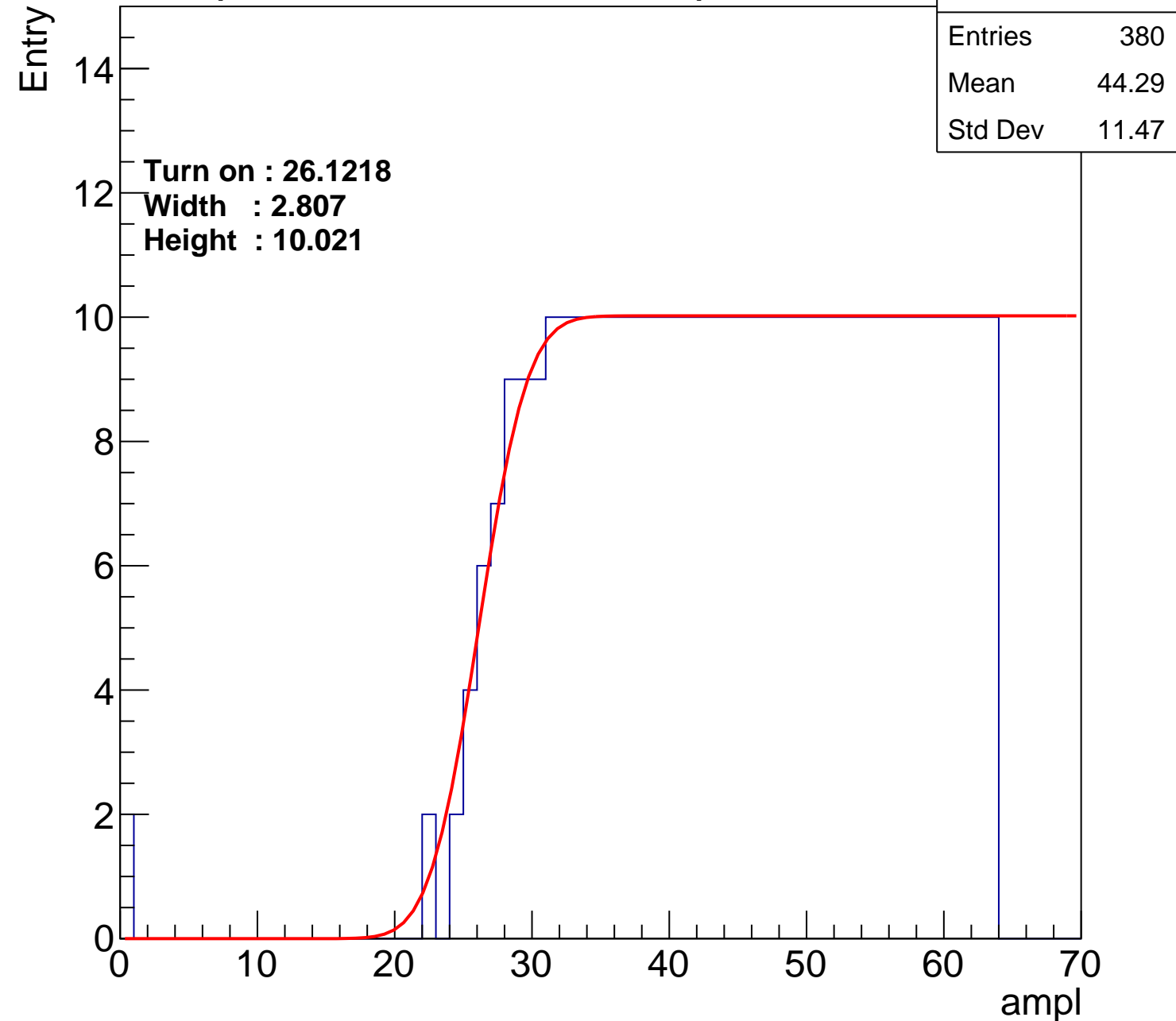
Width : 2.807

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch15

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.25
Std Dev	11.58

Turn on : 26.2998

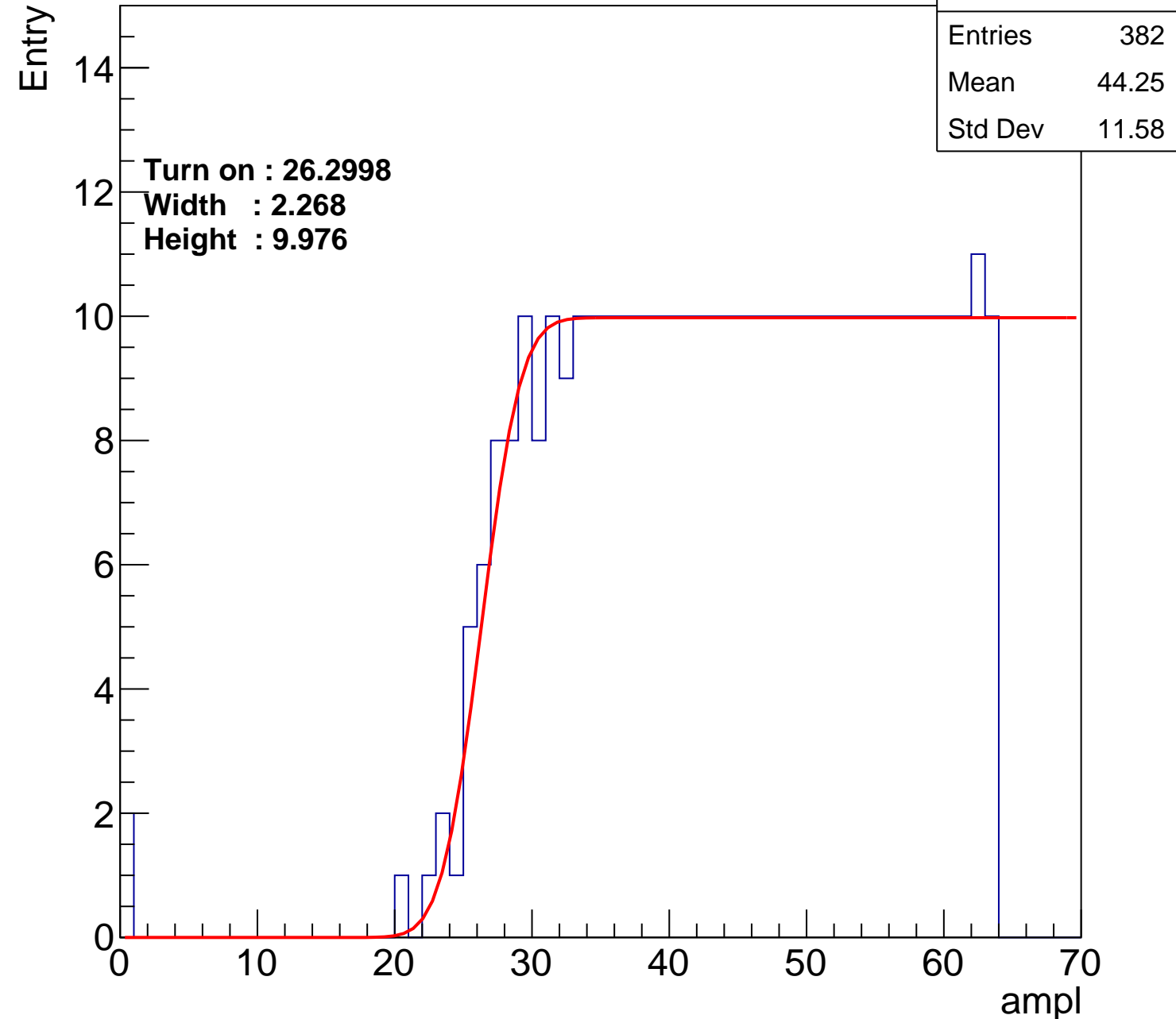
Width : 2.268

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch16

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.98
Std Dev	10.91

**Turn on : 27.6486**

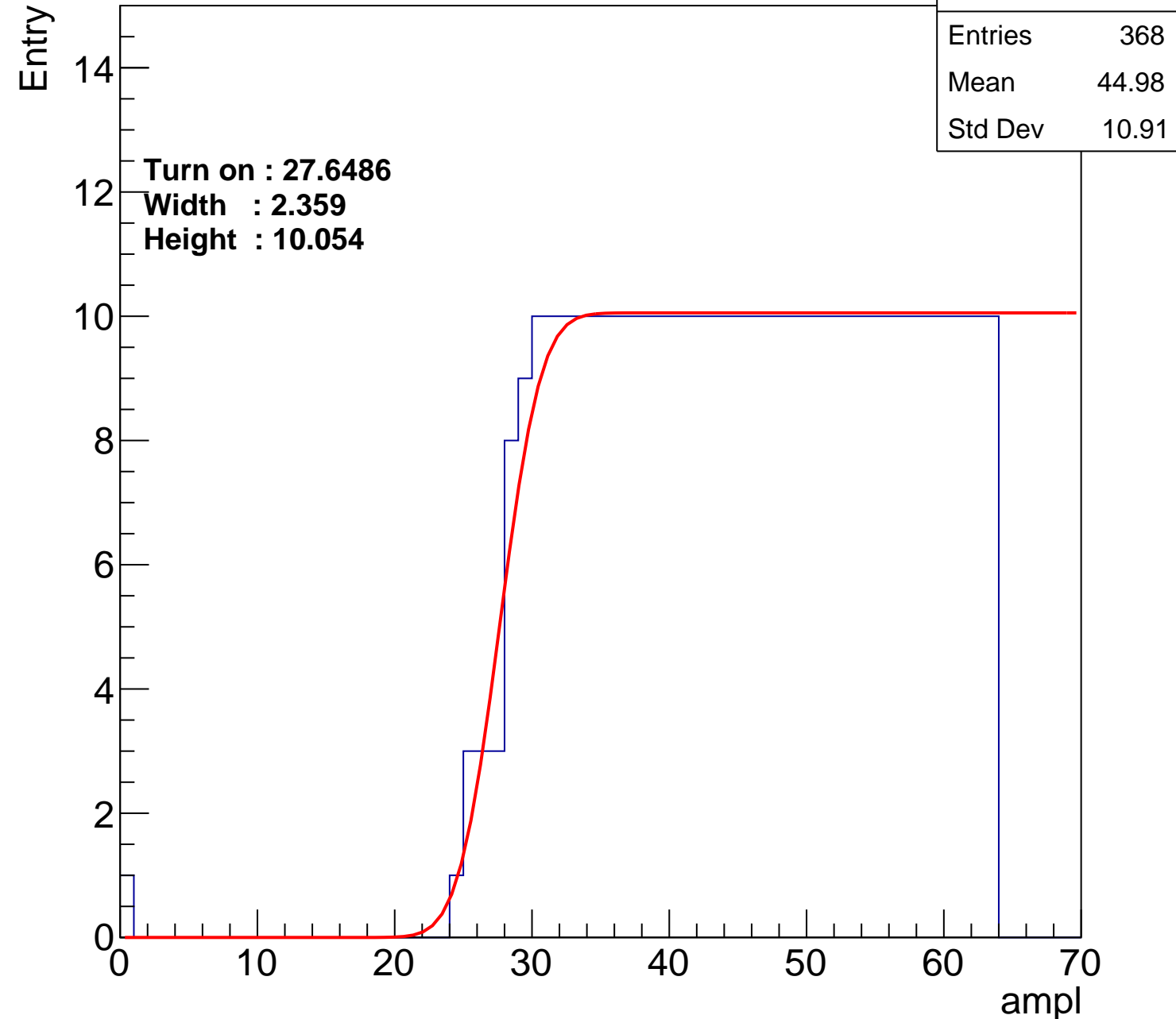
**Width : 2.359**

**Height : 10.054**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch17

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.46
Std Dev	11.42

**Turn on : 26.7787**

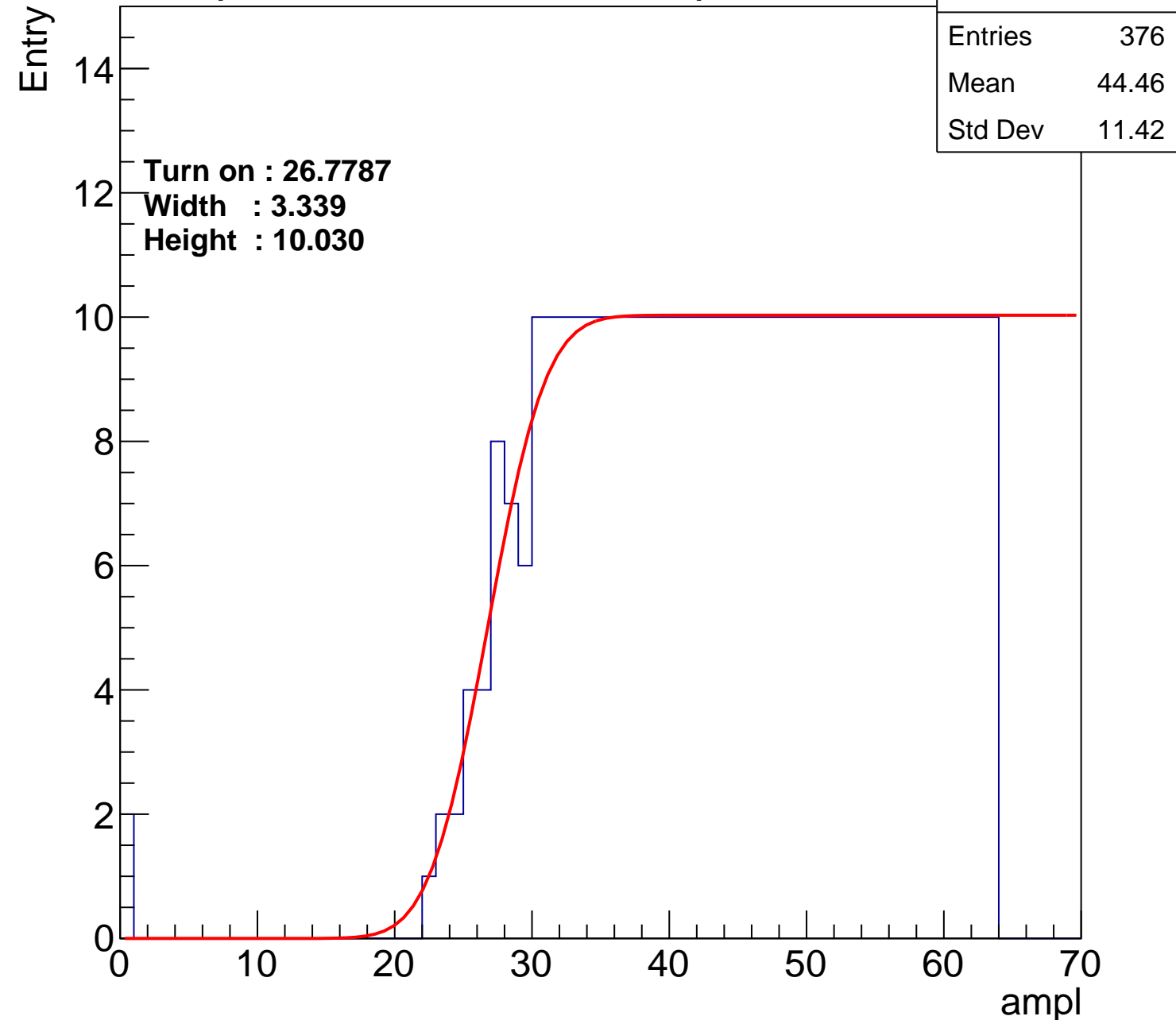
**Width : 3.339**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch18

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.98
Std Dev	11.16

**Turn on : 27.8445**

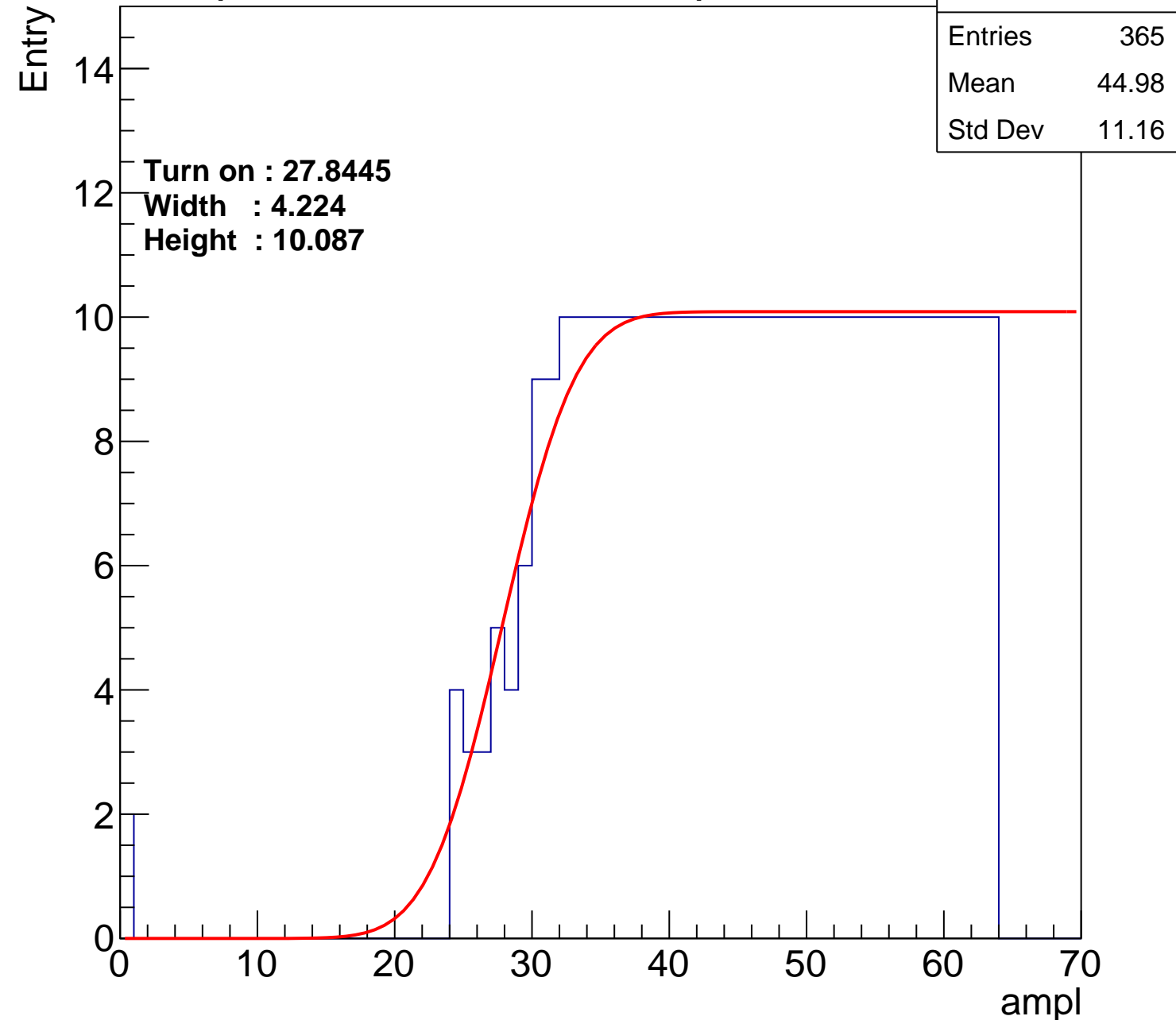
**Width : 4.224**

**Height : 10.087**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch19

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.51
Std Dev	11.19

**Turn on : 26.8202**

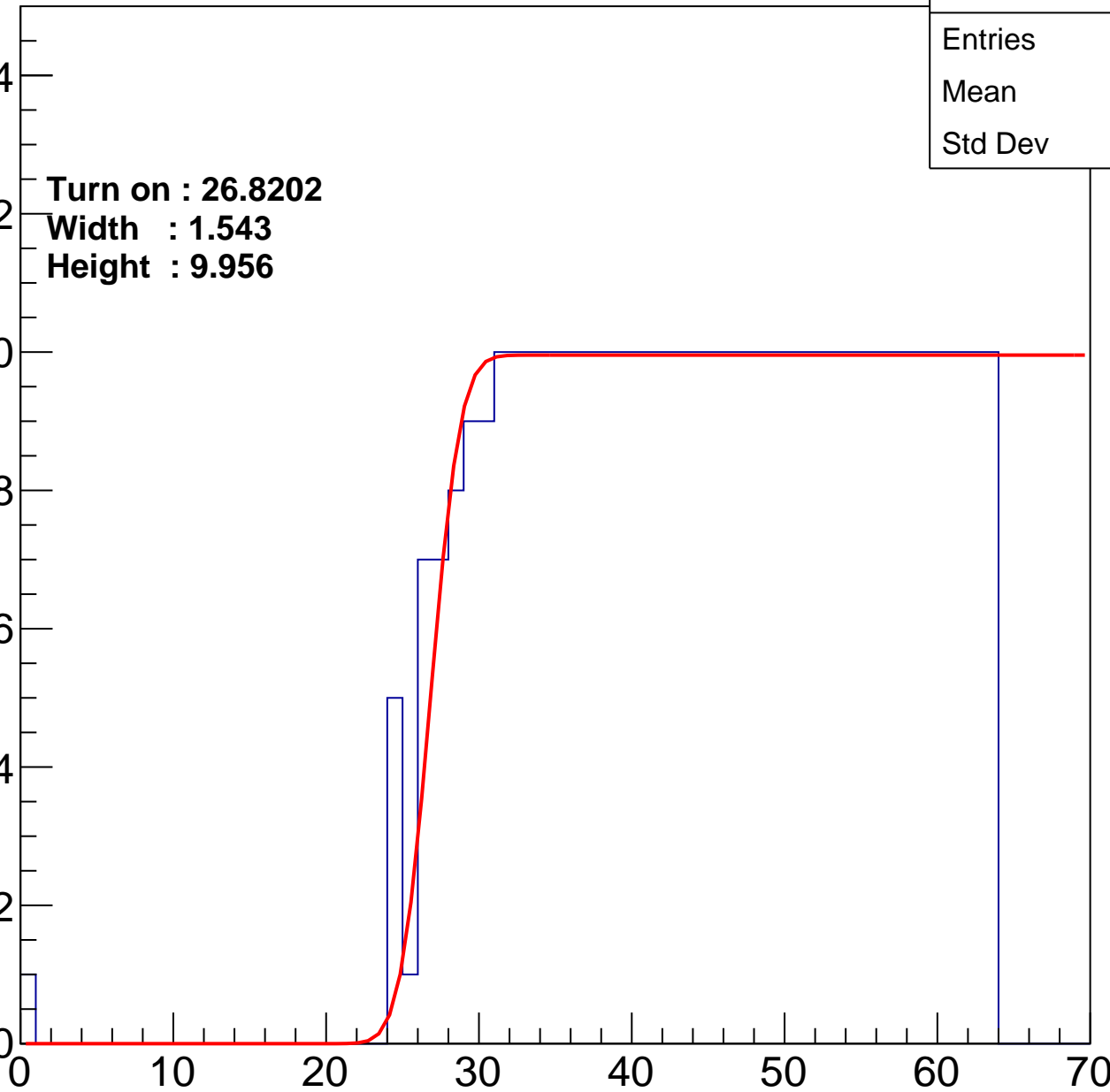
**Width : 1.543**

**Height : 9.956**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch20

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	401
Mean	43.28
Std Dev	11.92

Turn on : 24.3825

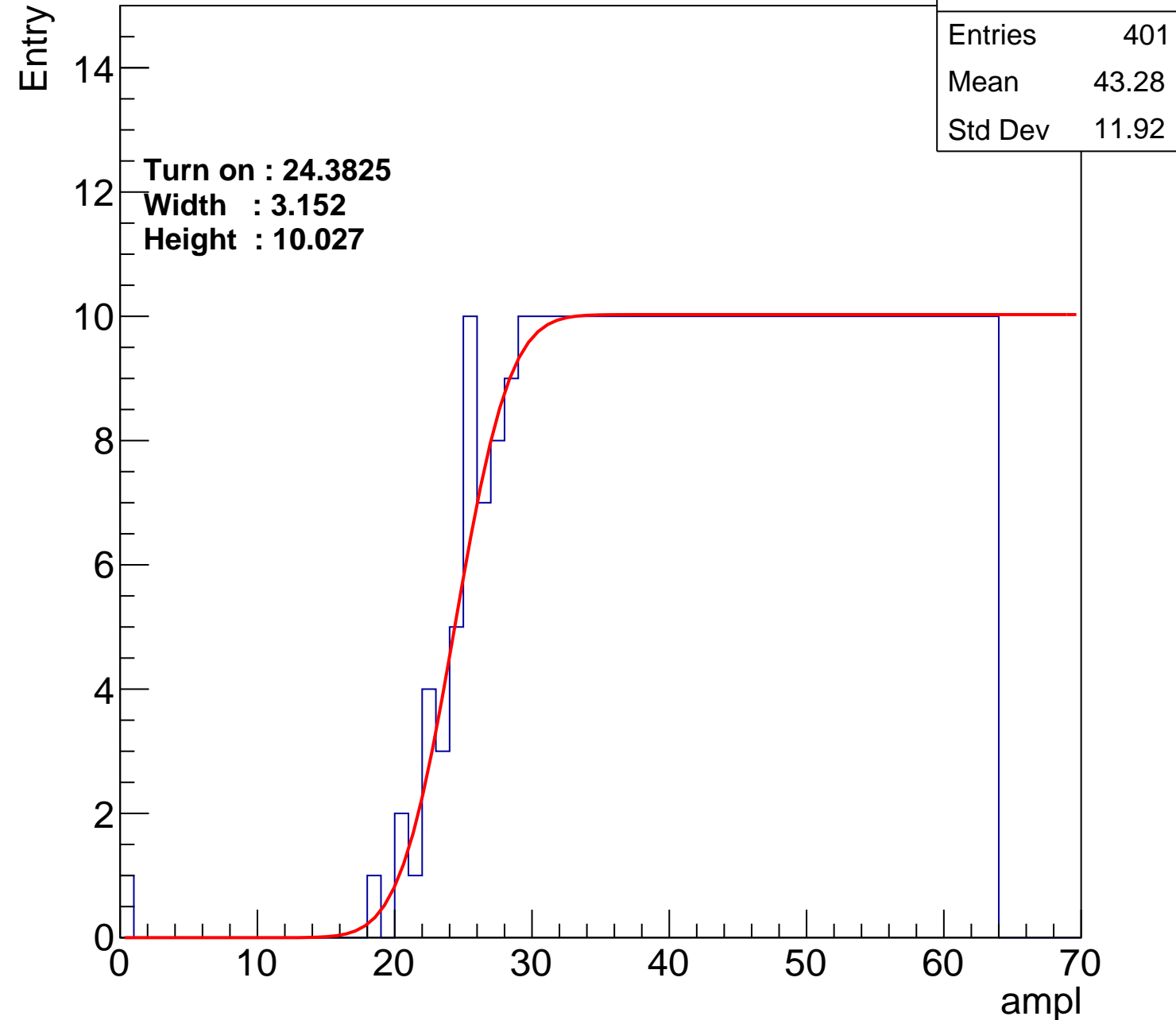
Width : 3.152

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch21

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.87
Std Dev	11.09

Turn on : 27.8269

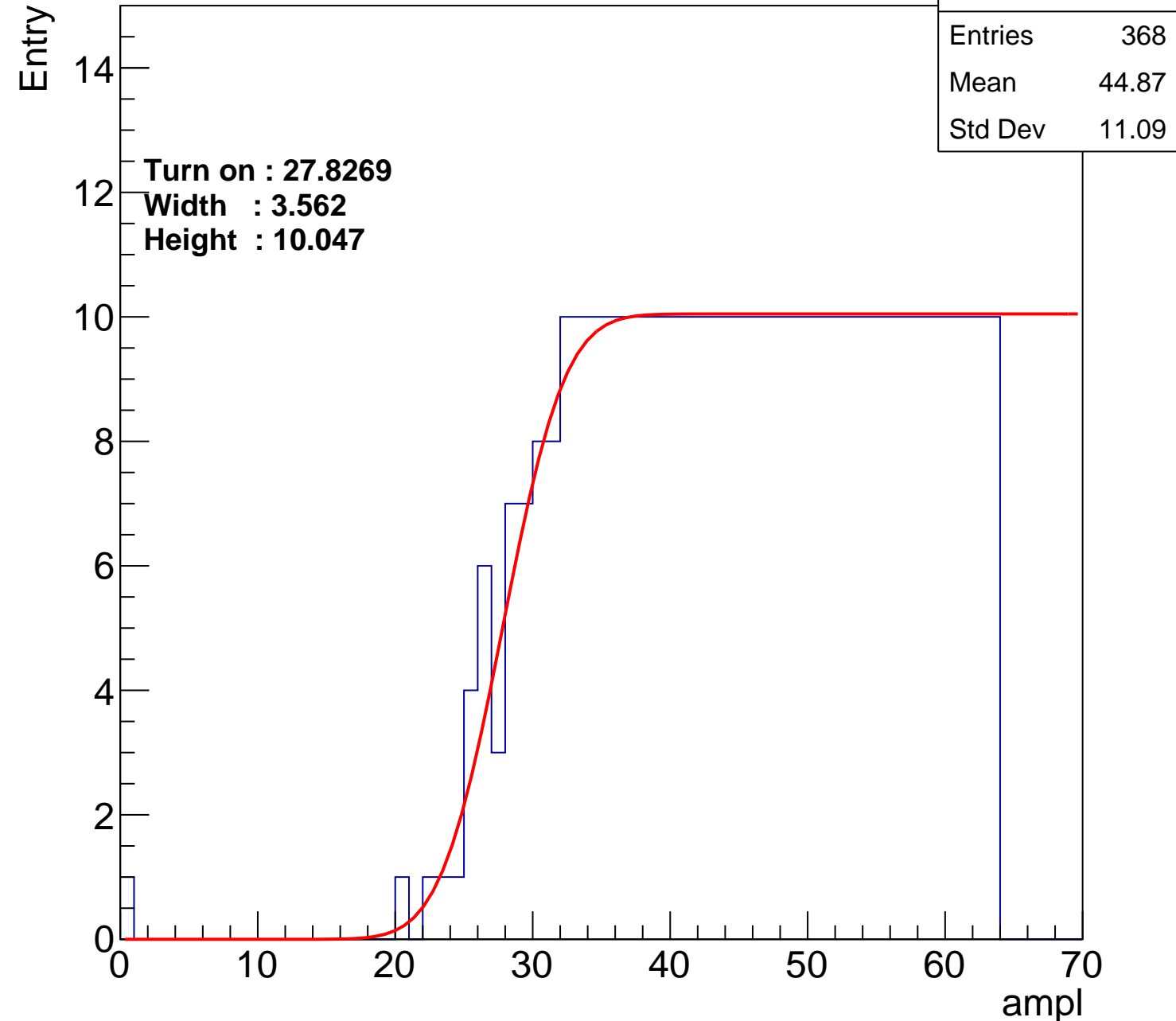
Width : 3.562

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch22

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.3418

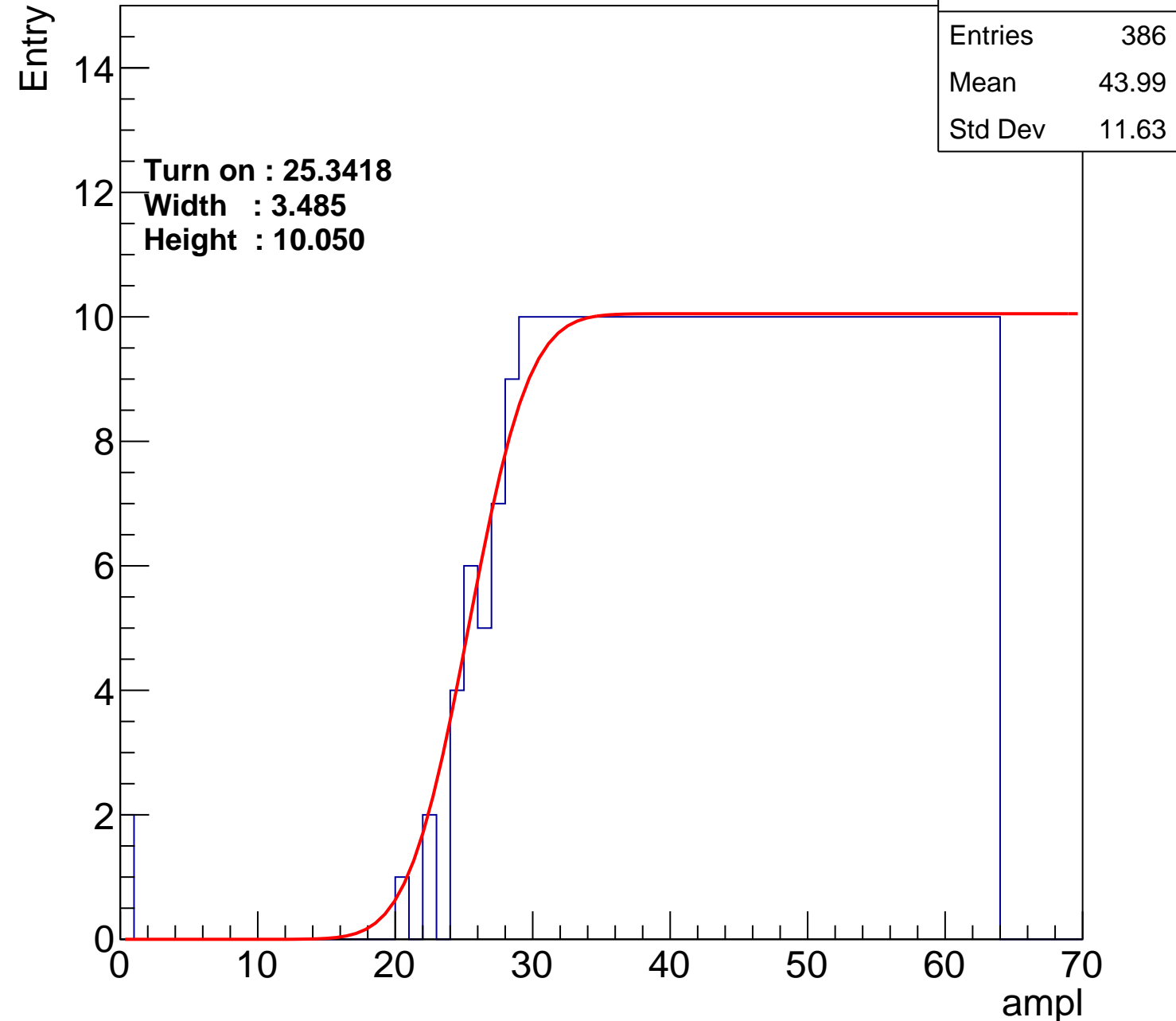
Width : 3.485

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch23

calib\_packv5\_042523\_0143.root, FC#5, port B1

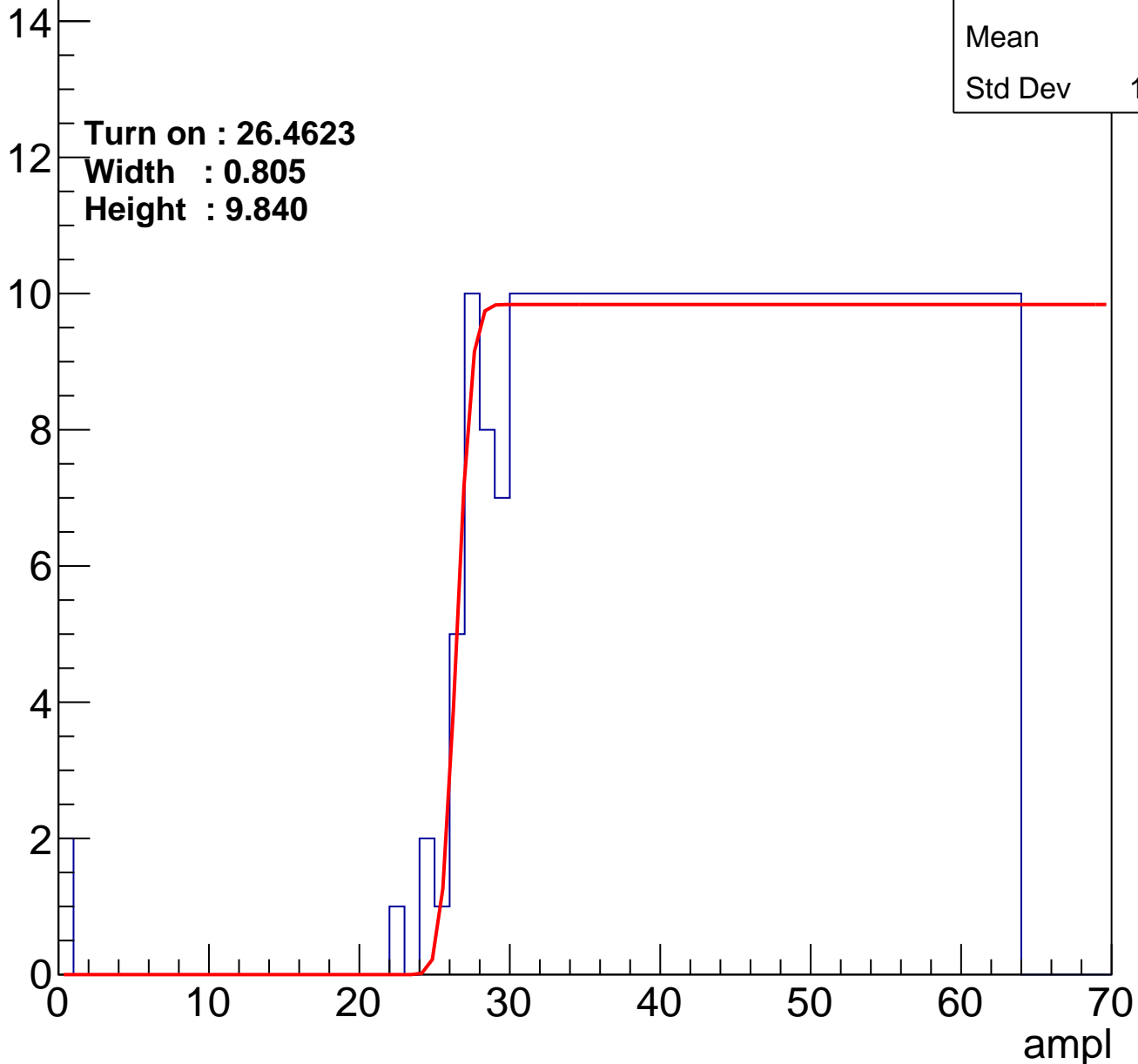
Entries	376
Mean	44.5
Std Dev	11.35

Turn on : 26.4623

Width : 0.805

Height : 9.840

Entry



# B0L000S, U19-ch24

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.85
Std Dev	11.35

Turn on : 28.2172

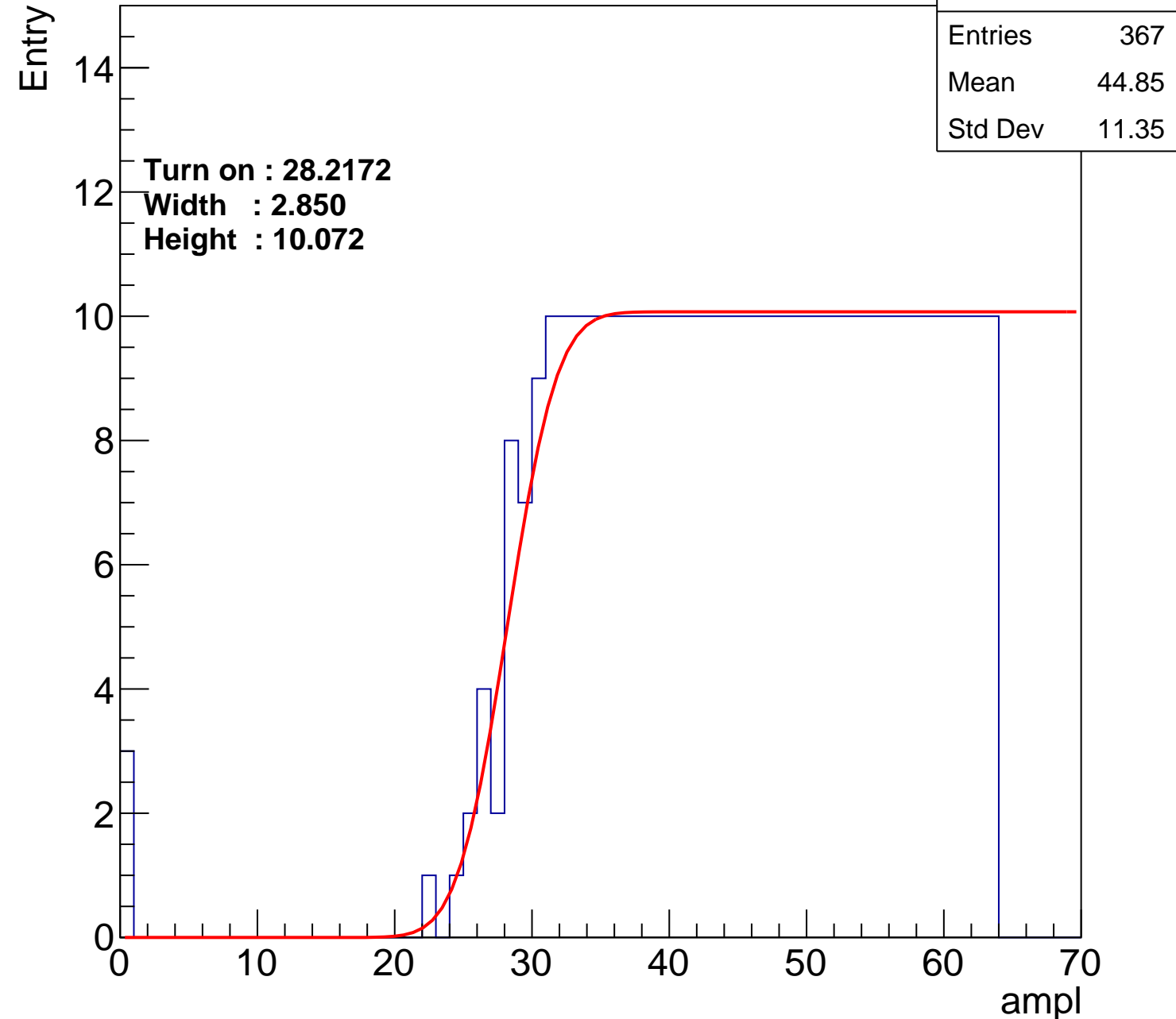
Width : 2.850

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch25

calib\_packv5\_042523\_0143.root, FC#5, port B1

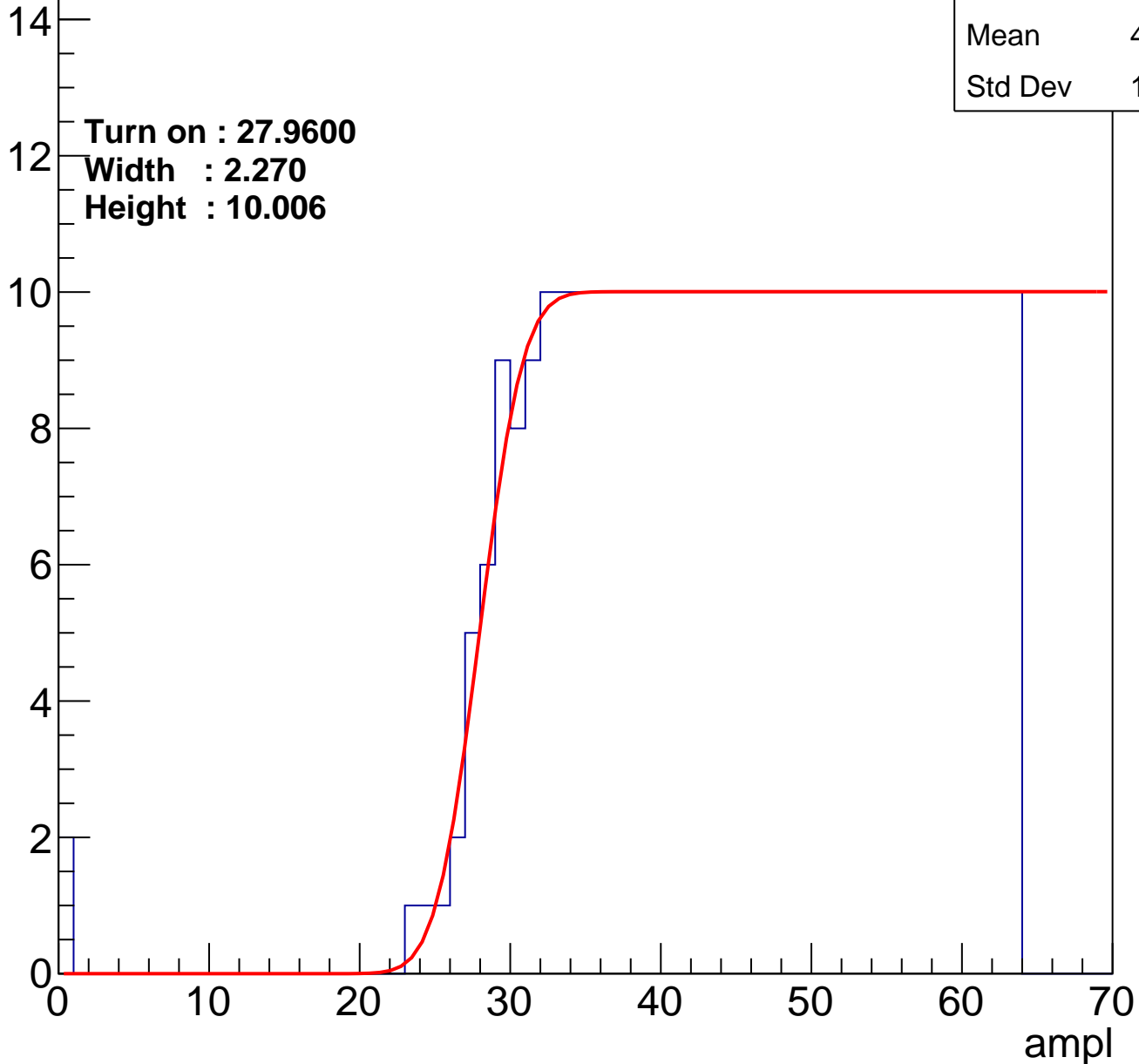
Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 27.9600

Width : 2.270

Height : 10.006

Entry



# B0L000S, U19-ch26

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	44.01
Std Dev	11.53

Turn on : 25.7603

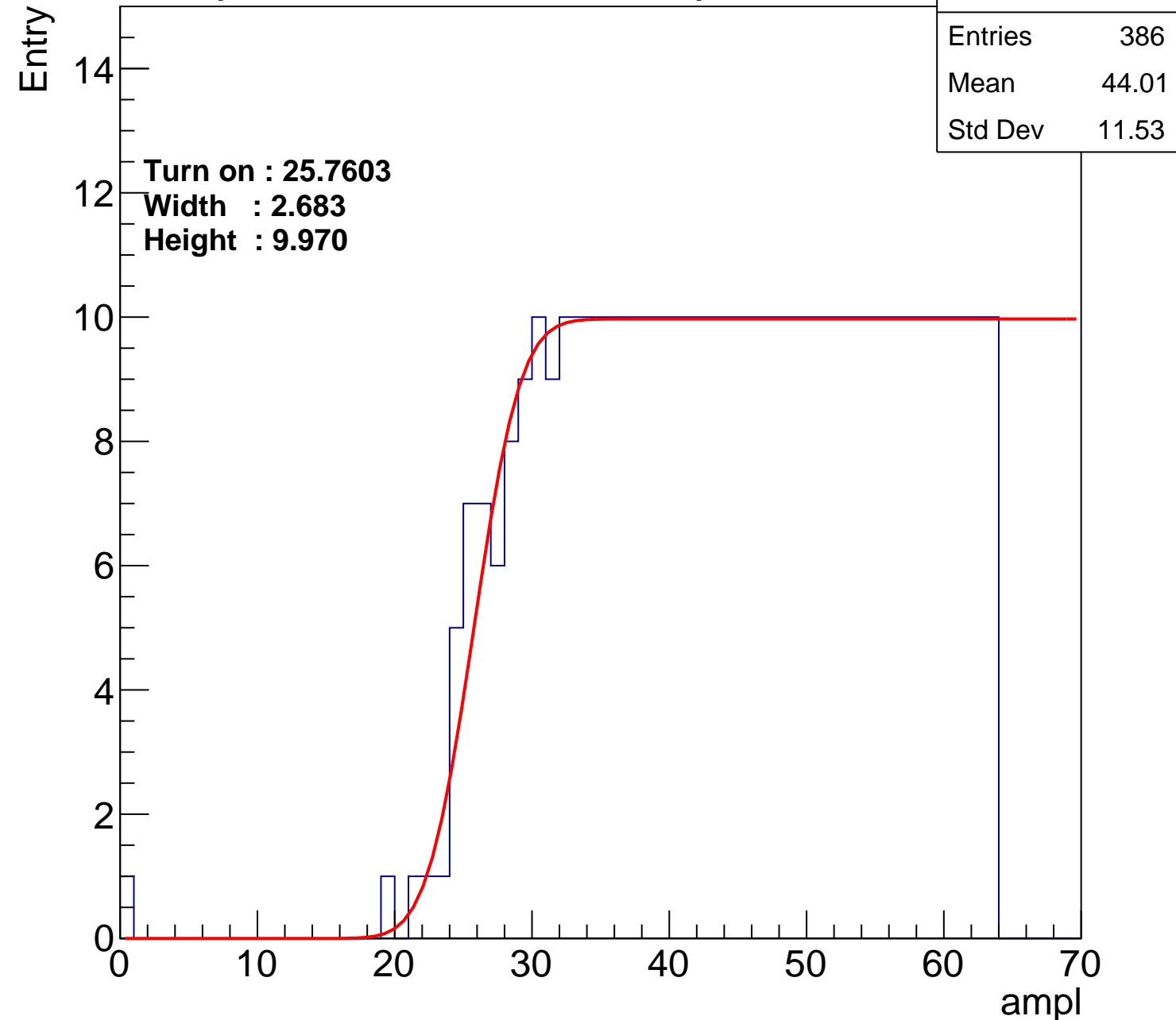
Width : 2.683

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch27

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.24
Std Dev	11.54

Turn on : 26.1962

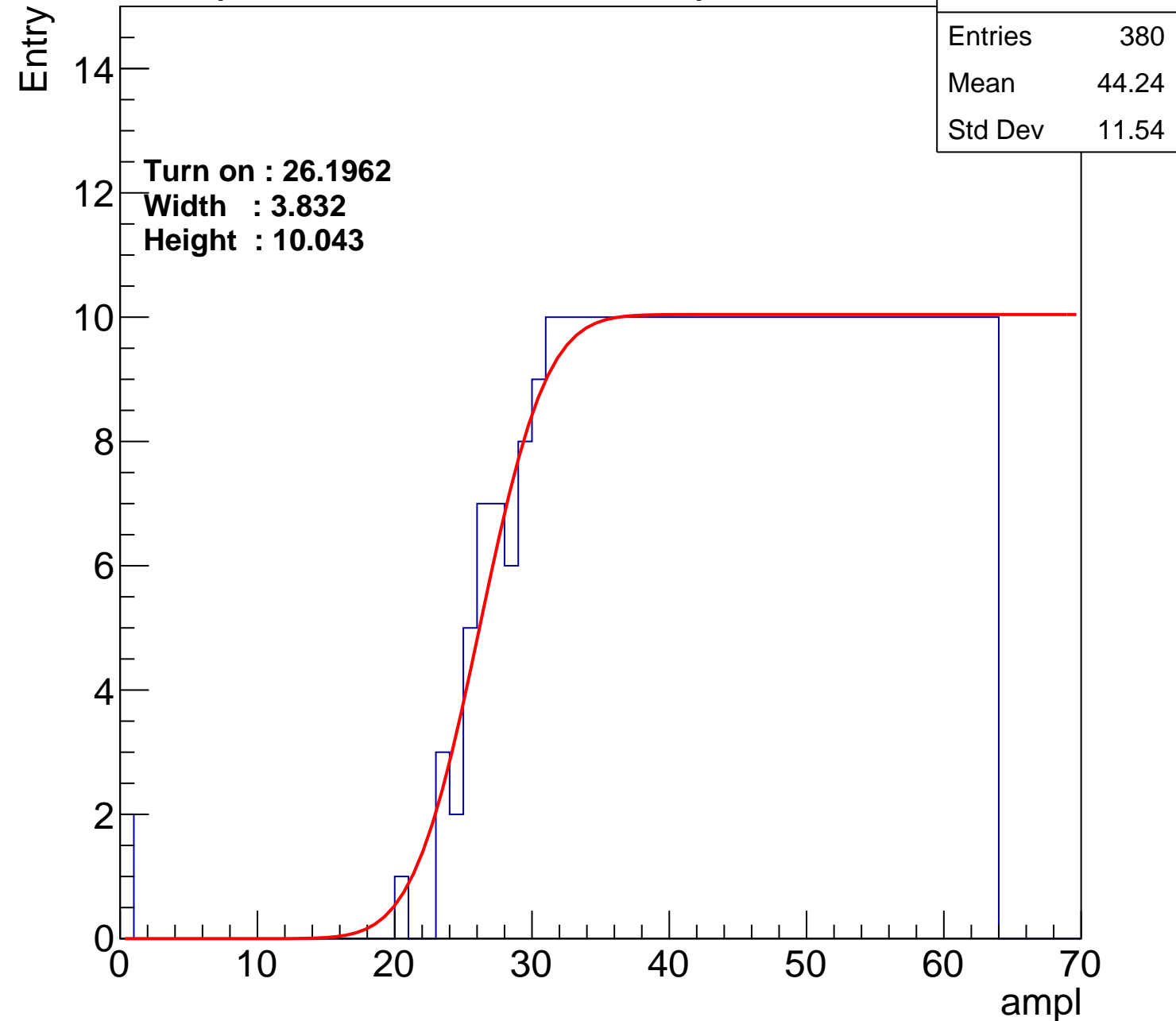
Width : 3.832

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch28

calib\_packv5\_042523\_0143.root, FC#5, port B1

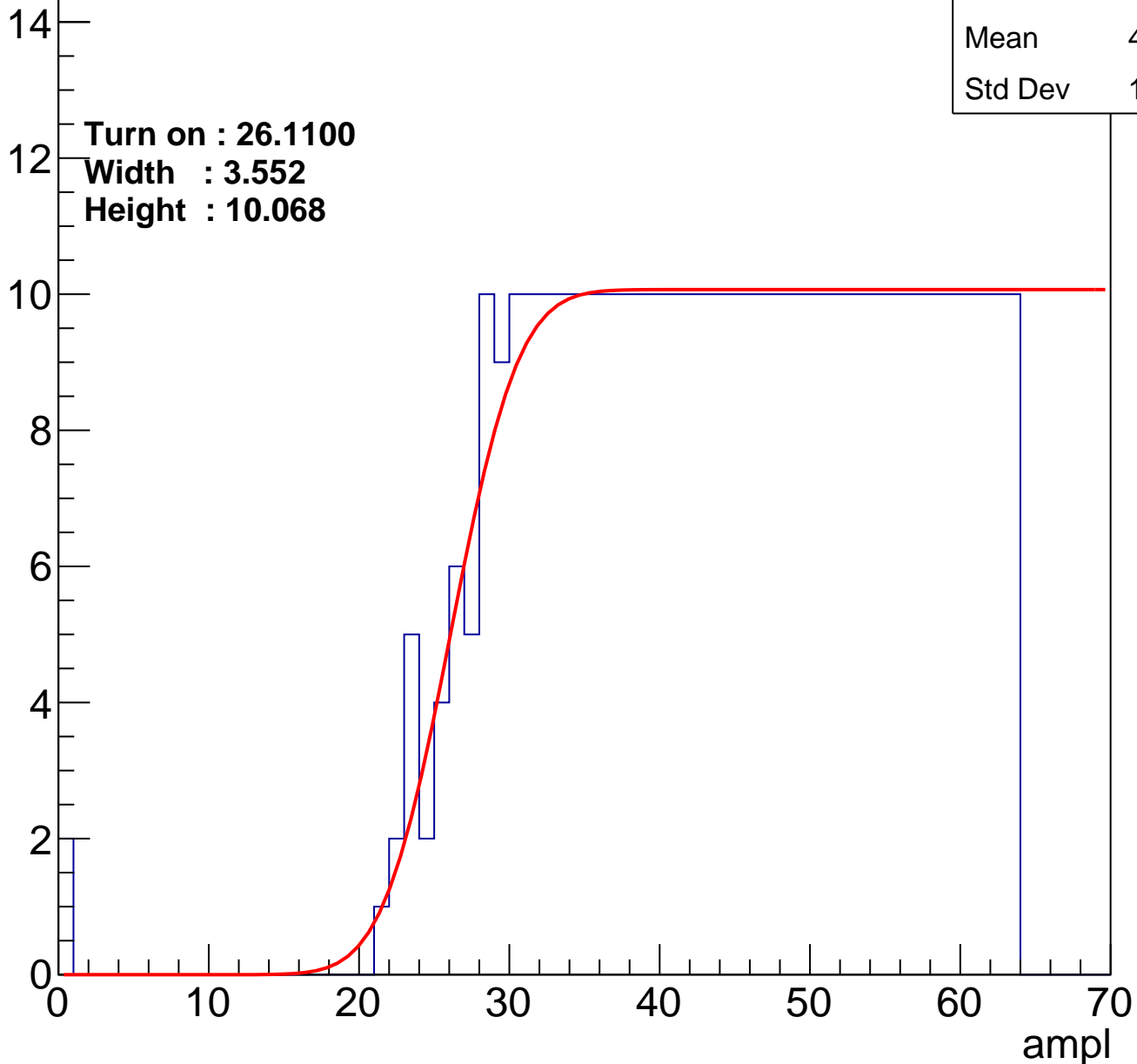
Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 26.1100

Width : 3.552

Height : 10.068

Entry



# B0L000S, U19-ch29

calib\_packv5\_042523\_0143.root, FC#5, port B1

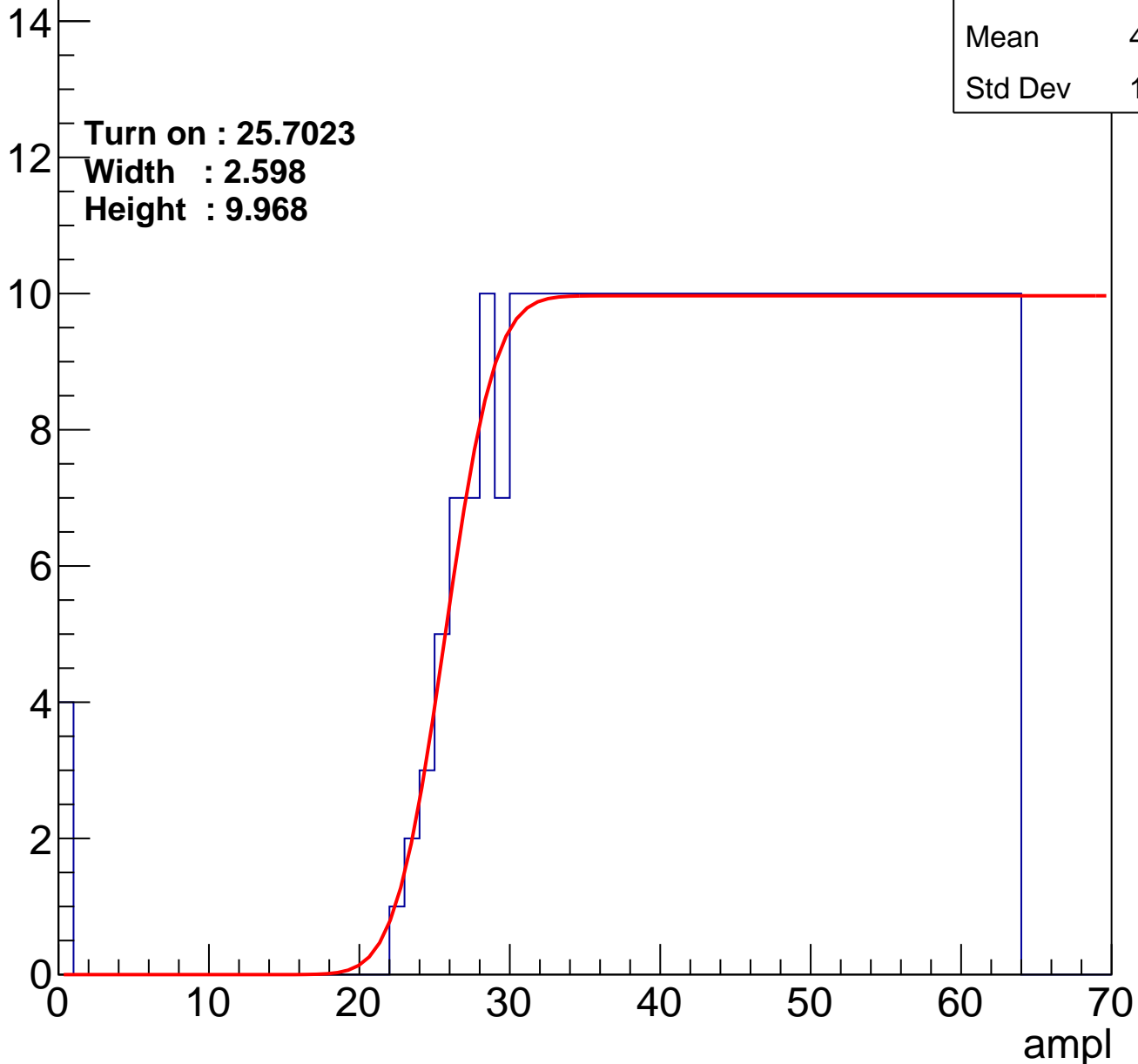
Entries	386
Mean	43.86
Std Dev	11.98

**Turn on : 25.7023**

**Width : 2.598**

**Height : 9.968**

Entry



# B0L000S, U19-ch30

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.78
Std Dev	11.6

Turn on : 27.9070

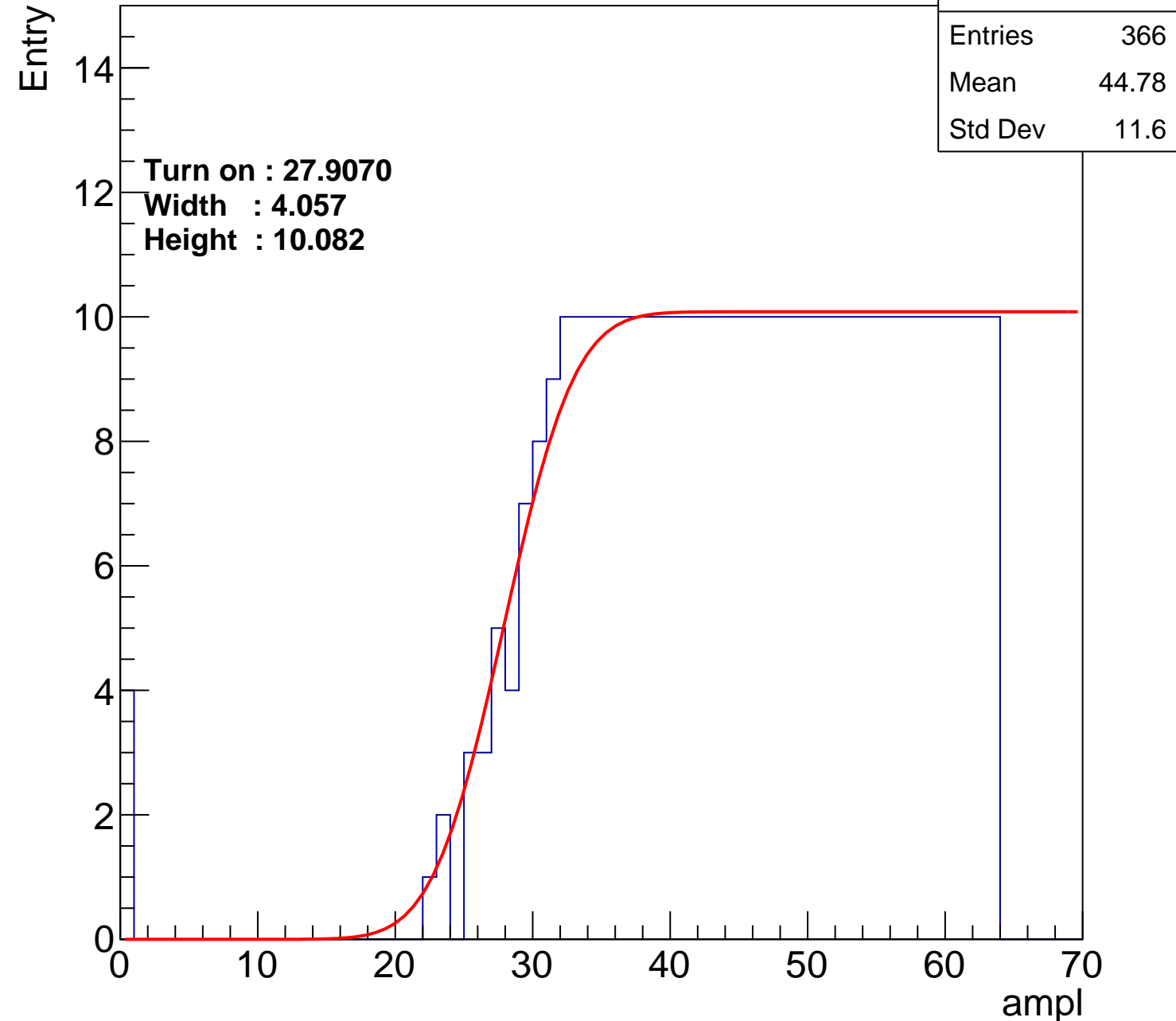
Width : 4.057

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch31

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 25.9154

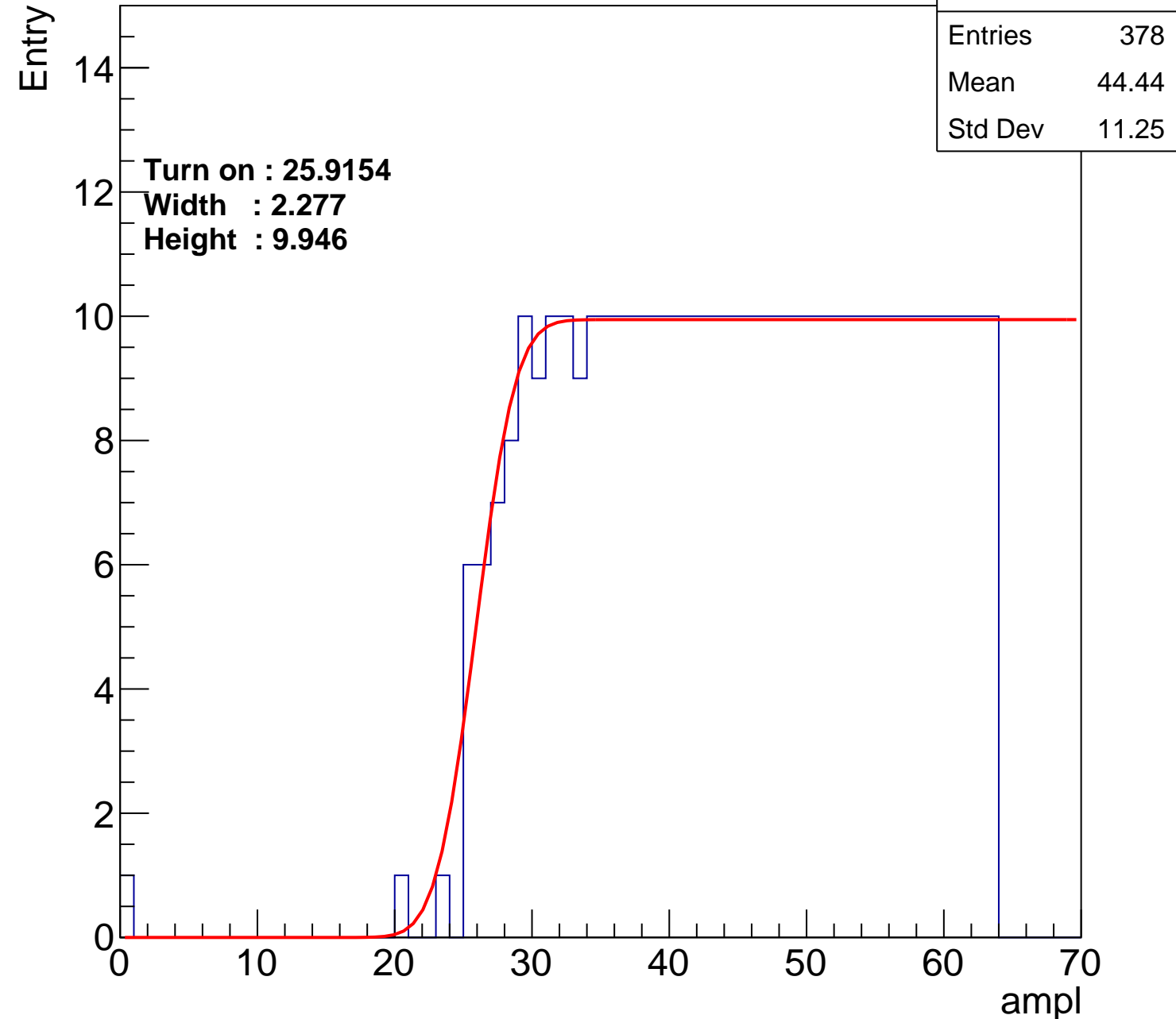
Width : 2.277

Height : 9.946

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch32

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.85
Std Dev	11.36

**Turn on : 27.7067**

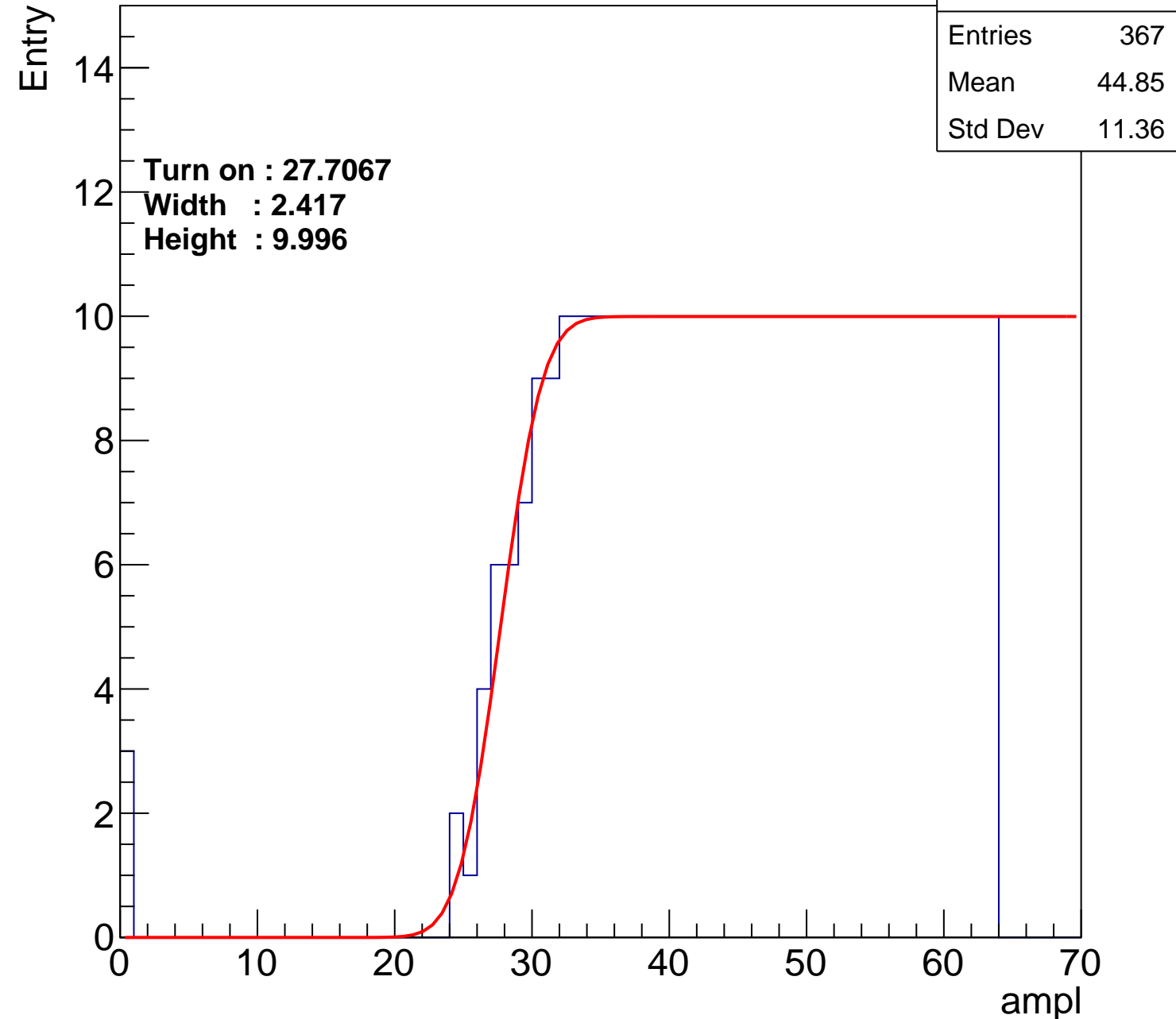
**Width : 2.417**

**Height : 9.996**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch33

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.39
Std Dev	11.73

Turn on : 27.3749

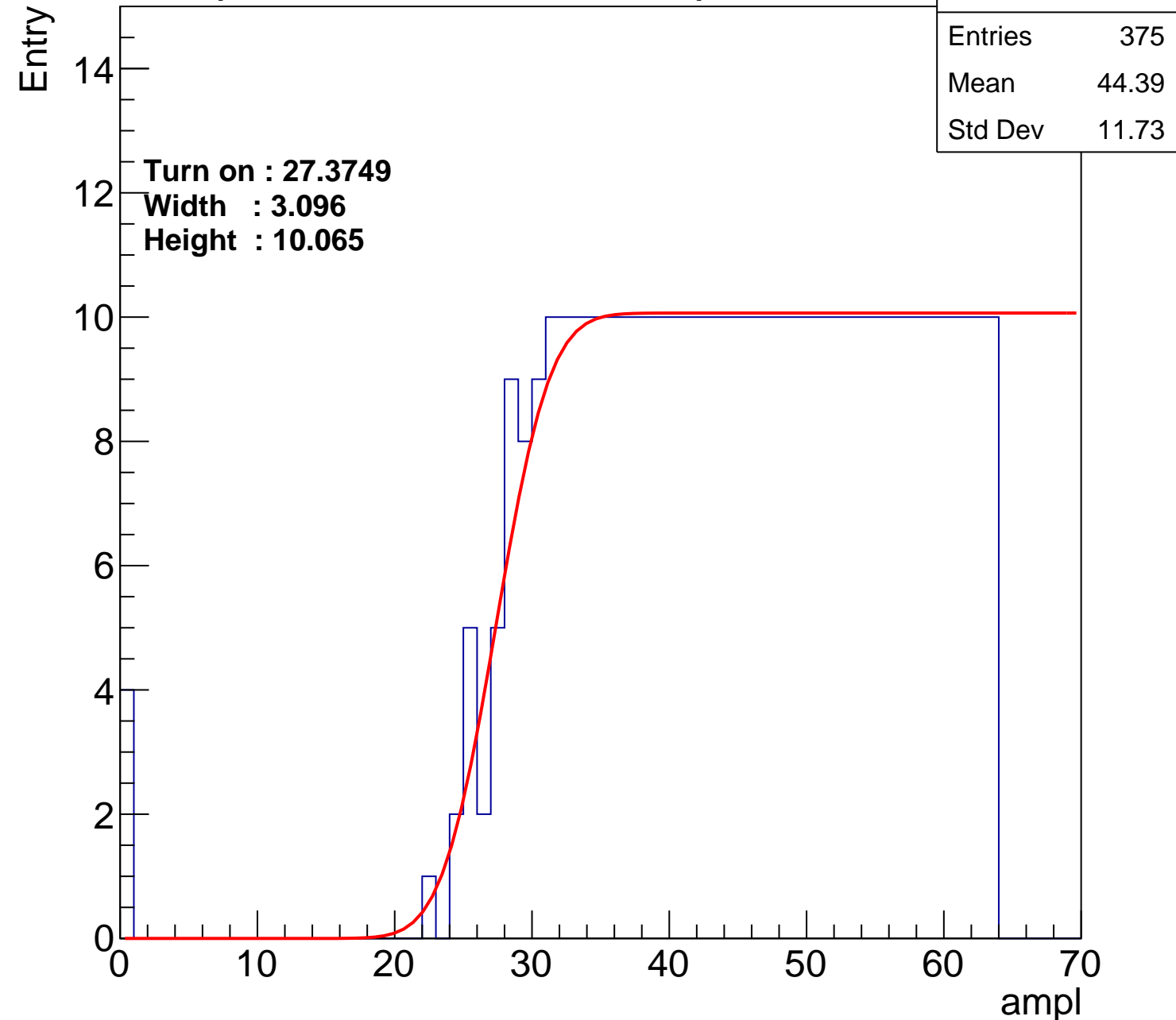
Width : 3.096

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch34

calib\_packv5\_042523\_0143.root, FC#5, port B1

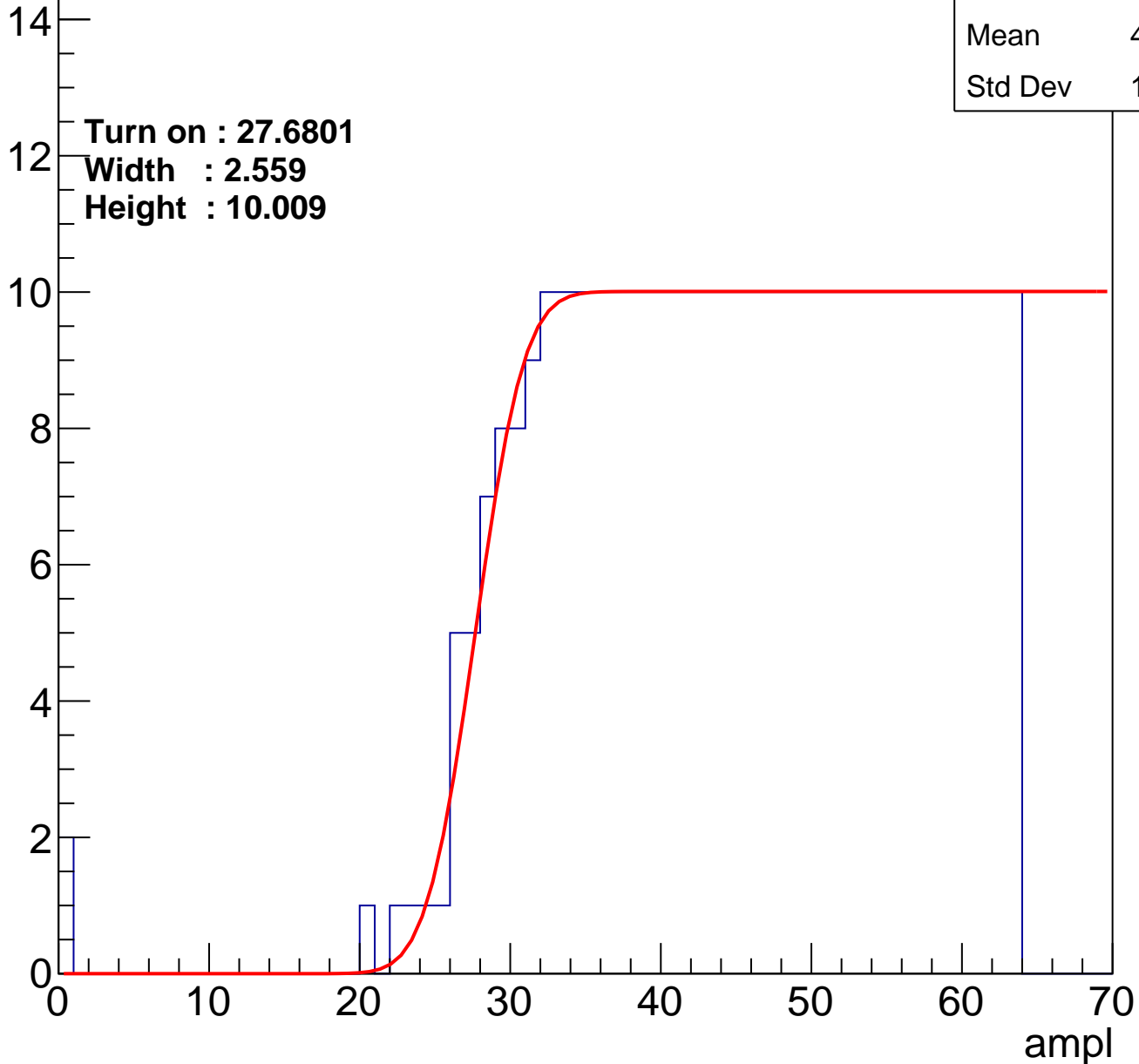
Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 27.6801

Width : 2.559

Height : 10.009

Entry



**B0L000S, U19-ch35**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	378
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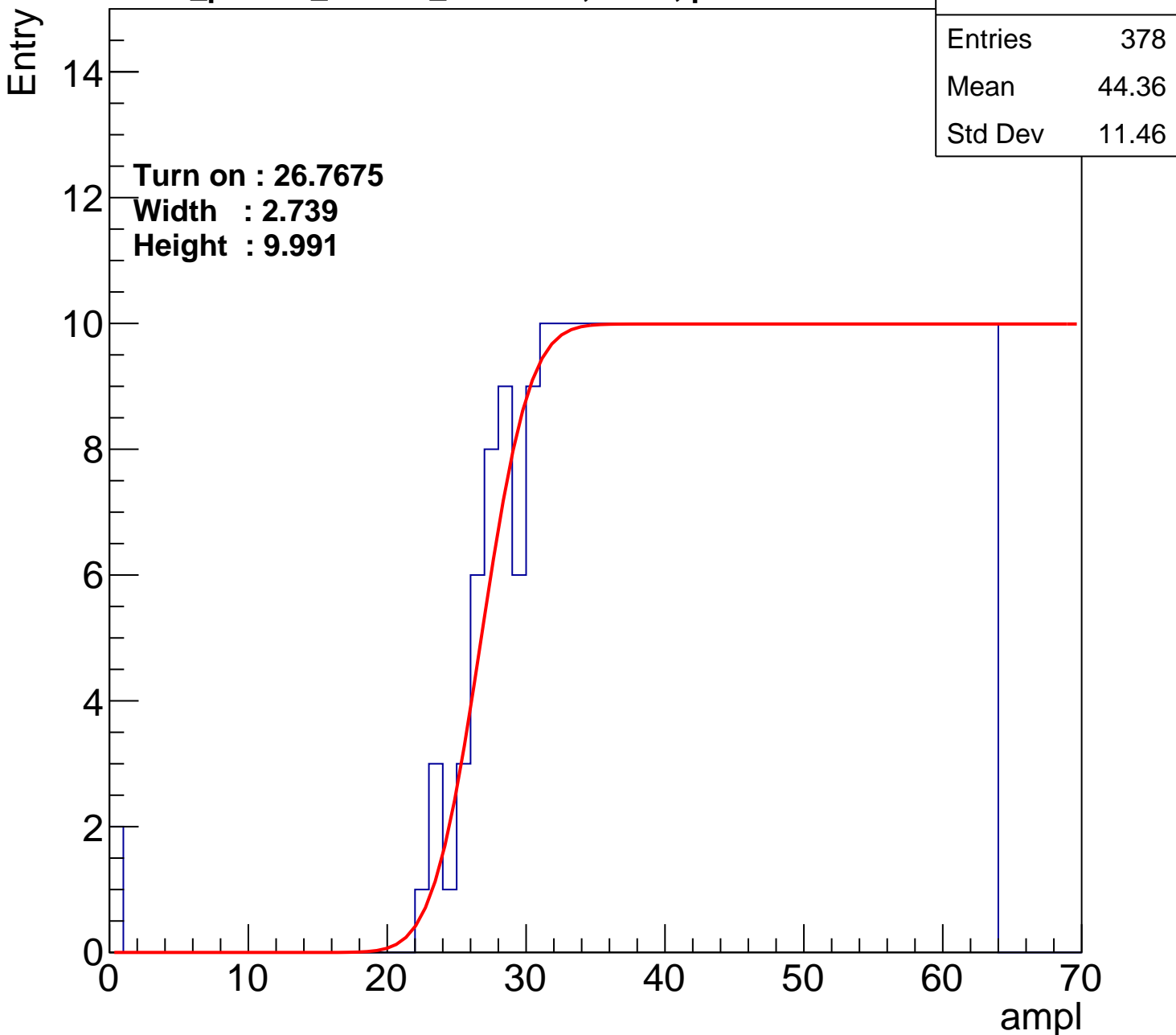
Mean	44.36
------	-------

Std Dev	11.46
---------	-------

**Turn on : 26.7675**

**Width : 2.739**

**Height : 9.991**

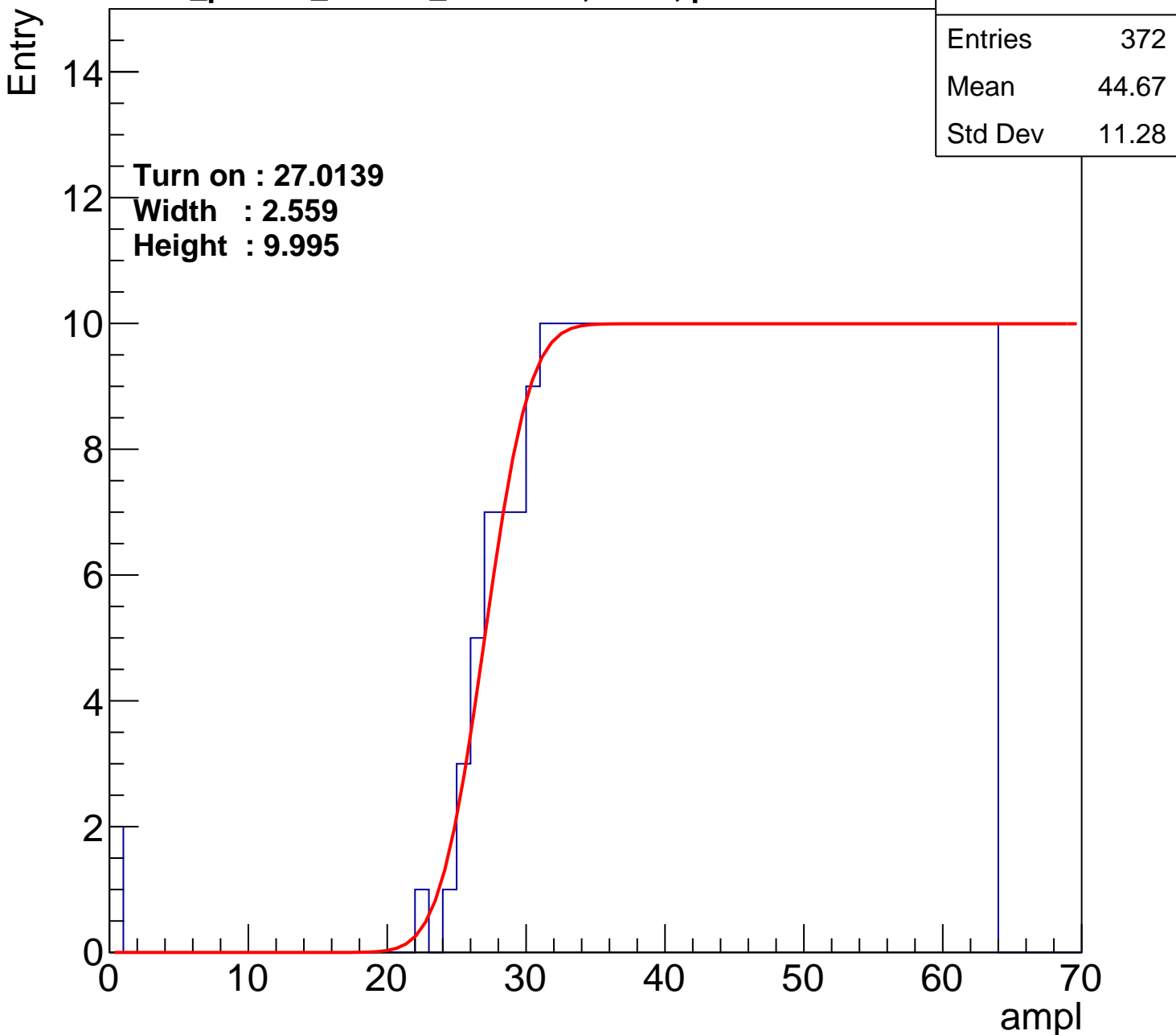


**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	372
Mean	44.67
Std Dev	11.28

**Height : 9.995**



# B0L000S, U19-ch37

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.75
Std Dev	11.76

Turn on : 25.1191

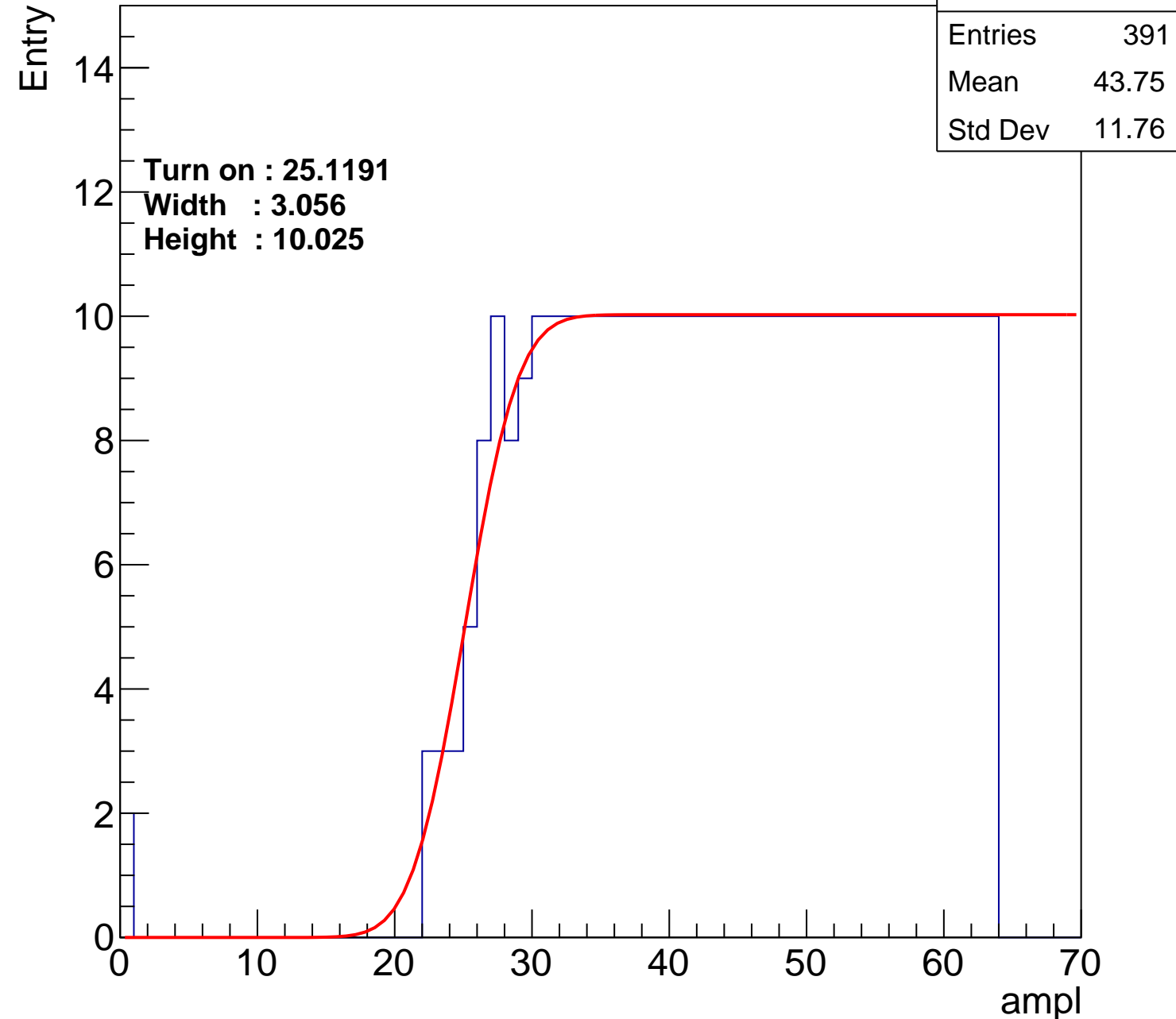
Width : 3.056

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch38

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.19
Std Dev	11.84

Turn on : 26.9226

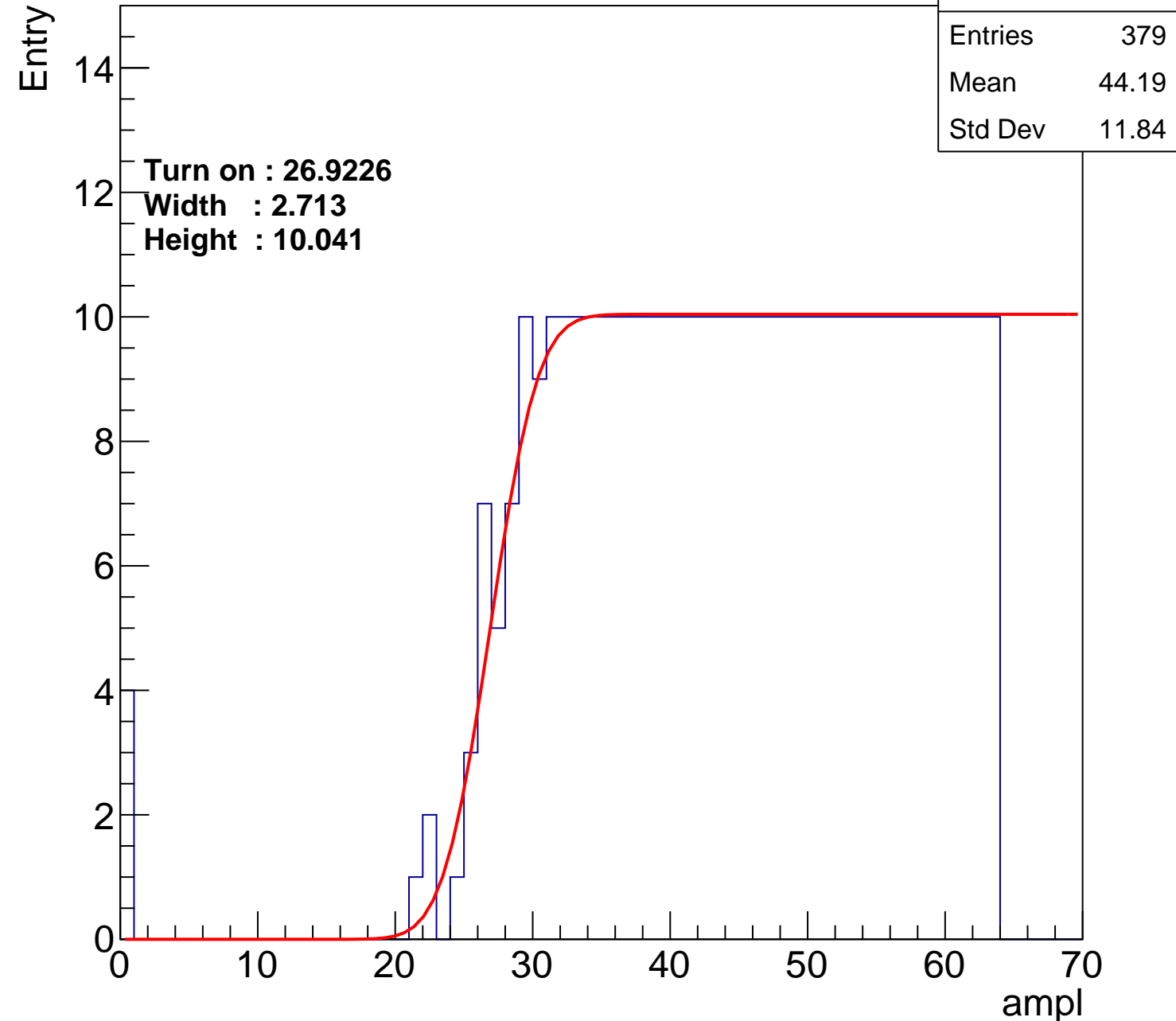
Width : 2.713

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch39

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.69
Std Dev	11.26

Turn on : 27.0254

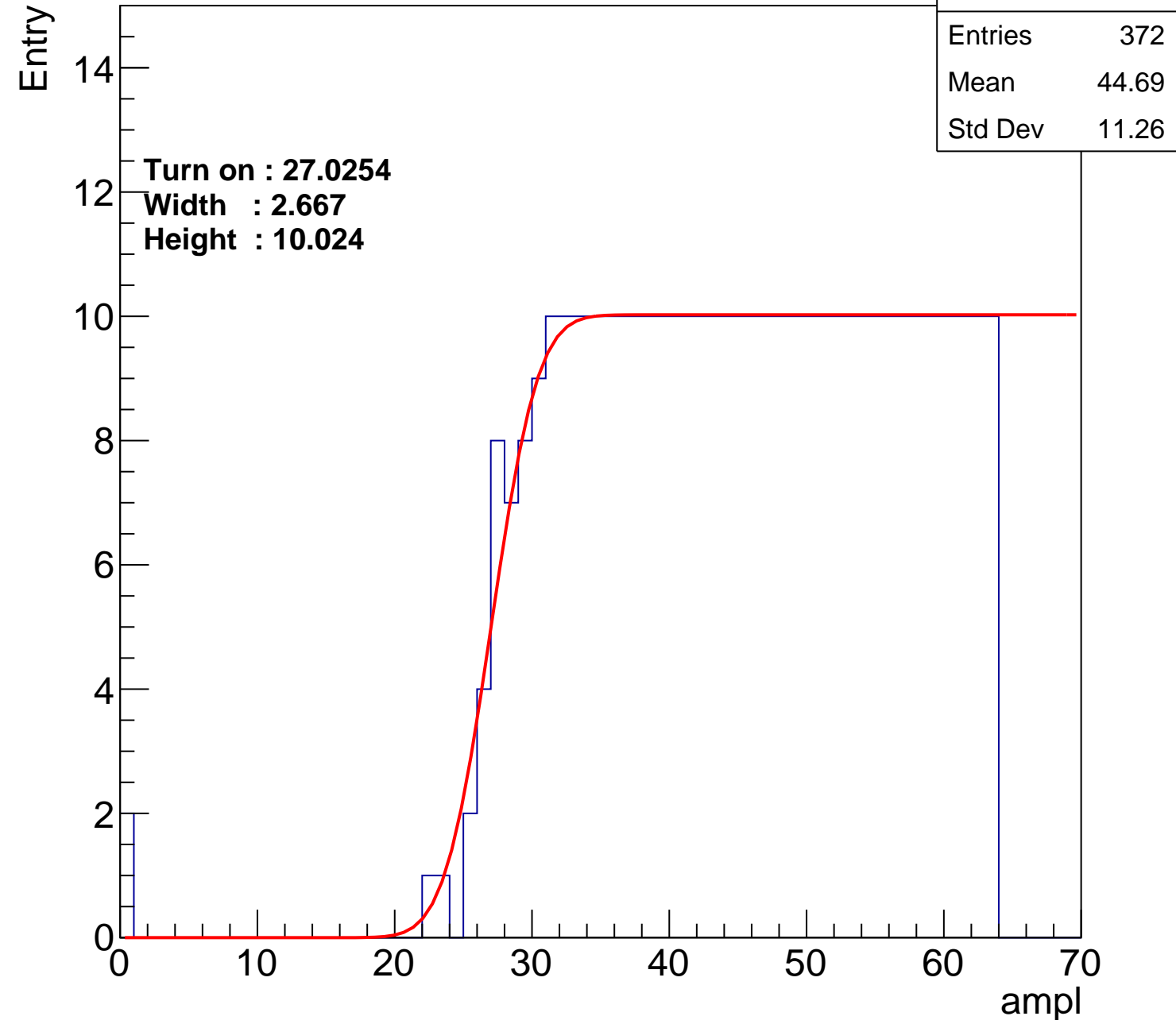
Width : 2.667

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch40

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.56
Std Dev	11.16

Turn on : 26.4978

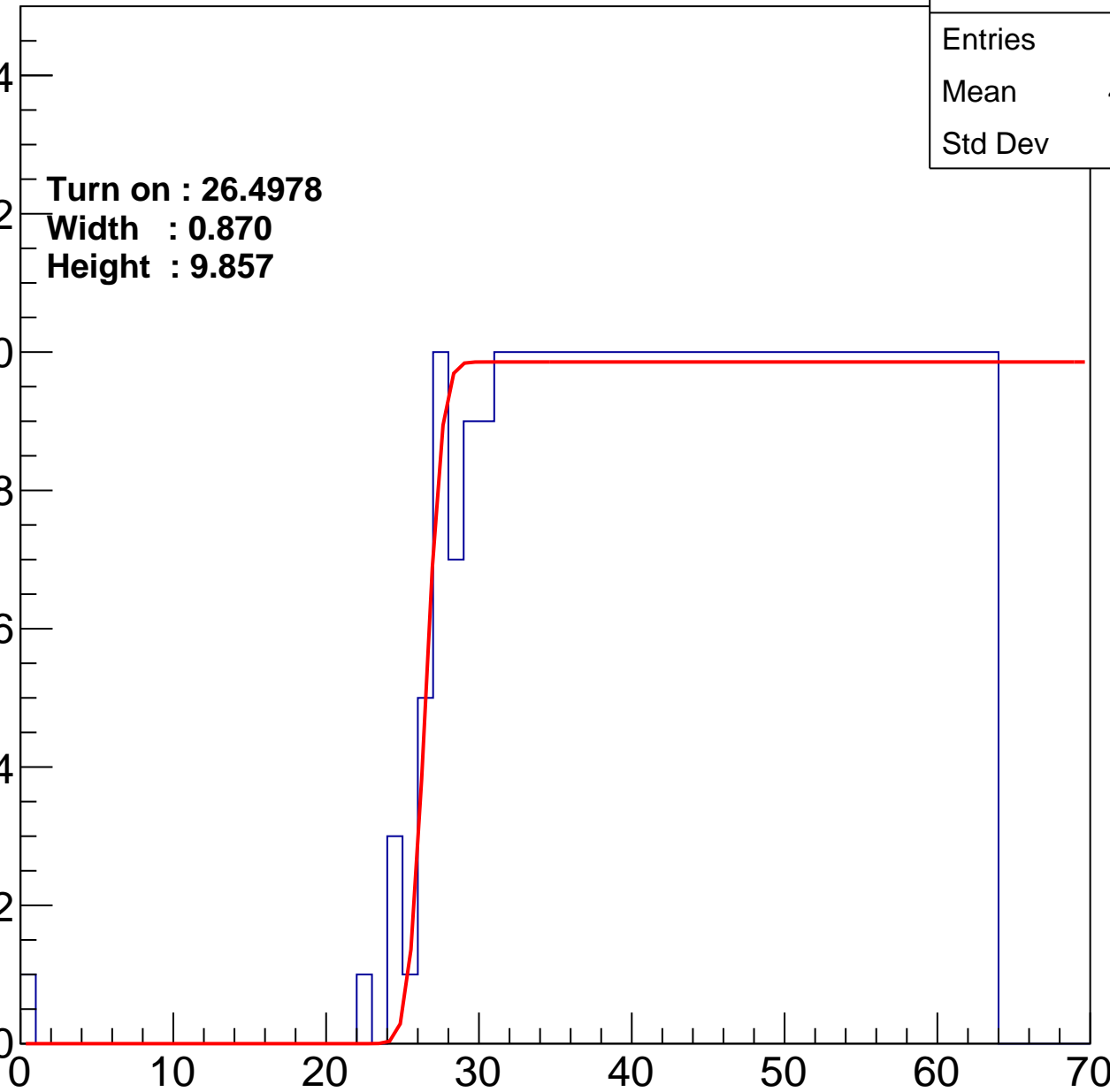
Width : 0.870

Height : 9.857

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch41

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.1155

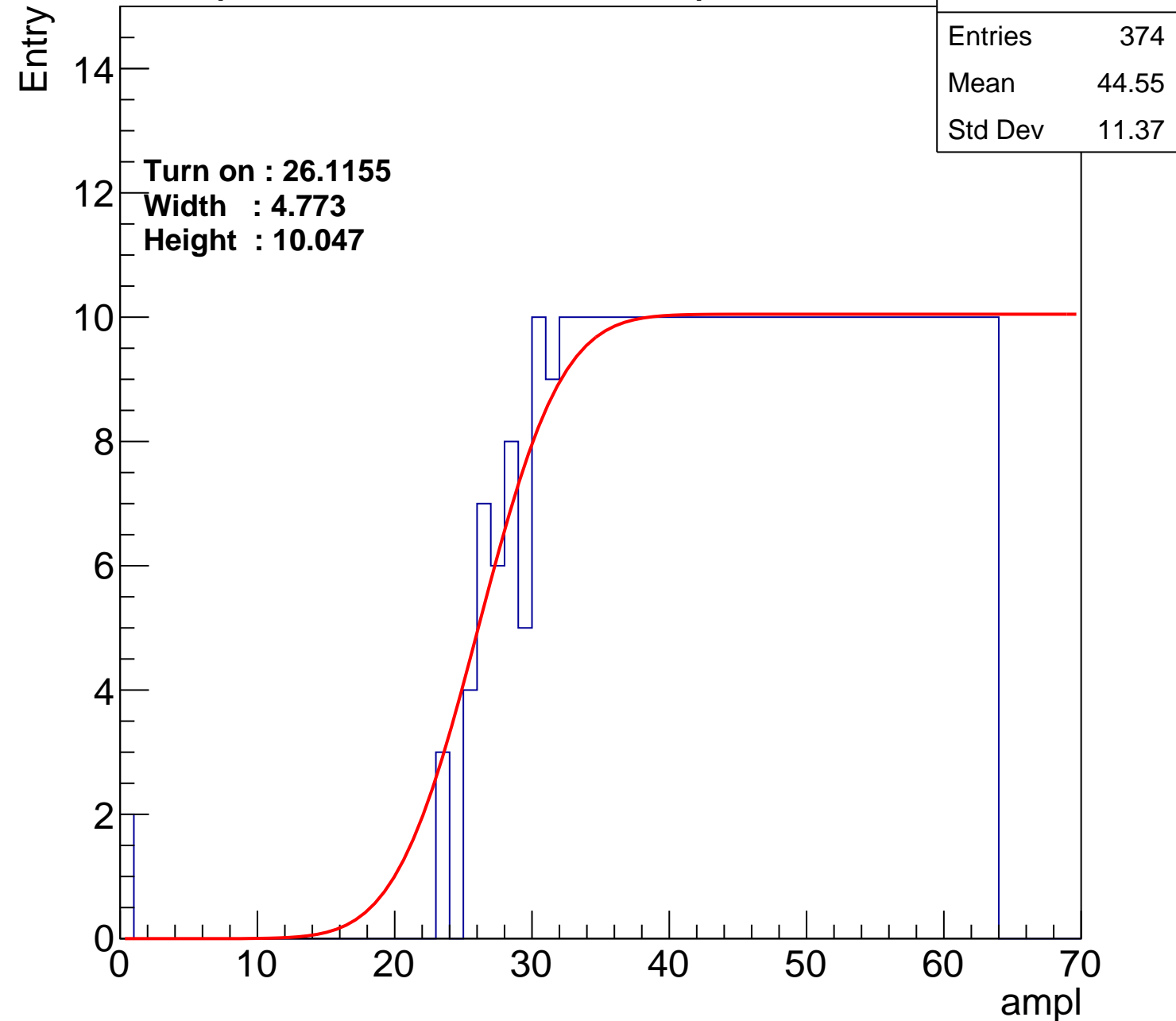
Width : 4.773

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch42

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.3173

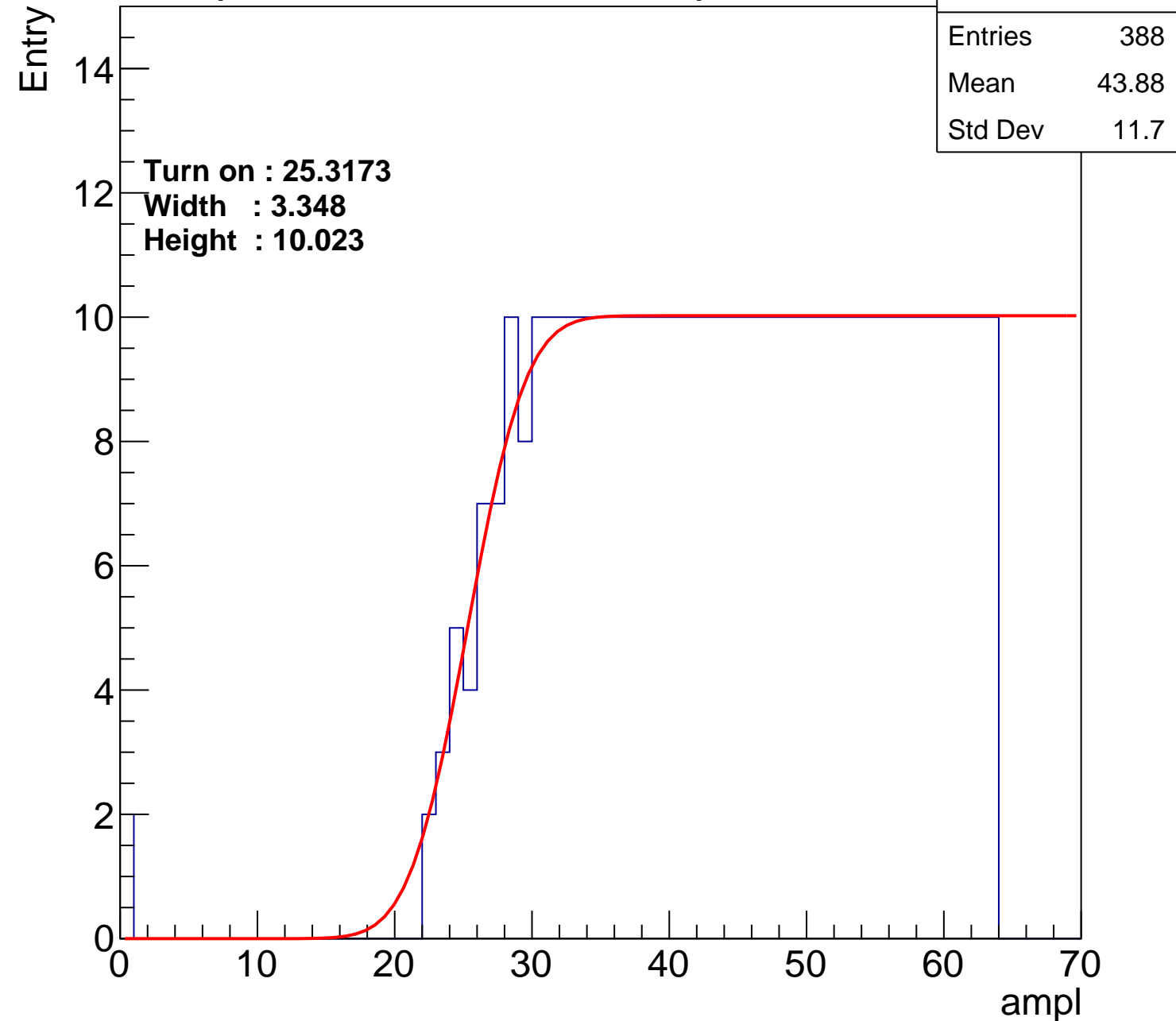
Width : 3.348

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch43

calib\_packv5\_042523\_0143.root, FC#5, port B1

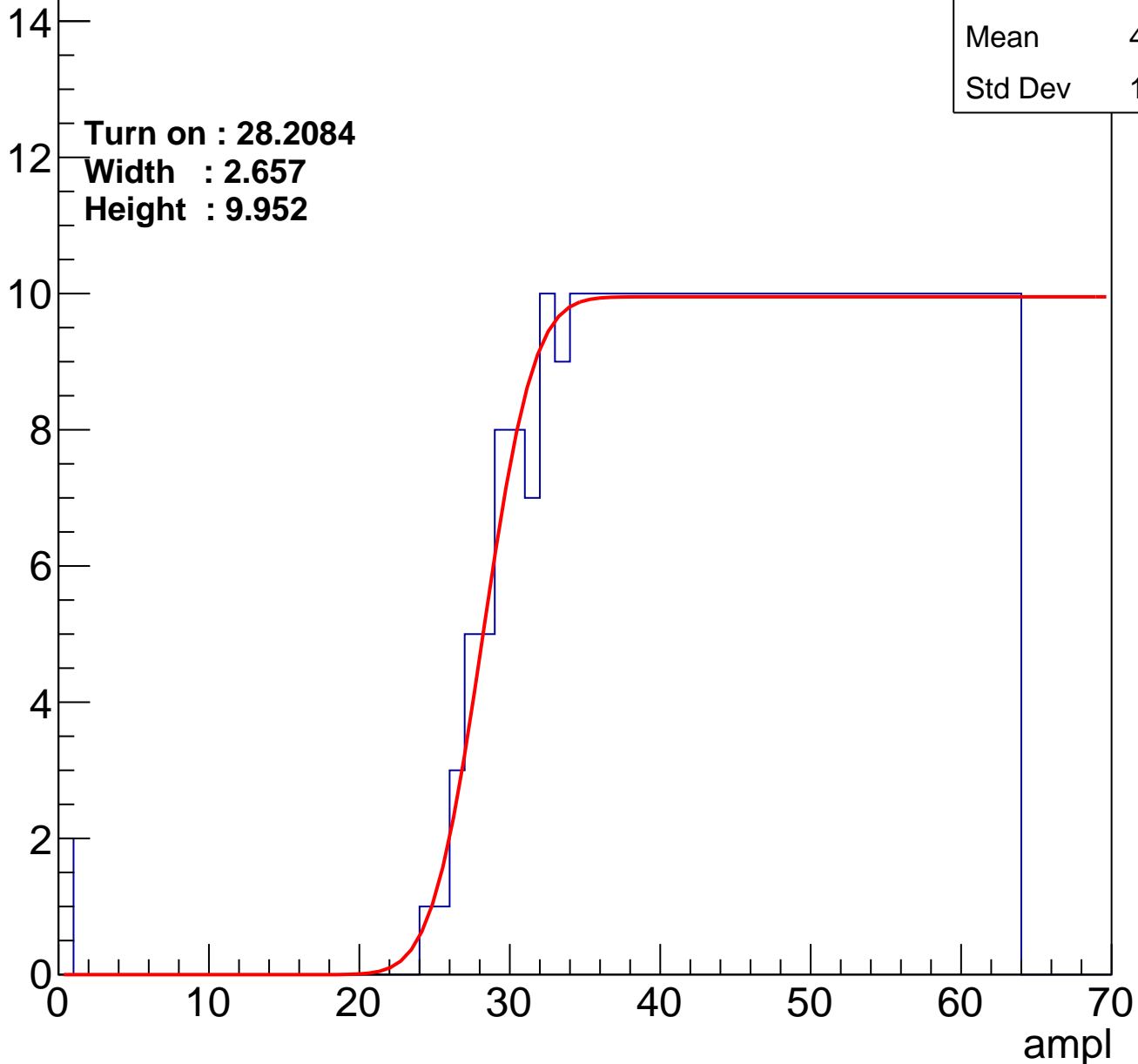
Entries	359
Mean	45.29
Std Dev	10.99

Turn on : 28.2084

Width : 2.657

Height : 9.952

Entry



# B0L000S, U19-ch44

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 27.1756

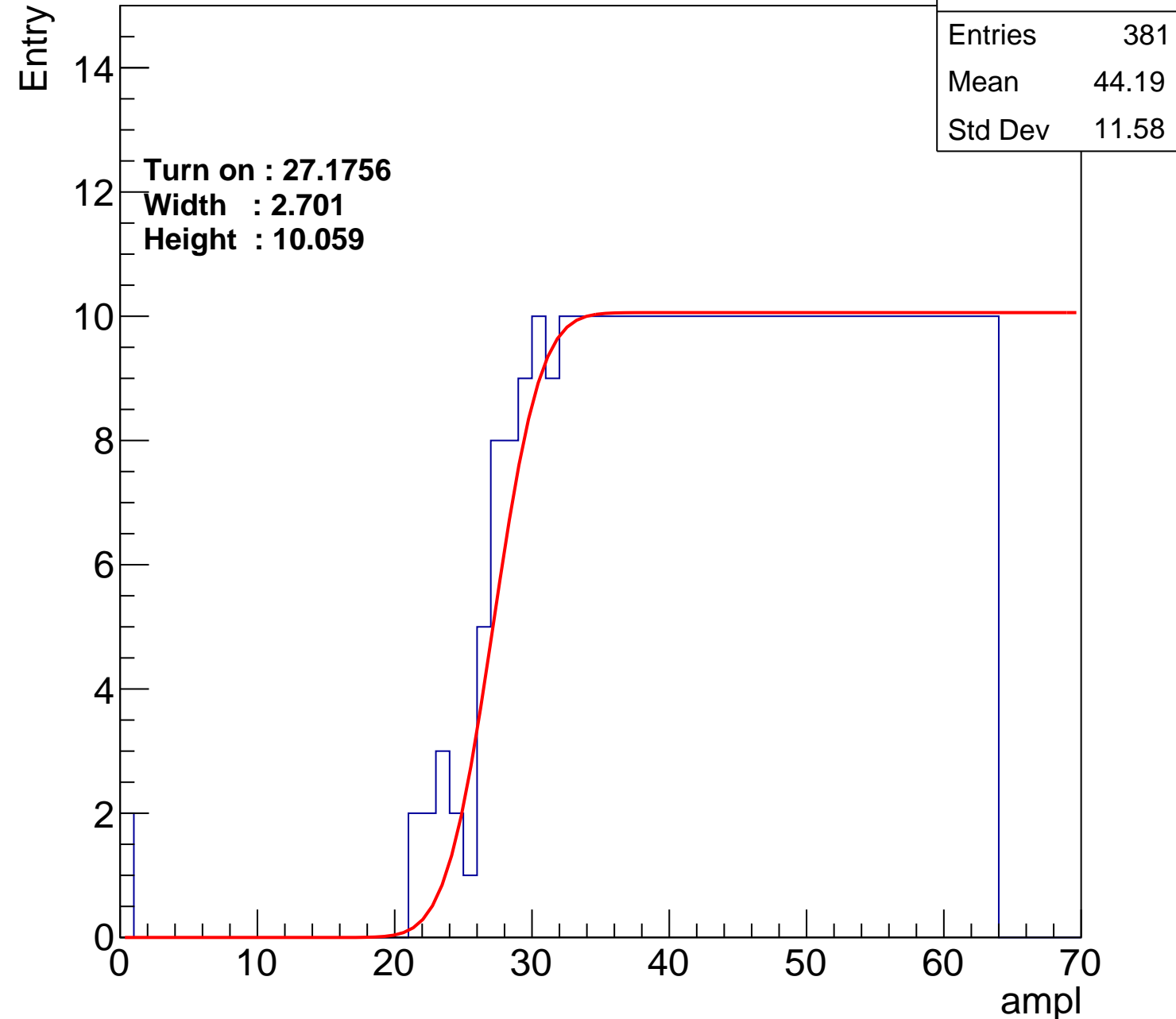
Width : 2.701

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch45

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.3
Std Dev	10.97

Turn on : 28.6514

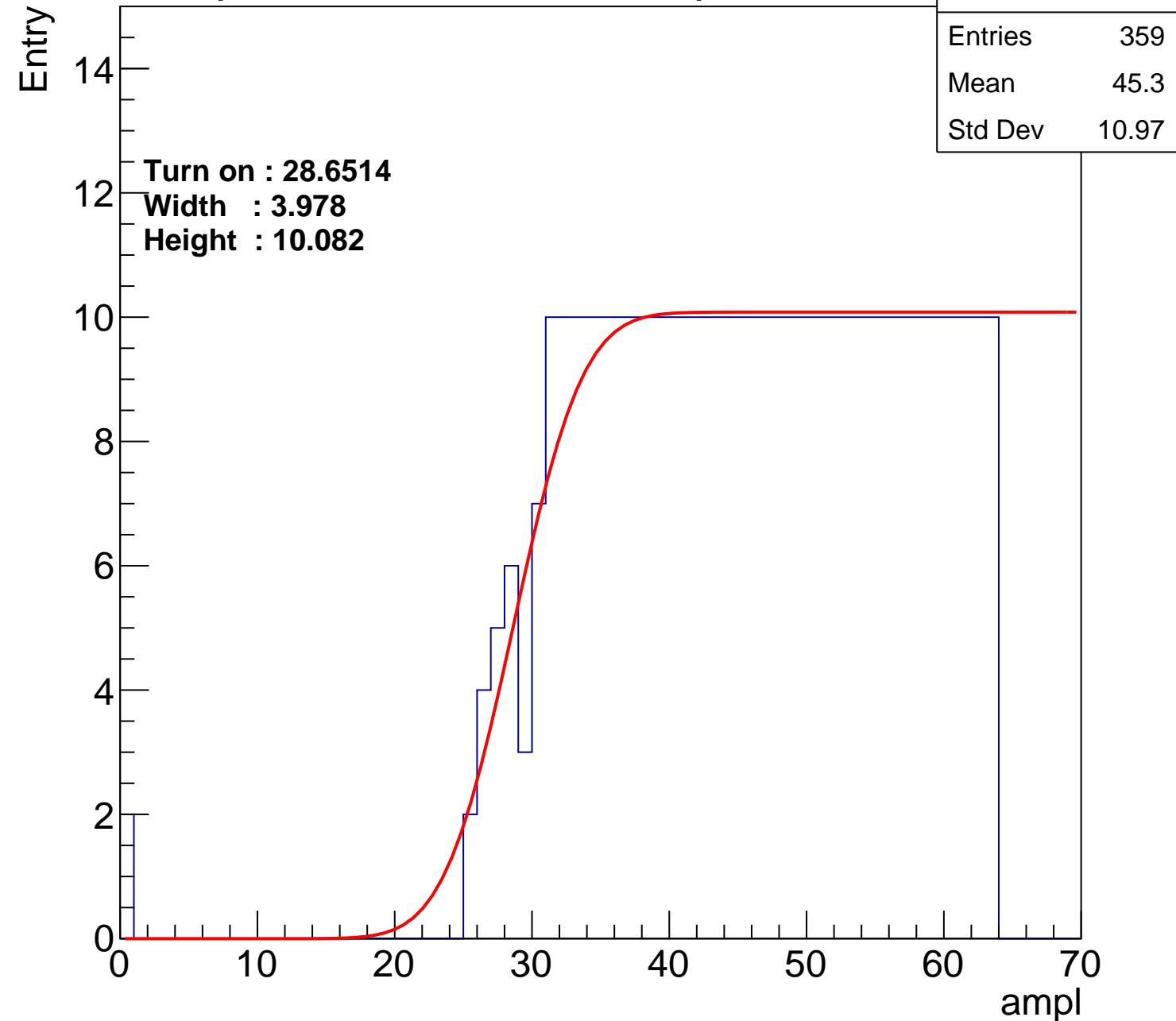
Width : 3.978

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch46

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.13
Std Dev	11.95

**Turn on : 26.5138**

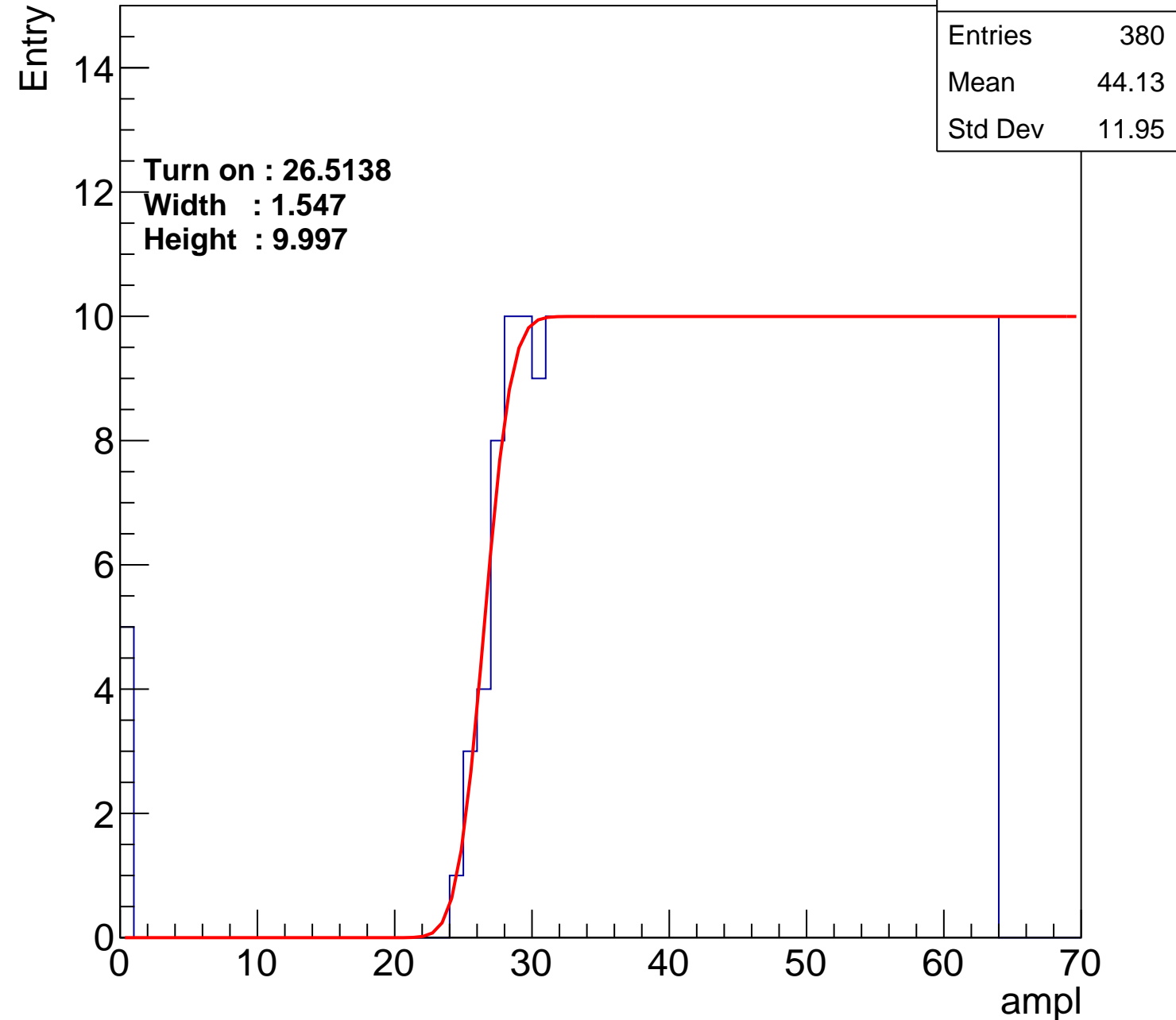
**Width : 1.547**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch47

calib\_packv5\_042523\_0143.root, FC#5, port B1

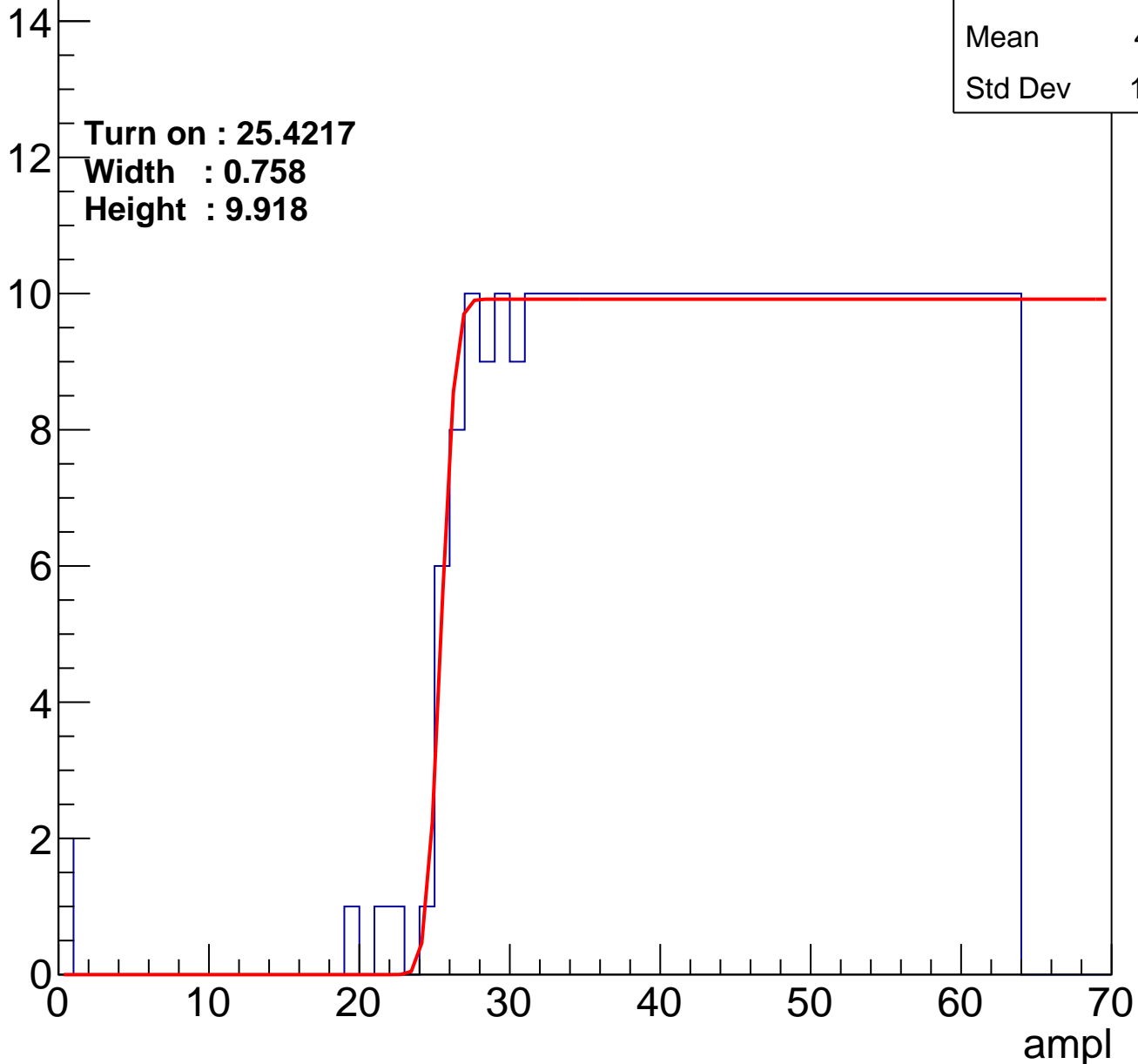
Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.4217

Width : 0.758

Height : 9.918

Entry



# B0L000S, U19-ch48

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.5
Std Dev	11.21

**Turn on : 26.6235**

**Width : 2.891**

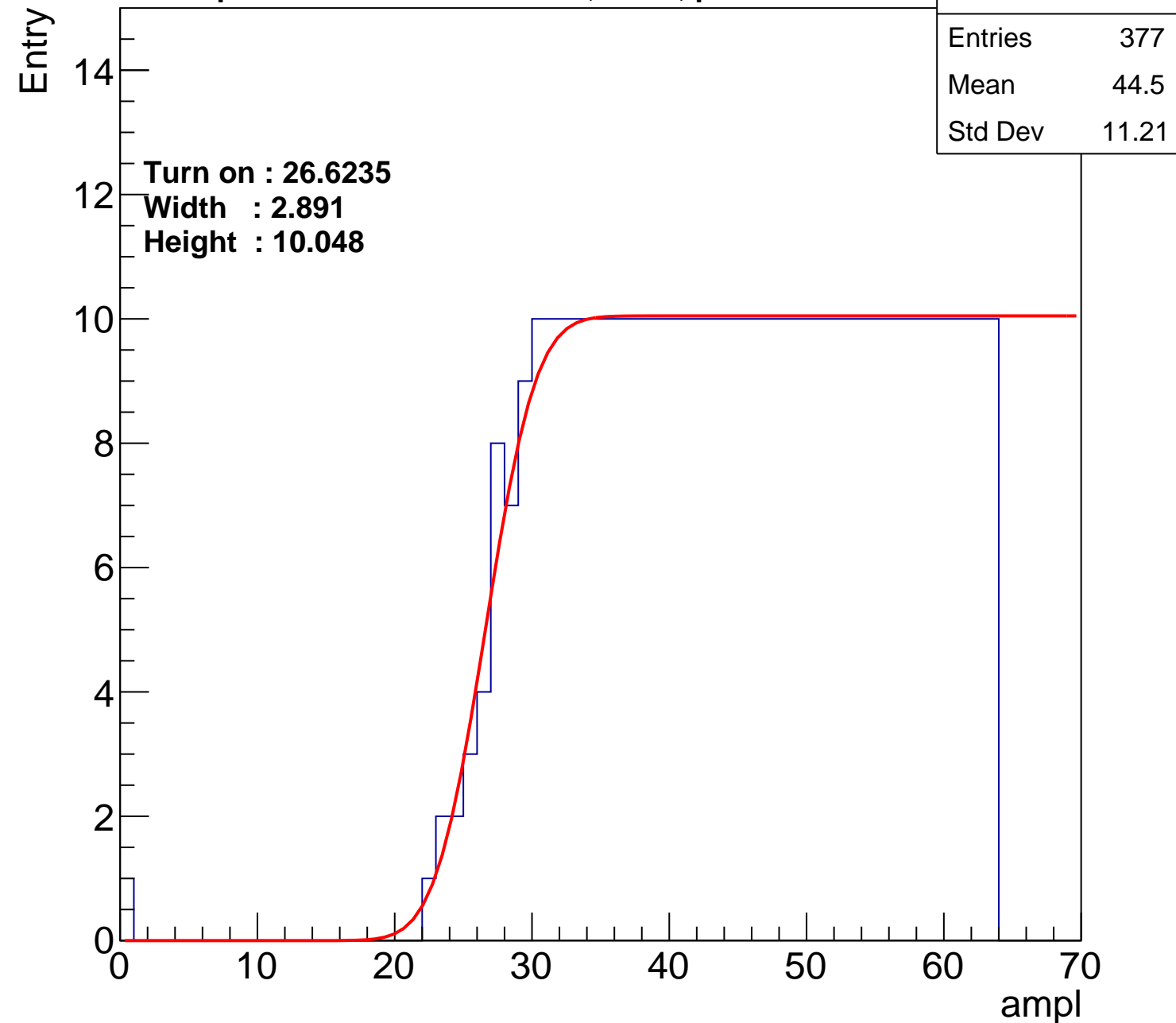
**Height : 10.048**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch49

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.29
Std Dev	11.53

Turn on : 26.4441

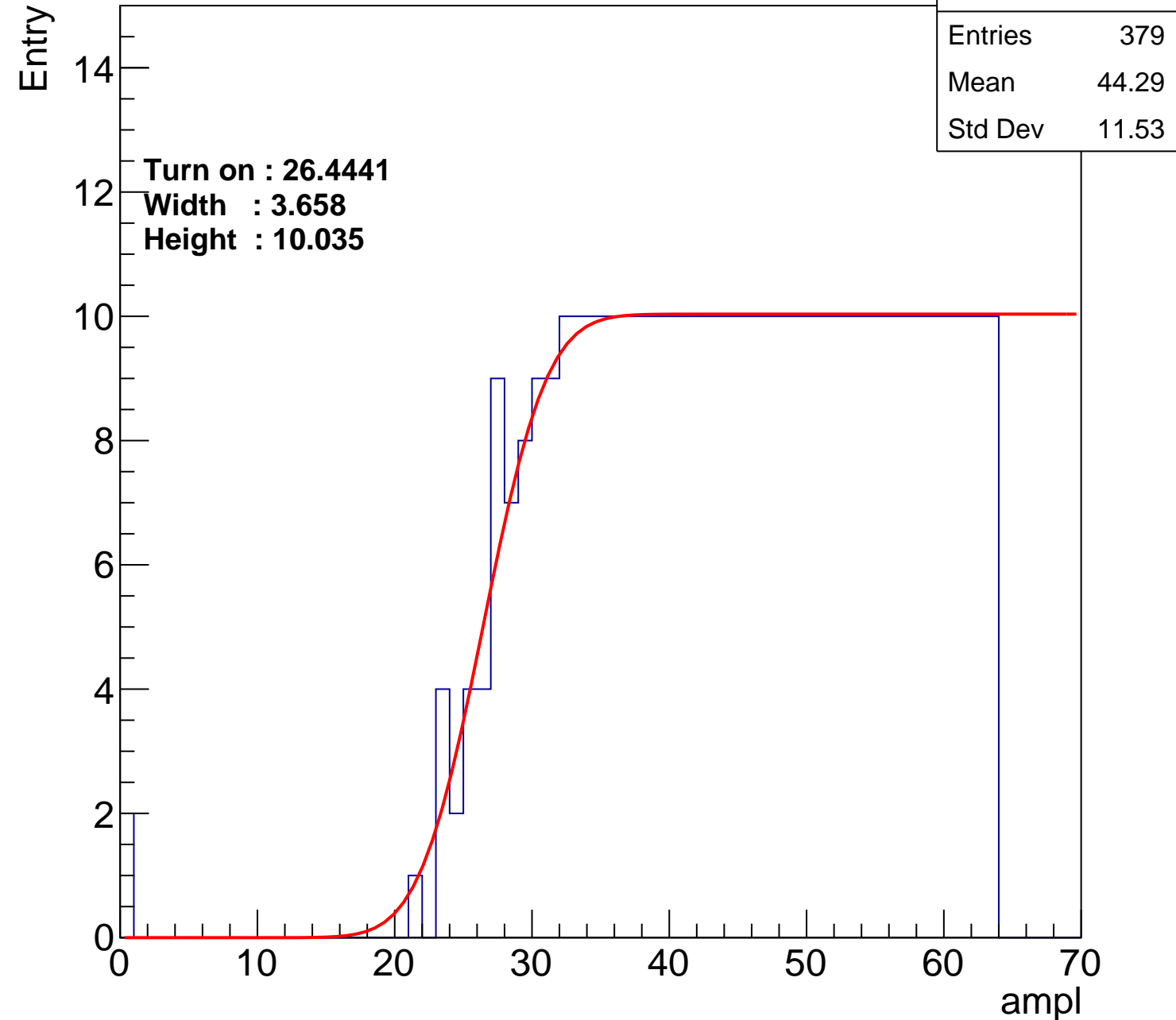
Width : 3.658

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch50

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.93
Std Dev	11.29

**Turn on : 27.8424**

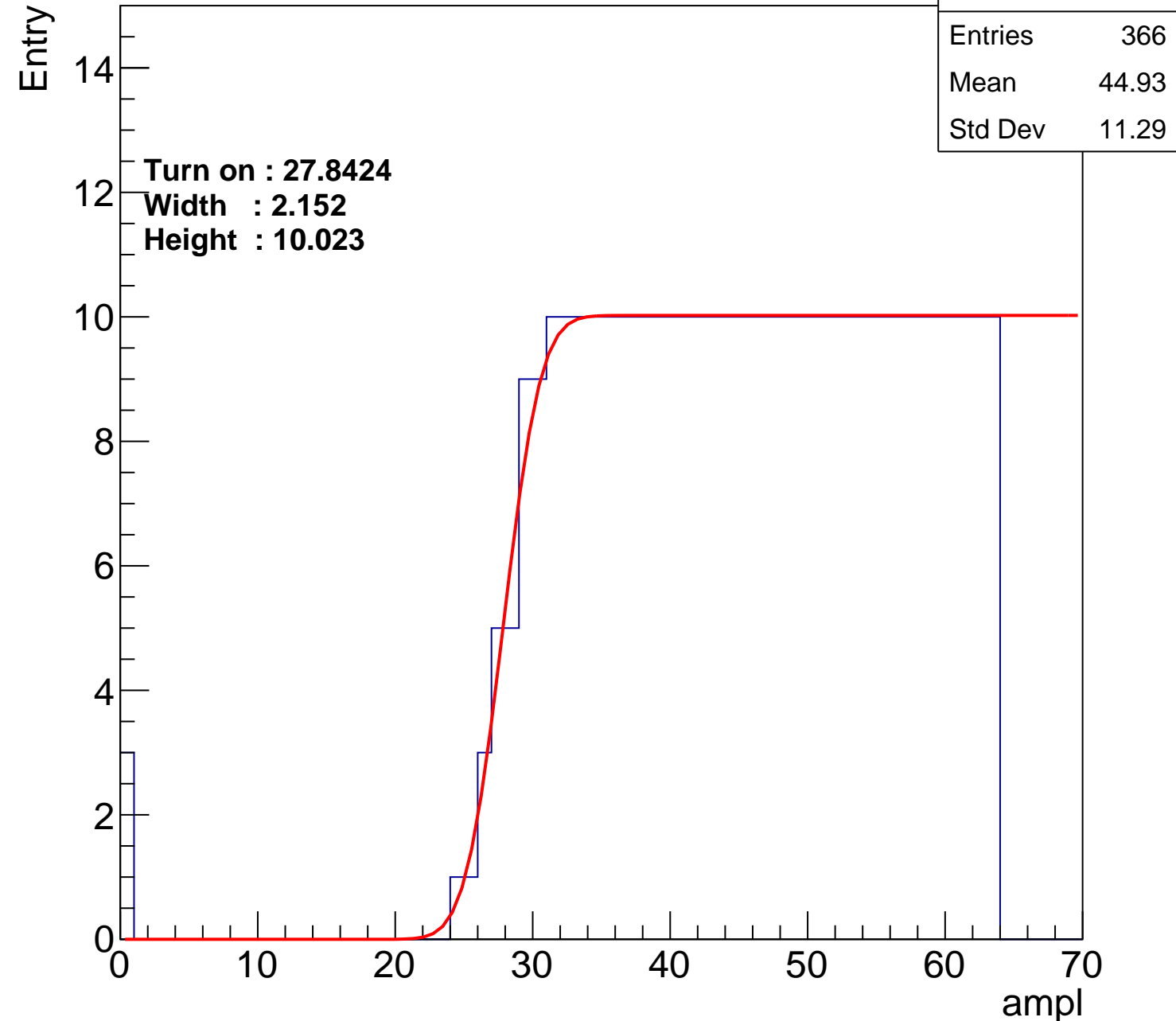
**Width : 2.152**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch51

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	363
Mean	45.14
Std Dev	10.93

**Turn on : 29.0970**

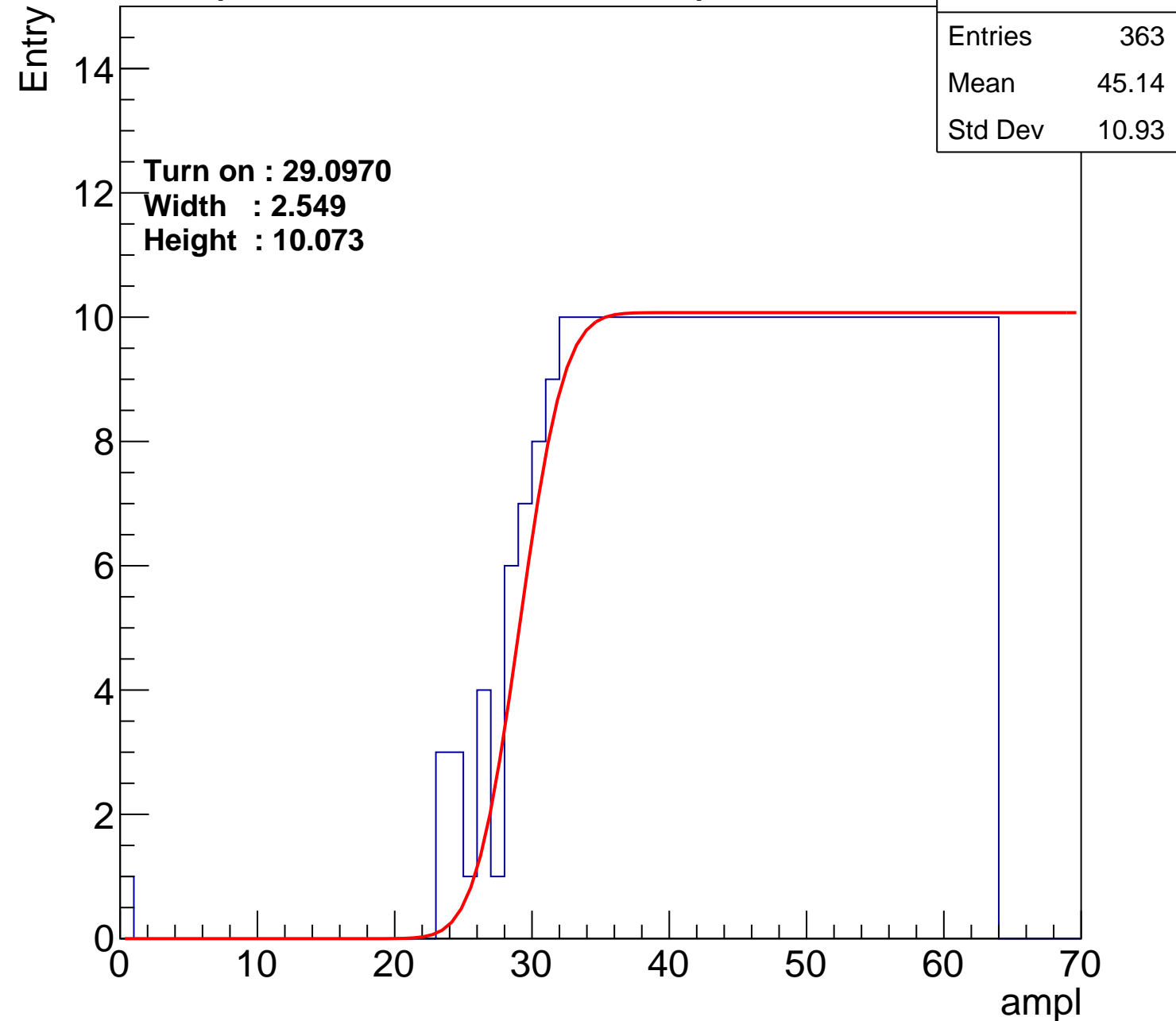
**Width : 2.549**

**Height : 10.073**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch52

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.5313

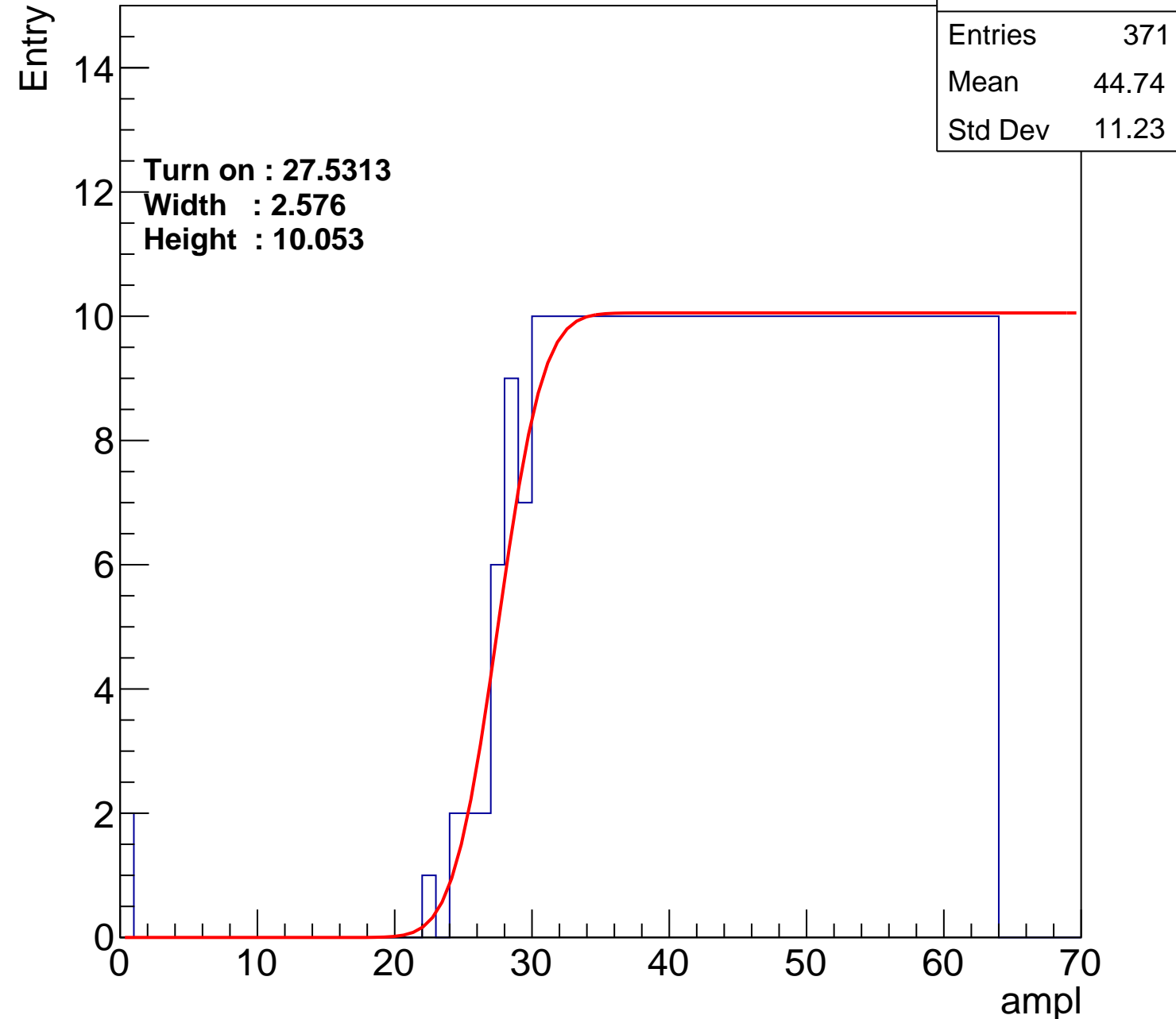
Width : 2.576

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch53

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 26.0393

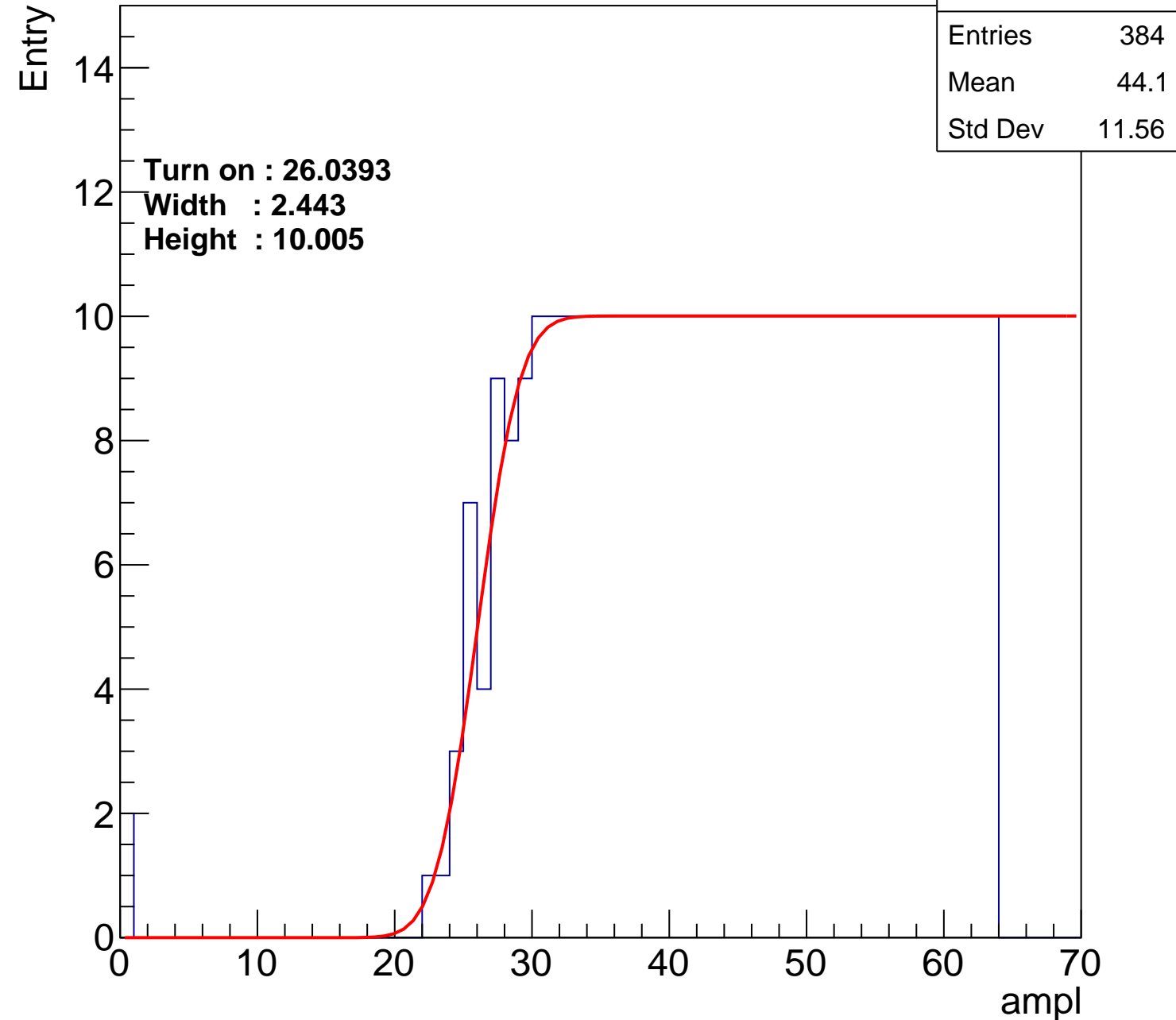
Width : 2.443

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 25.6592  
Width : 2.798  
Height : 10.016





# B0L000S, U19-ch55

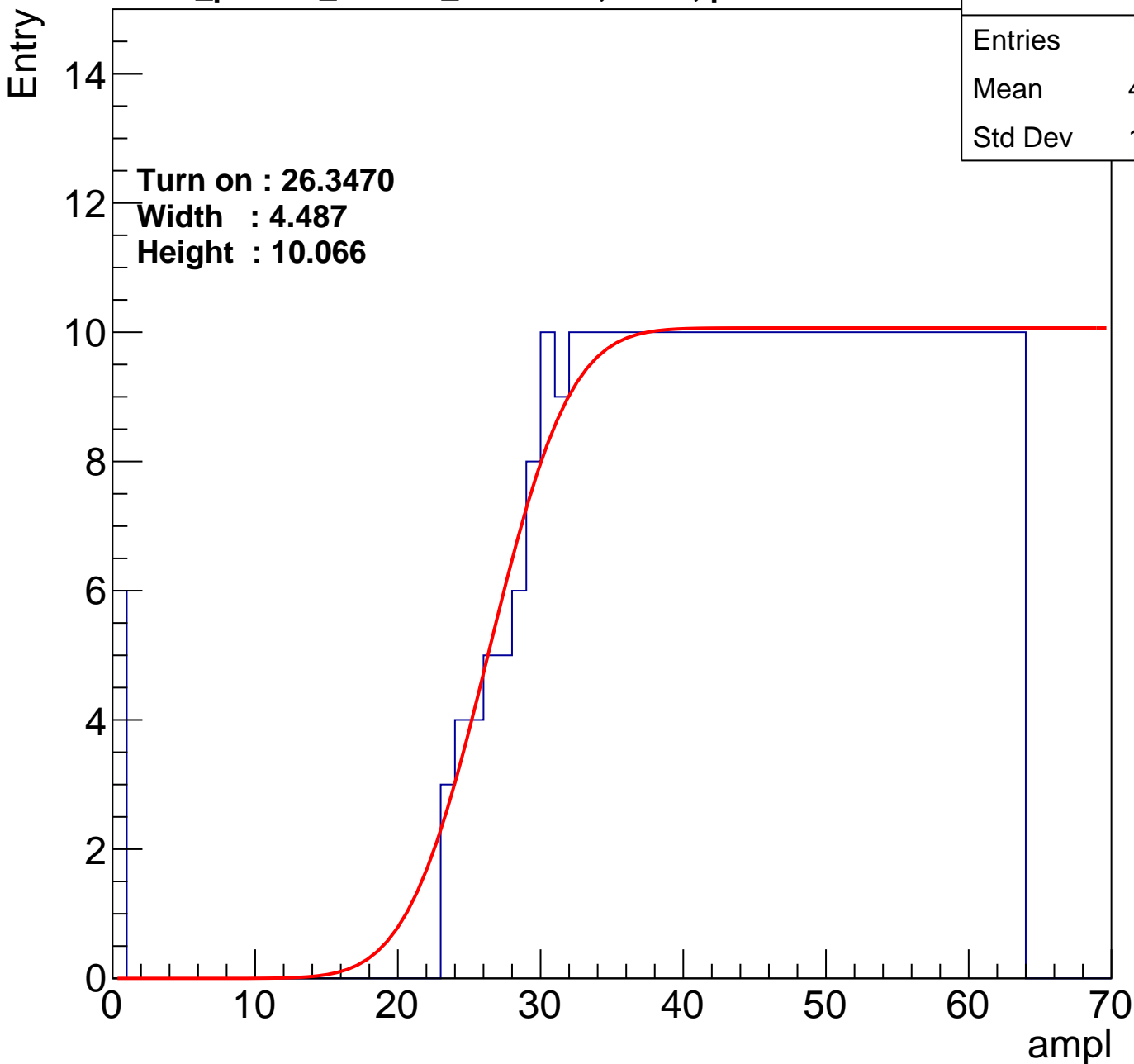
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	380
Mean	43.97
Std Dev	12.25

**Turn on : 26.3470**

**Width : 4.487**

**Height : 10.066**



# B0L000S, U19-ch56

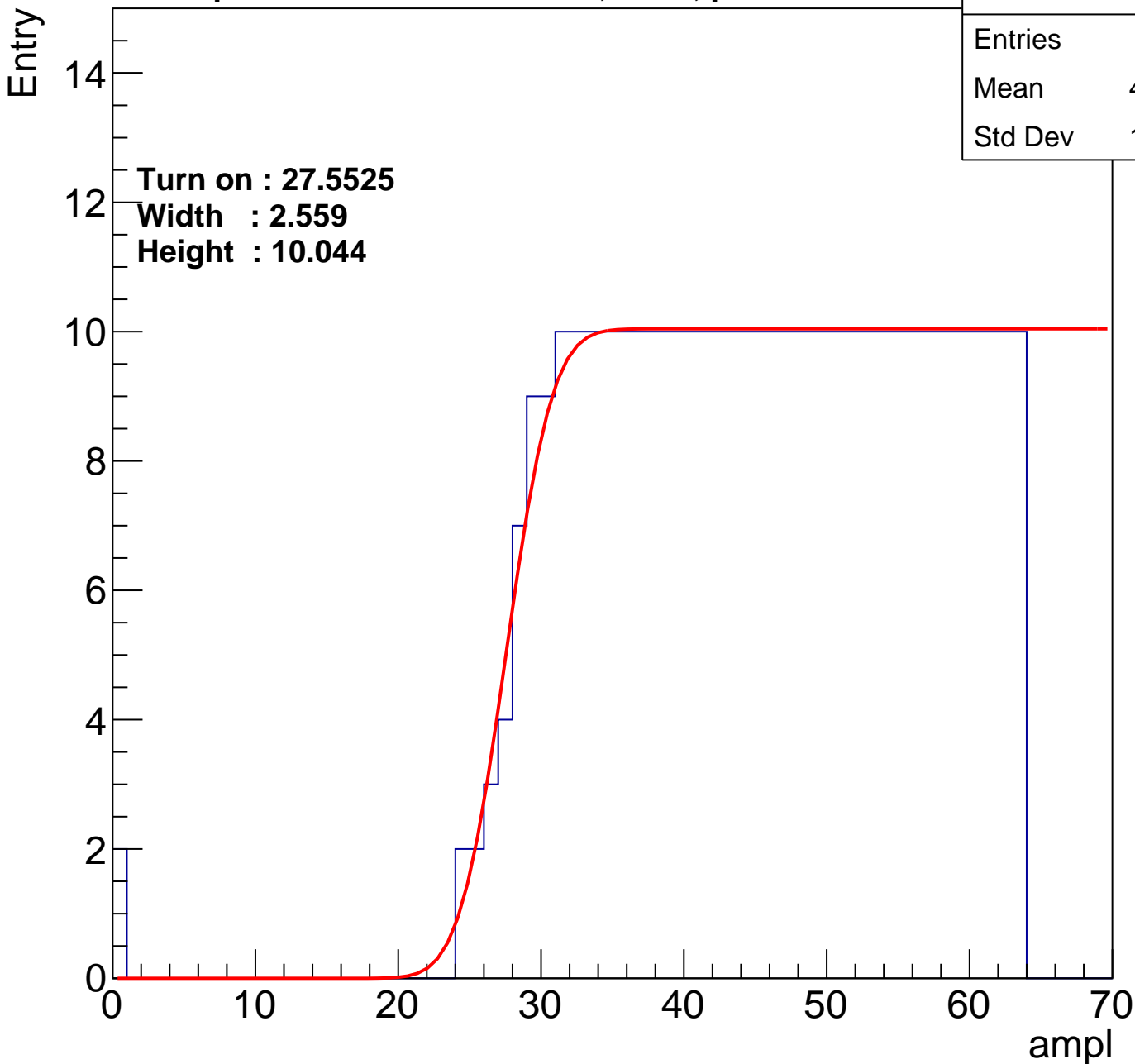
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	368
Mean	44.89
Std Dev	11.14

**Turn on : 27.5525**

**Width : 2.559**

**Height : 10.044**



# B0L000S, U19-ch57

calib\_packv5\_042523\_0143.root, FC#5, port B1

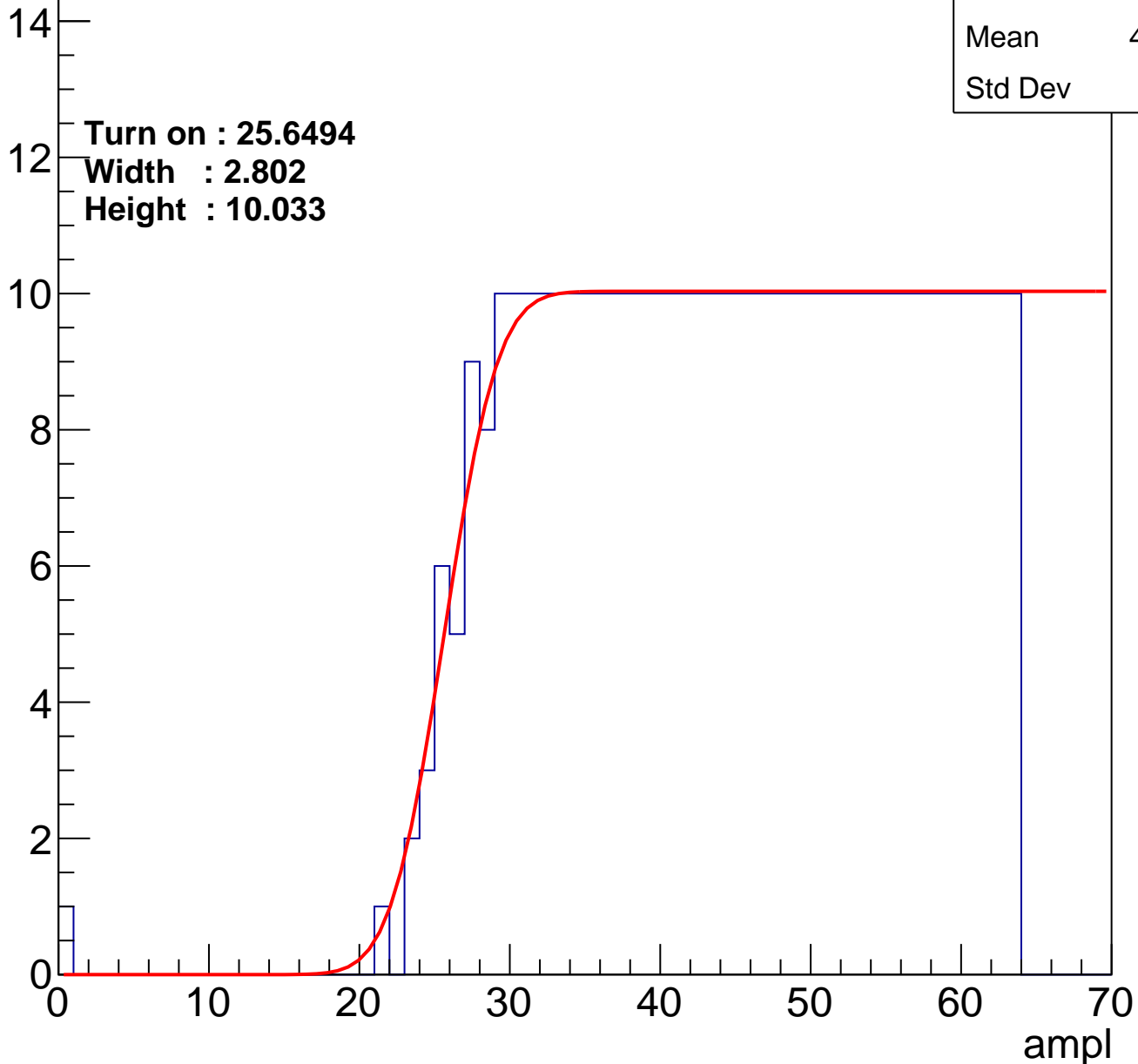
Entries	385
Mean	44.12
Std Dev	11.4

**Turn on : 25.6494**

**Width : 2.802**

**Height : 10.033**

Entry



# B0L000S, U19-ch58

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.39
Std Dev	10.73

Turn on : 28.3732

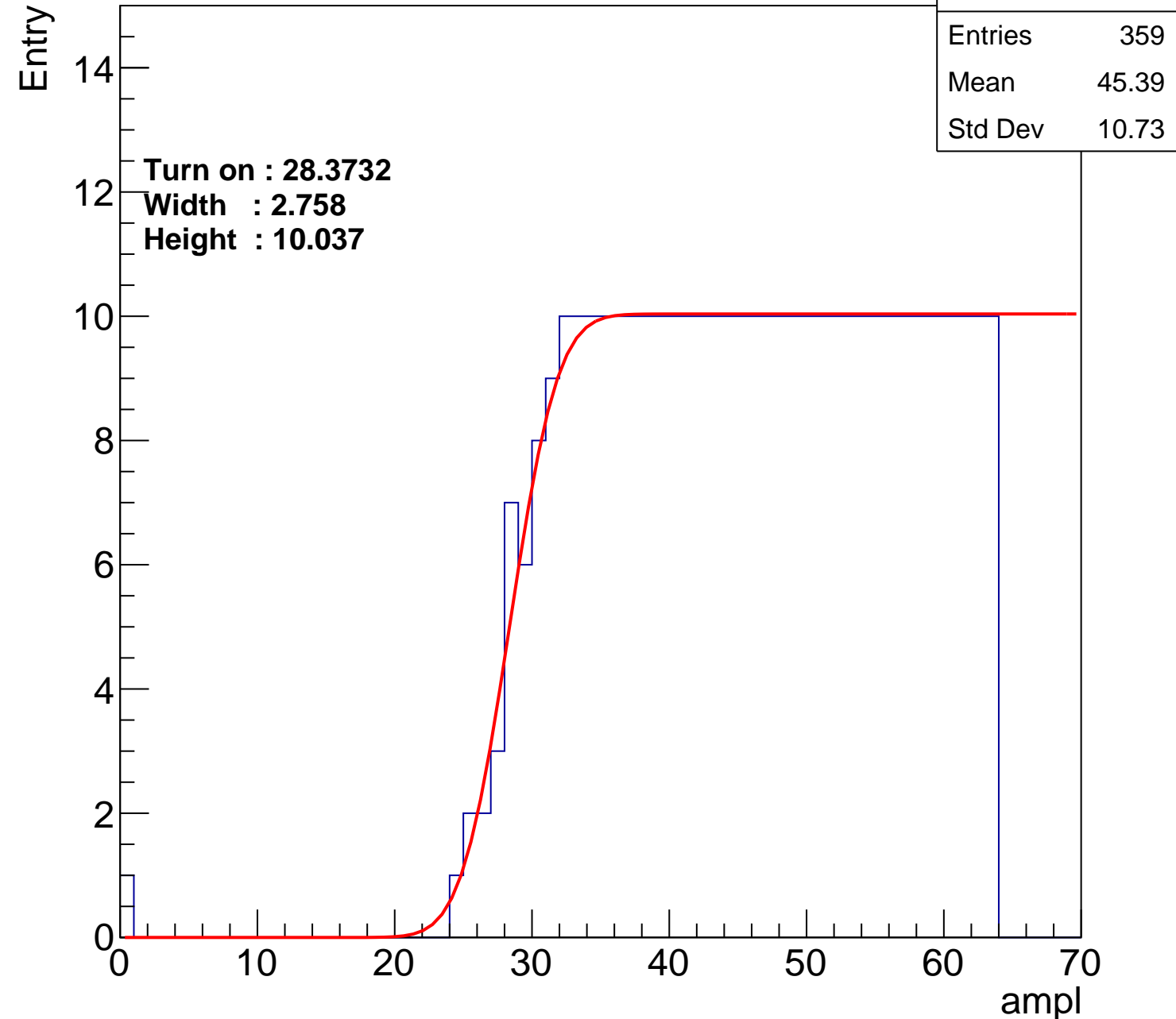
Width : 2.758

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	374
Mean	44.6
Std Dev	11.29

Std Dev	11.29
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**Height : 10.017**



# B0L000S, U19-ch60

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.99
Std Dev	10.95

**Turn on : 27.2877**

**Width : 1.873**

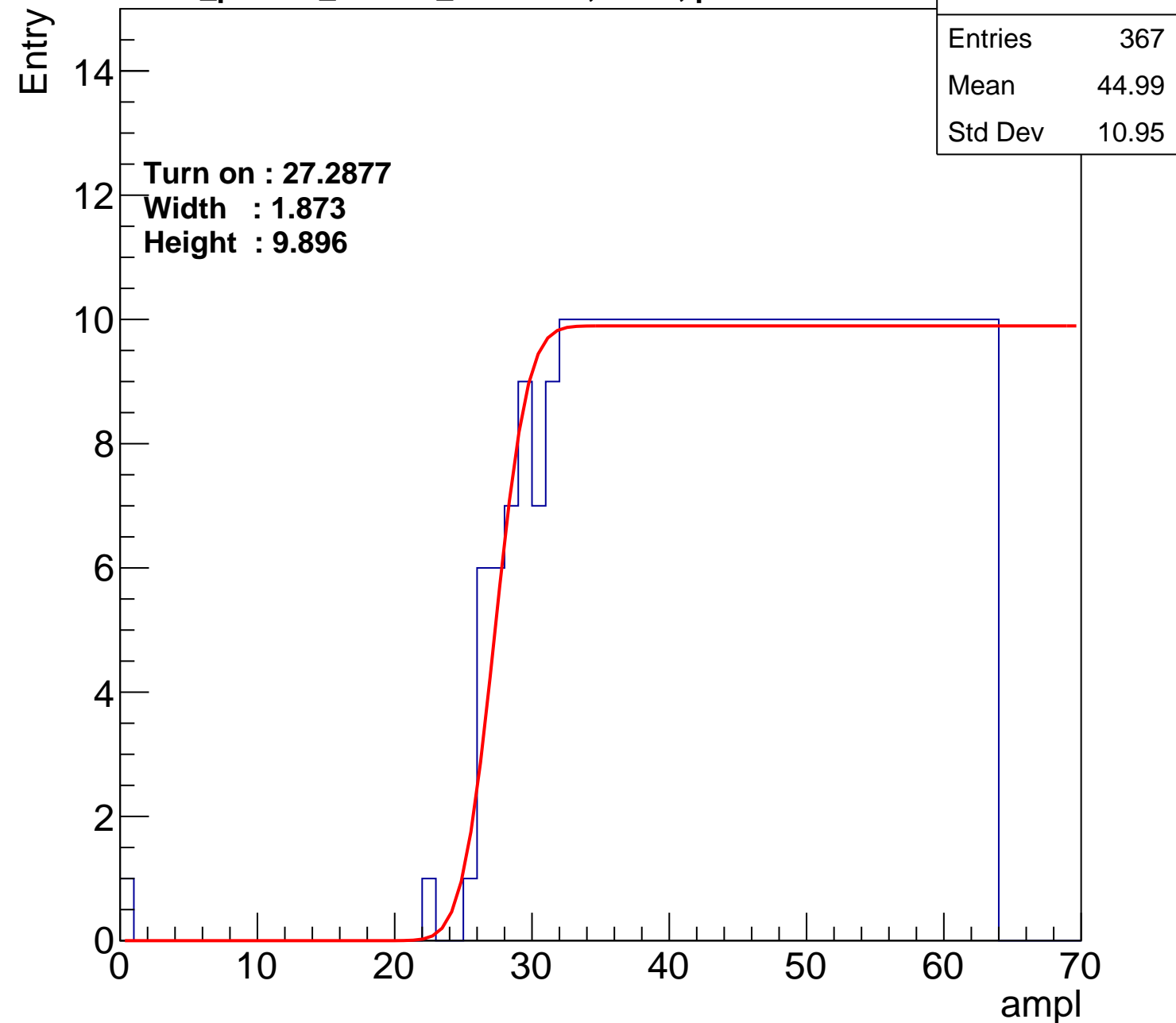
**Height : 9.896**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch61

calib\_packv5\_042523\_0143.root, FC#5, port B1

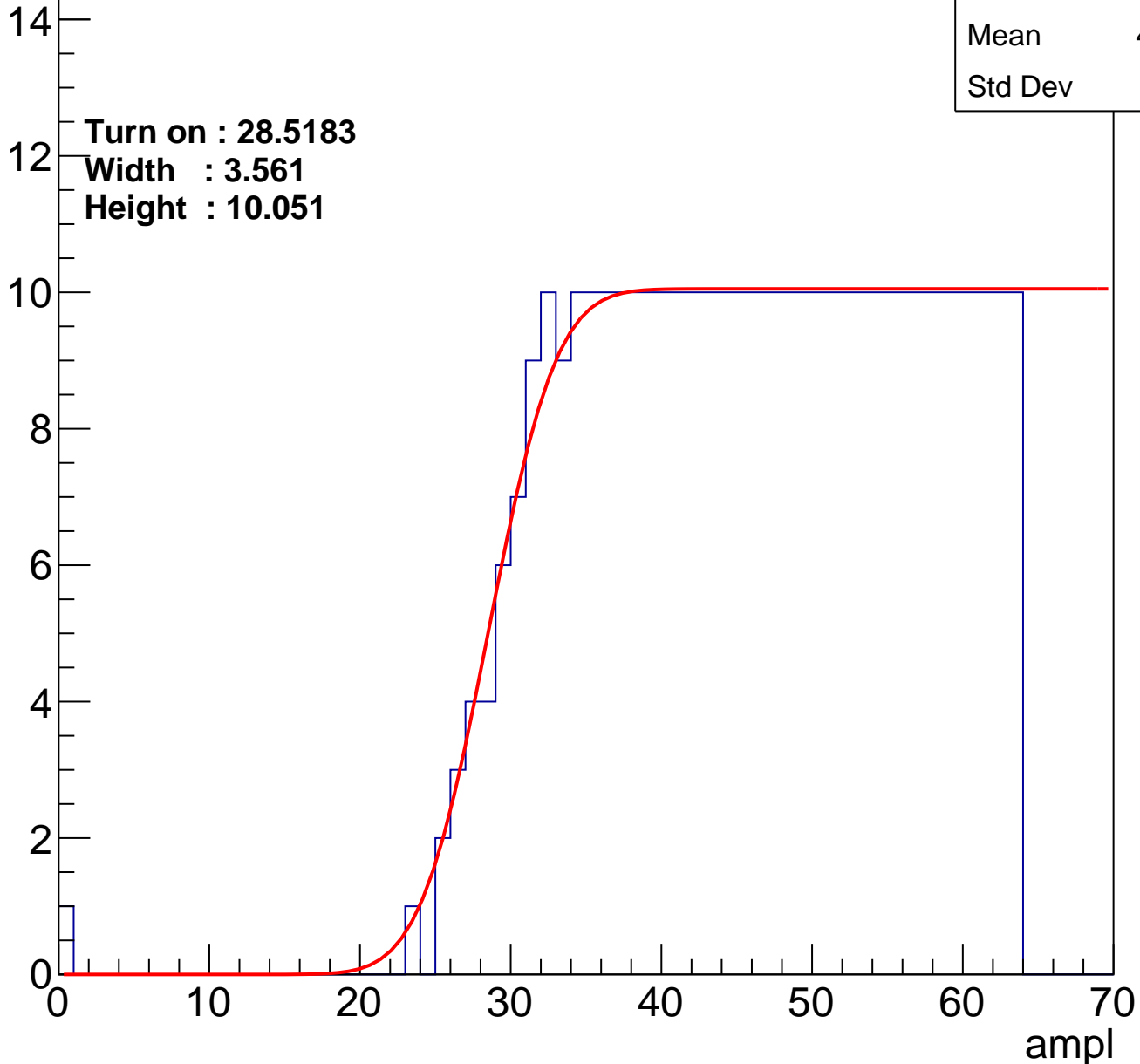
Entries	356
Mean	45.51
Std Dev	10.7

**Turn on : 28.5183**

**Width : 3.561**

**Height : 10.051**

Entry



# B0L000S, U19-ch62

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.48
Std Dev	11.2

**Turn on : 26.7083**

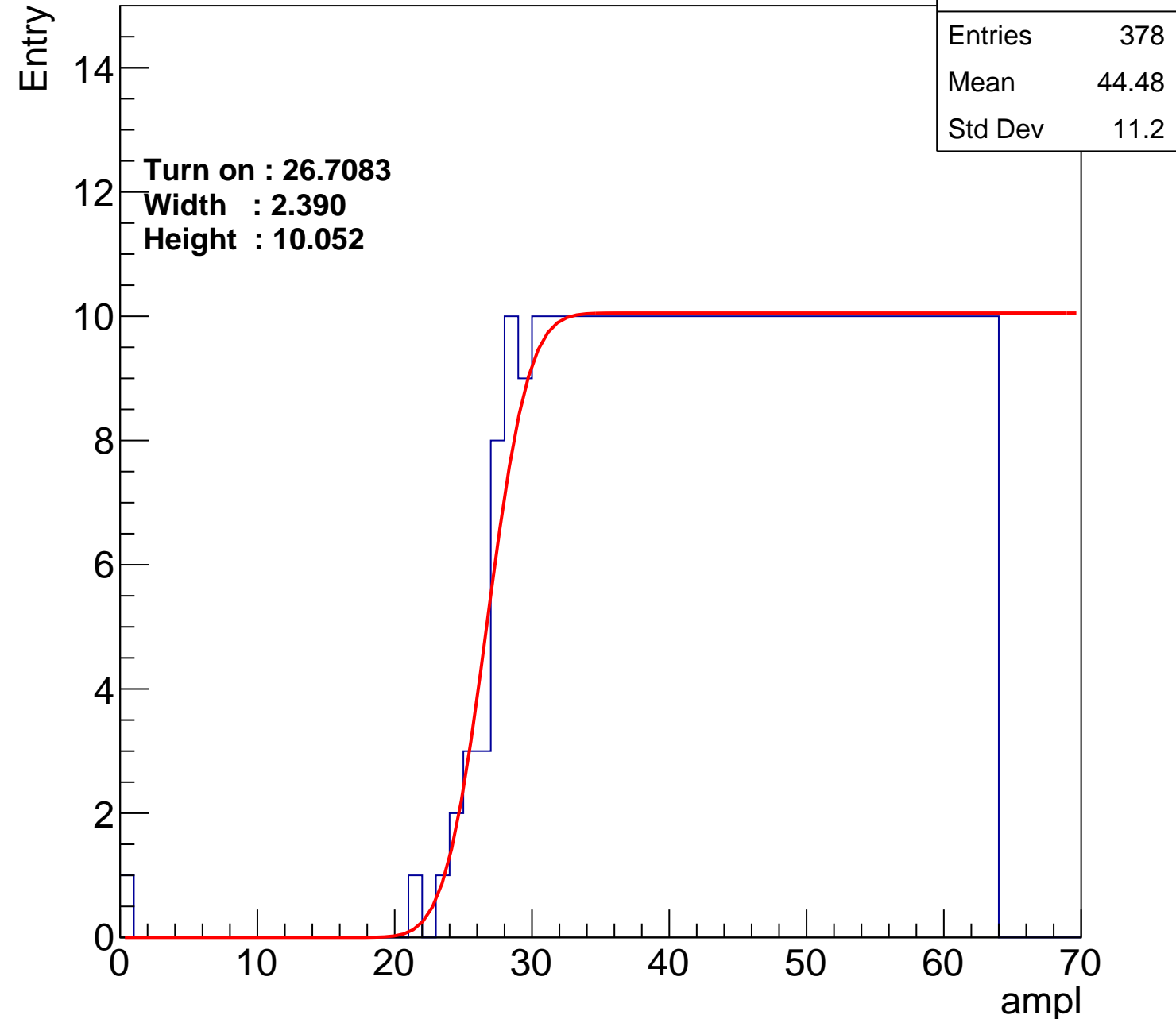
**Width : 2.390**

**Height : 10.052**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch63

calib\_packv5\_042523\_0143.root, FC#5, port B1

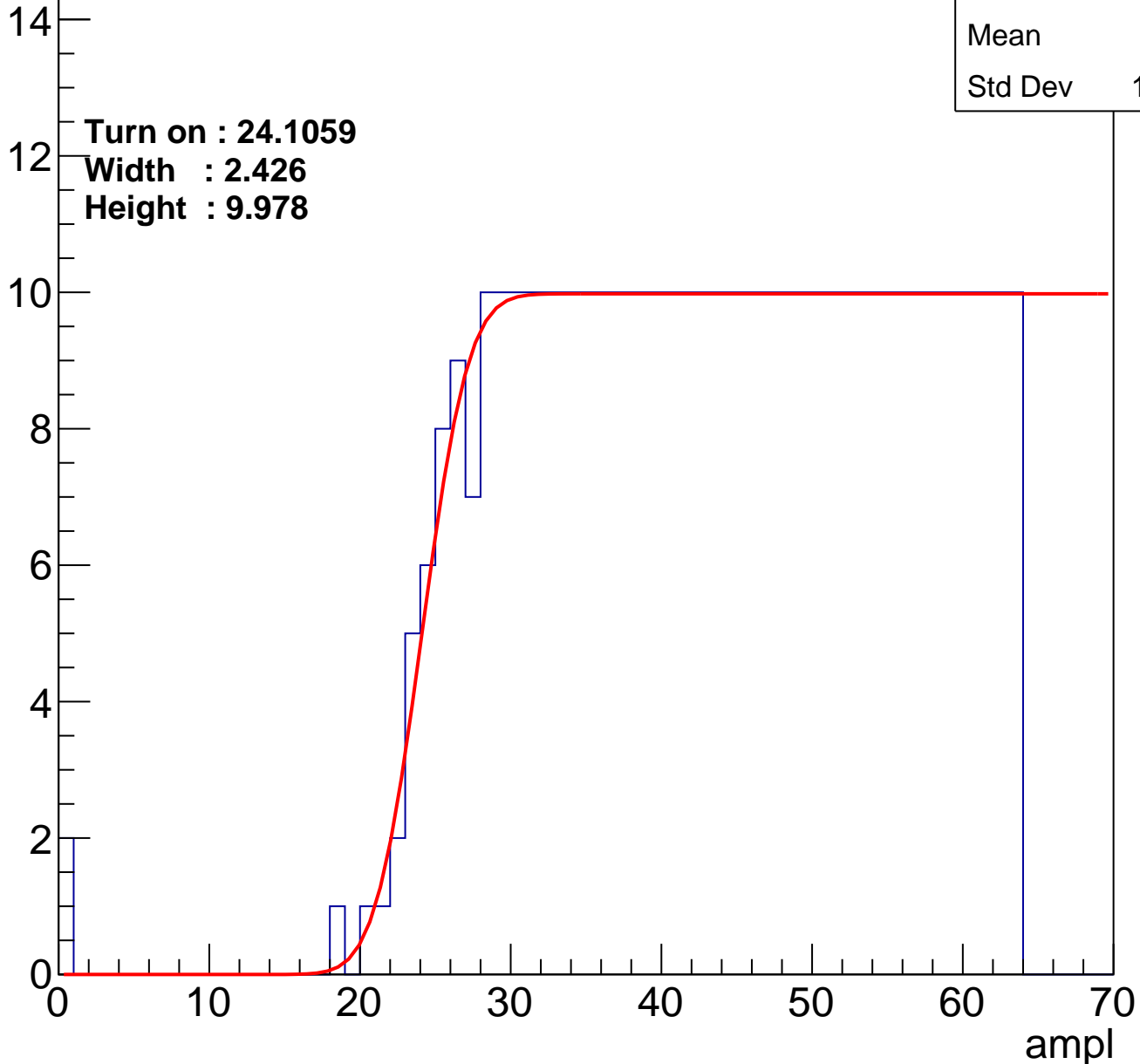
Entries	402
Mean	43.2
Std Dev	12.06

Turn on : 24.1059

Width : 2.426

Height : 9.978

Entry



# B0L000S, U19-ch64

calib\_packv5\_042523\_0143.root, FC#5, port B1

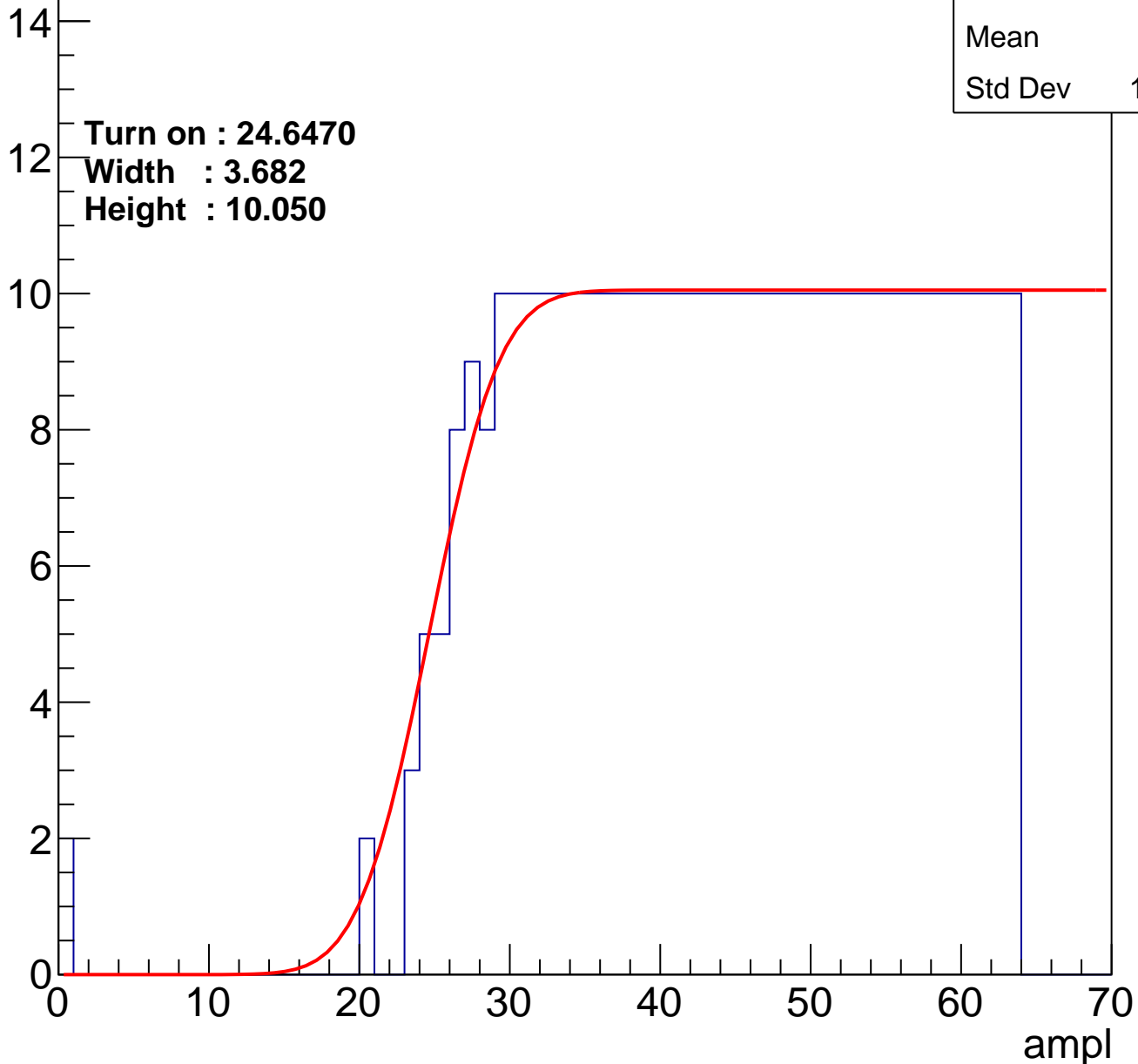
Entries	392
Mean	43.7
Std Dev	11.79

**Turn on : 24.6470**

**Width : 3.682**

**Height : 10.050**

Entry



# B0L000S, U19-ch65

calib\_packv5\_042523\_0143.root, FC#5, port B1

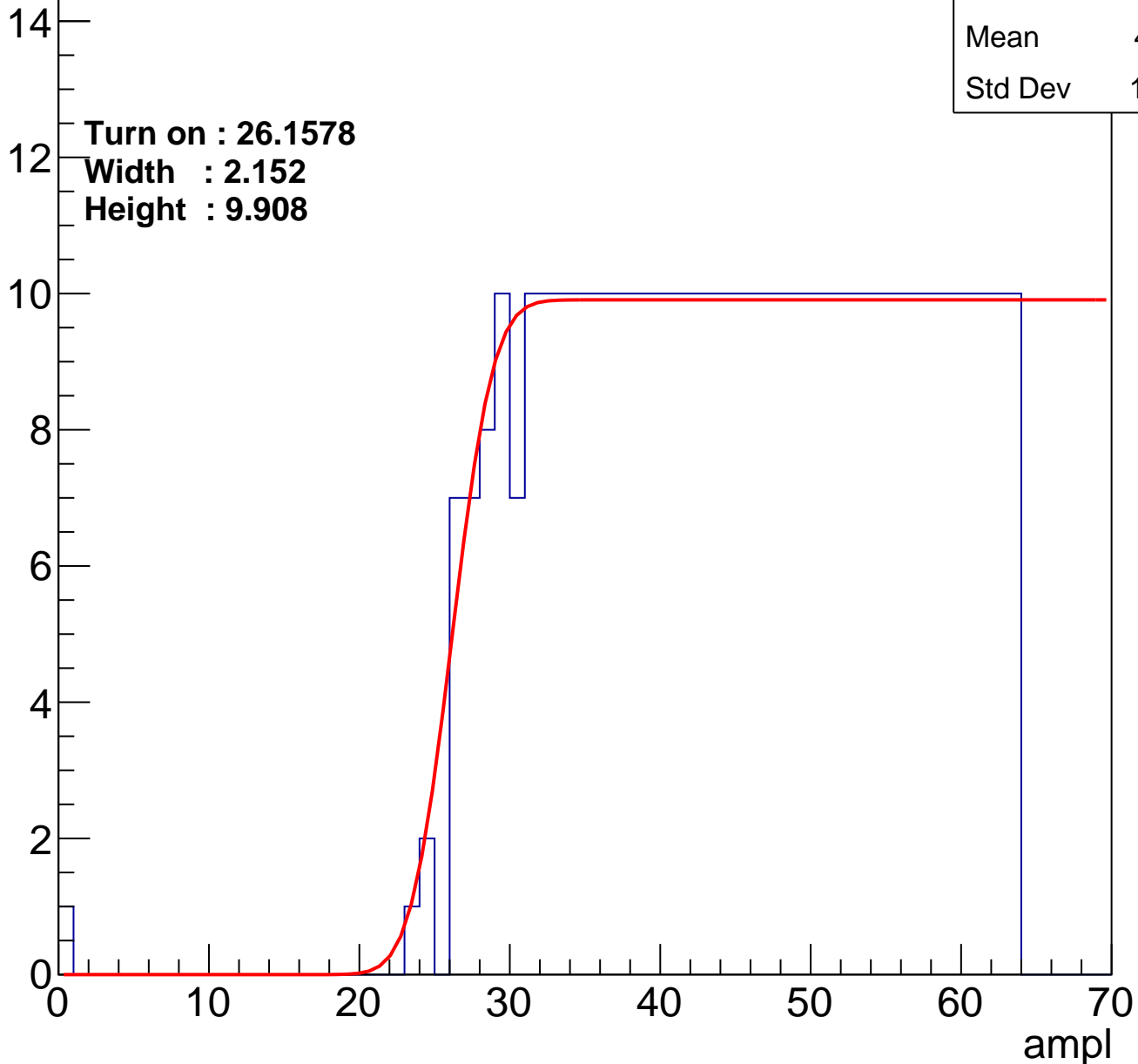
Entries	373
Mean	44.71
Std Dev	11.09

**Turn on : 26.1578**

**Width : 2.152**

**Height : 9.908**

Entry



# B0L000S, U19-ch66

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.96
Std Dev	11.45

Turn on : 25.0592

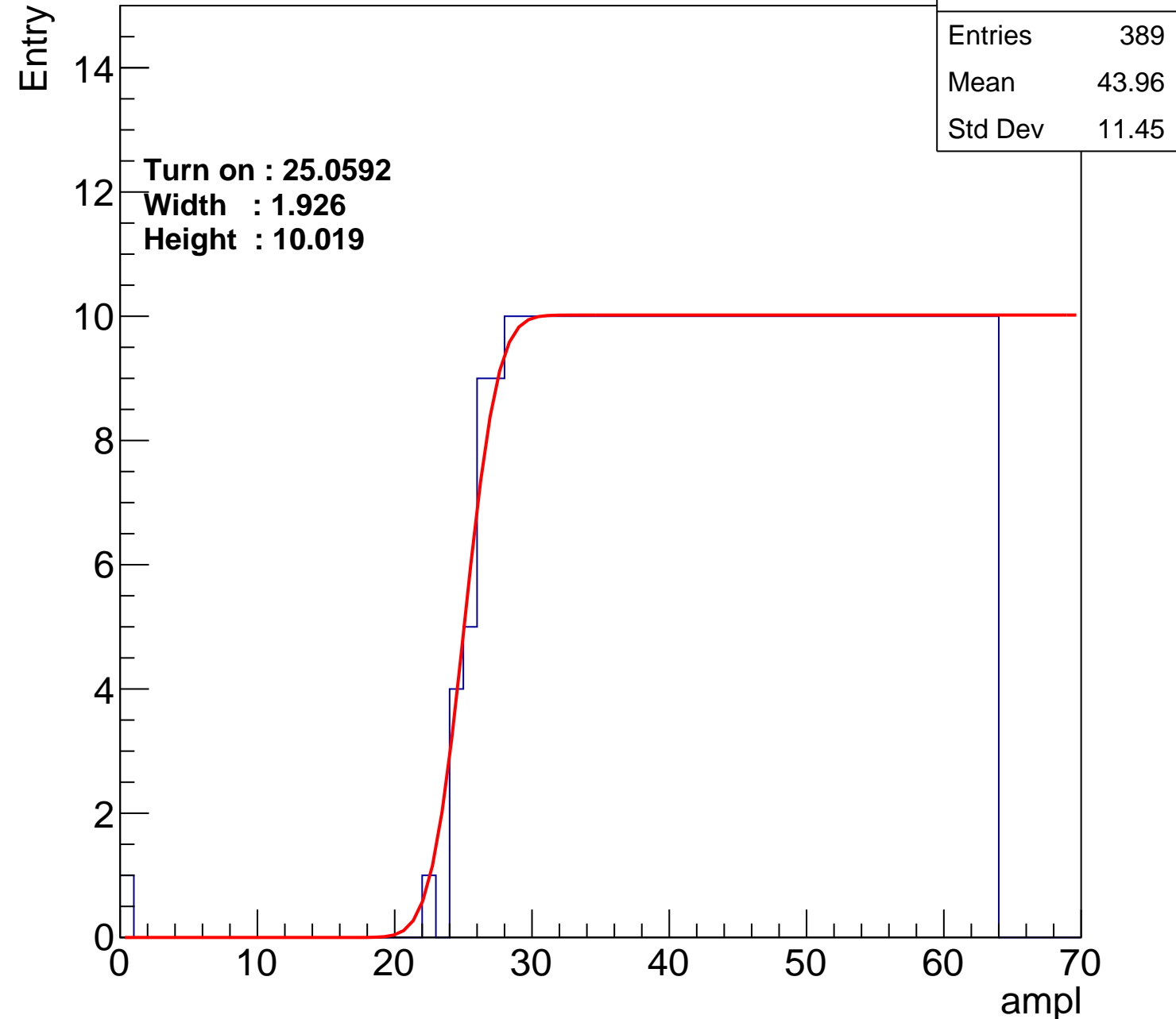
Width : 1.926

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch67

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.45
Std Dev	11.7

Turn on : 27.2541

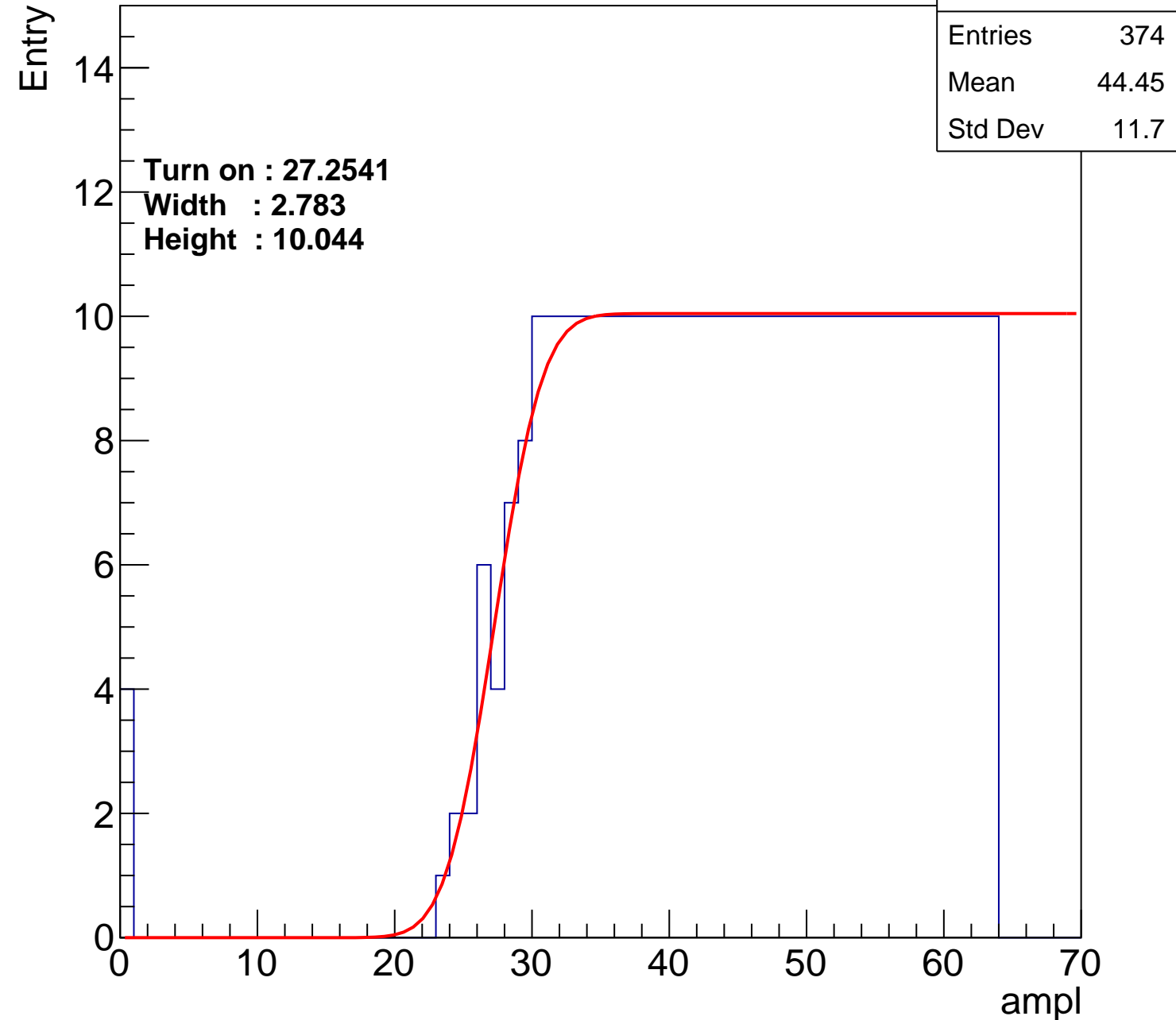
Width : 2.783

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch68

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.27
Std Dev	11.52

Turn on : 26.5846

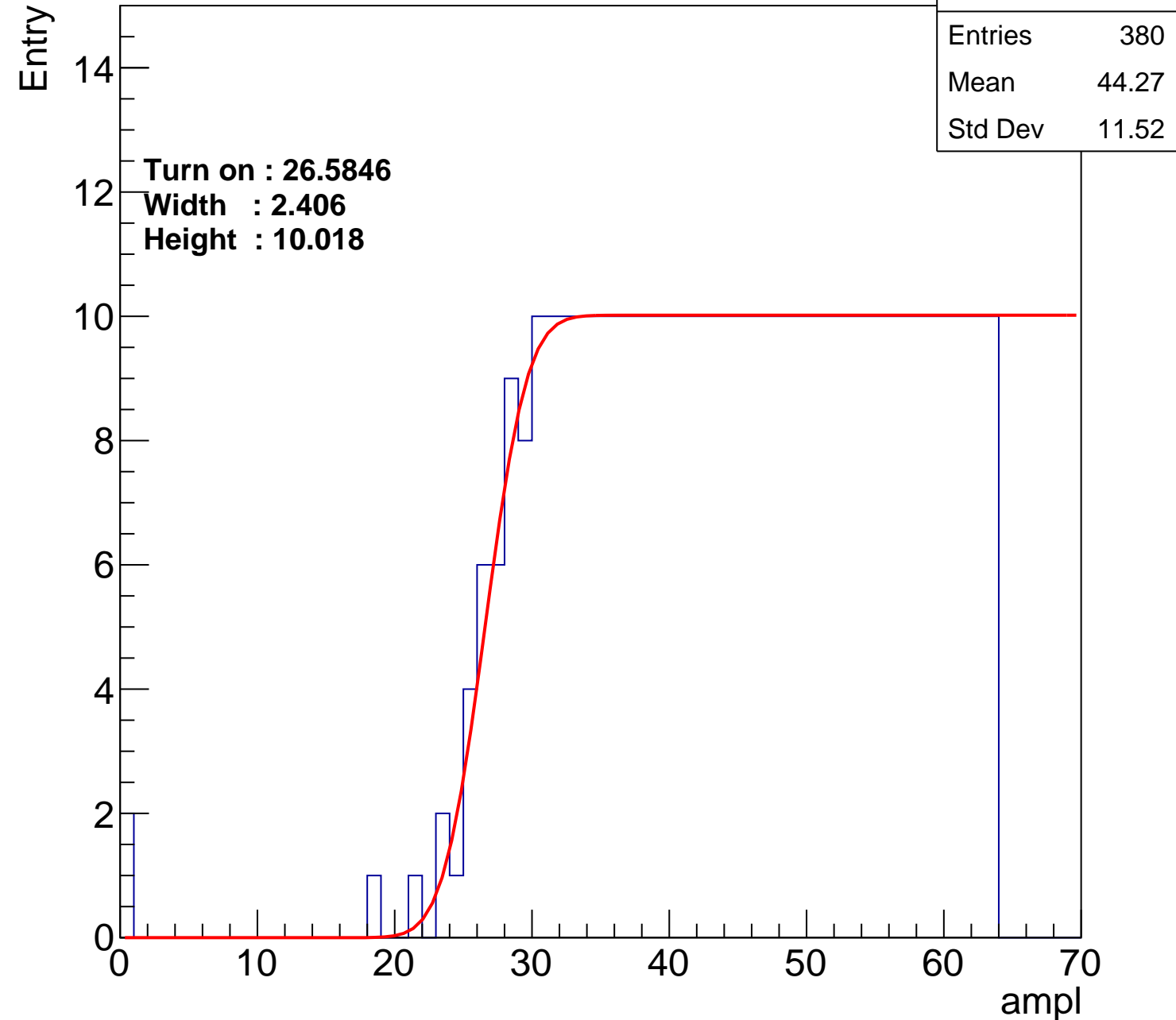
Width : 2.406

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch69

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.65
Std Dev	11.19

Turn on : 27.0261

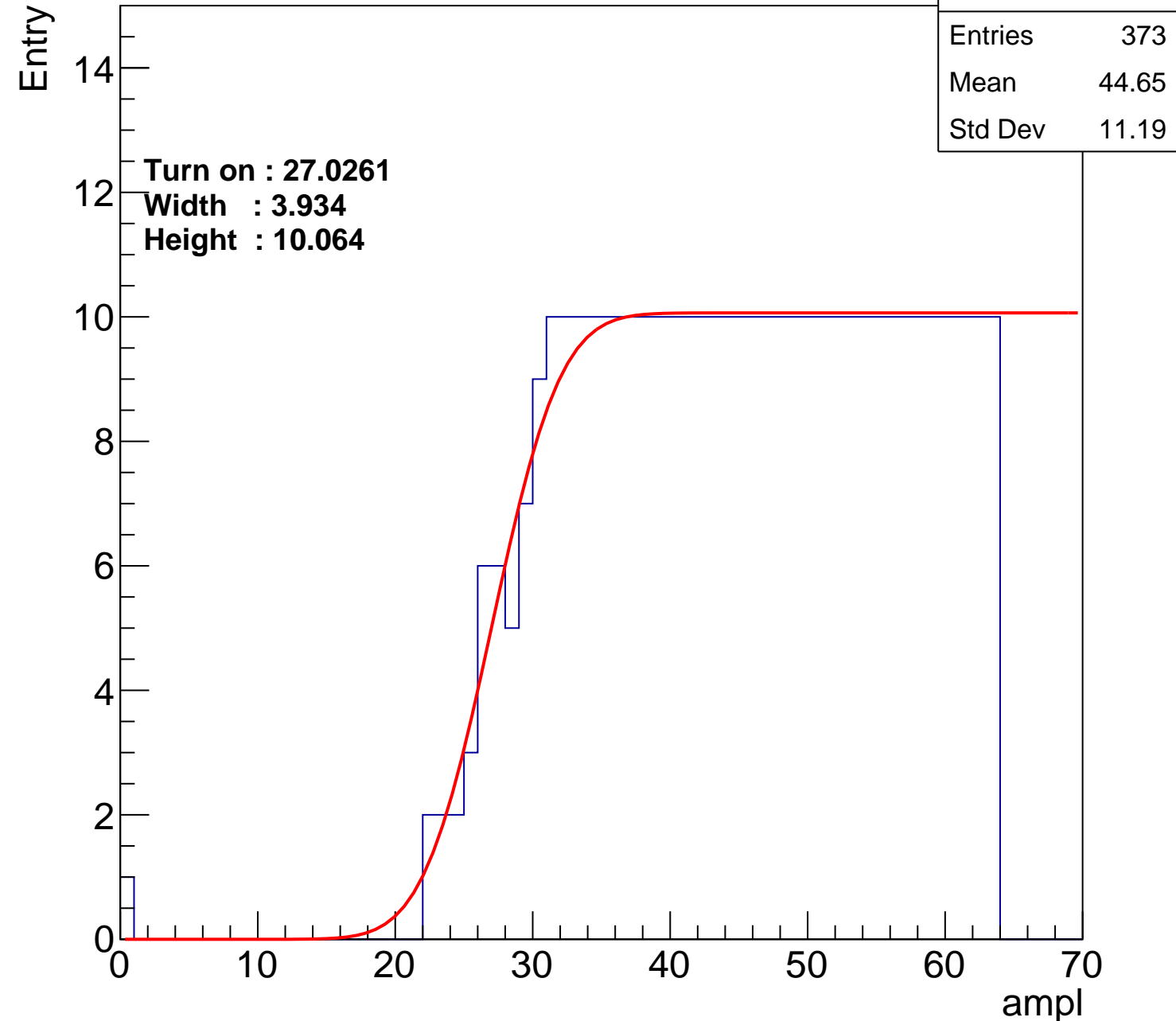
Width : 3.934

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch70

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.82
Std Dev	11.08

**Turn on : 27.5402**

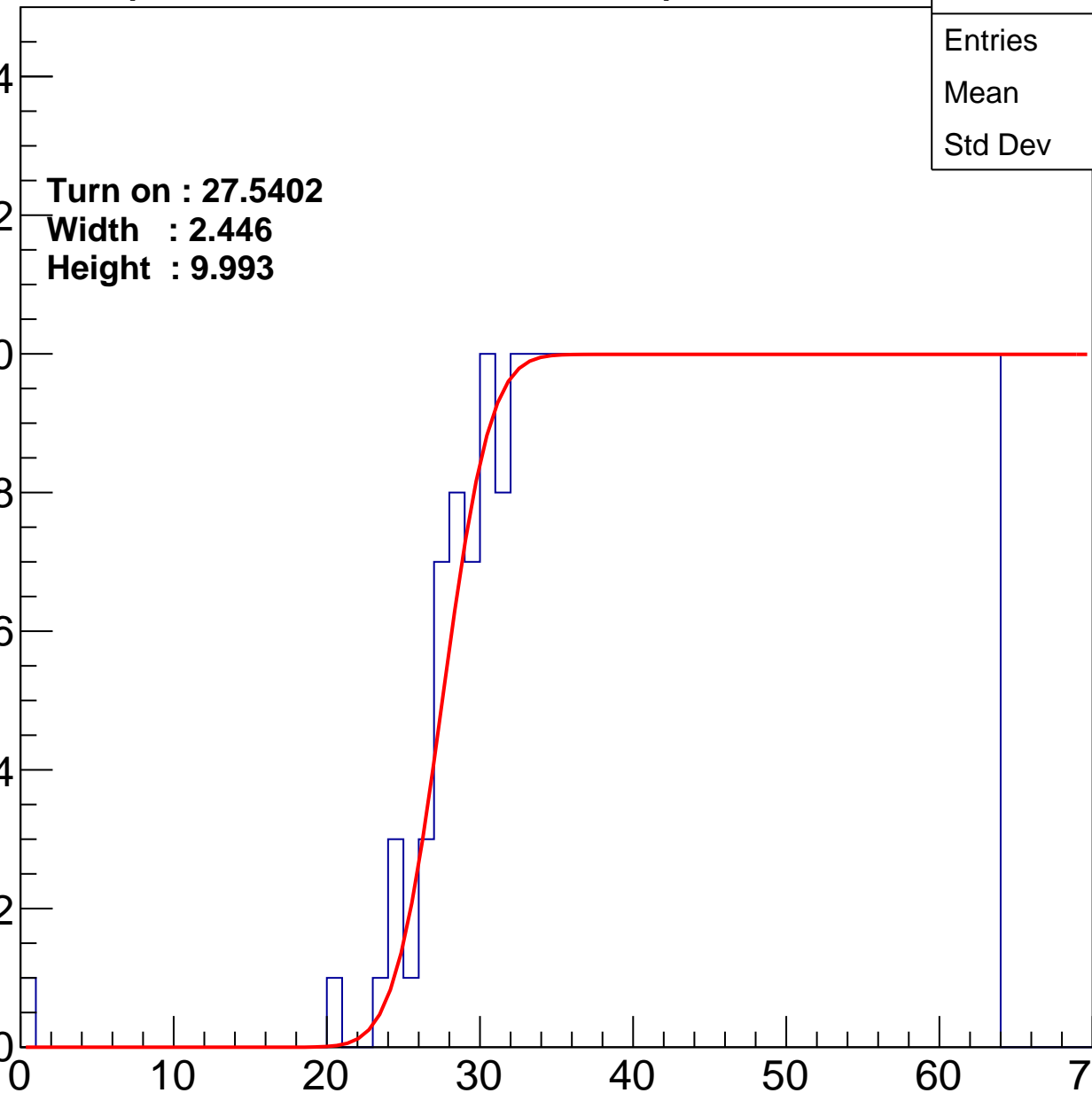
**Width : 2.446**

**Height : 9.993**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch71

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	400
Mean	43.37
Std Dev	11.83

**Turn on : 24.3807**

**Width : 2.664**

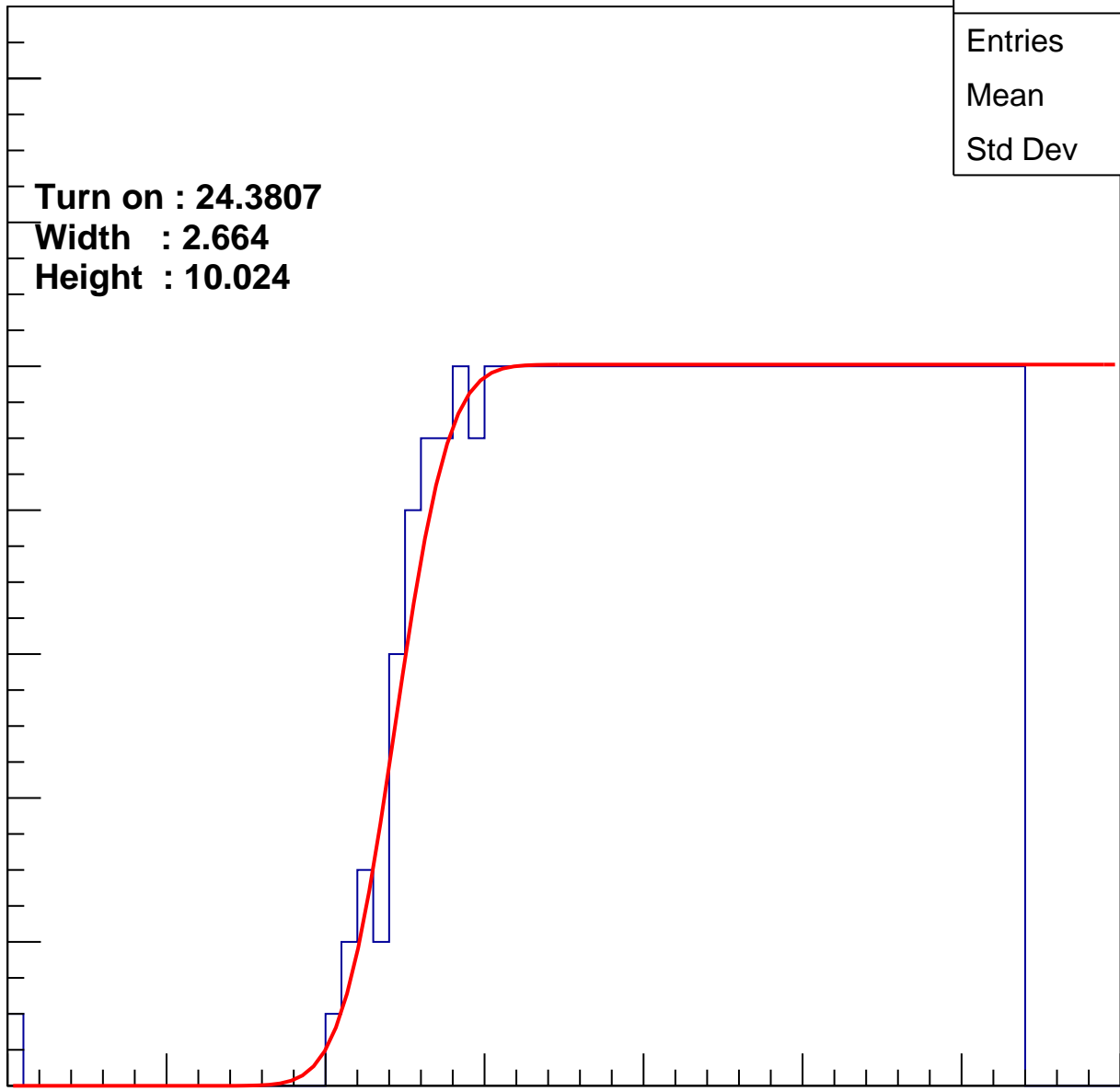
**Height : 10.024**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U19-ch72

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.84
Std Dev	11.59

Turn on : 25.6436

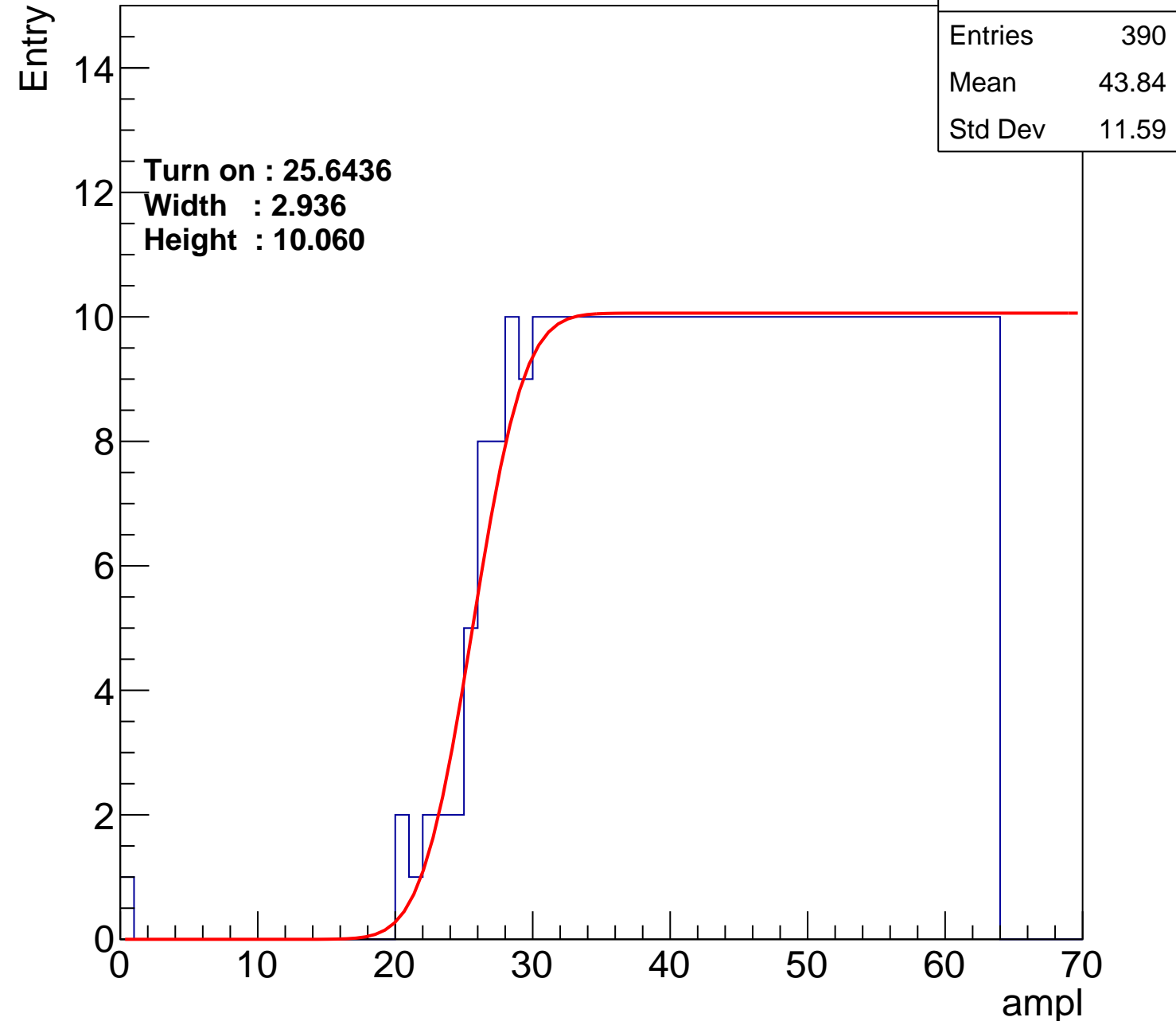
Width : 2.936

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch73

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.23
Std Dev	11.7

**Turn on : 26.4953**

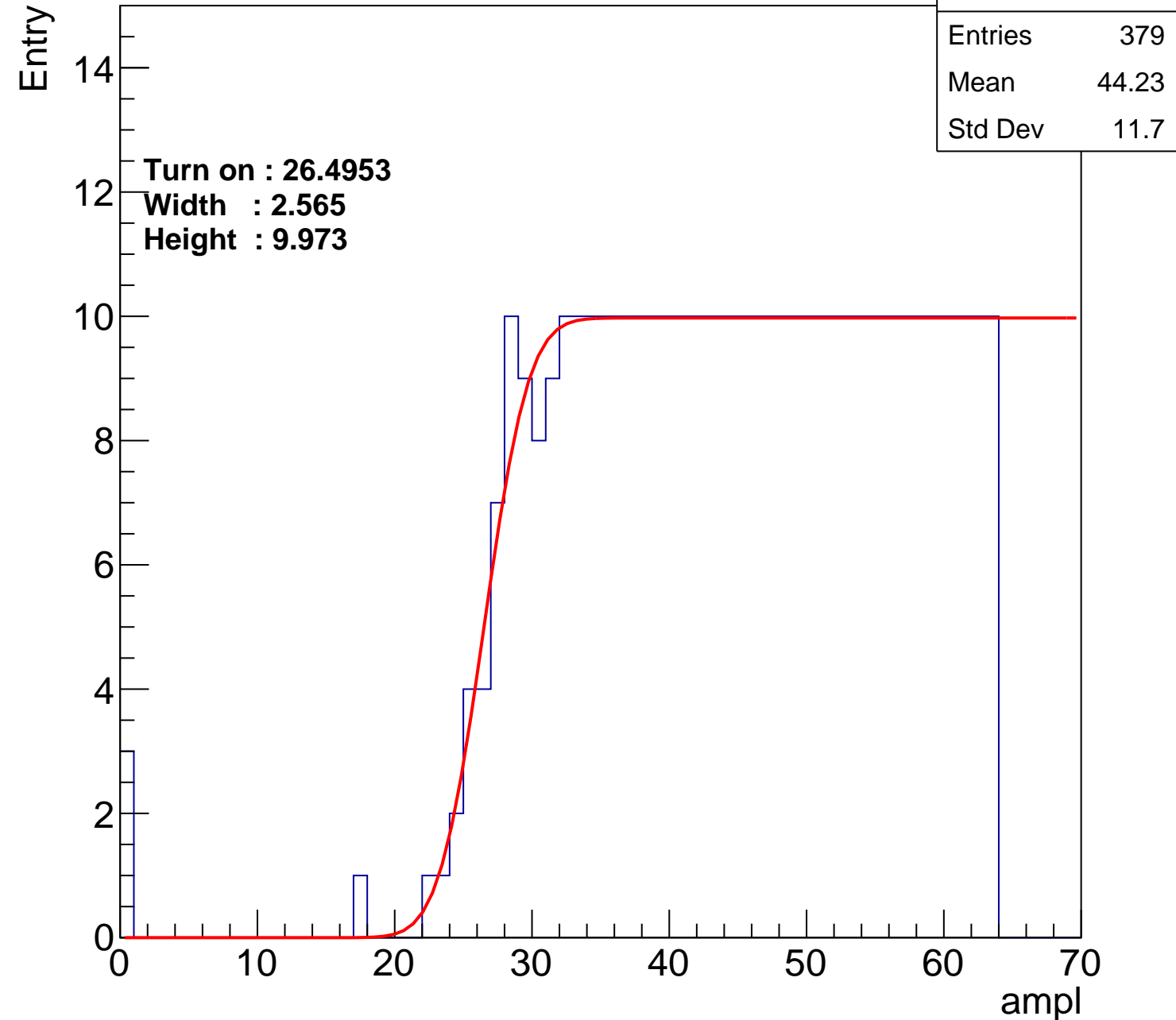
**Width : 2.565**

**Height : 9.973**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch74

calib\_packv5\_042523\_0143.root, FC#5, port B1

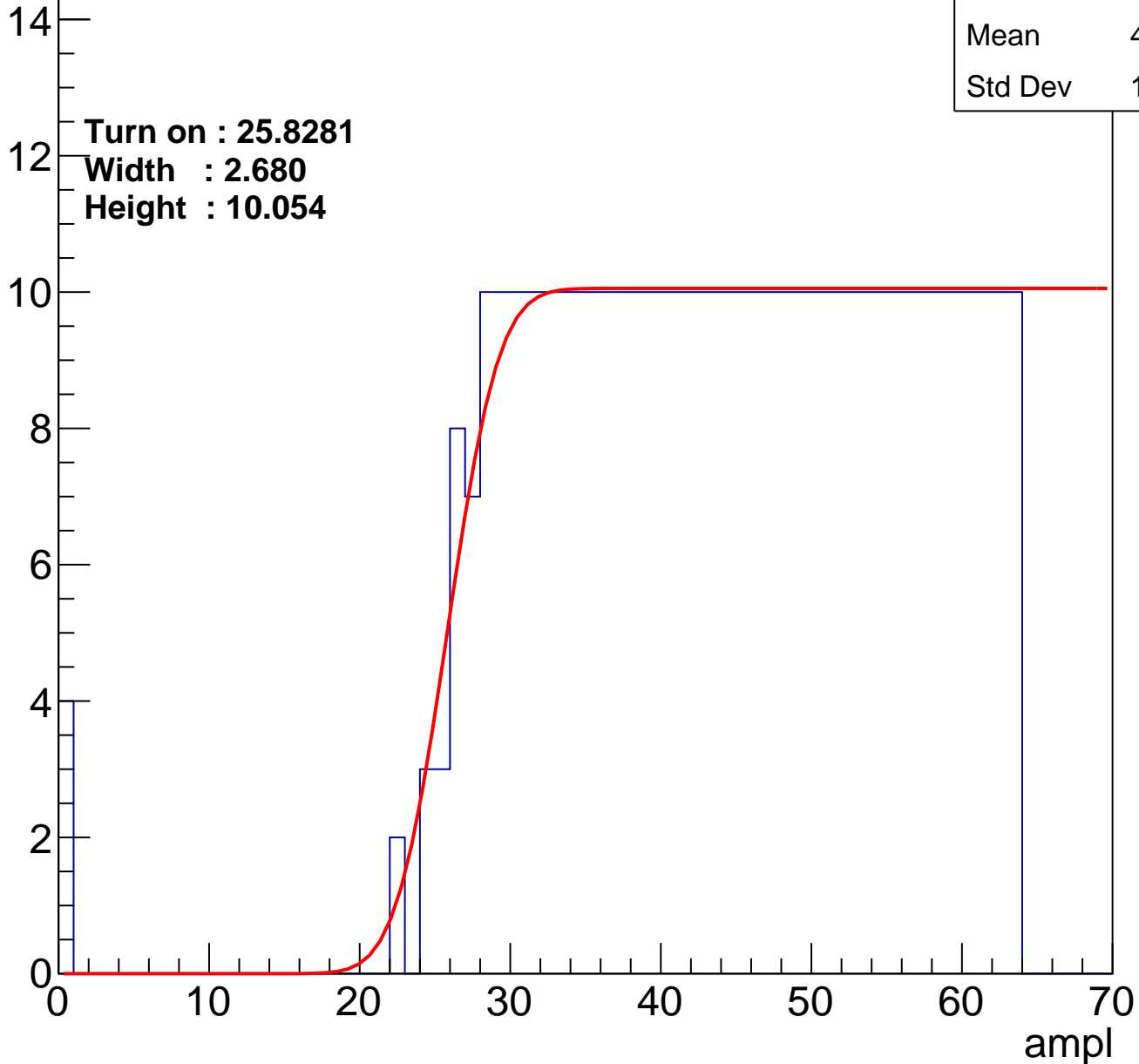
Entries	387
Mean	43.84
Std Dev	11.95

Turn on : 25.8281

Width : 2.680

Height : 10.054

Entry



# B0L000S, U19-ch75

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.21
Std Dev	11.97

Turn on : 27.2785

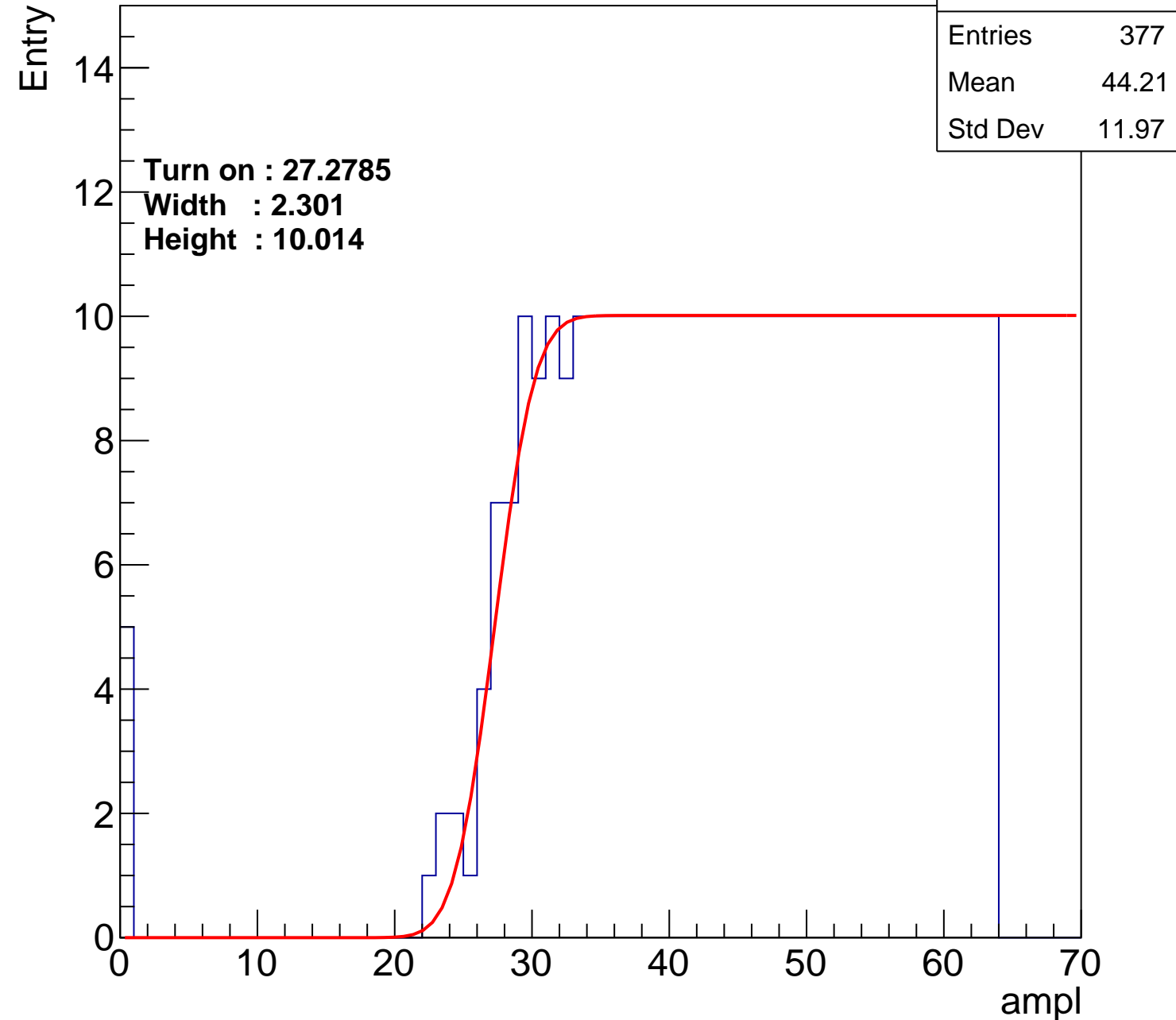
Width : 2.301

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch76

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 26.9466

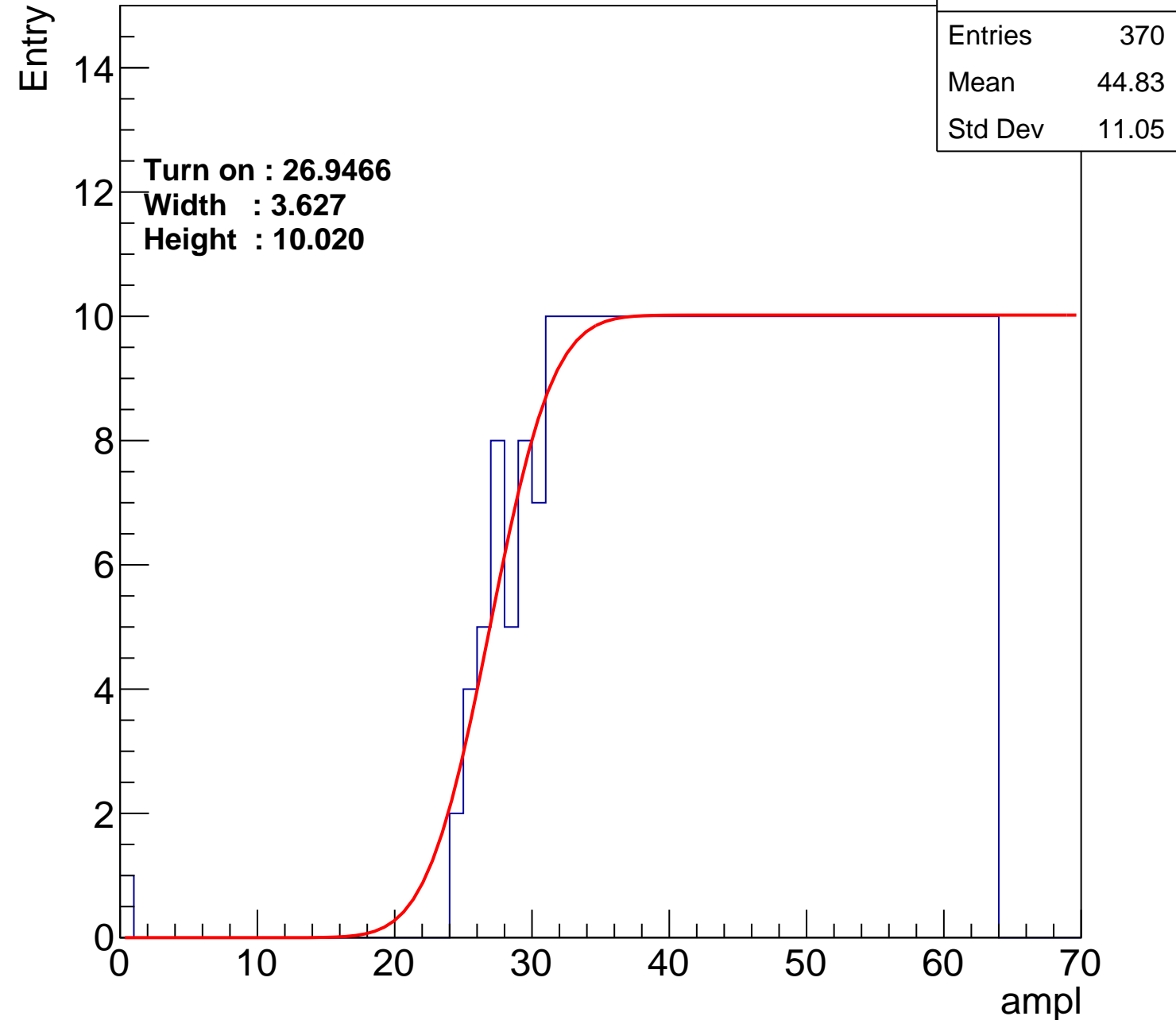
Width : 3.627

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch77

calib\_packv5\_042523\_0143.root, FC#5, port B1

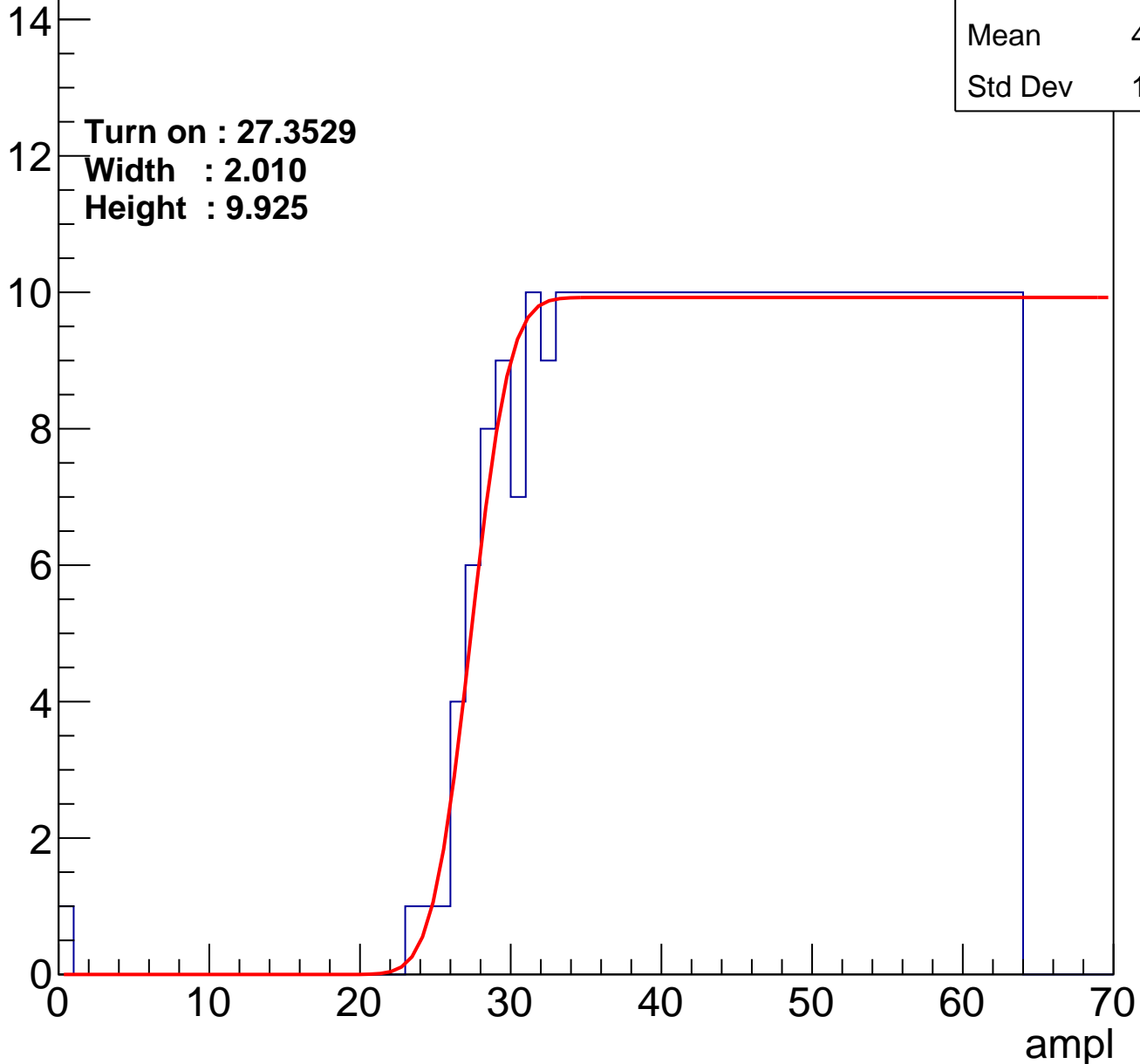
Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.3529

Width : 2.010

Height : 9.925

Entry



# B0L000S, U19-ch78

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.6216

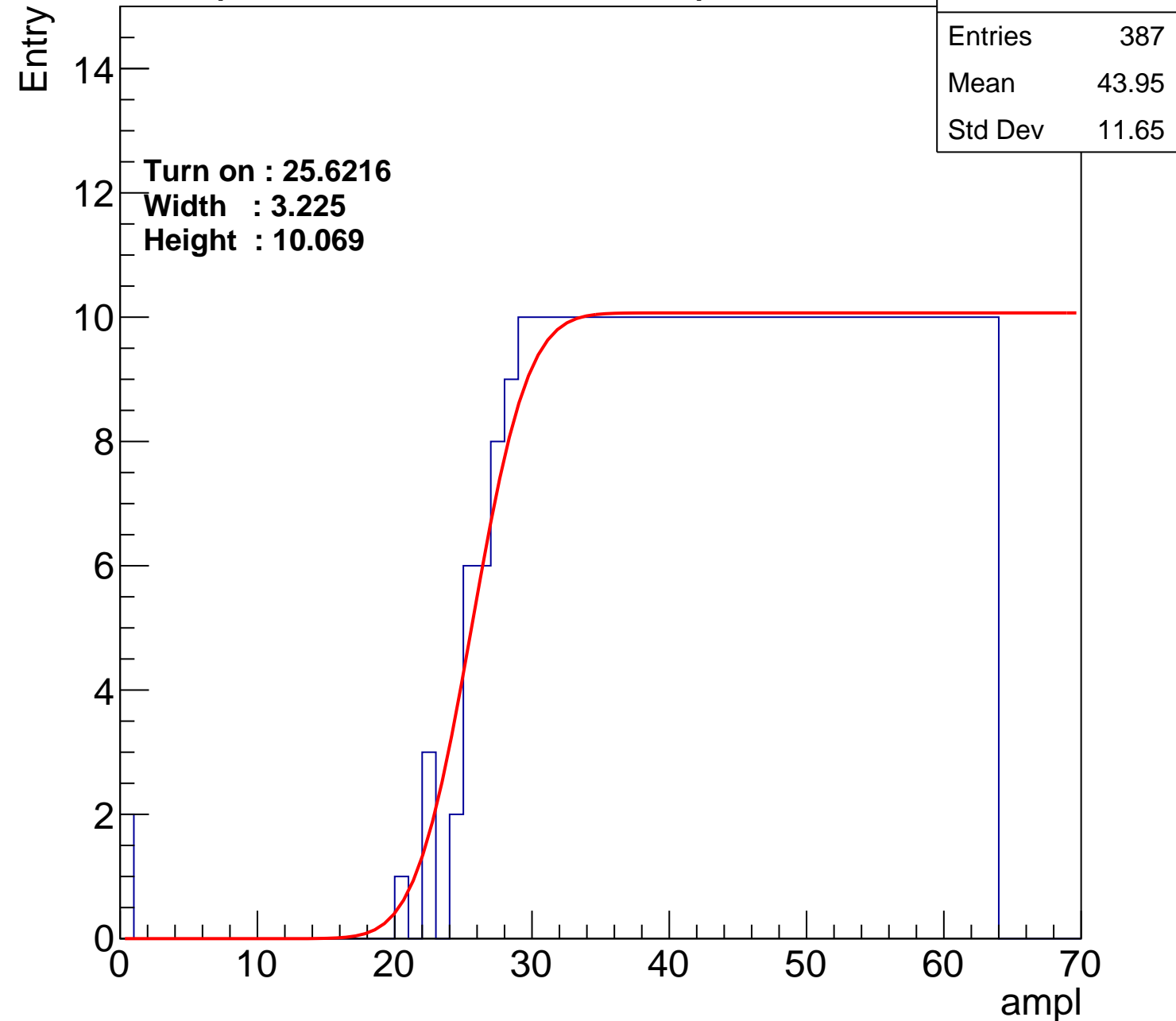
Width : 3.225

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch79

calib\_packv5\_042523\_0143.root, FC#5, port B1

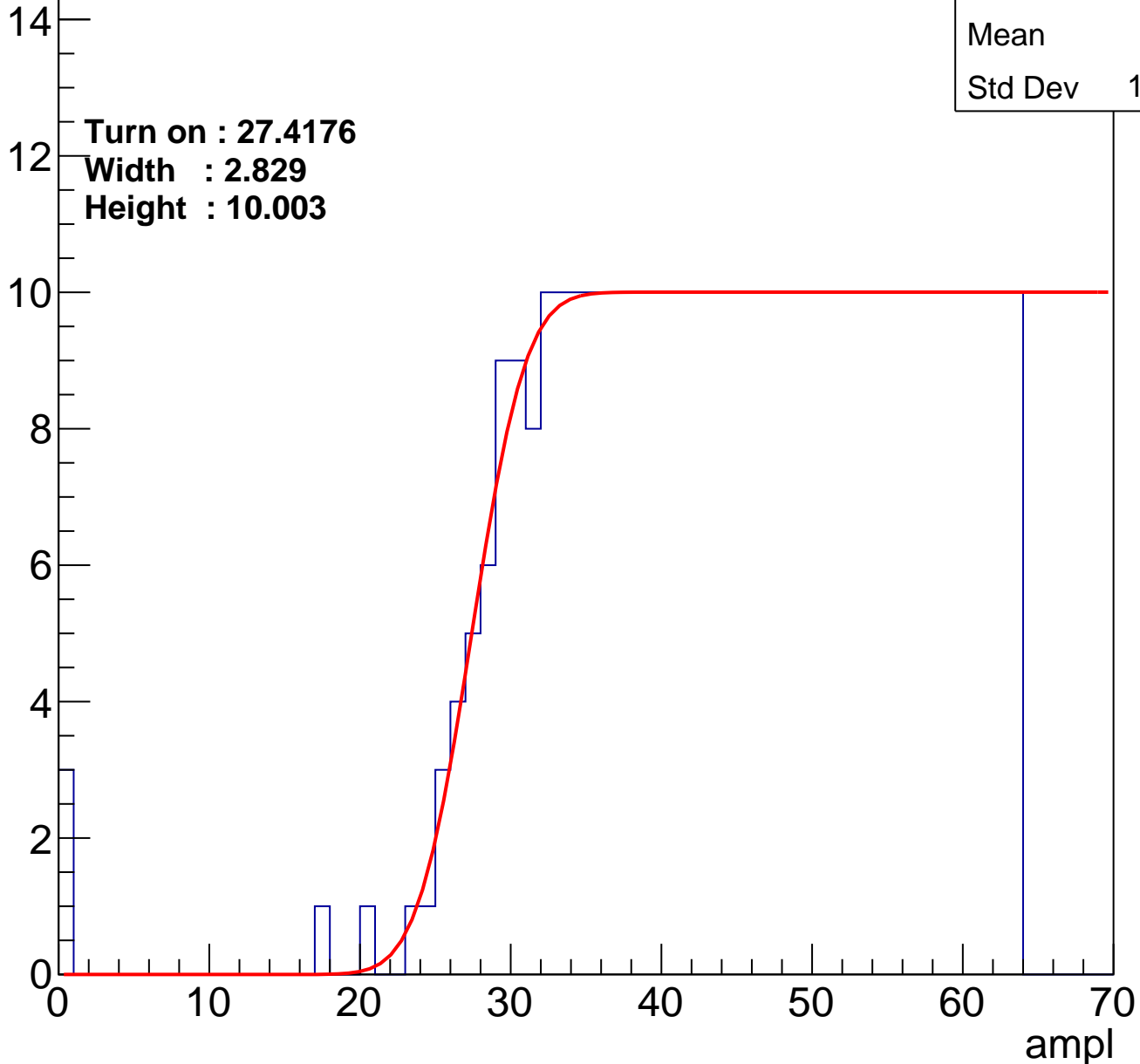
Entries	371
Mean	44.6
Std Dev	11.55

Turn on : 27.4176

Width : 2.829

Height : 10.003

Entry



# B0L000S, U19-ch80

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.61
Std Dev	11.87

Turn on : 25.1456

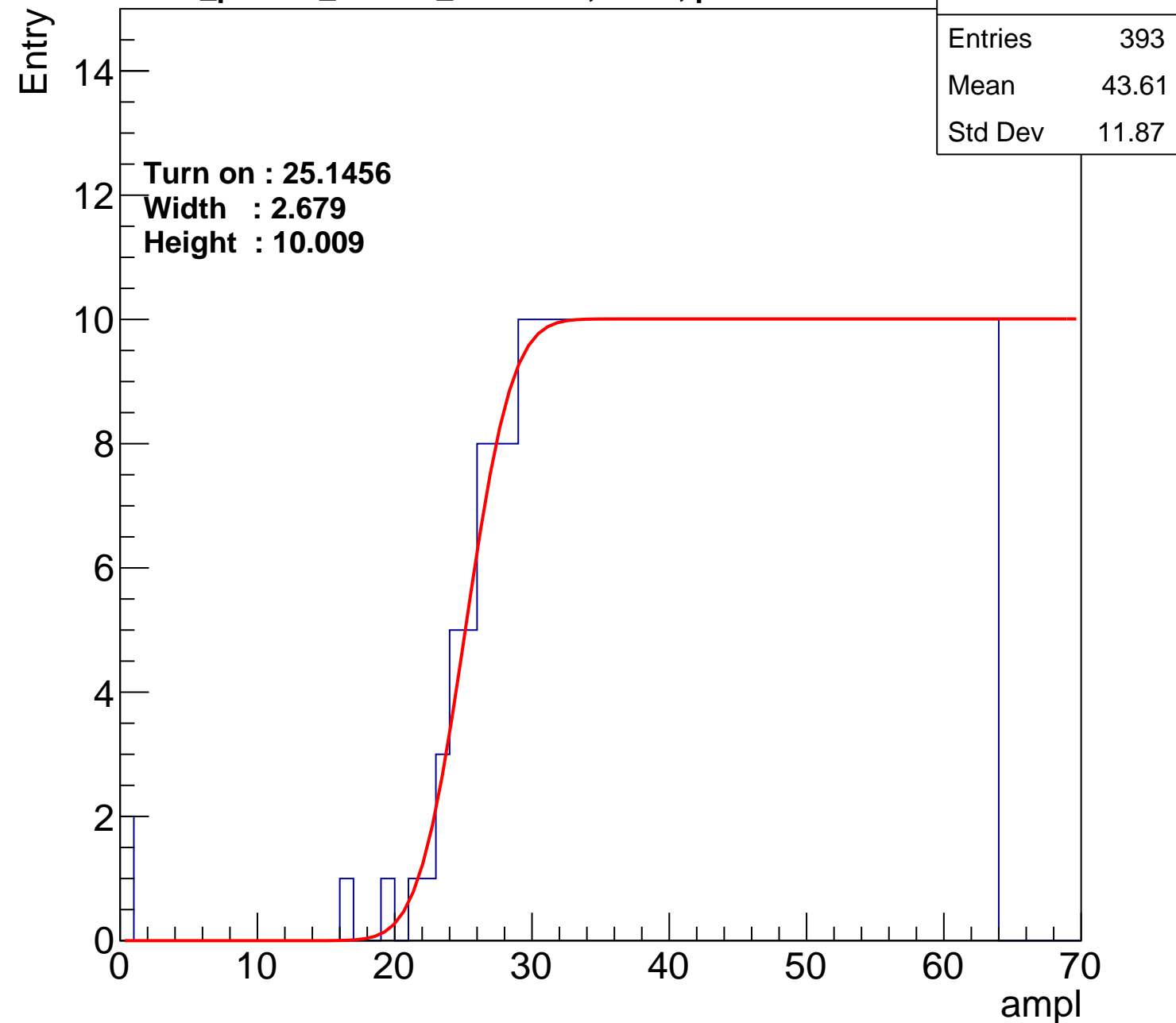
Width : 2.679

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch81

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	45
Std Dev	11.13

Turn on : 28.0819

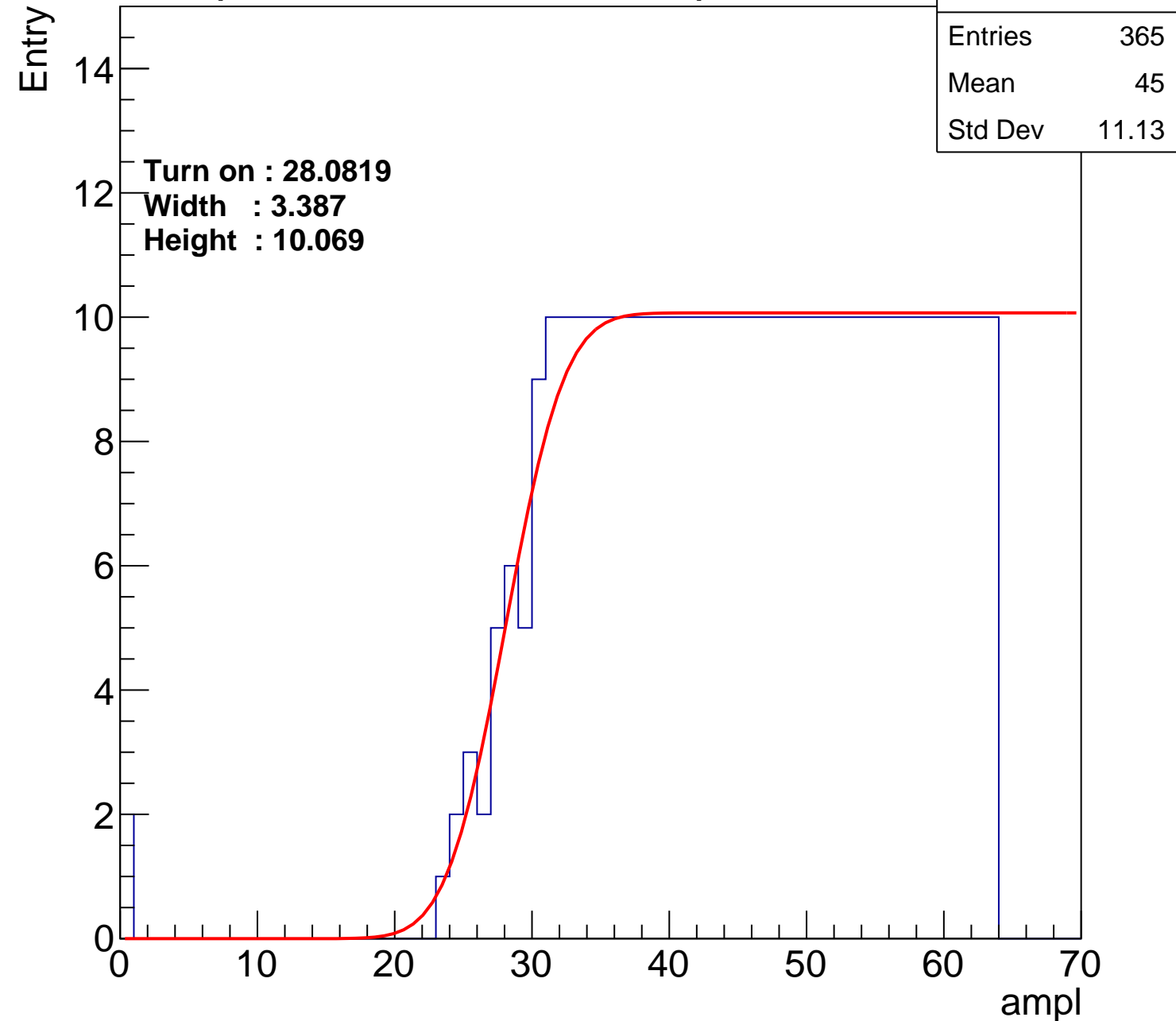
Width : 3.387

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch82

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.5748

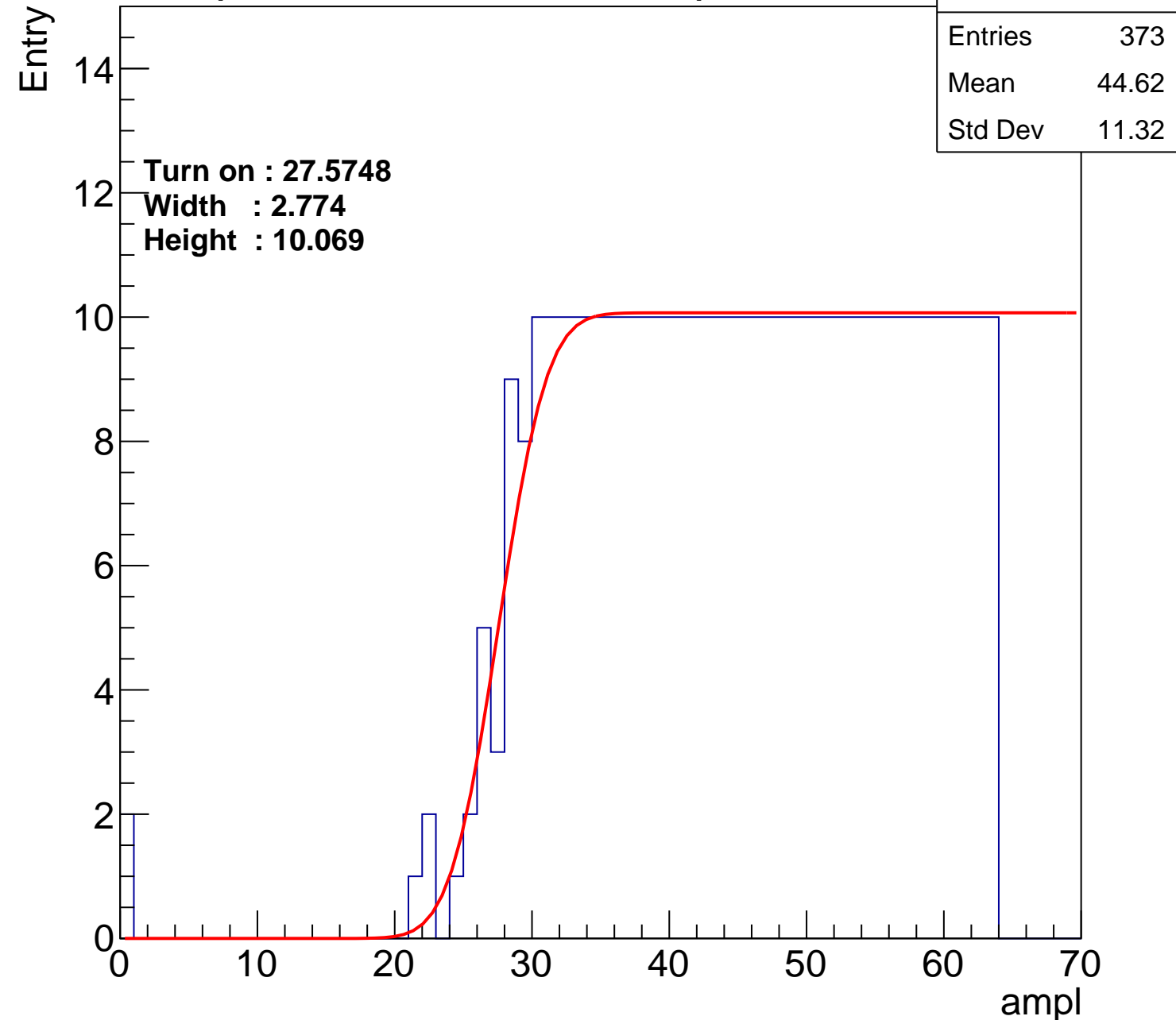
Width : 2.774

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch83

calib\_packv5\_042523\_0143.root, FC#5, port B1

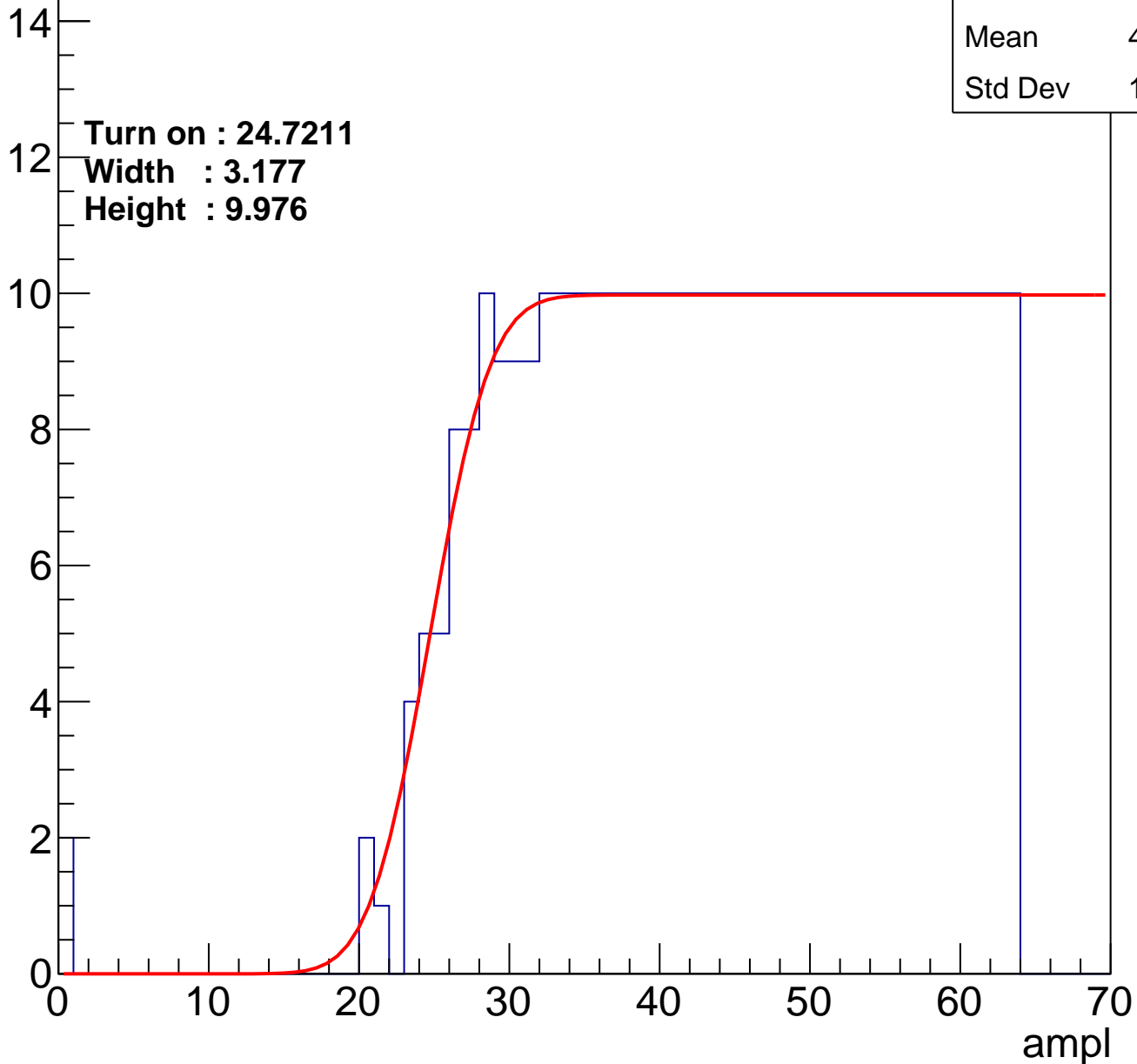
Entries	392
Mean	43.65
Std Dev	11.85

Turn on : 24.7211

Width : 3.177

Height : 9.976

Entry



# B0L000S, U19-ch84

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.93
Std Dev	11.8

Turn on : 25.5381

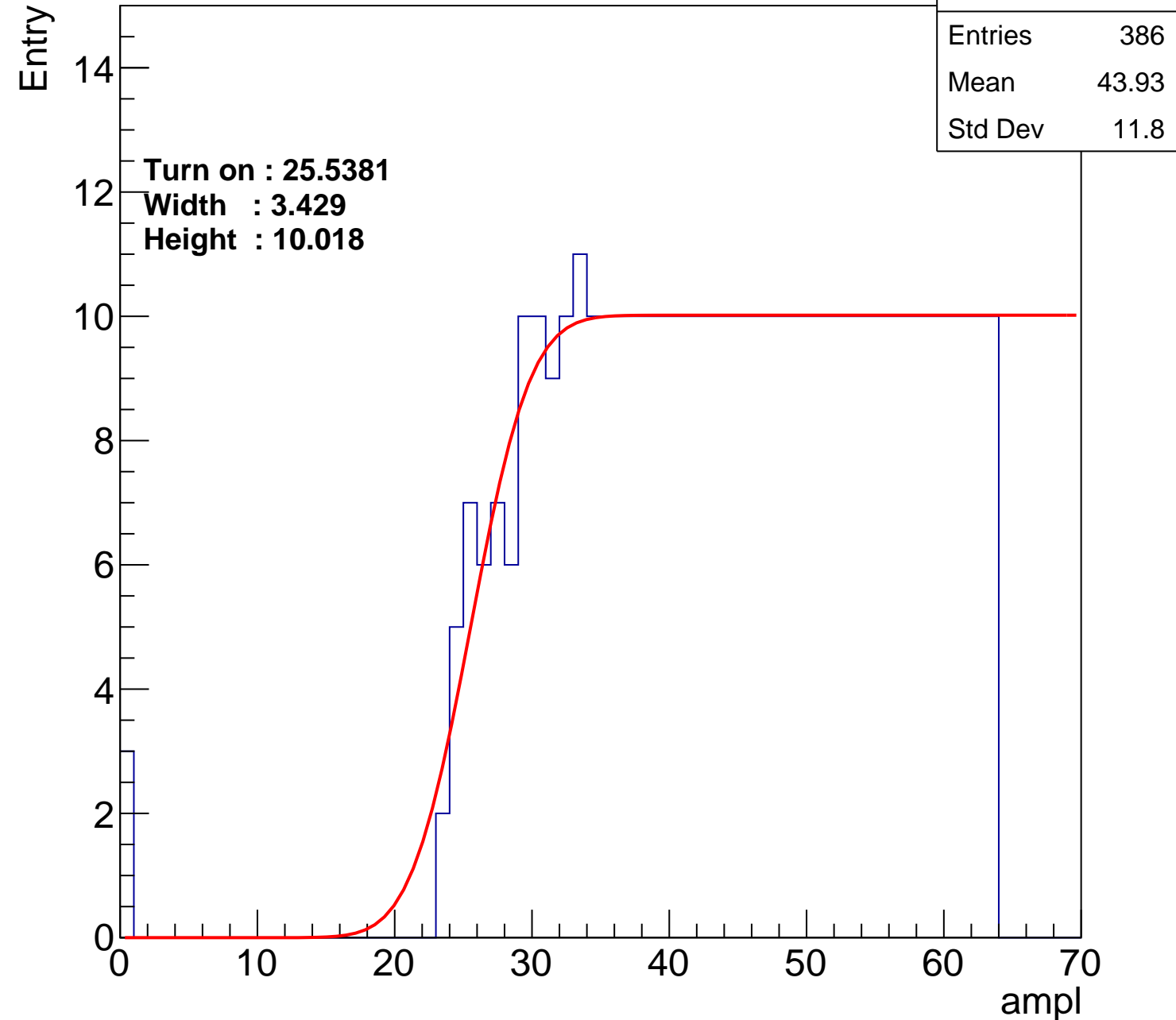
Width : 3.429

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch85

calib\_packv5\_042523\_0143.root, FC#5, port B1

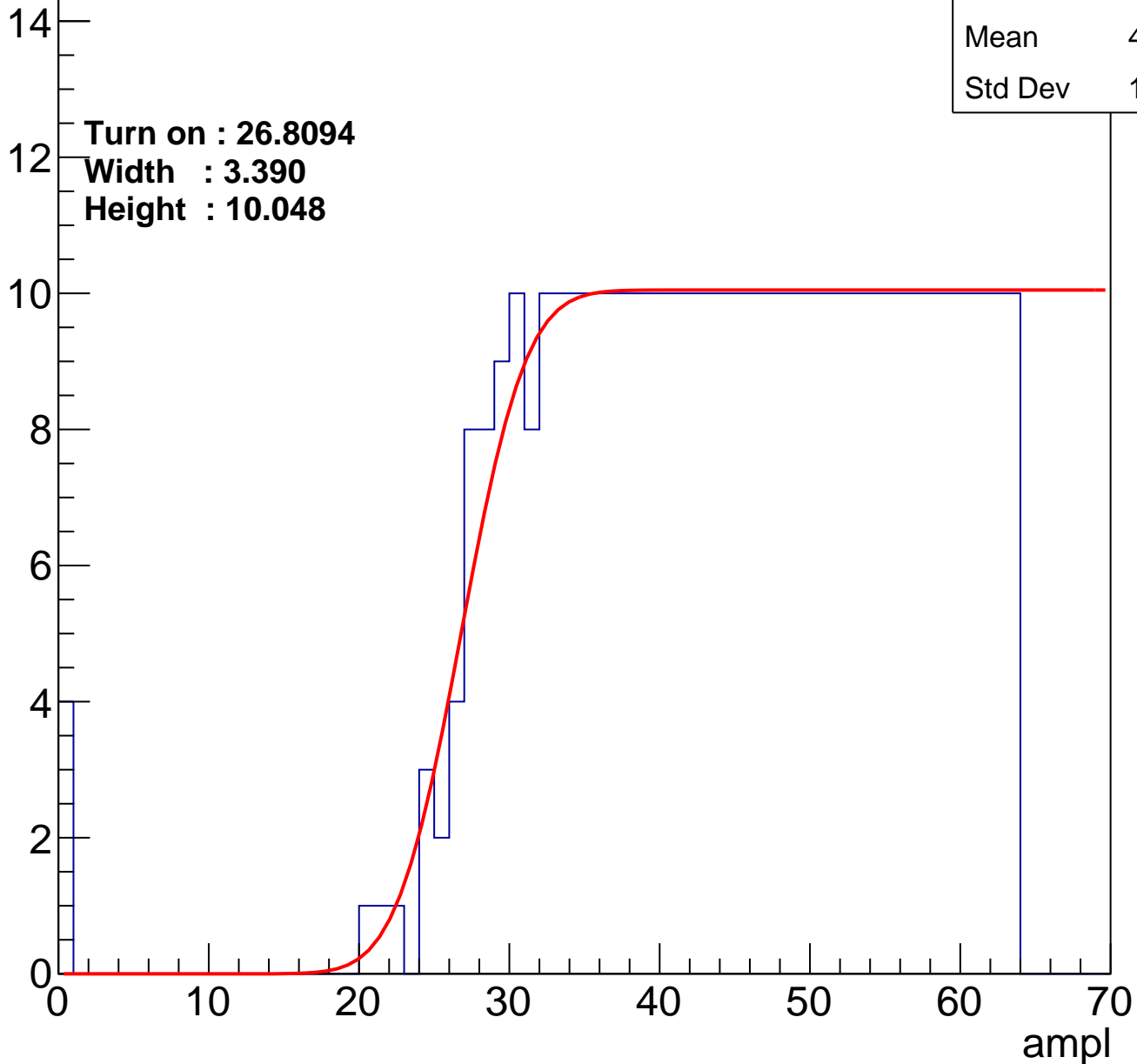
Entries	379
Mean	44.16
Std Dev	11.87

Turn on : 26.8094

Width : 3.390

Height : 10.048

Entry



# B0L000S, U19-ch86

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.9773

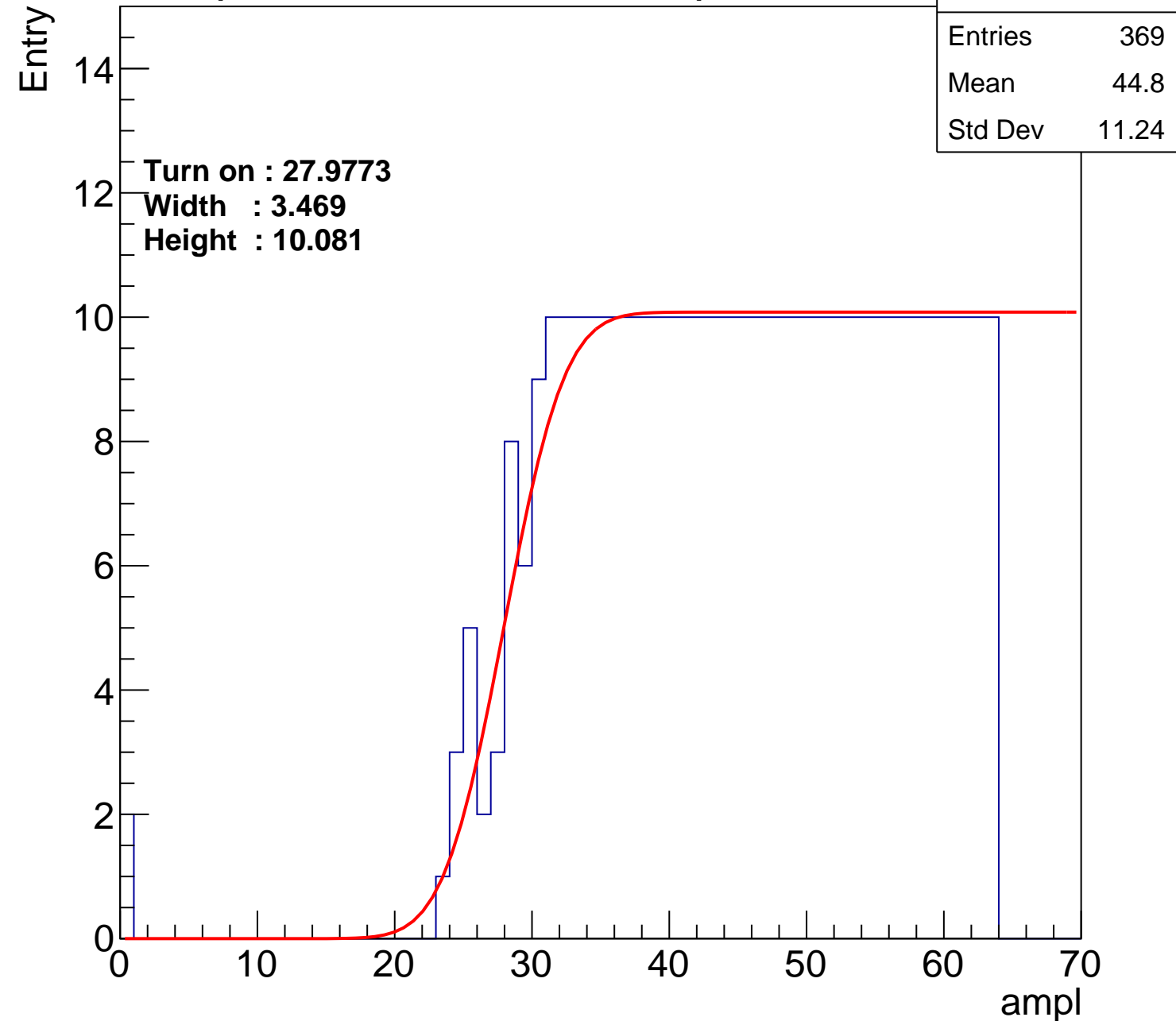
Width : 3.469

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch87

calib\_packv5\_042523\_0143.root, FC#5, port B1

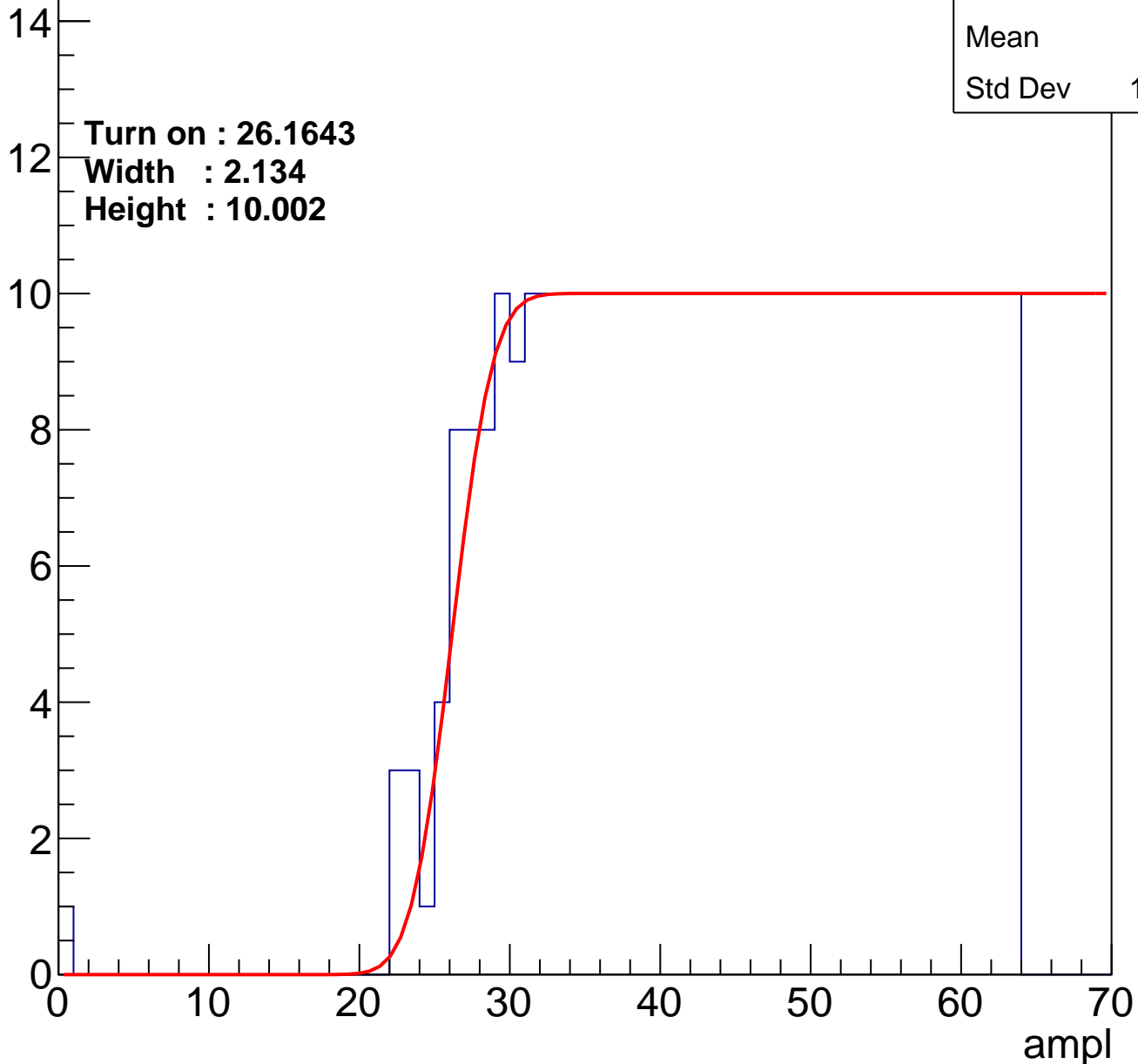
Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 26.1643

Width : 2.134

Height : 10.002

Entry



# B0L000S, U19-ch88

calib\_packv5\_042523\_0143.root, FC#5, port B1

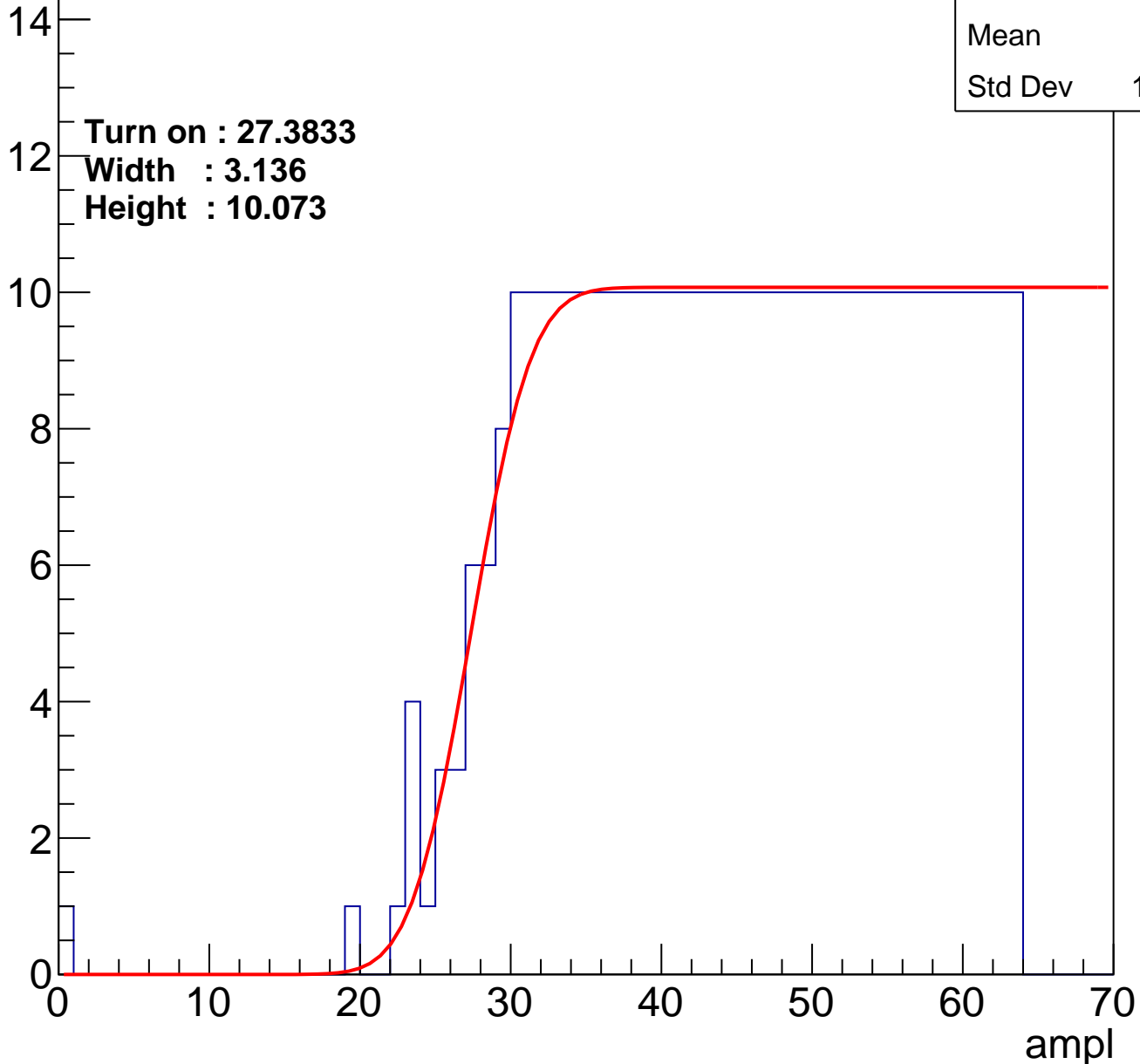
Entries	374
Mean	44.6
Std Dev	11.22

Turn on : 27.3833

Width : 3.136

Height : 10.073

Entry



# B0L000S, U19-ch89

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.73
Std Dev	11.96

Turn on : 25.2978

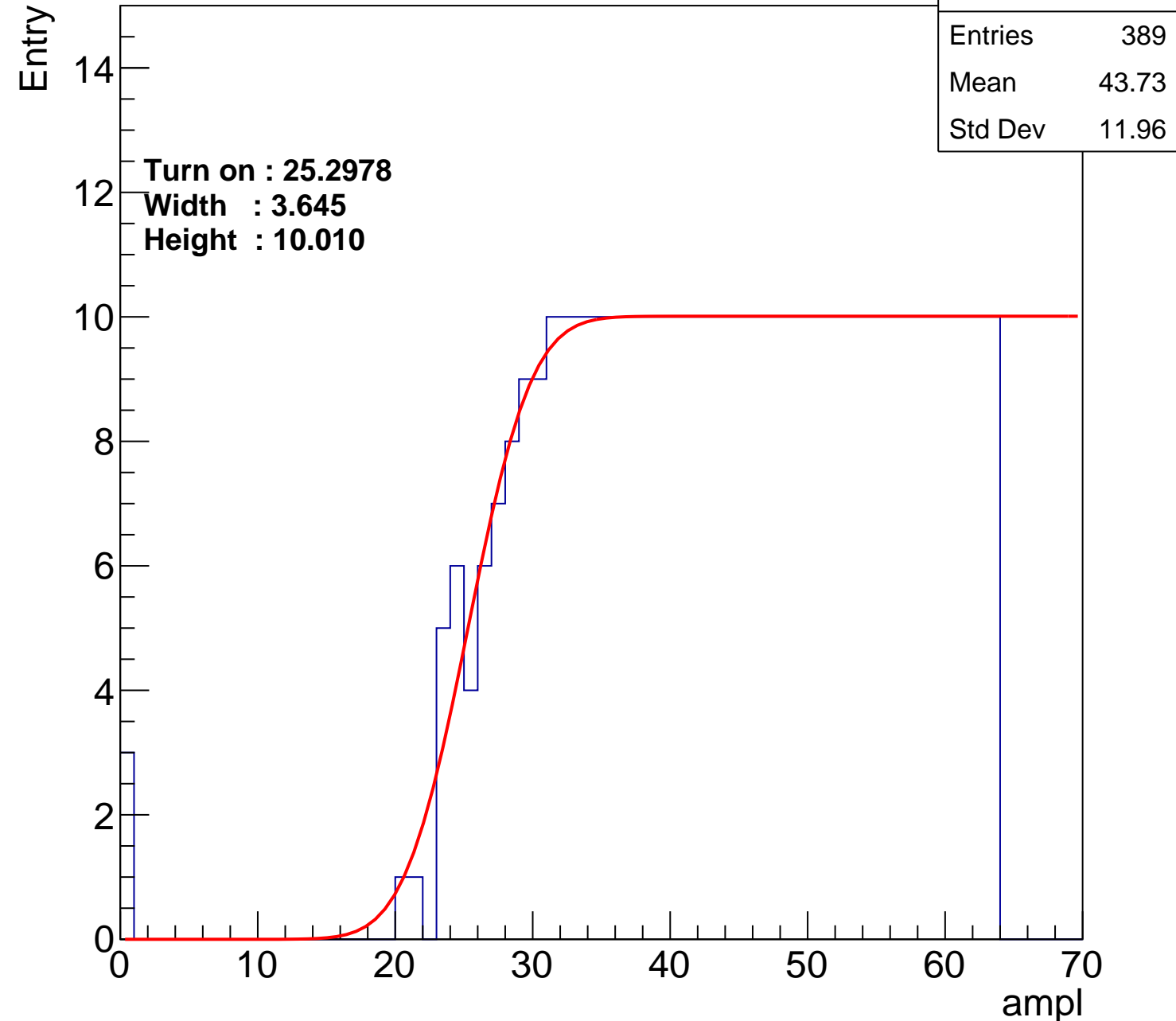
Width : 3.645

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch90

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	351
Mean	45.67
Std Dev	10.81

**Turn on : 29.4776**

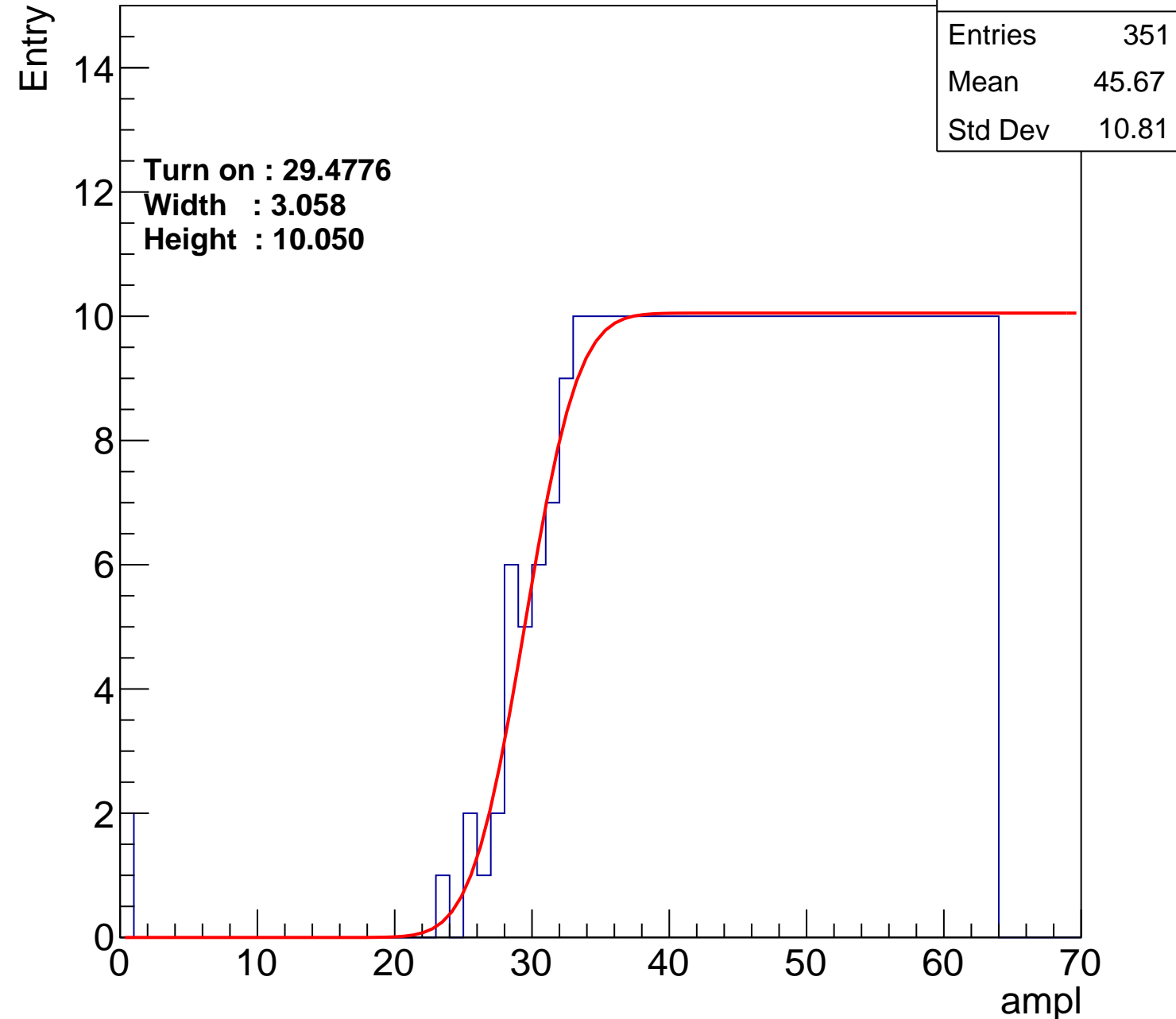
**Width : 3.058**

**Height : 10.050**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch91

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	44.82
Std Dev	11.54

Turn on : 27.8502

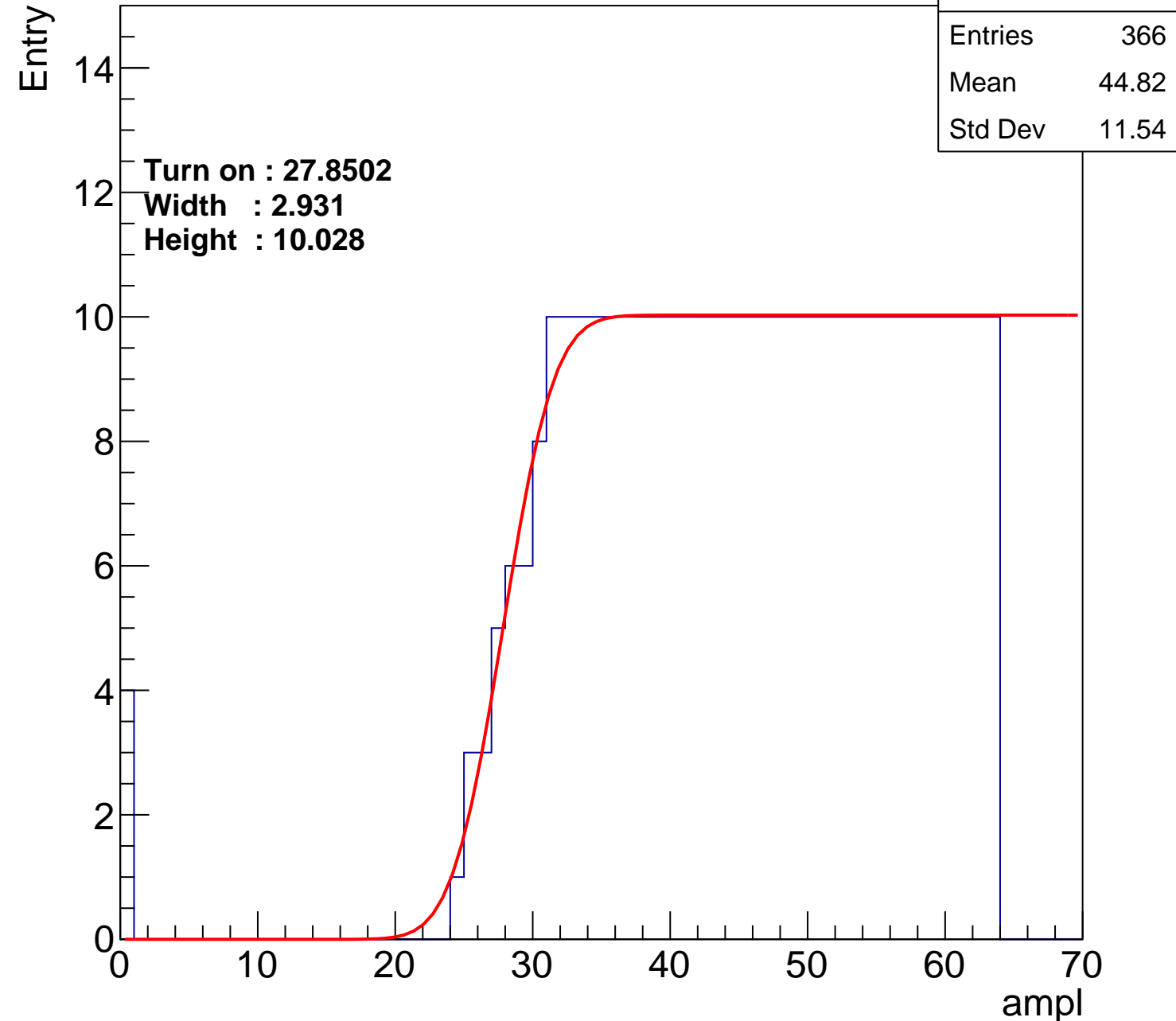
Width : 2.931

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch92

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	43.93
Std Dev	11.85

Turn on : 26.1282

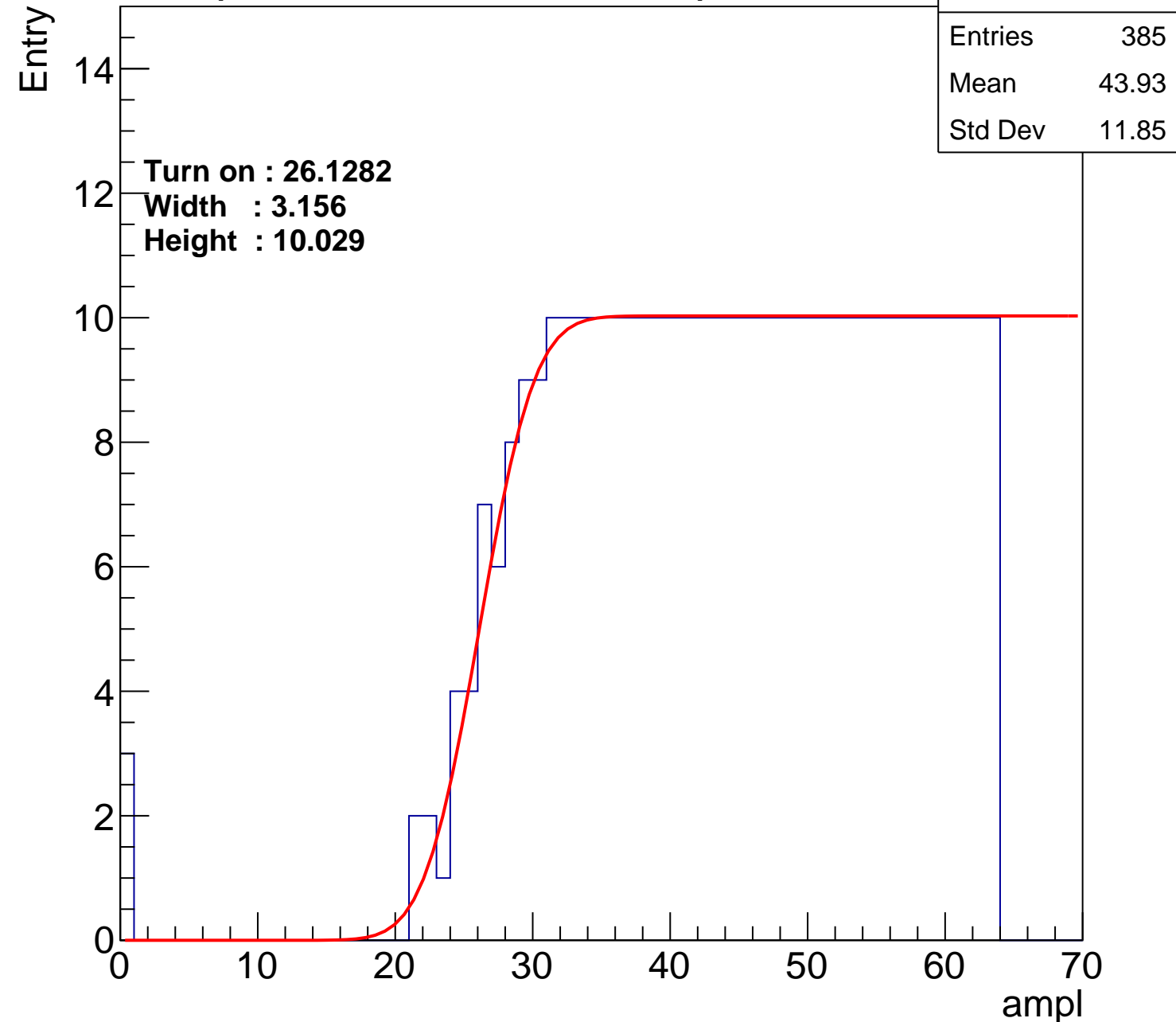
Width : 3.156

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch93

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.25
Std Dev	10.84

Turn on : 28.2203

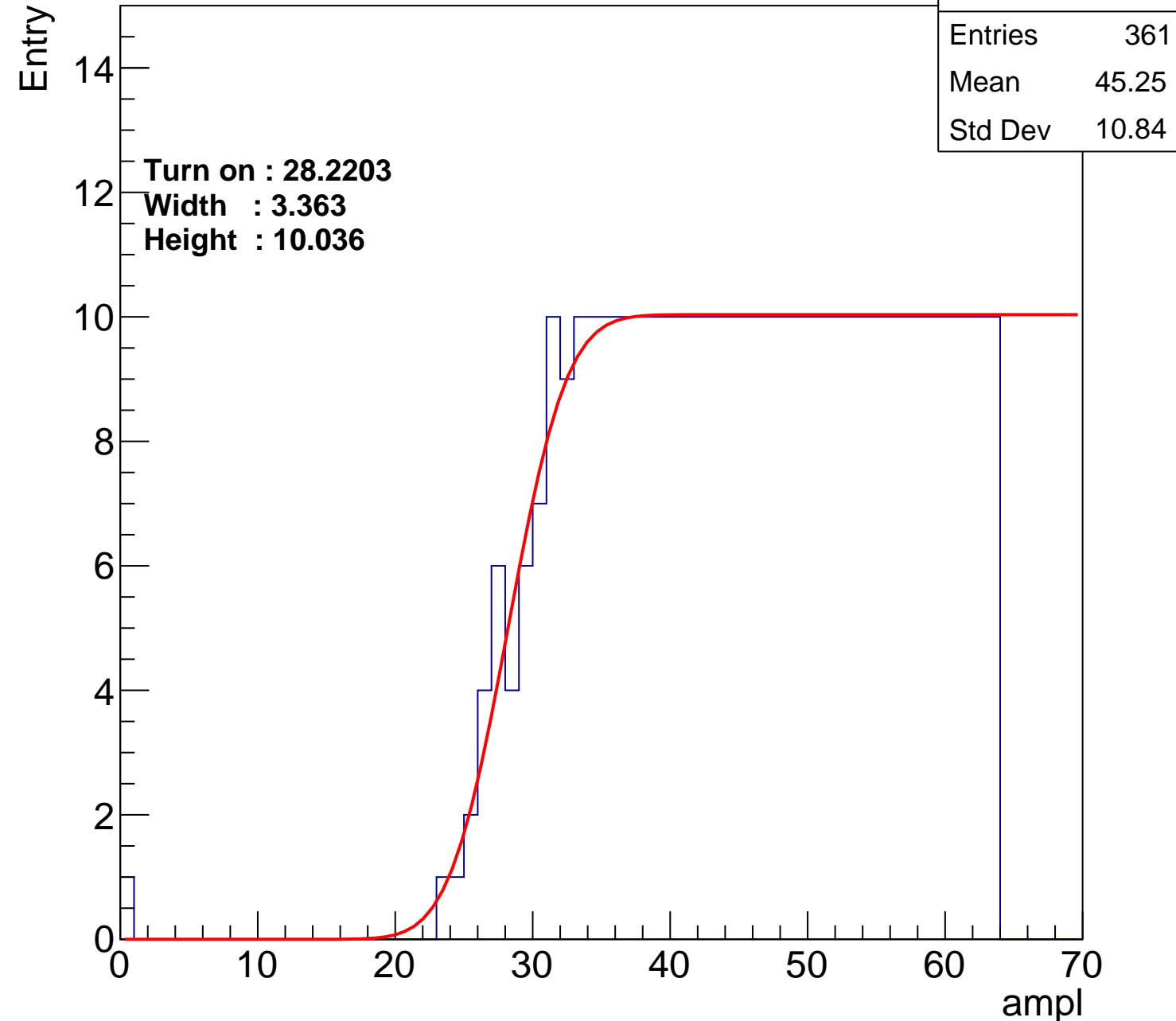
Width : 3.363

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch94

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.95
Std Dev	11.57

**Turn on : 25.4320**

**Width : 3.369**

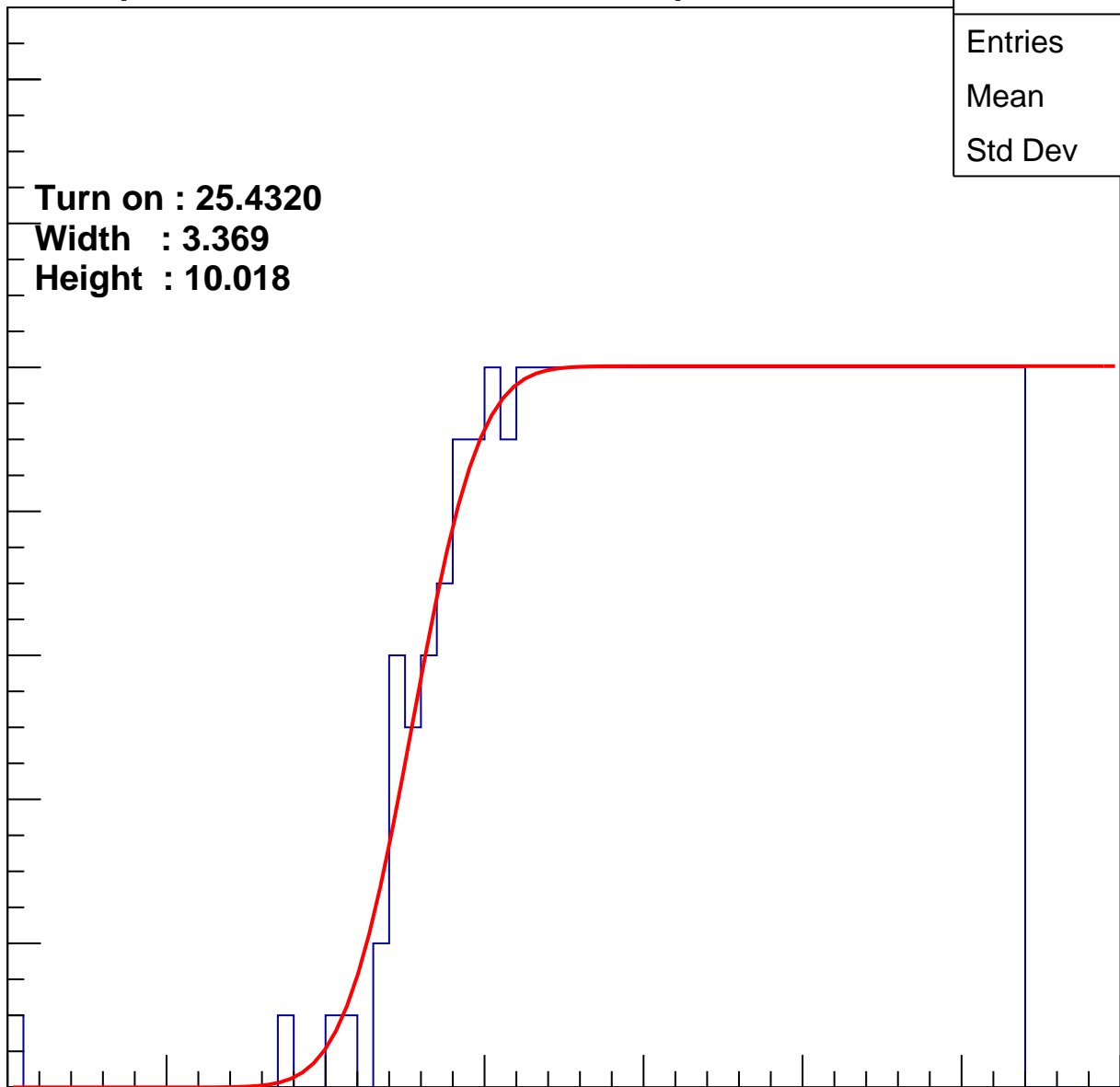
**Height : 10.018**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B0L000S, U19-ch95

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 27.4243

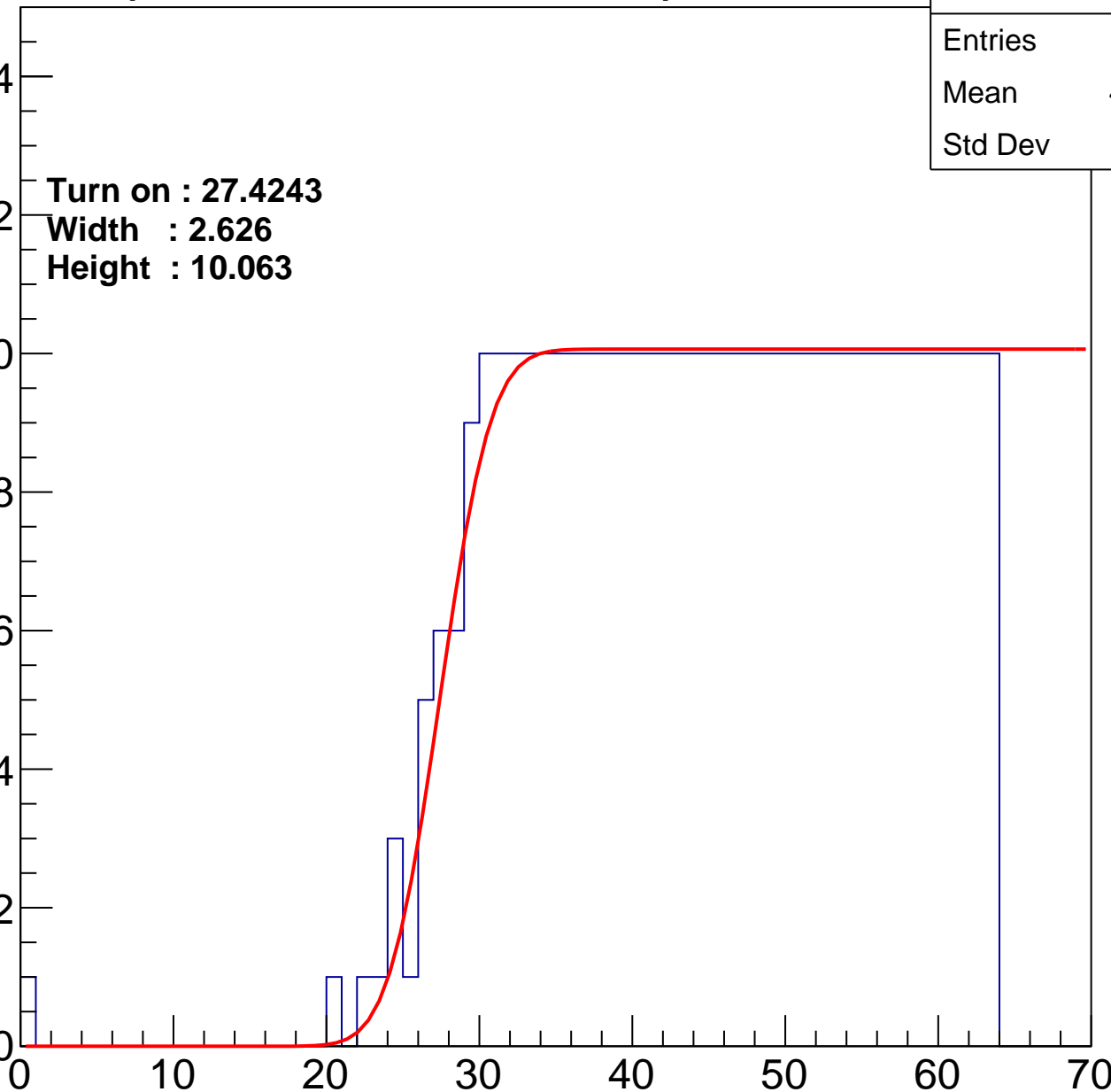
Width : 2.626

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch96

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.7
Std Dev	11.42

Turn on : 27.0584

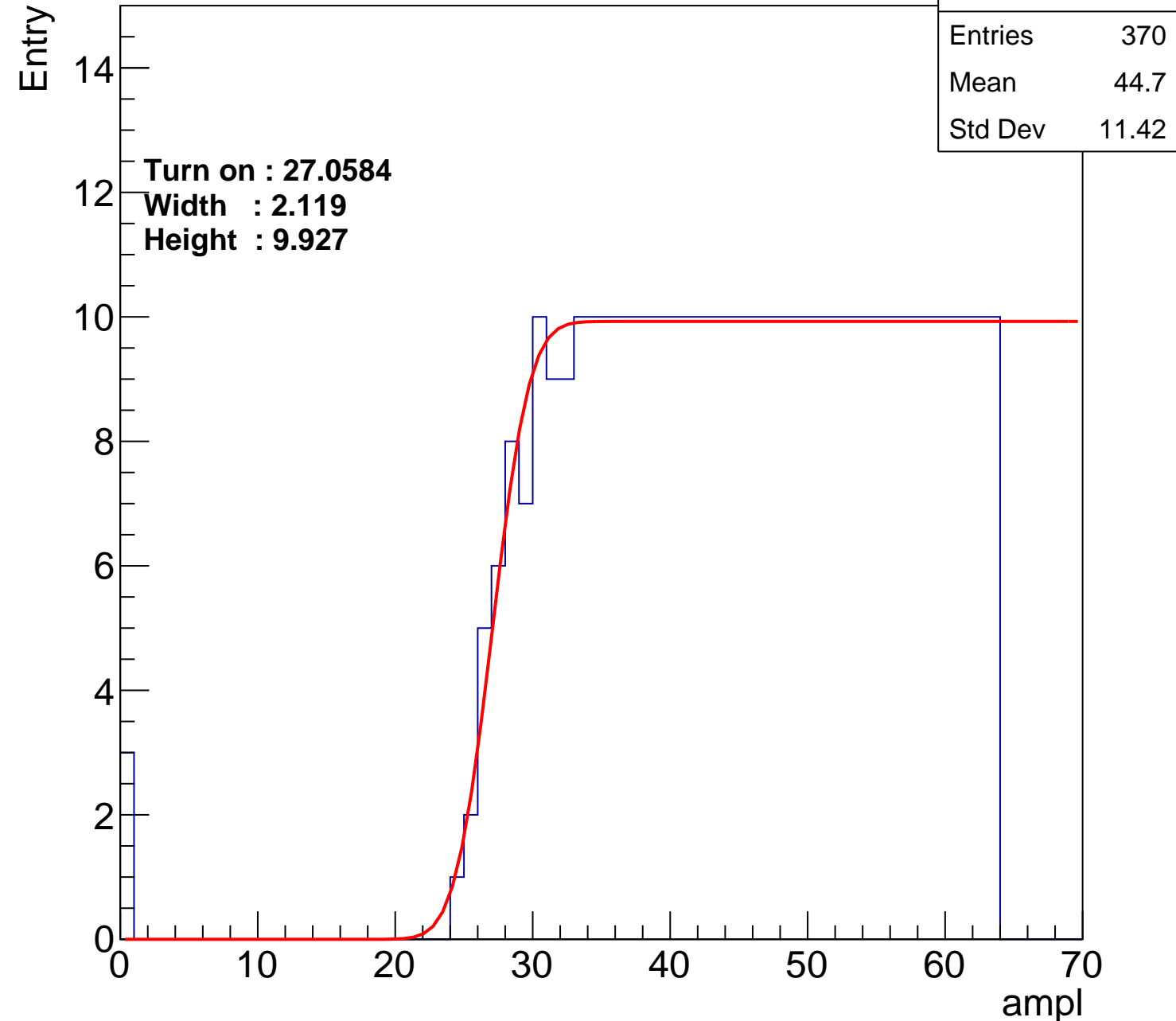
Width : 2.119

Height : 9.927

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch97

calib\_packv5\_042523\_0143.root, FC#5, port B1

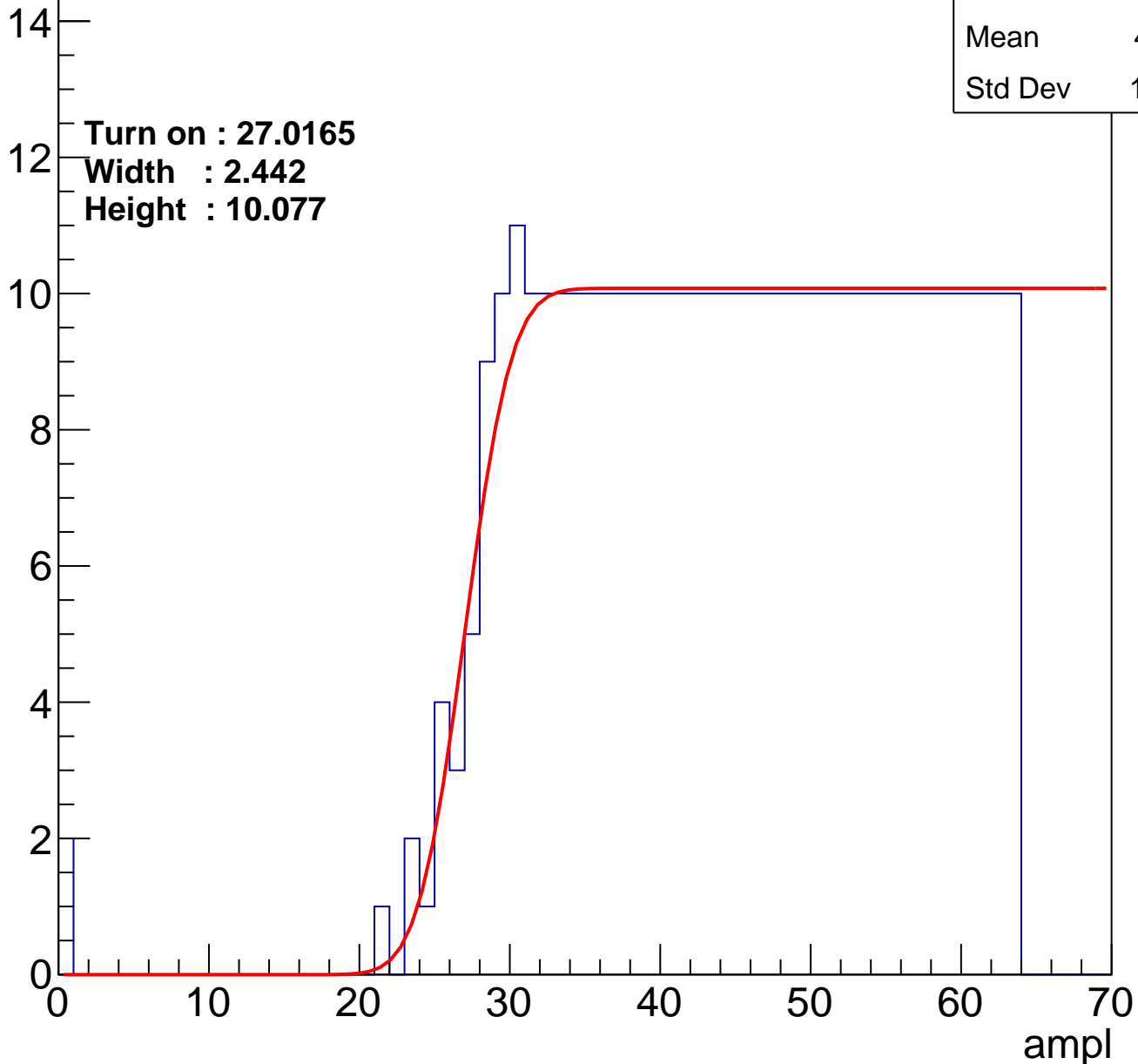
Entries	378
Mean	44.41
Std Dev	11.39

Turn on : 27.0165

Width : 2.442

Height : 10.077

Entry



# B0L000S, U19-ch98

calib\_packv5\_042523\_0143.root, FC#5, port B1

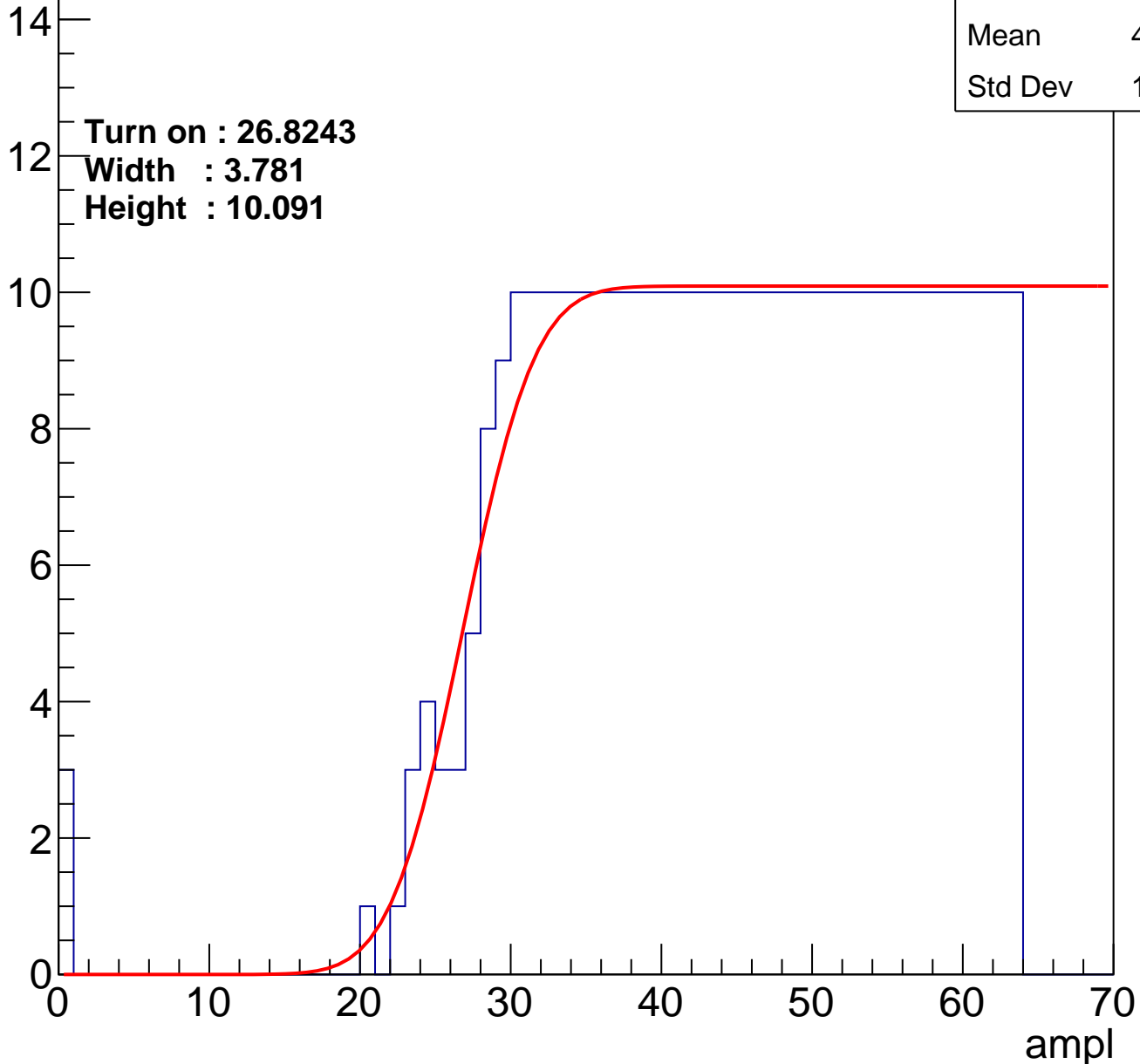
Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.8243

Width : 3.781

Height : 10.091

Entry



# B0L000S, U19-ch99

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	391
Mean	43.79
Std Dev	11.69

**Turn on : 24.9205**

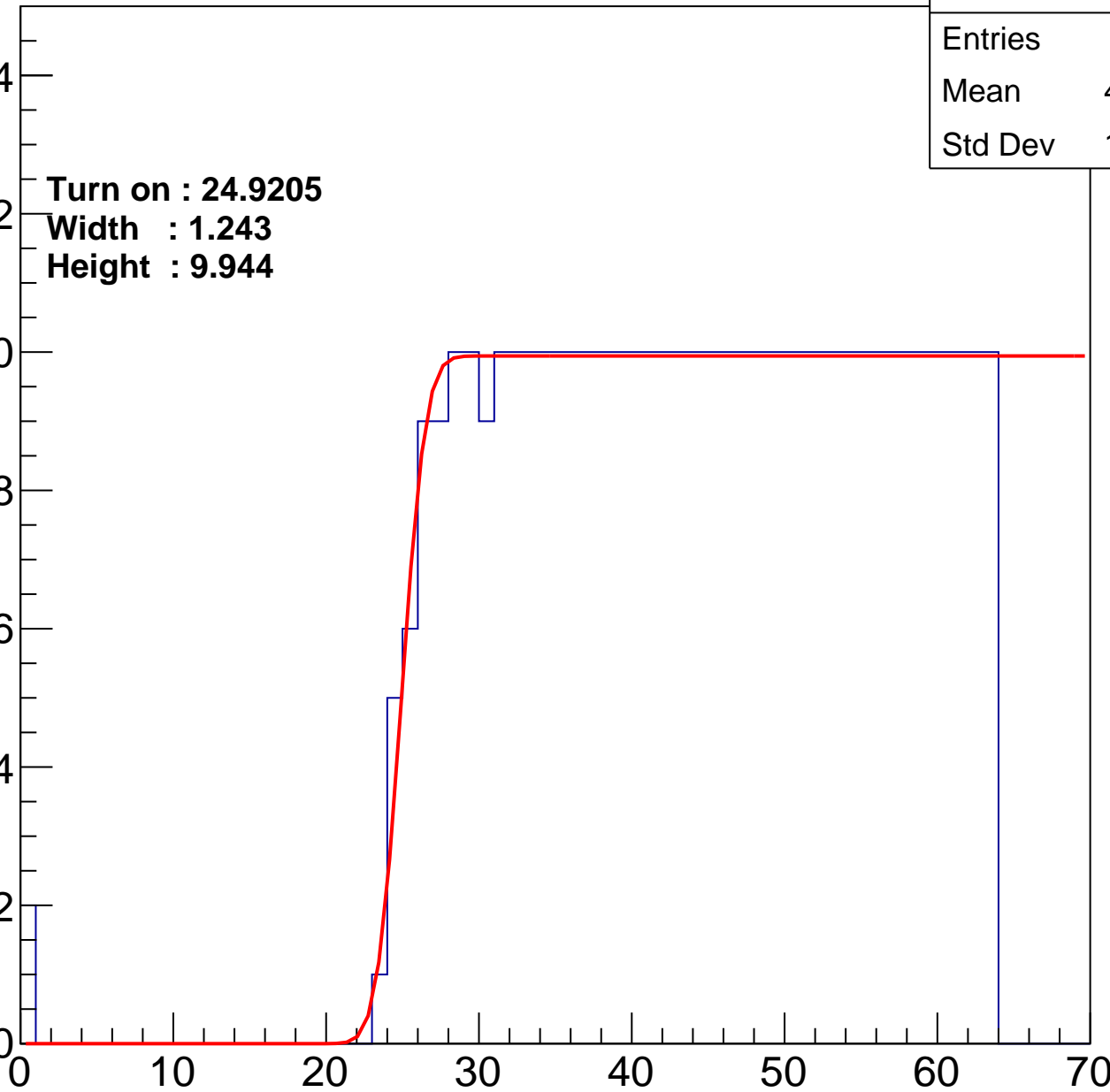
**Width : 1.243**

**Height : 9.944**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch100

calib\_packv5\_042523\_0143.root, FC#5, port B1

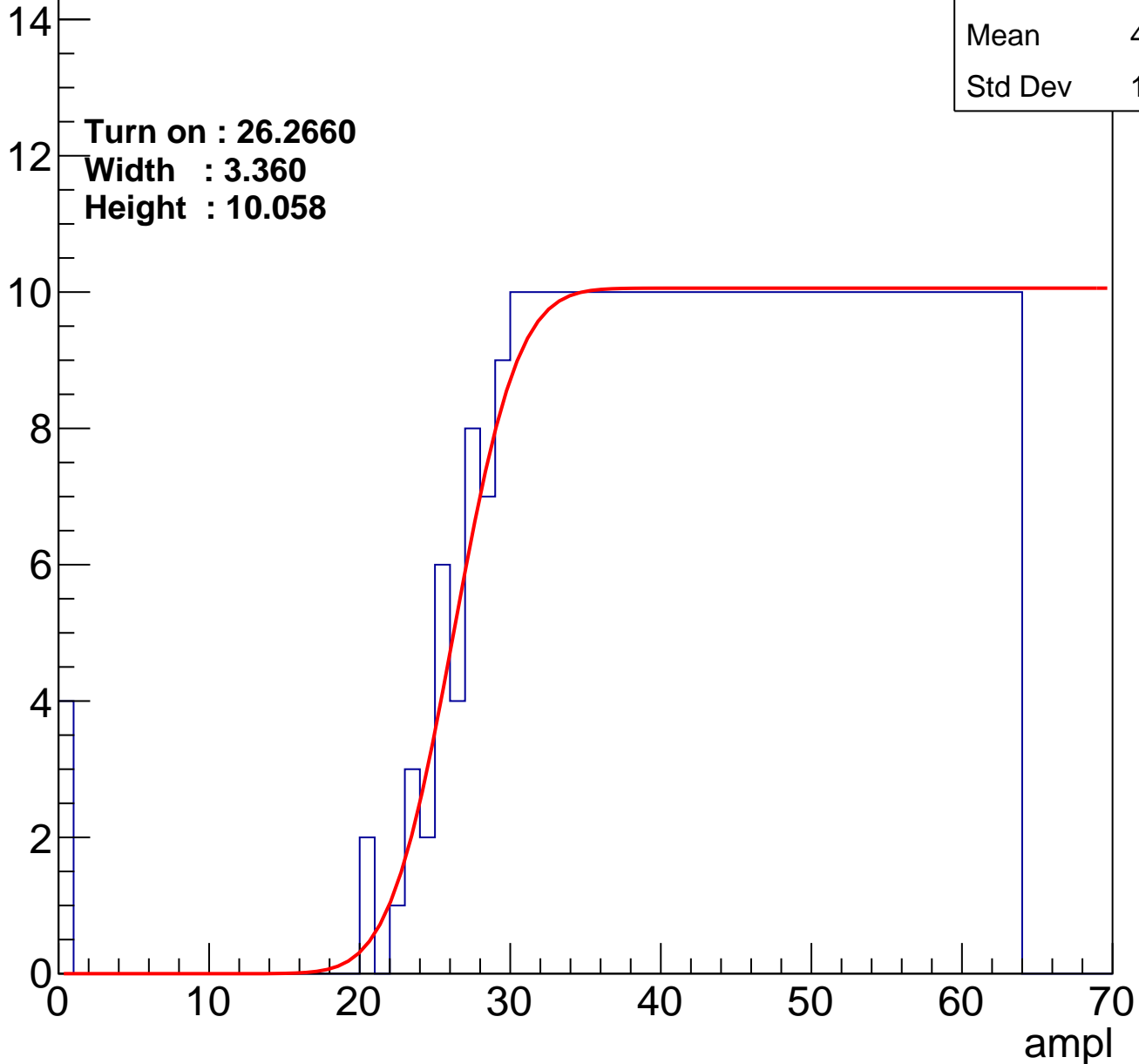
Entries	386
Mean	43.82
Std Dev	12.04

**Turn on : 26.2660**

**Width : 3.360**

**Height : 10.058**

Entry



# B0L000S, U19-ch101

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	43.9
Std Dev	12.13

**Turn on : 26.2668**

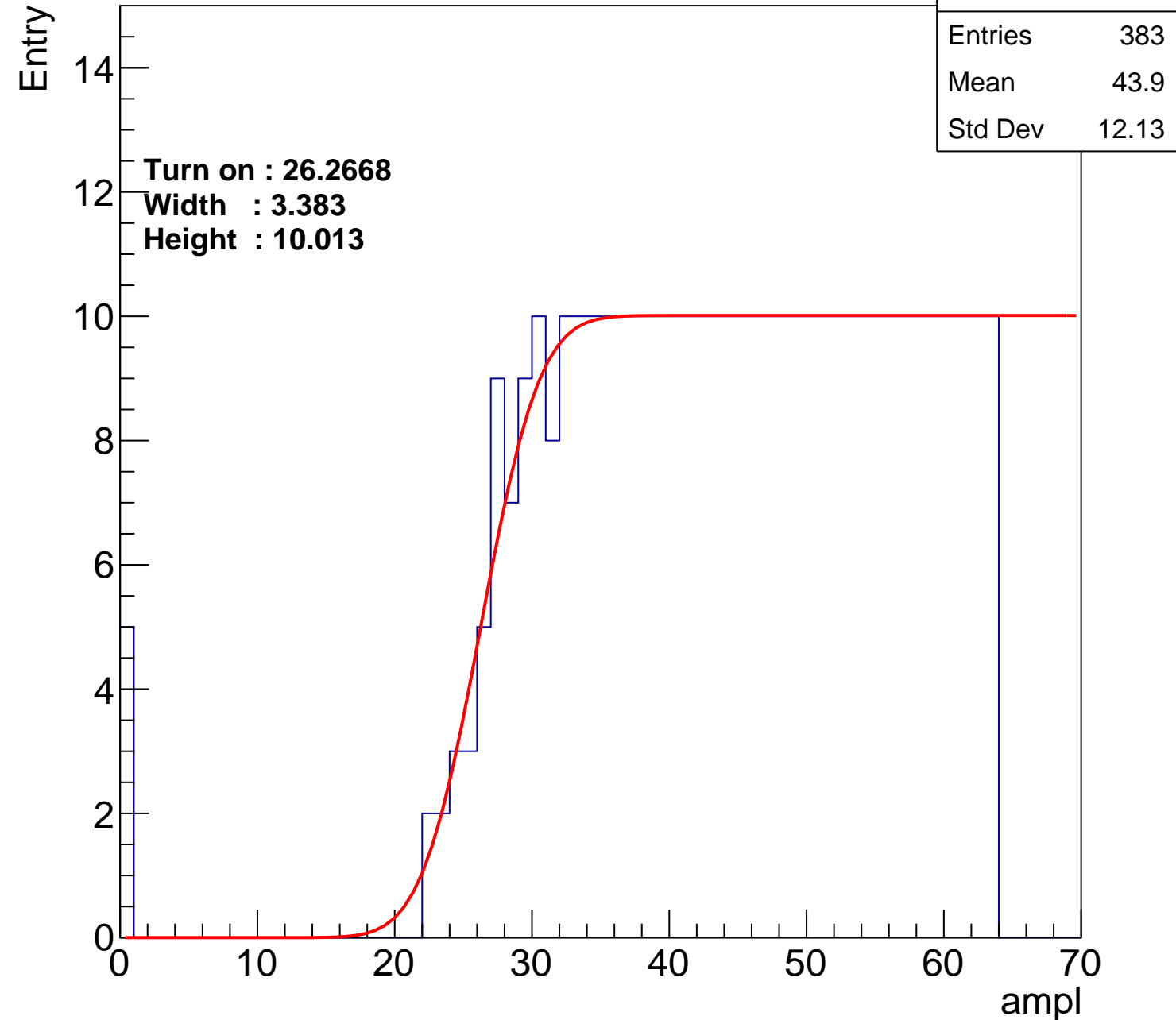
**Width : 3.383**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch102

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 24.5966

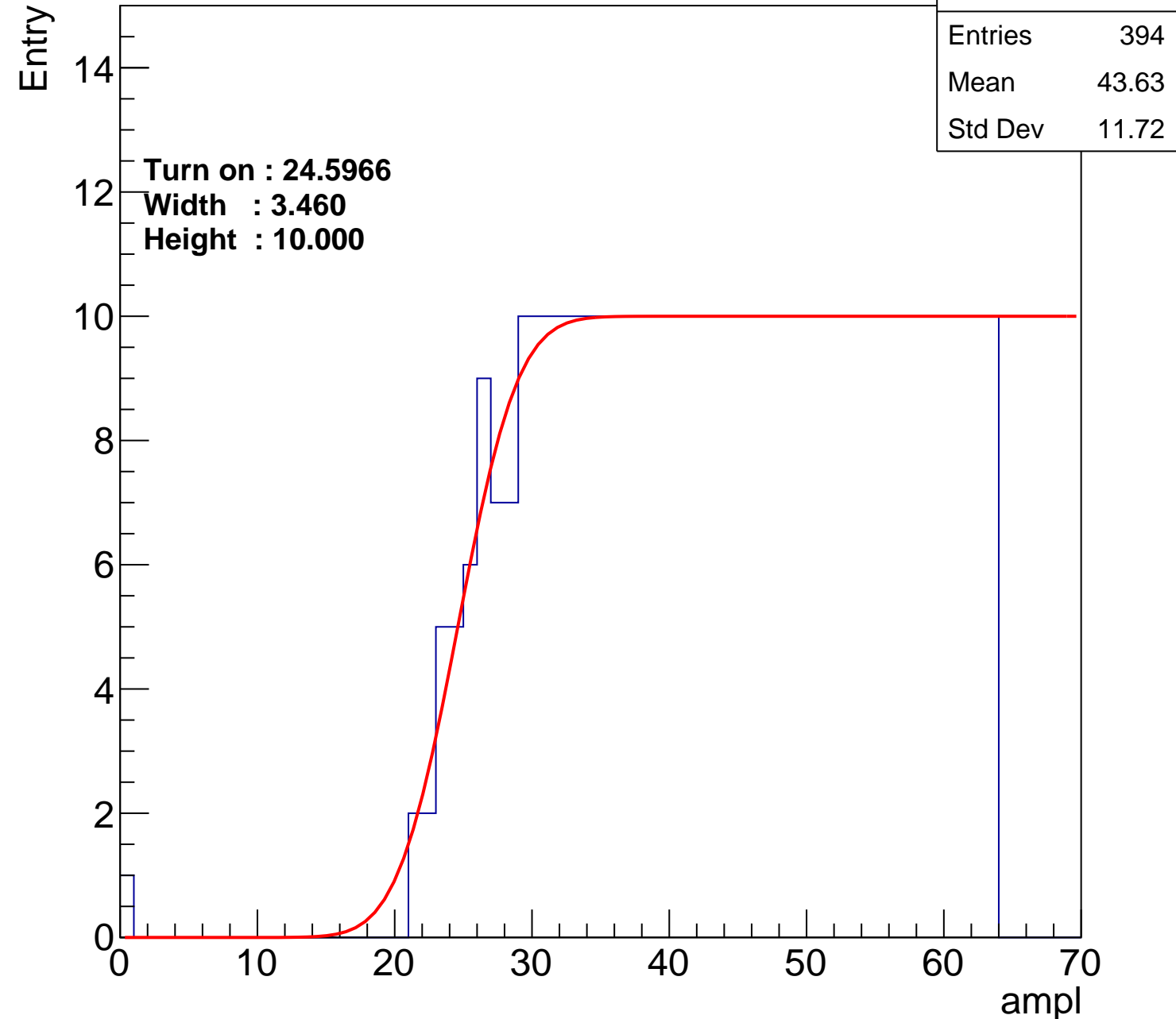
Width : 3.460

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch103

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.42
Std Dev	11.28

Turn on : 25.8425

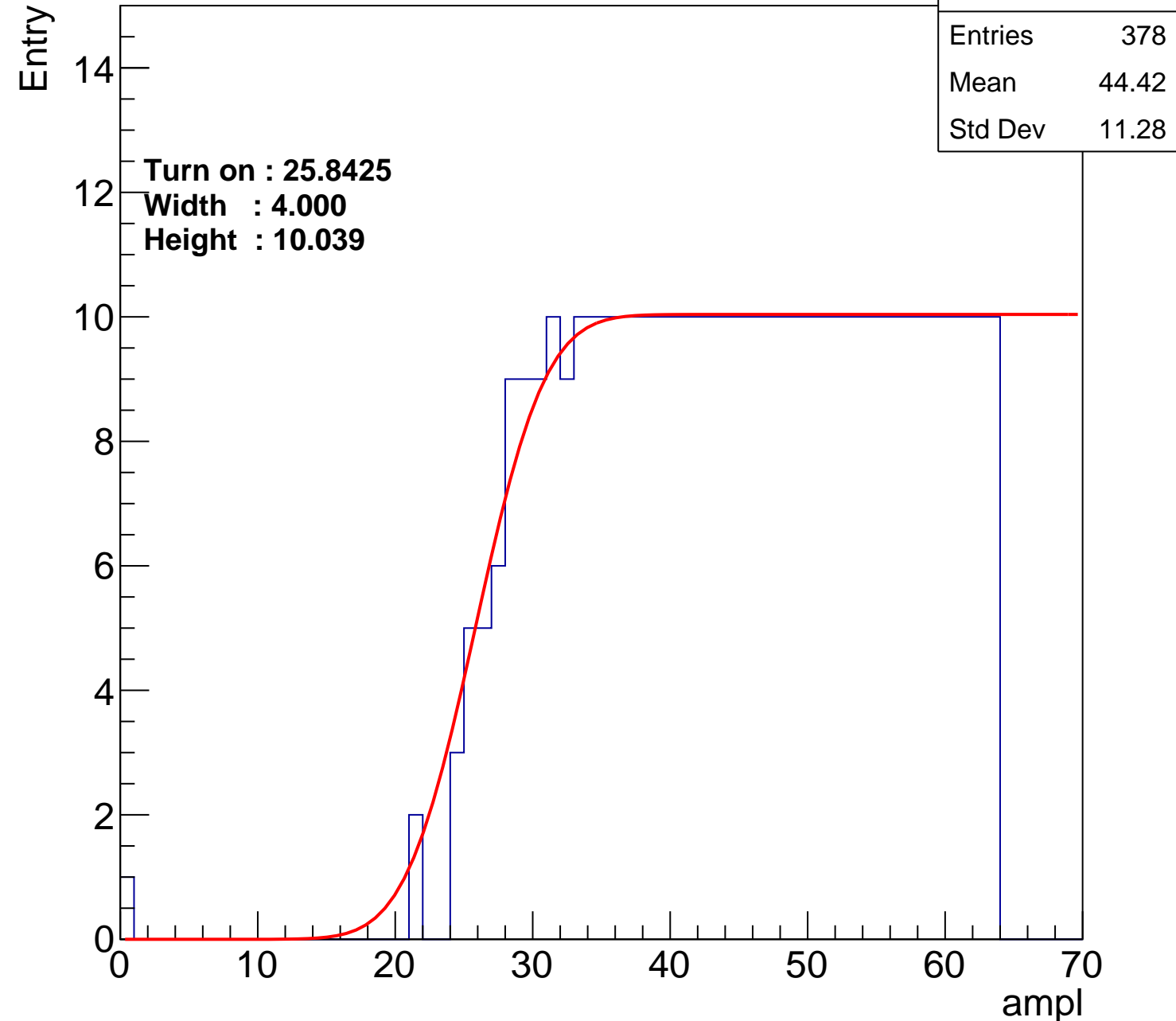
Width : 4.000

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch104

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.22
Std Dev	11.78

Turn on : 26.5274

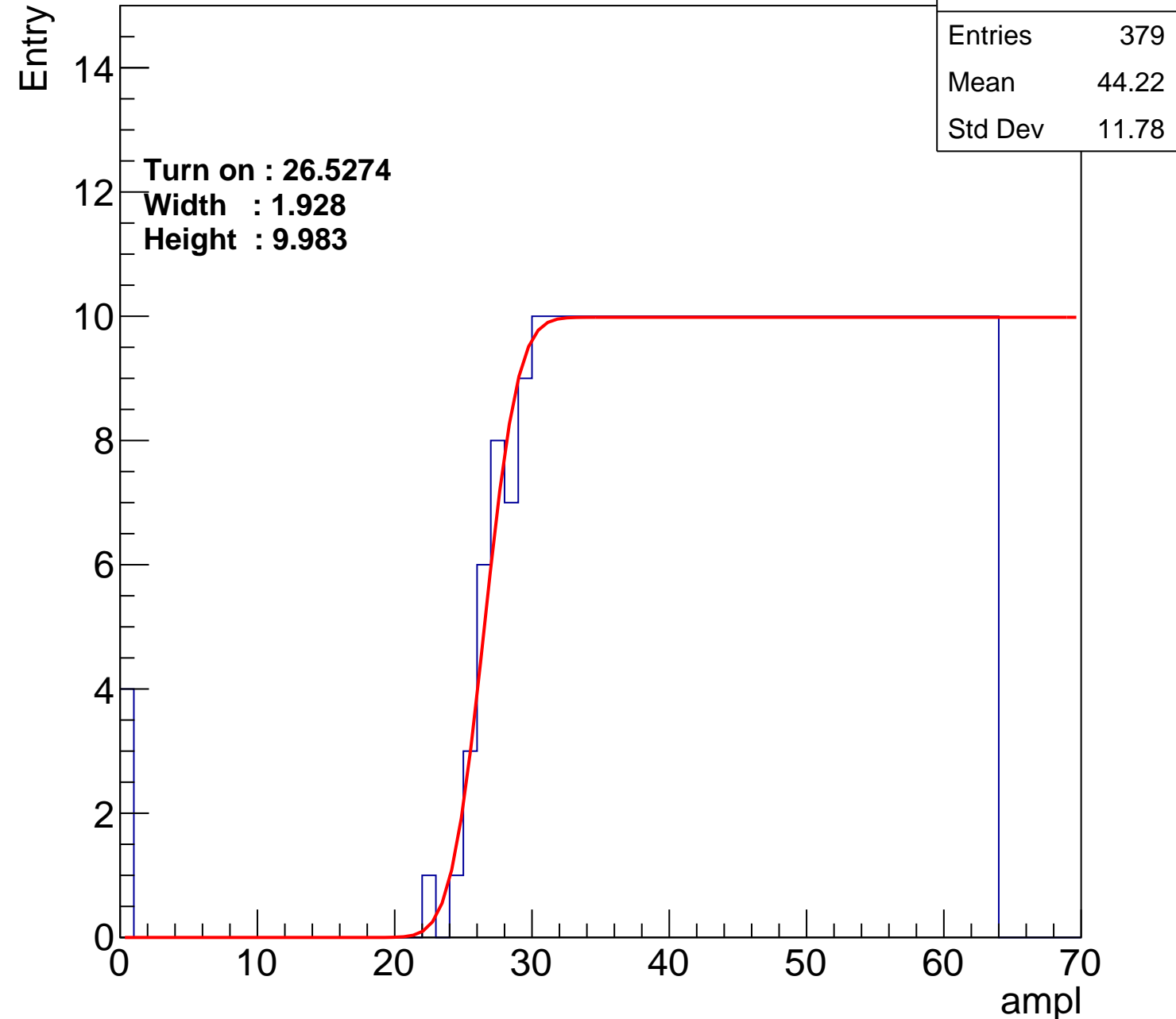
Width : 1.928

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch105

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.87
Std Dev	11.32

**Turn on : 27.7820**

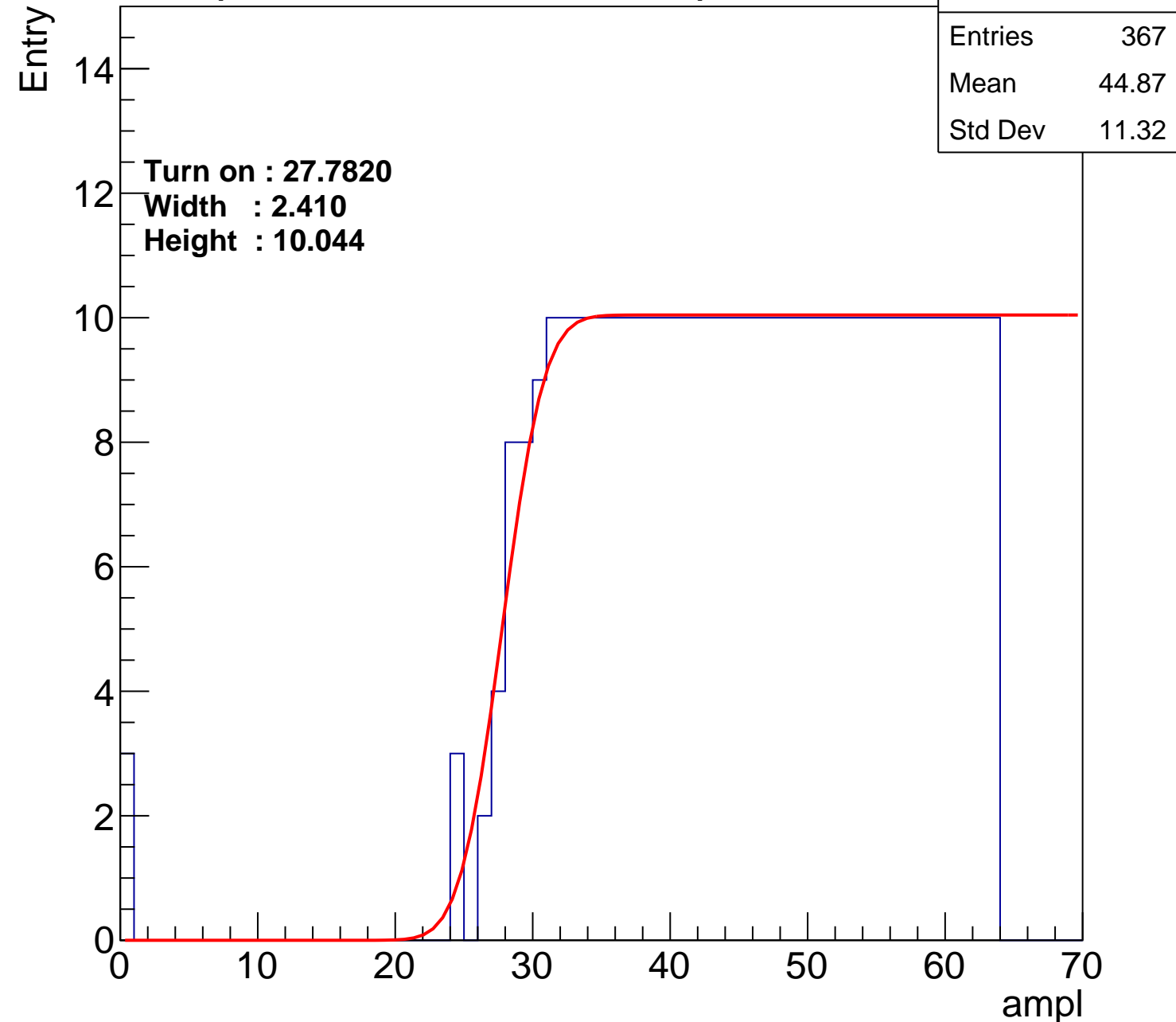
**Width : 2.410**

**Height : 10.044**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch106

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.1
Std Dev	11.44

**Turn on : 25.9400**

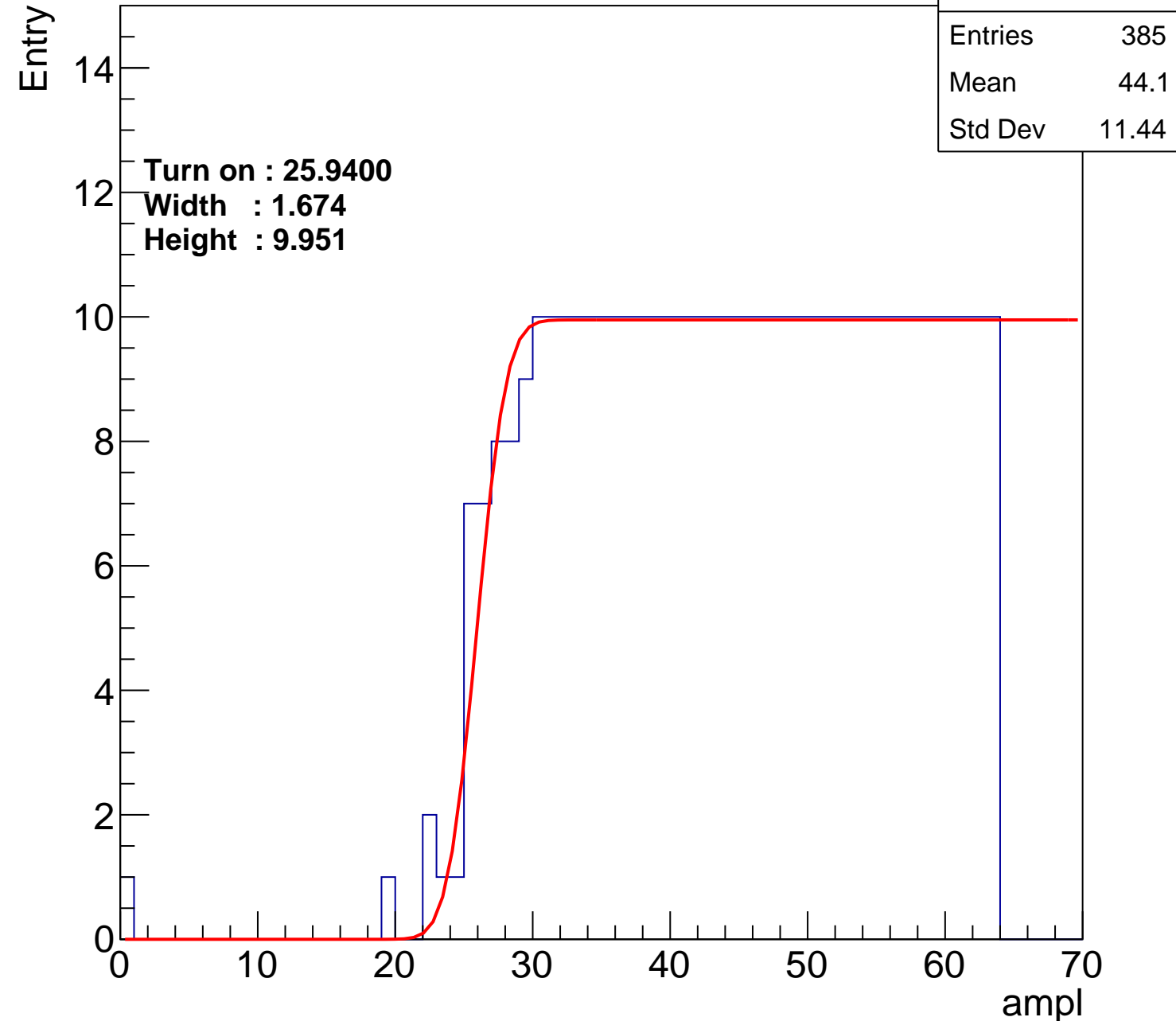
**Width : 1.674**

**Height : 9.951**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch107

calib\_packv5\_042523\_0143.root, FC#5, port B1

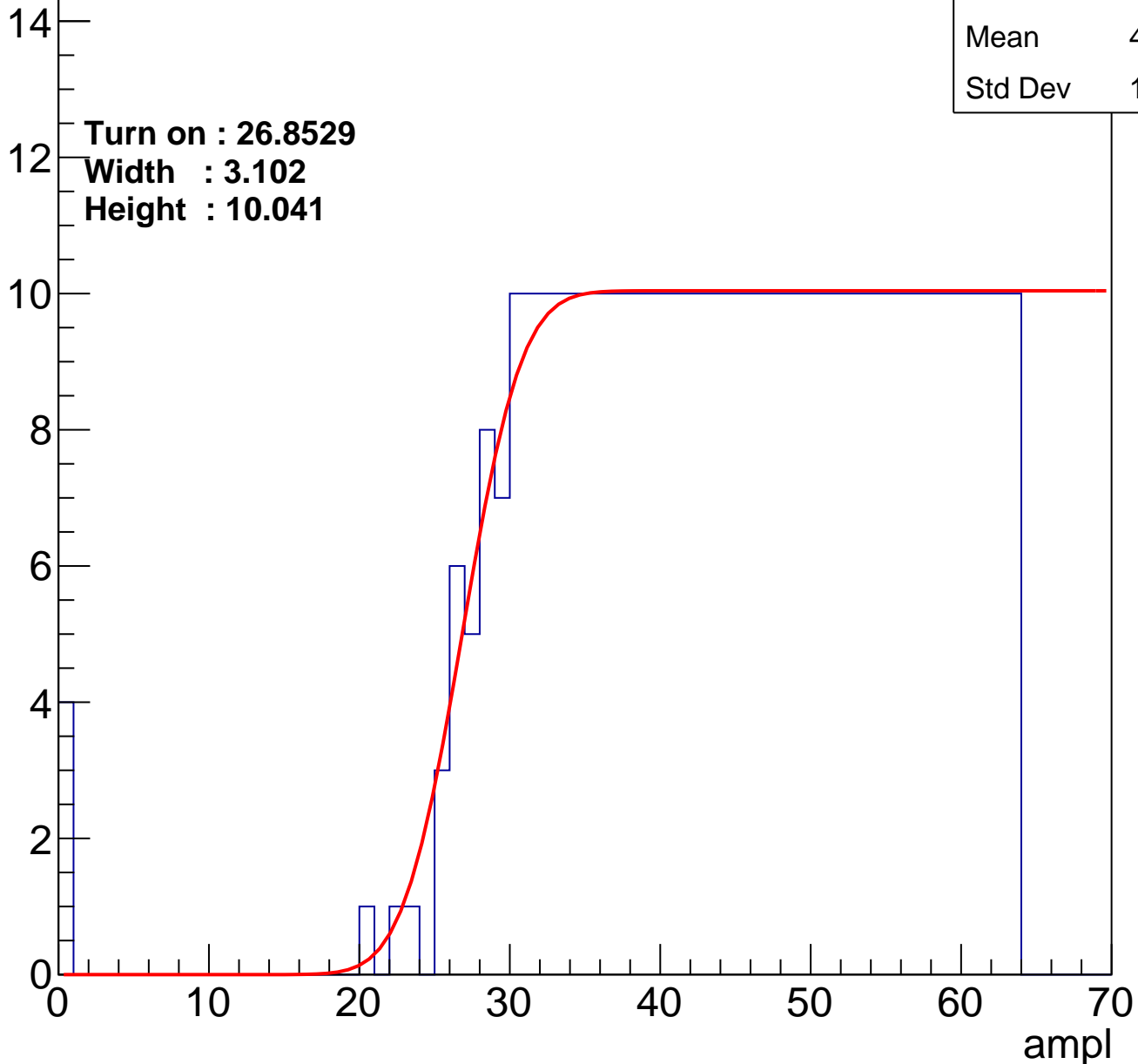
Entries	376
Mean	44.33
Std Dev	11.78

Turn on : 26.8529

Width : 3.102

Height : 10.041

Entry



# B0L000S, U19-ch108

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.51
Std Dev	11.19

**Turn on : 26.7327**

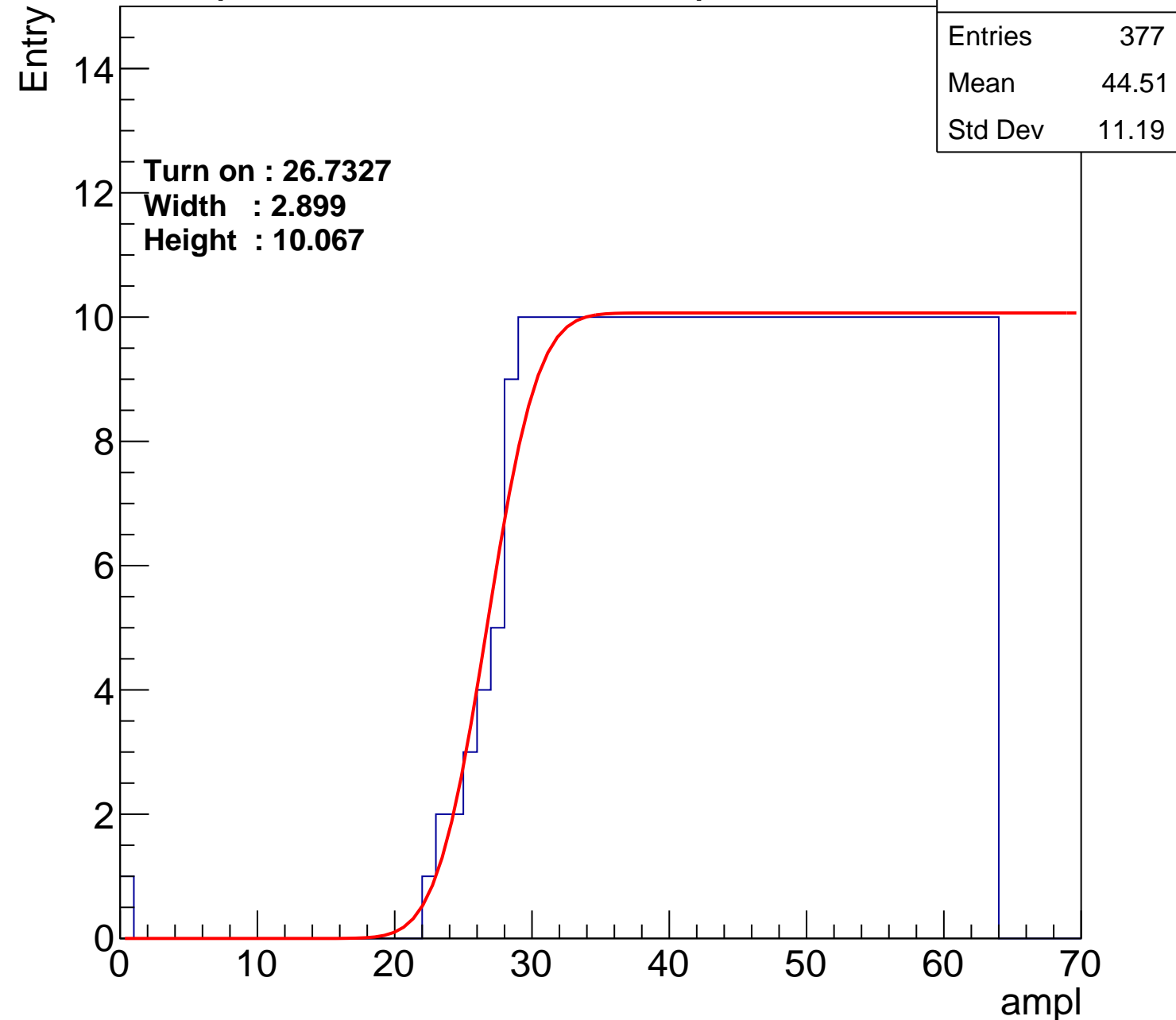
**Width : 2.899**

**Height : 10.067**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch109

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.36
Std Dev	11.51

**Turn on : 26.7697**

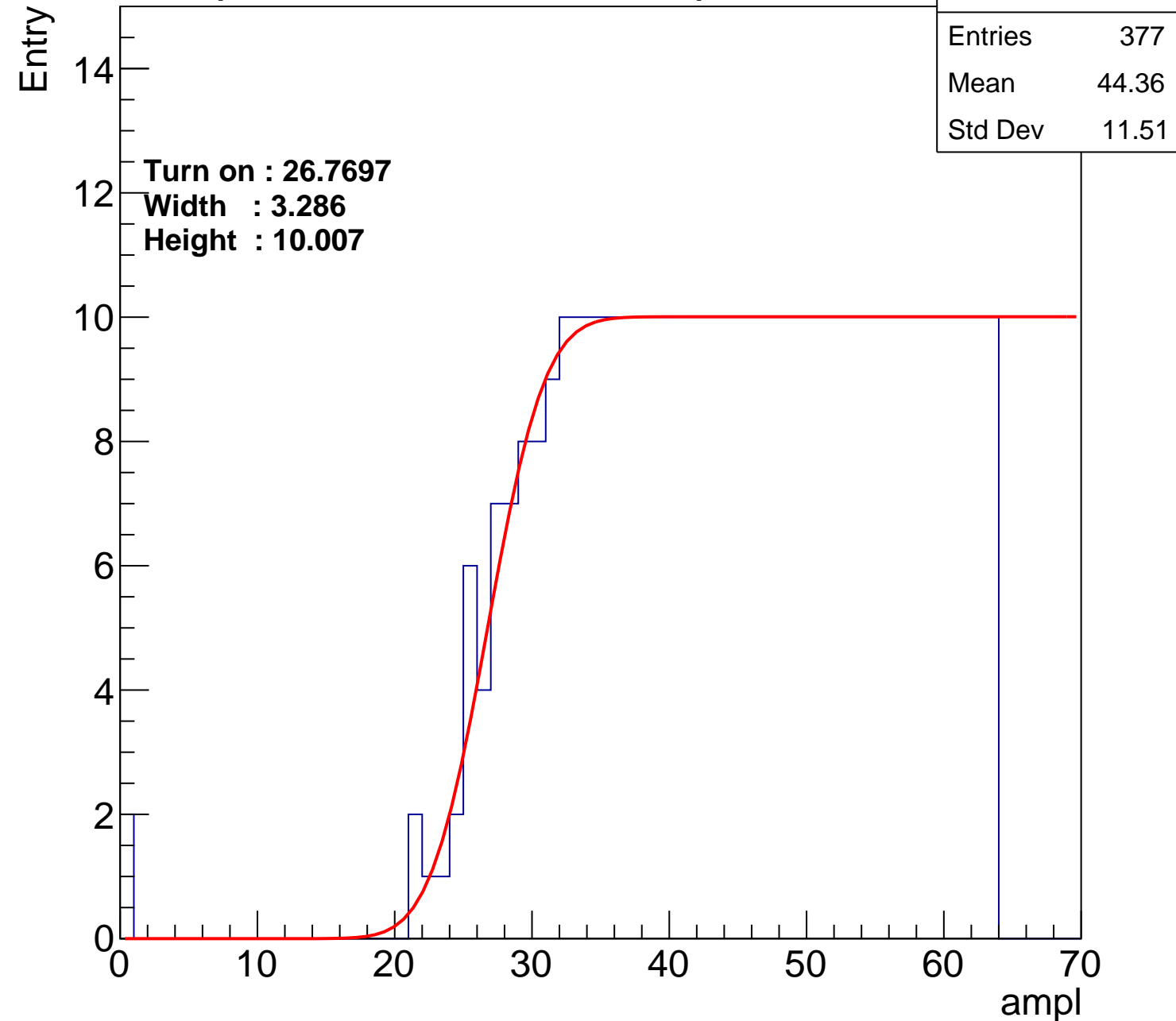
**Width : 3.286**

**Height : 10.007**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch110

calib\_packv5\_042523\_0143.root, FC#5, port B1

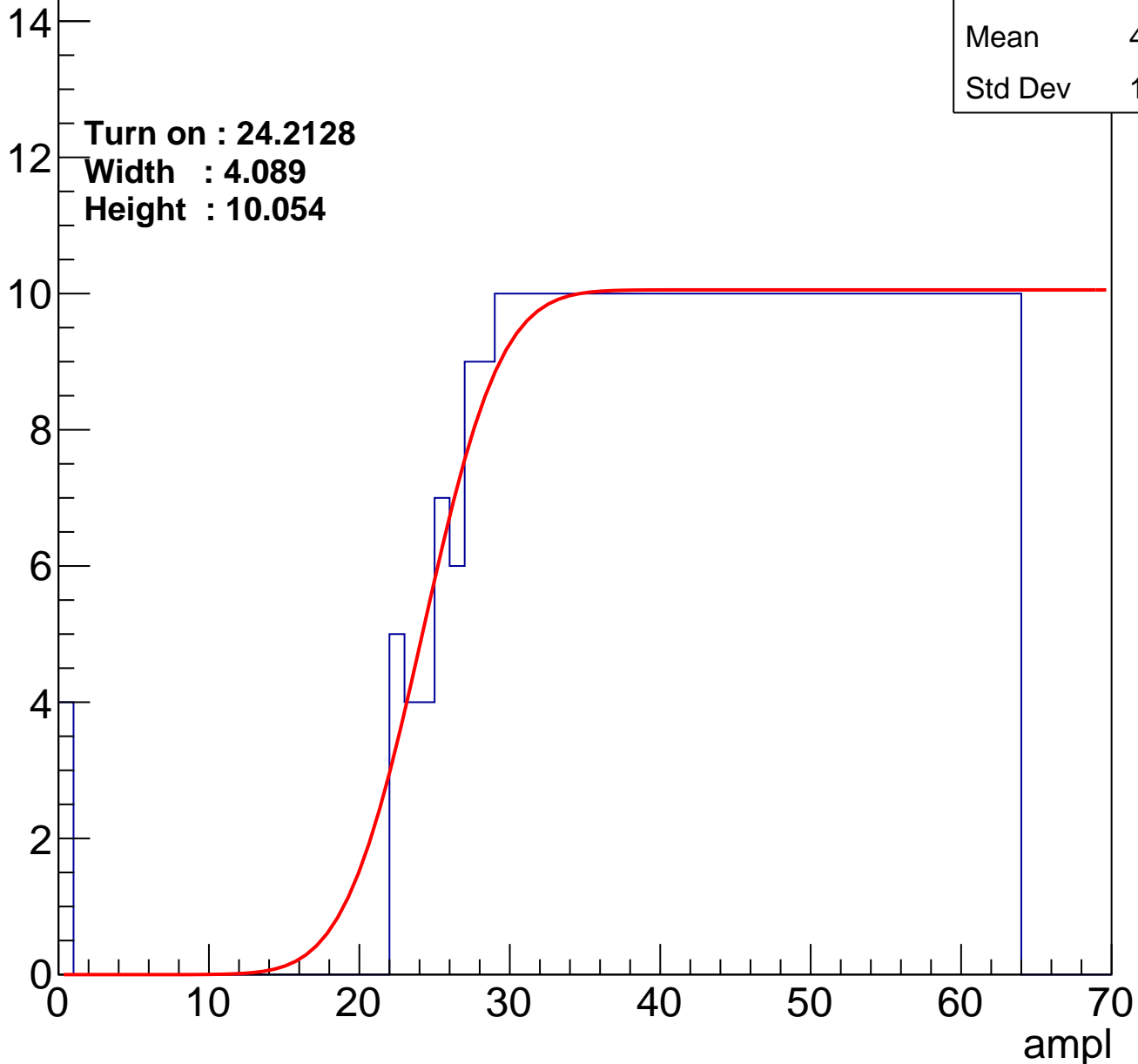
Entries	398
Mean	43.28
Std Dev	12.26

Turn on : 24.2128

Width : 4.089

Height : 10.054

Entry





# B0L000S, U19-ch111

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.23
Std Dev	11.45

**Turn on : 26.0972**

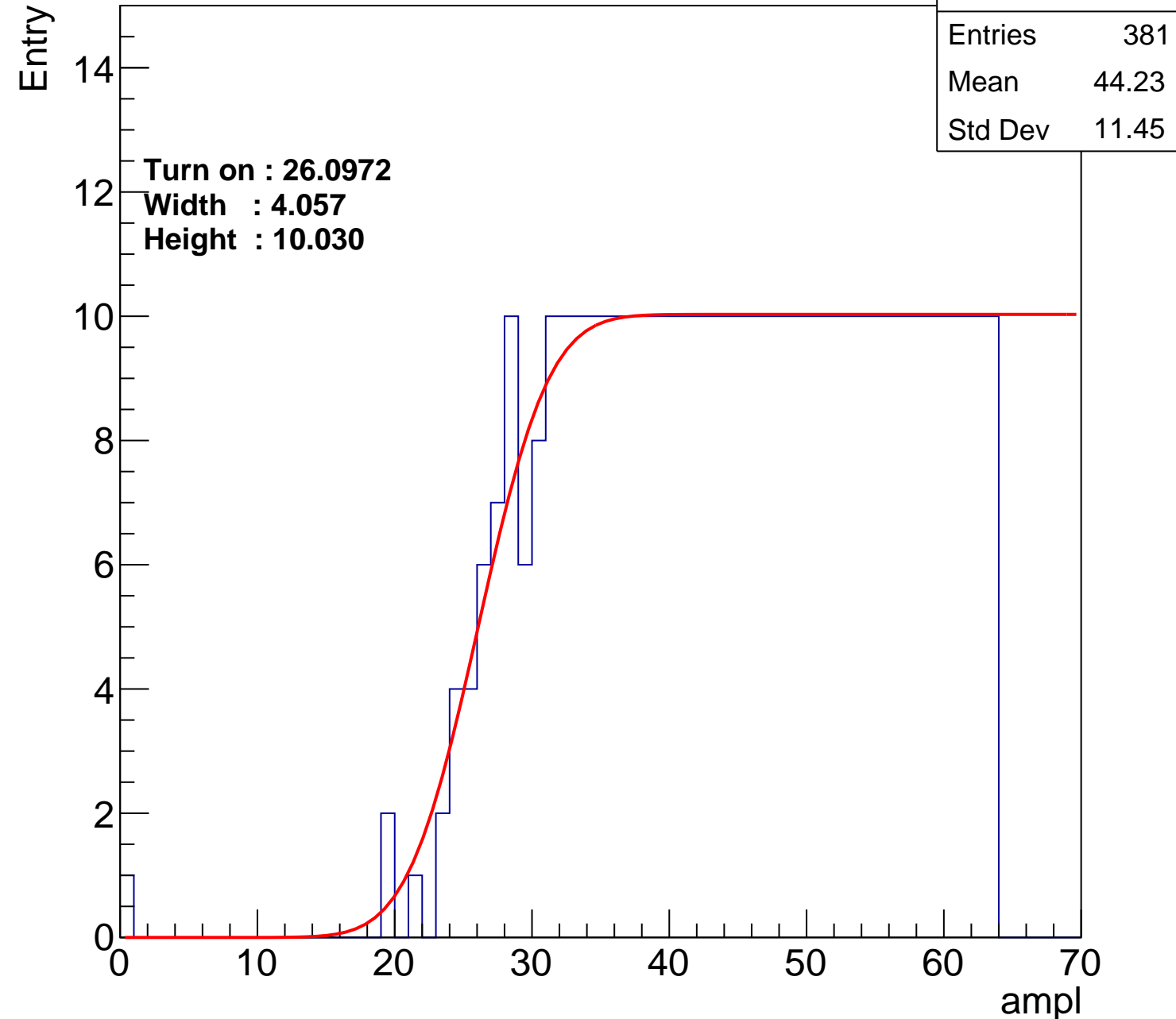
**Width : 4.057**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch112

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.47
Std Dev	11.21

Turn on : 26.3483

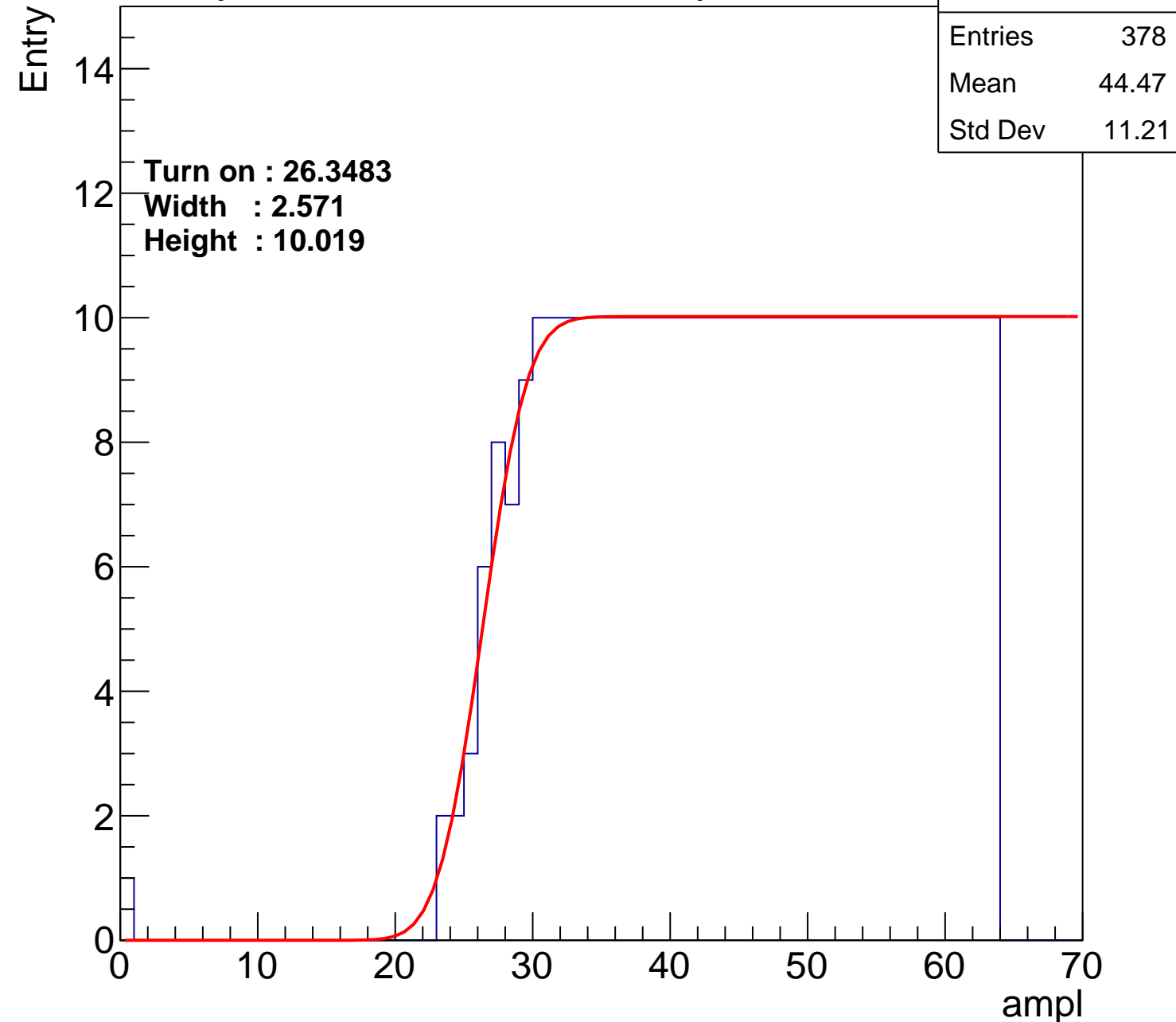
Width : 2.571

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch113

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	355
Mean	45.49
Std Dev	10.89

Turn on : 28.5210

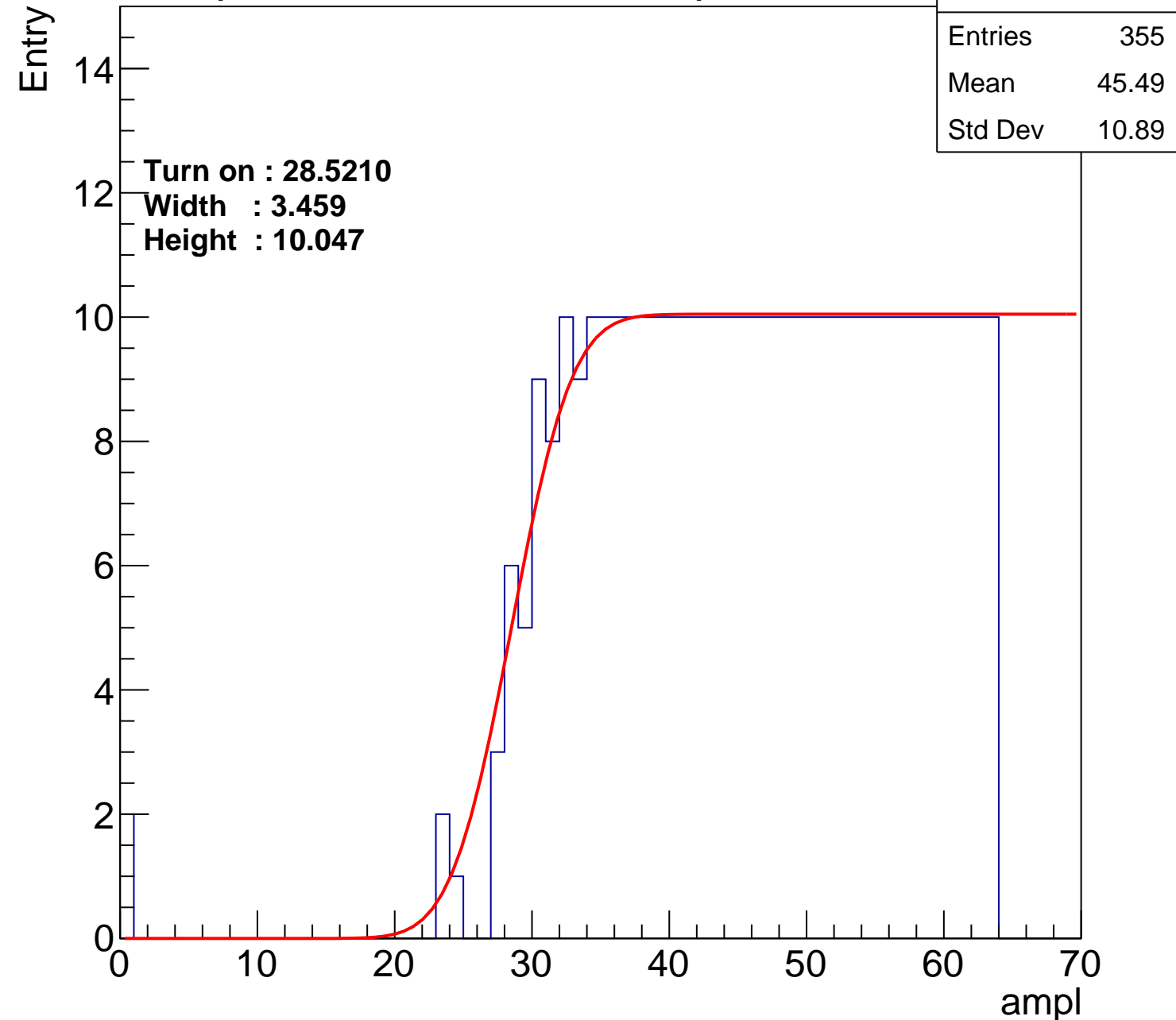
Width : 3.459

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch114

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 26.6691

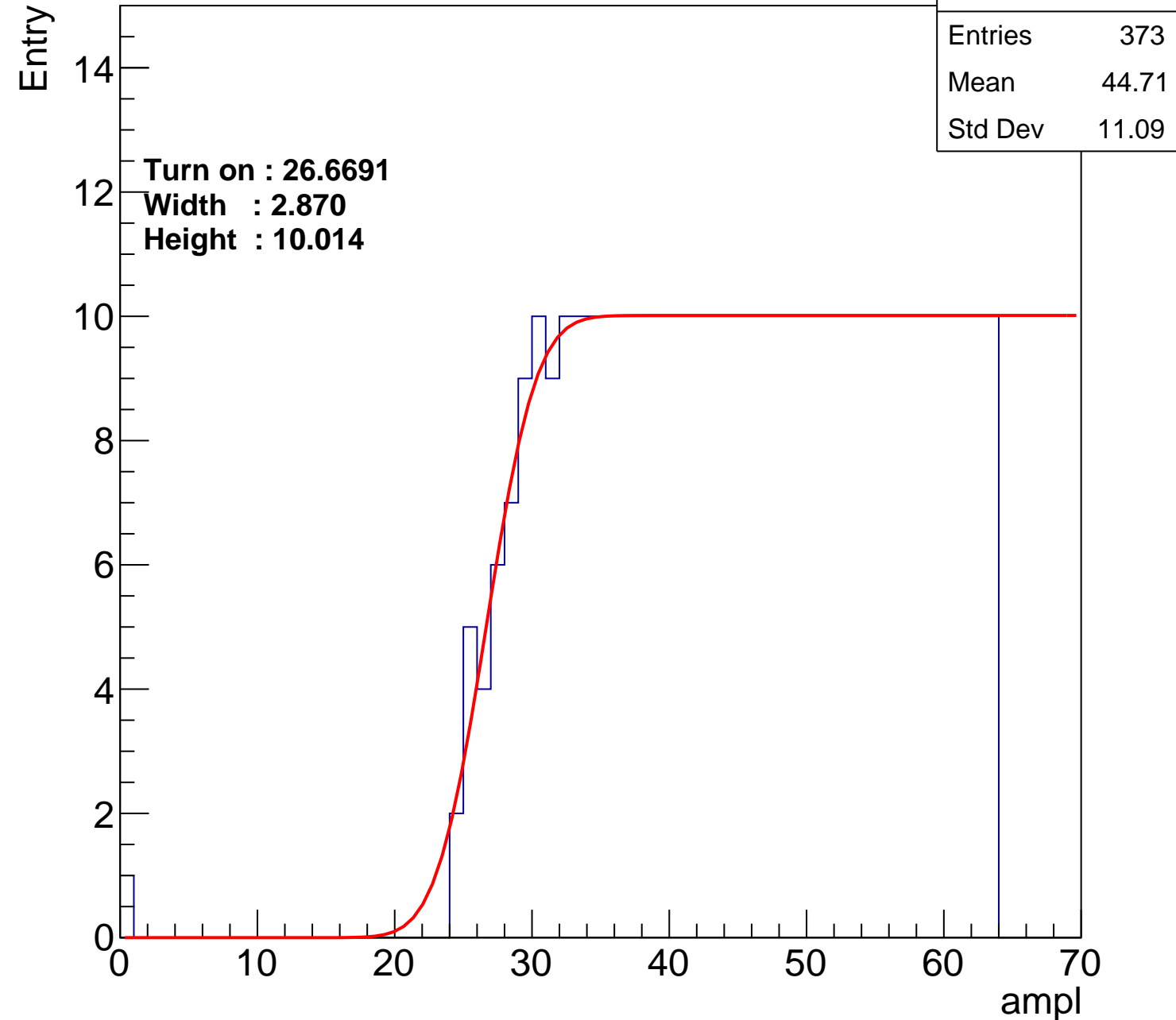
Width : 2.870

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch115

calib\_packv5\_042523\_0143.root, FC#5, port B1

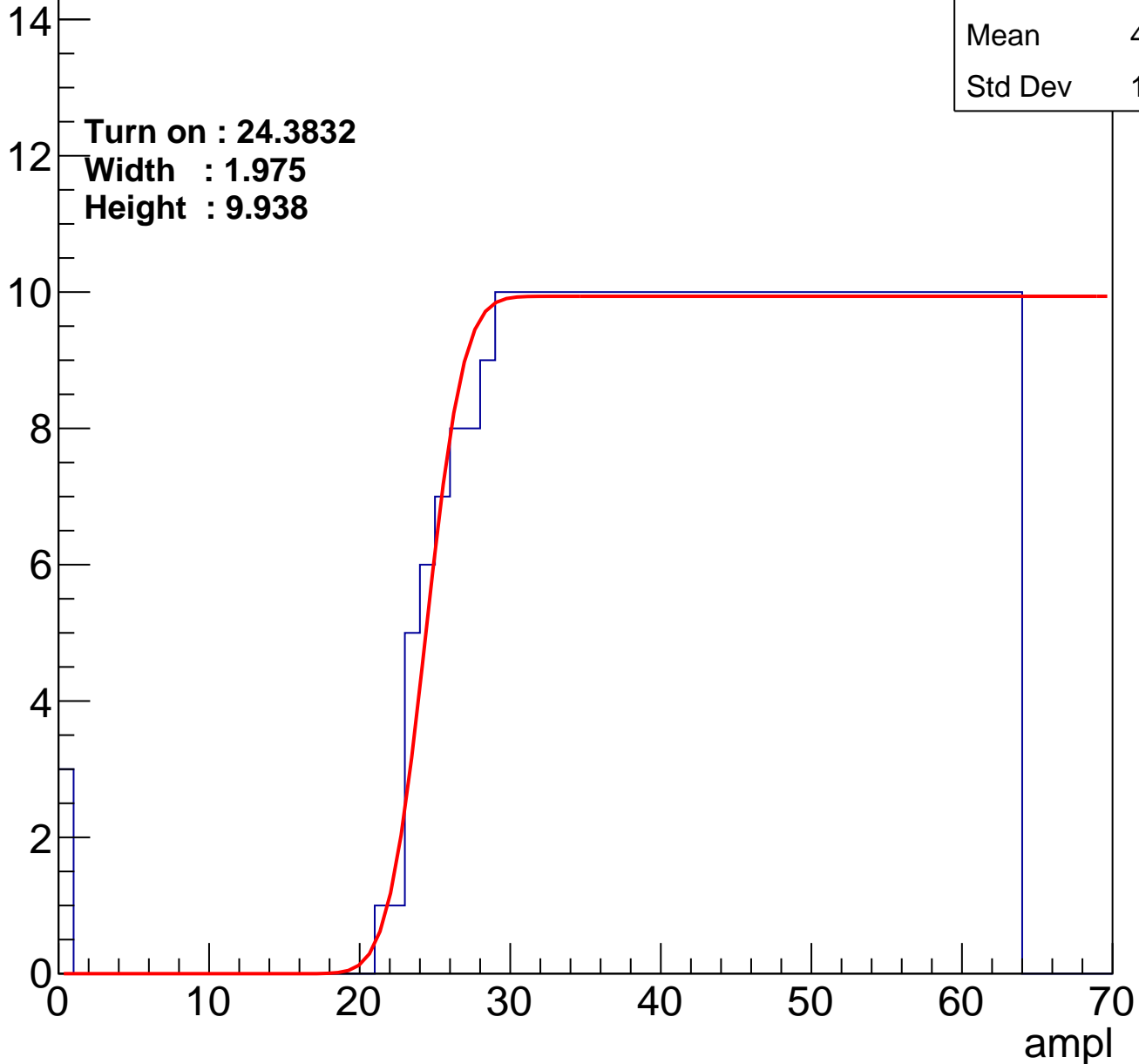
Entries	398
Mean	43.35
Std Dev	12.08

Turn on : 24.3832

Width : 1.975

Height : 9.938

Entry



# B0L000S, U19-ch116

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.8572

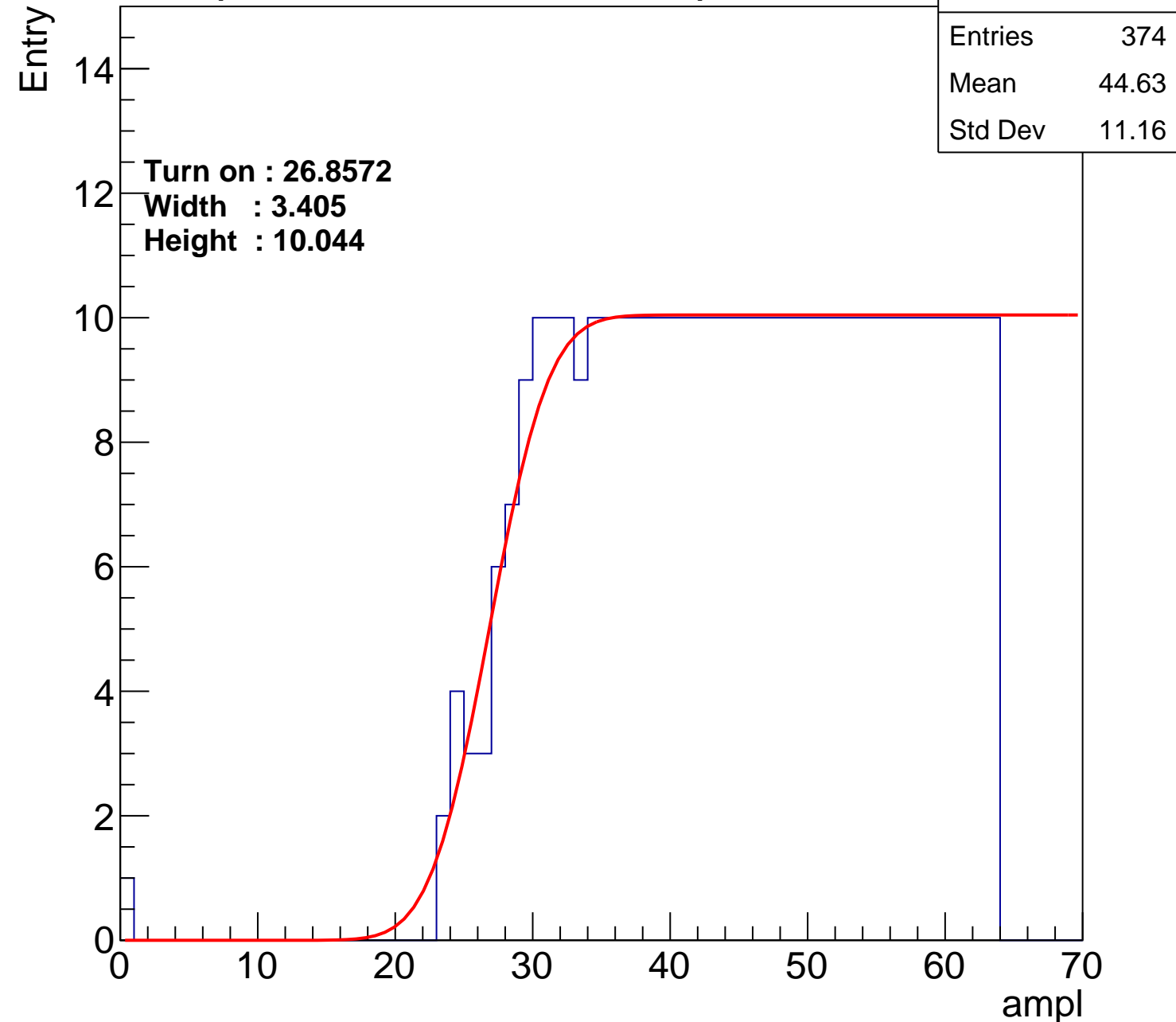
Width : 3.405

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch117

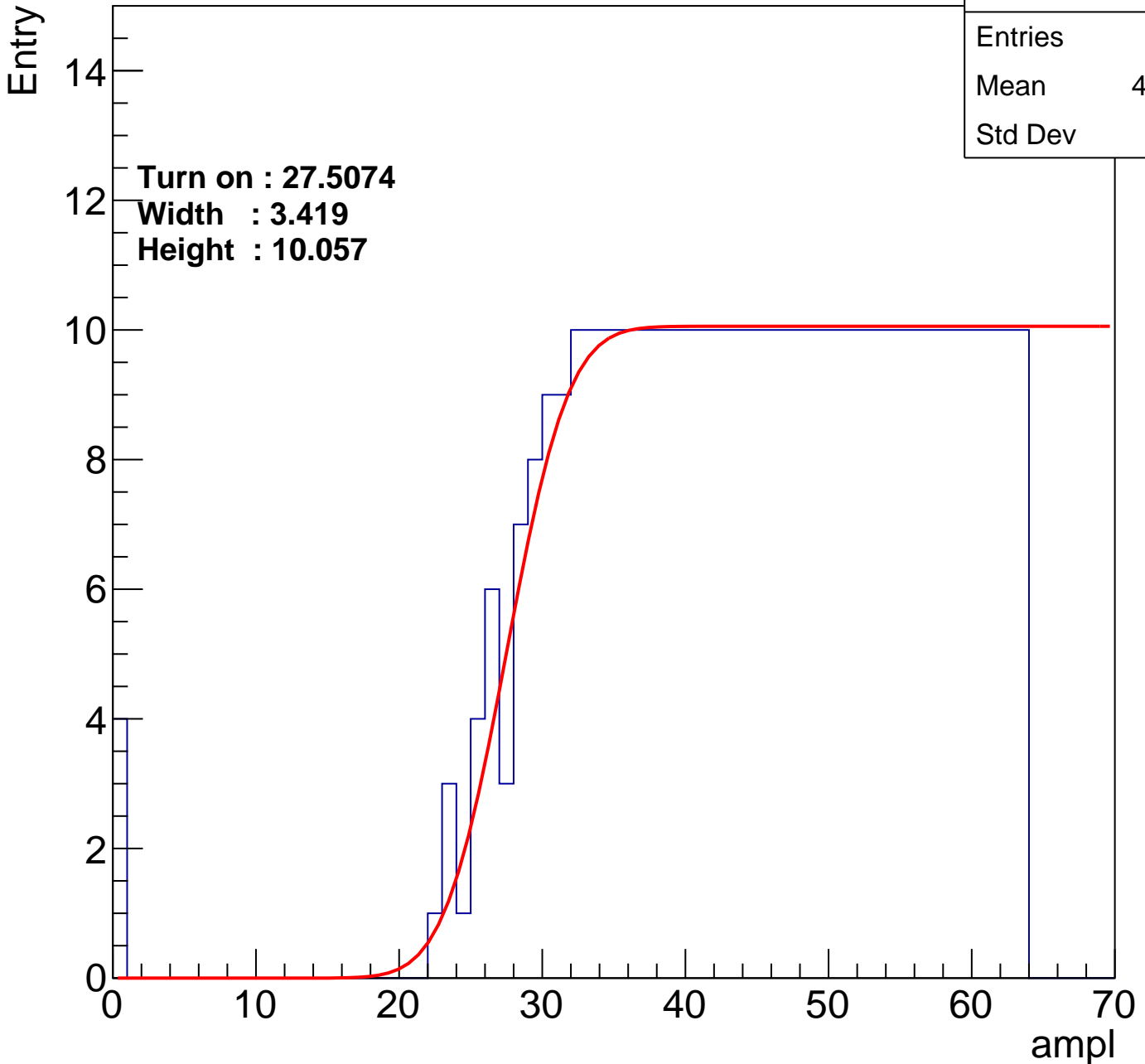
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.34
Std Dev	11.8

Turn on : 27.5074

Width : 3.419

Height : 10.057



# B0L000S, U19-ch118

calib\_packv5\_042523\_0143.root, FC#5, port B1

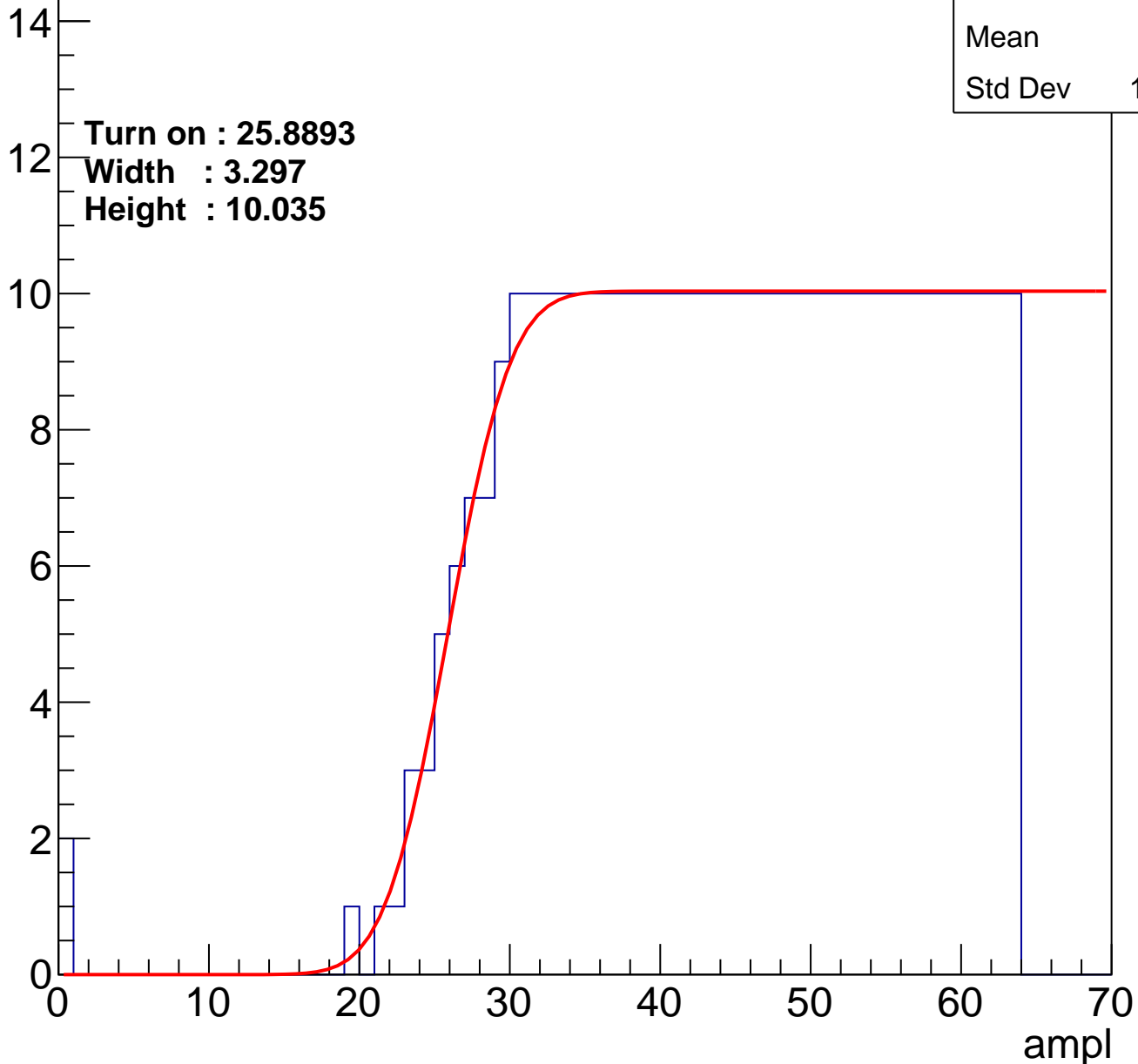
Entries	385
Mean	44
Std Dev	11.67

Turn on : 25.8893

Width : 3.297

Height : 10.035

Entry





# B0L000S, U19-ch119

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.21
Std Dev	11.8

**Turn on : 26.2131**

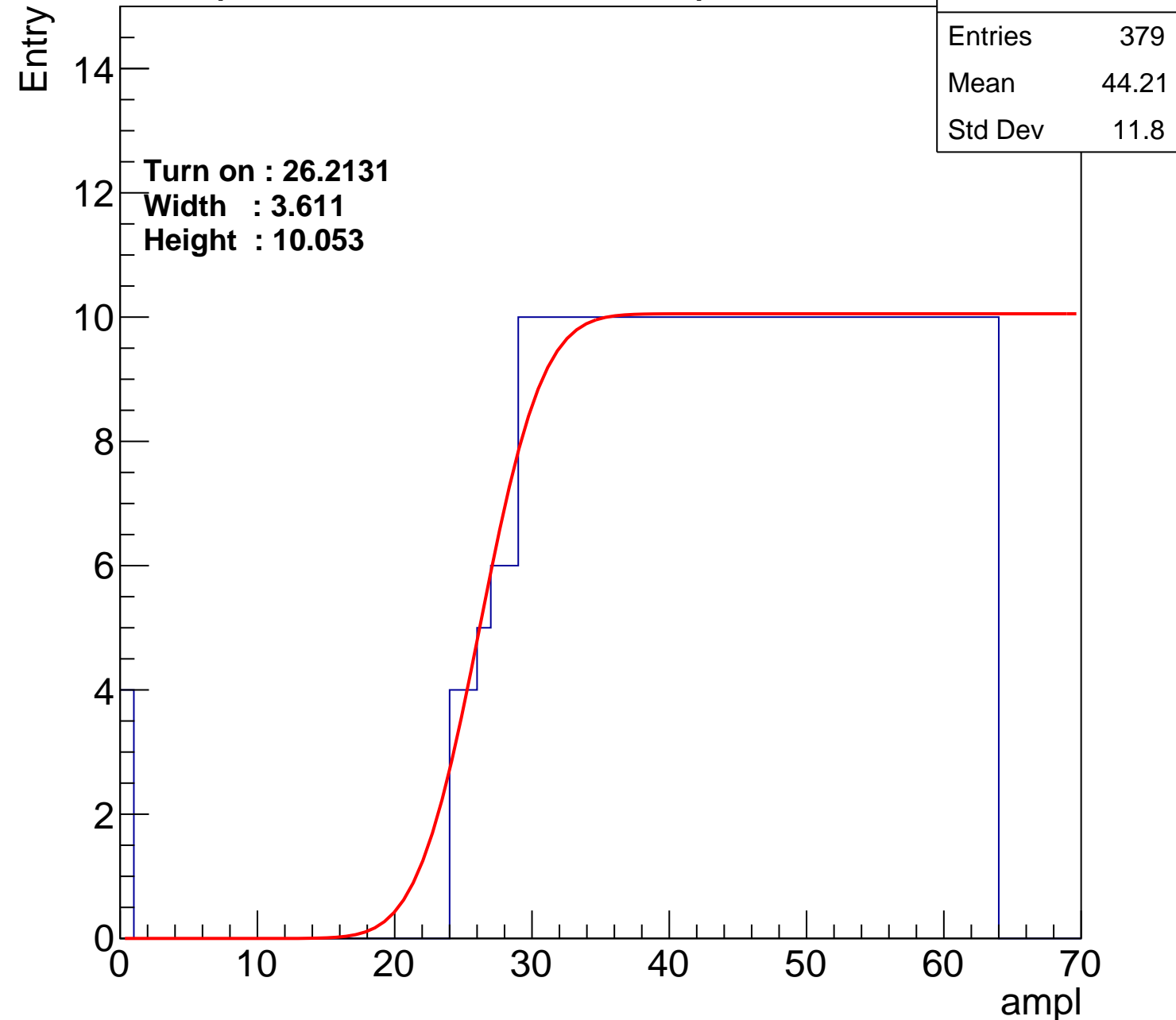
**Width : 3.611**

**Height : 10.053**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch120

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.56
Std Dev	11.17

**Turn on : 26.6724**

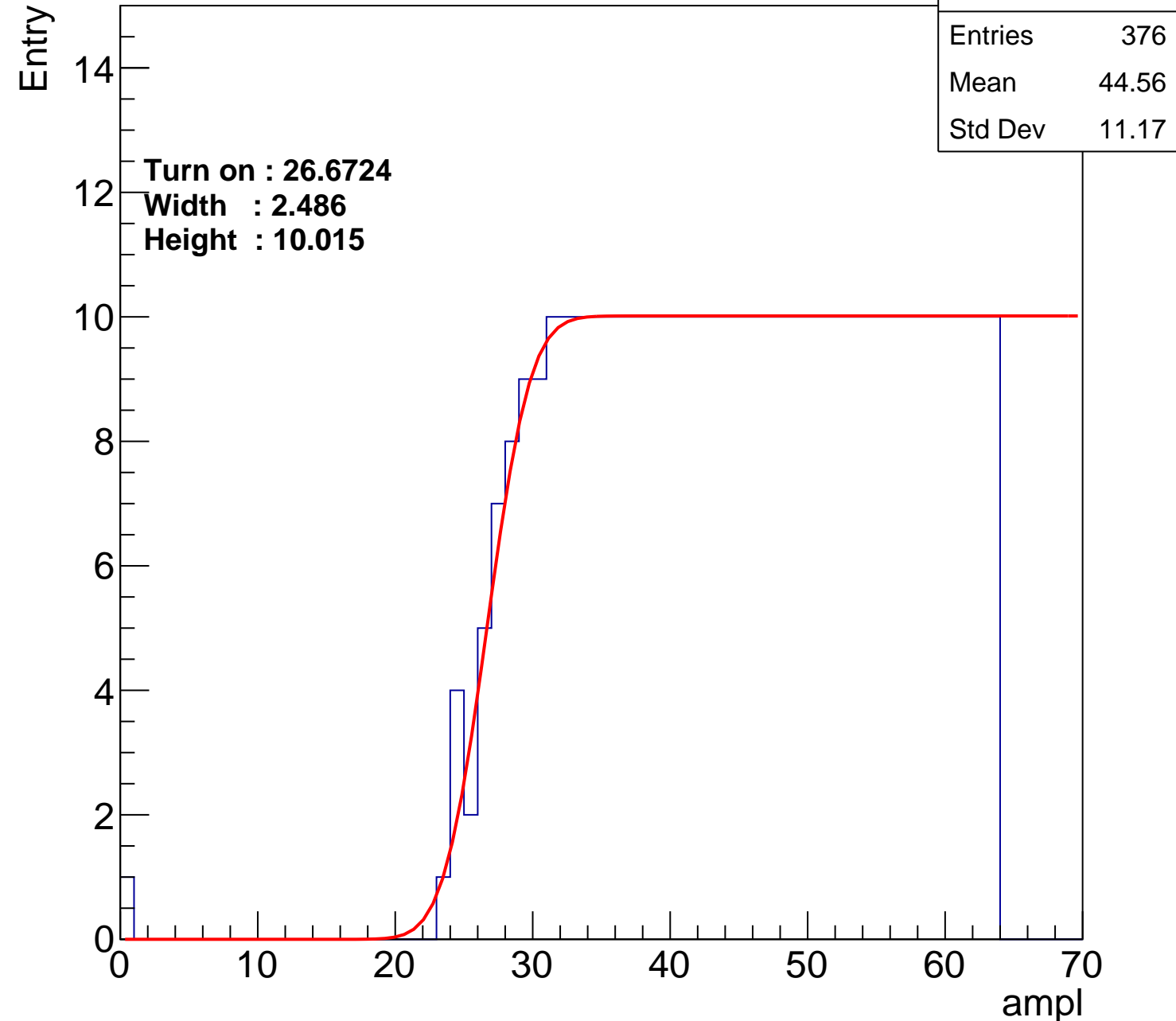
**Width : 2.486**

**Height : 10.015**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch121

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.53
Std Dev	11.36

**Turn on : 26.8724**

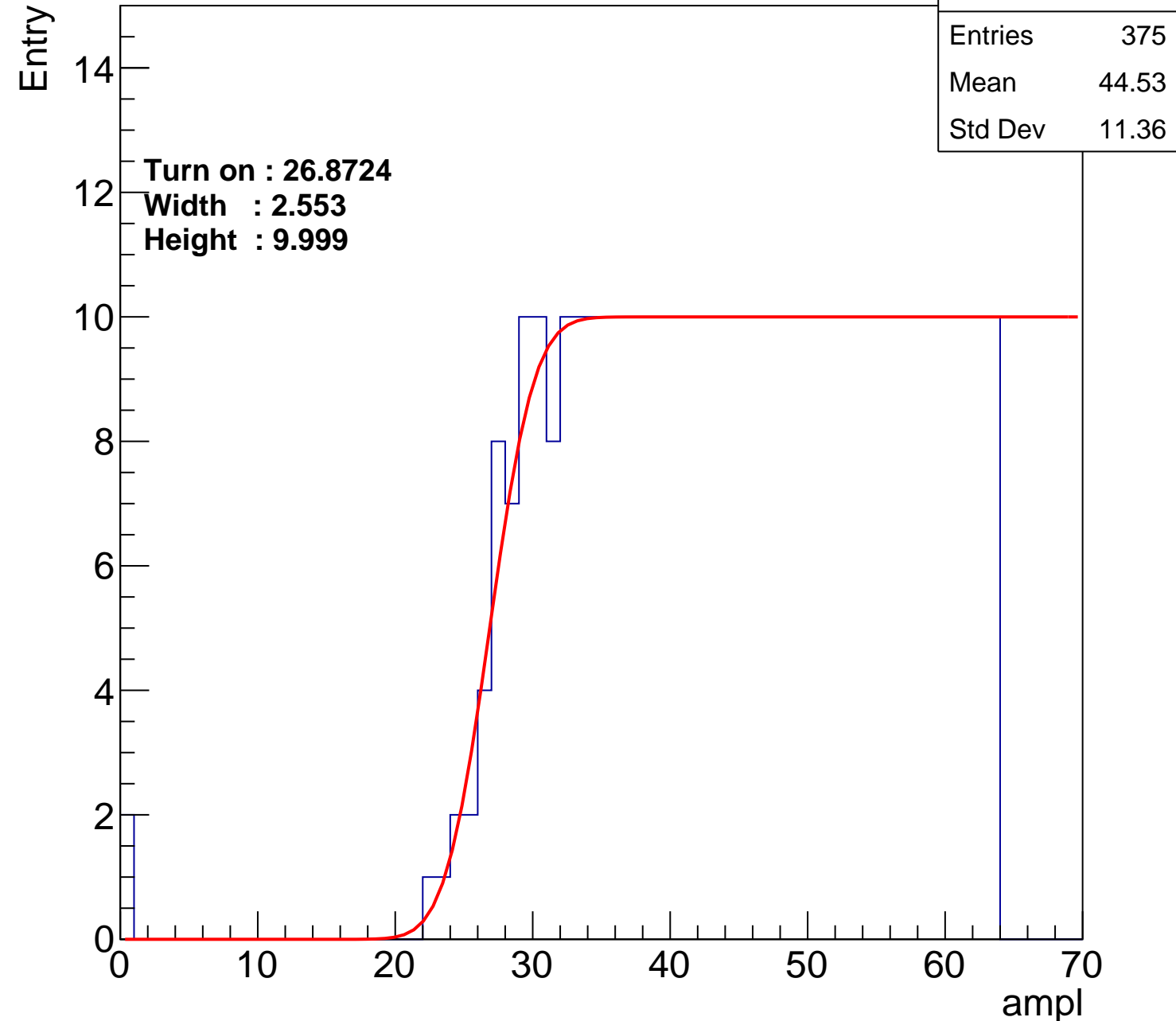
**Width : 2.553**

**Height : 9.999**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch122

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.22
Std Dev	12.02

**Turn on : 27.1357**

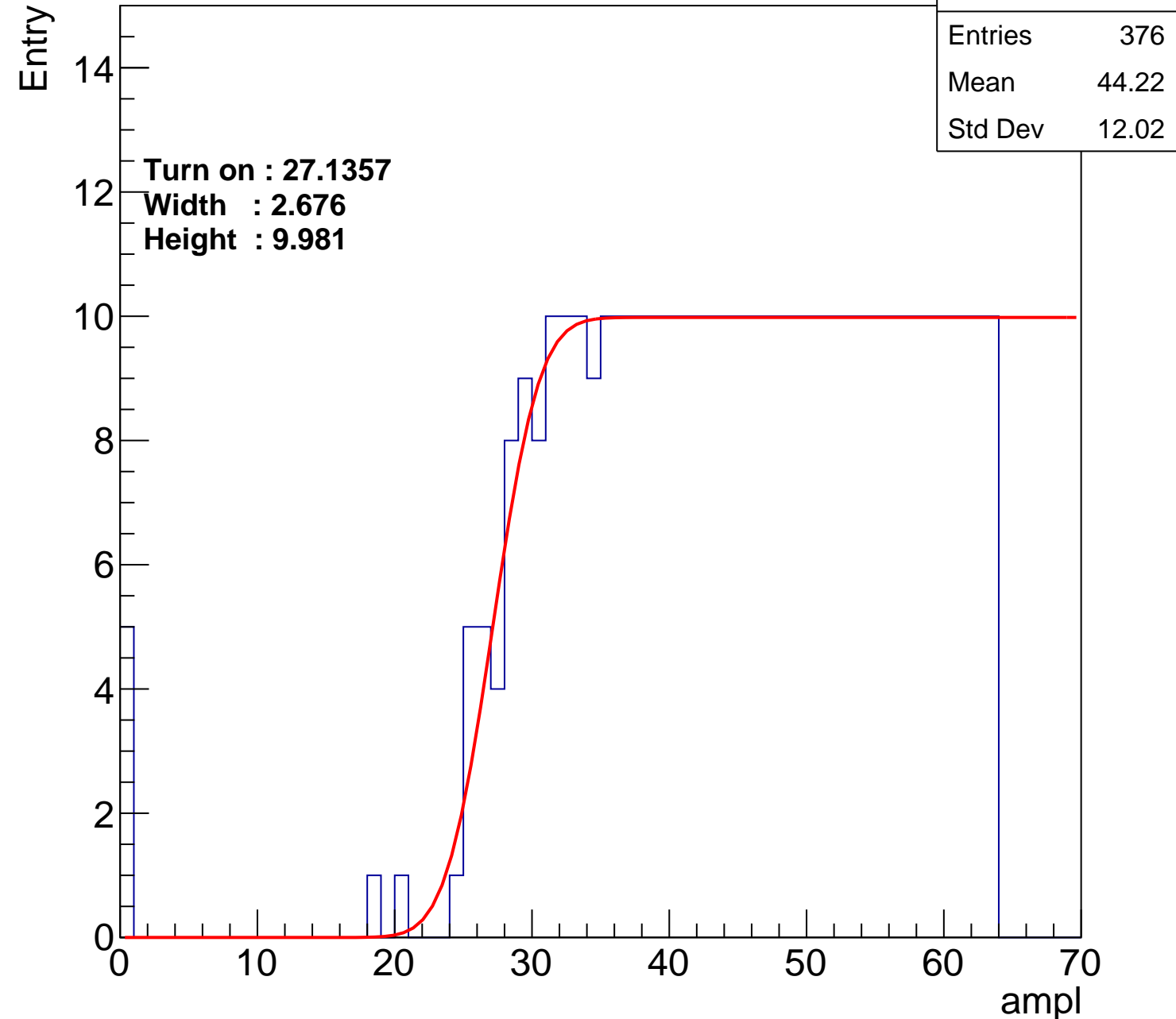
**Width : 2.676**

**Height : 9.981**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



B0L000S, U19-ch123

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	391
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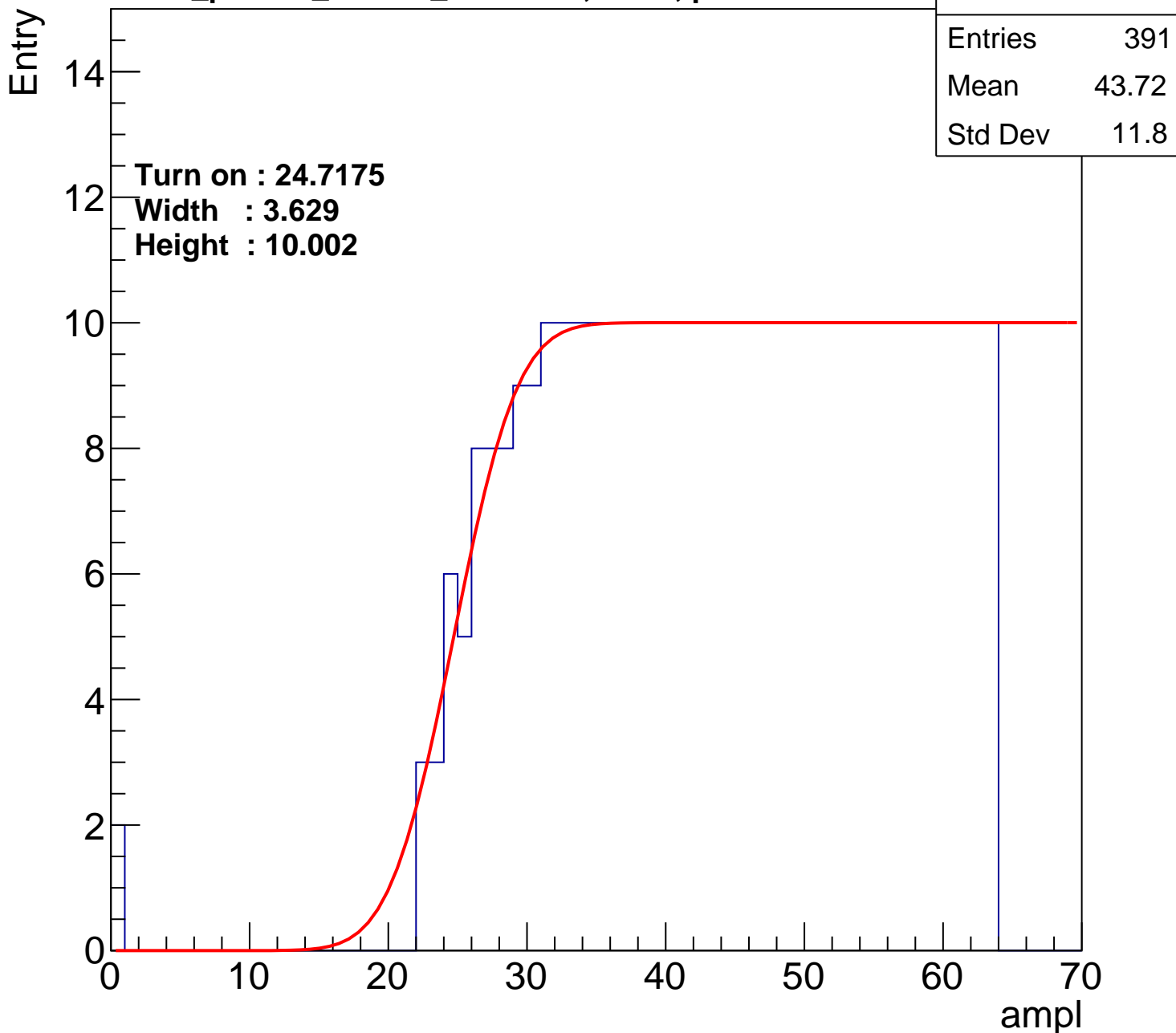
Mean	43.72
------	-------

Std Dev	11.8
---------	------

Turn on : 24.7175

**Width : 3.629**

**Height : 10.002**



# B0L000S, U19-ch124

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.23
Std Dev	11.64

Turn on : 26.6335

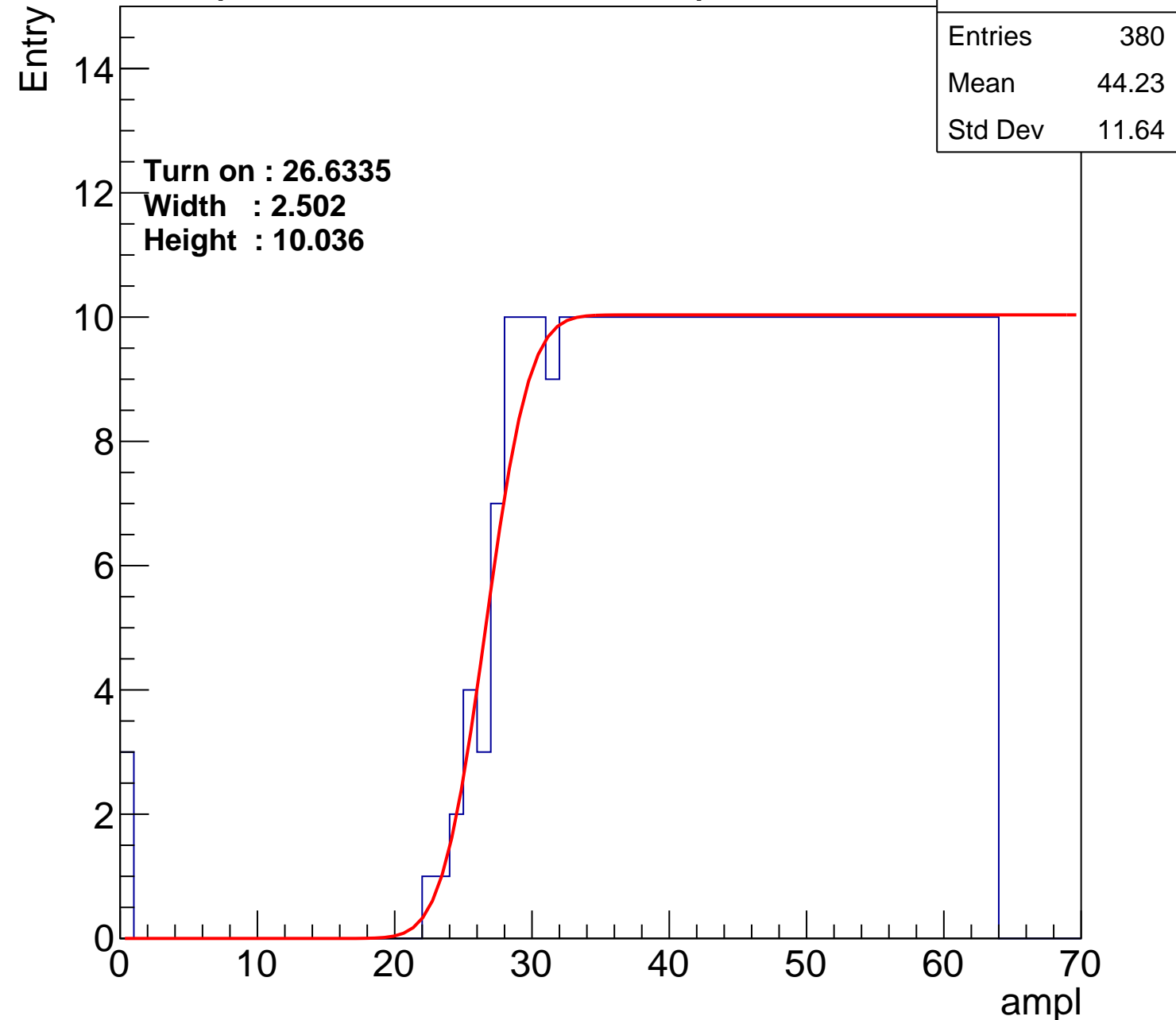
Width : 2.502

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch125

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	44.89
Std Dev	11.53

Turn on : 28.2229

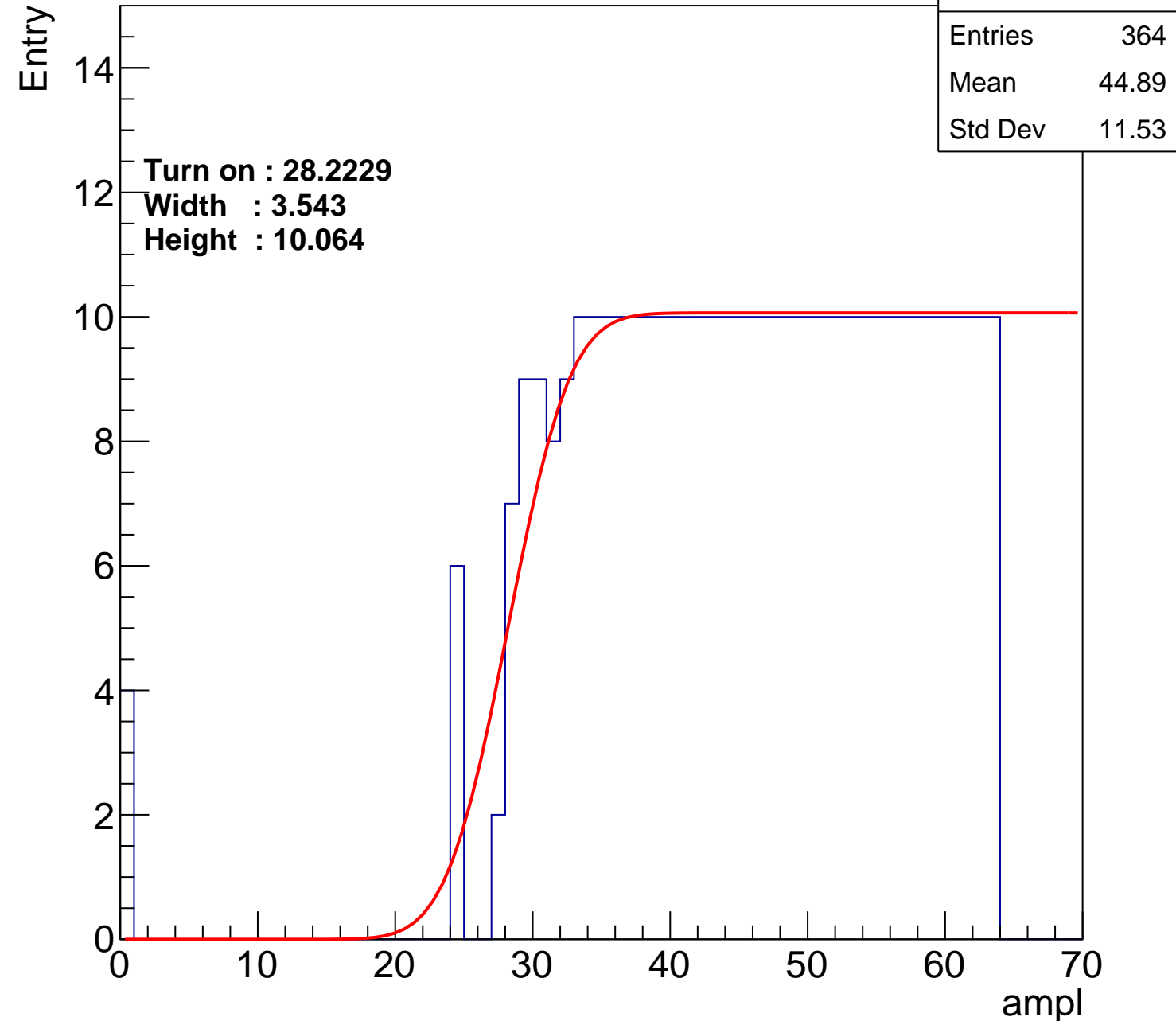
Width : 3.543

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch126

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.5928

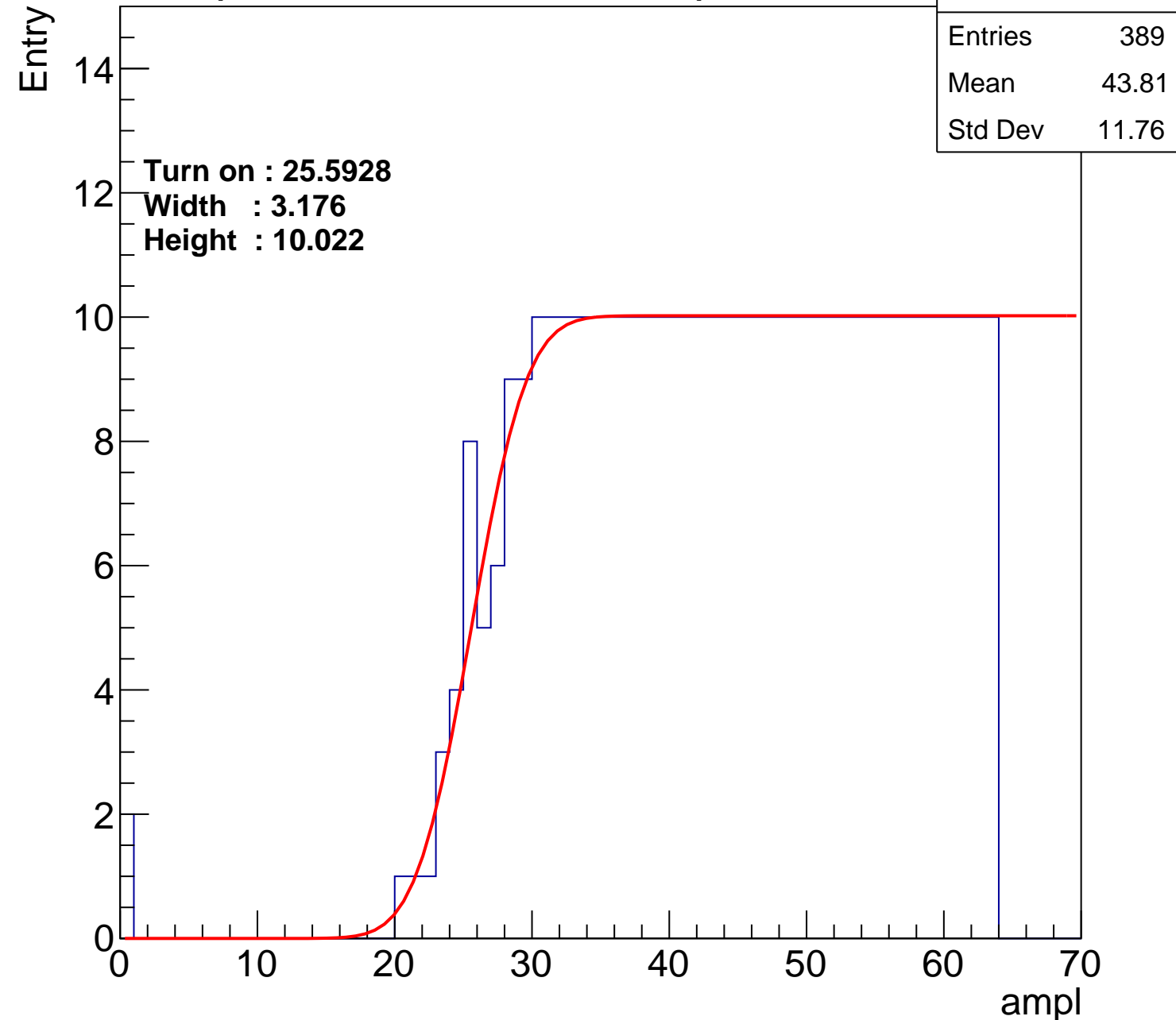
Width : 3.176

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U19-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 27.5713

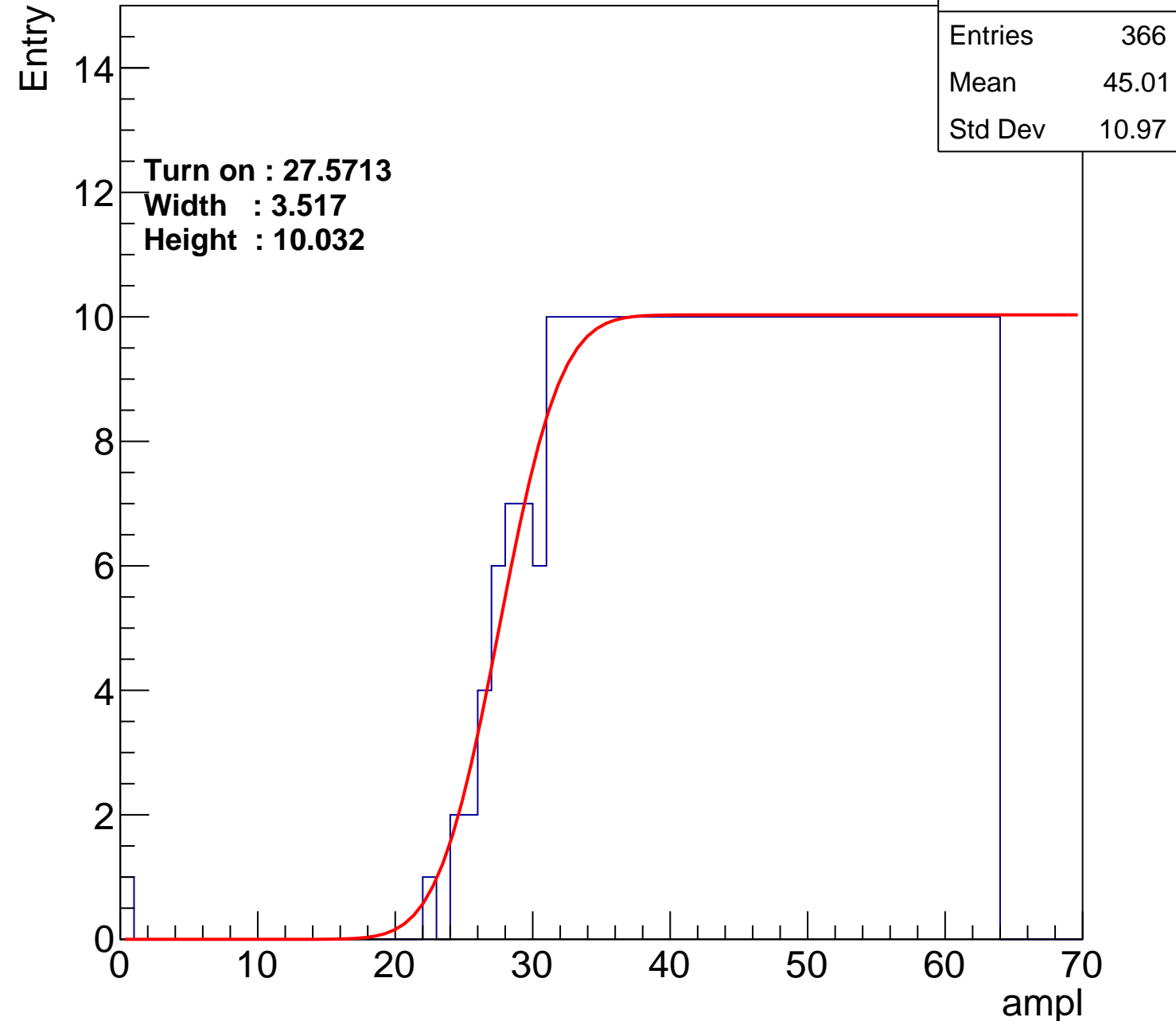
Width : 3.517

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U19-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 27.5713

Width : 3.517

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

