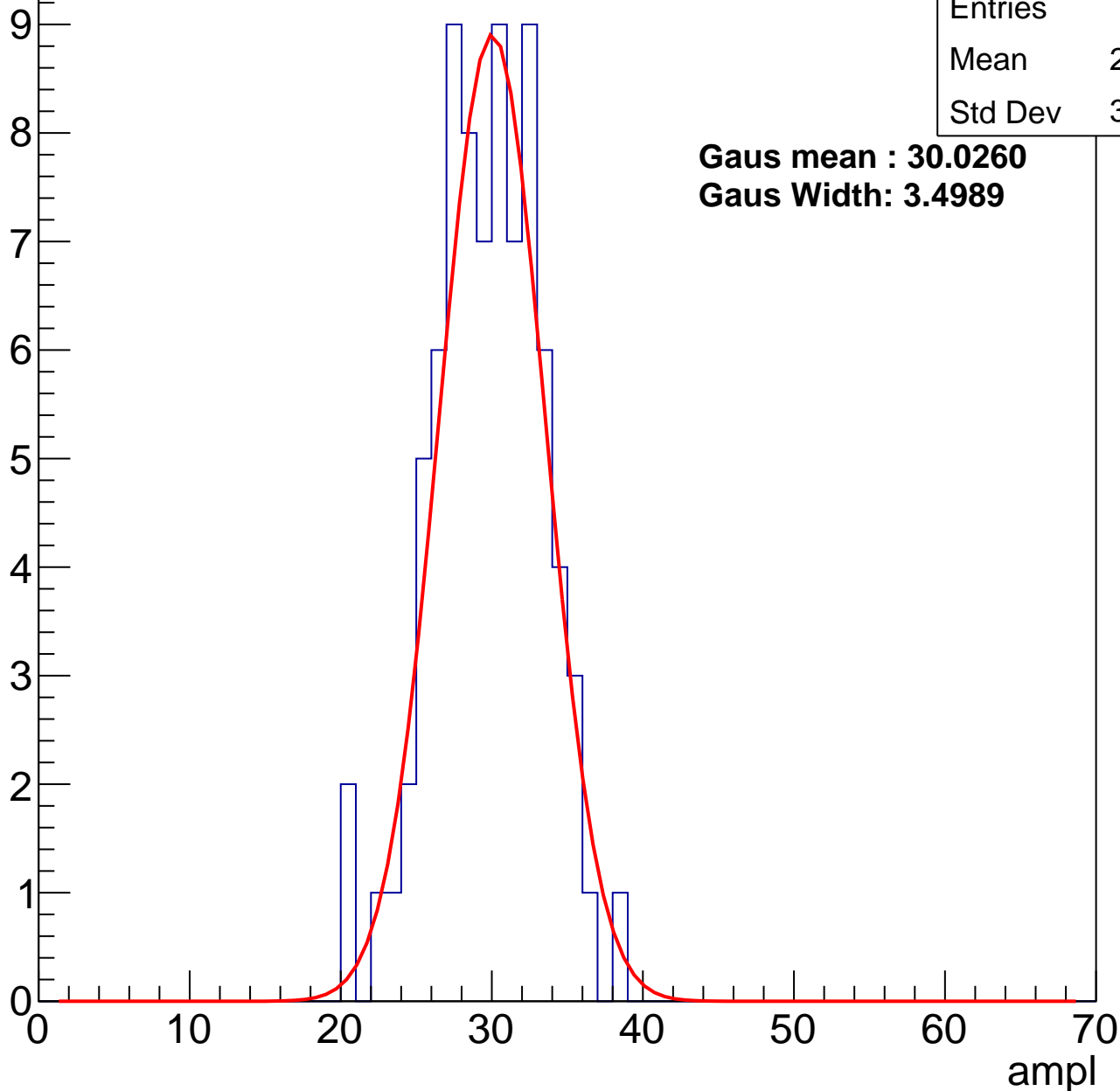


B0L001S, U7-ch0, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch0, adc1

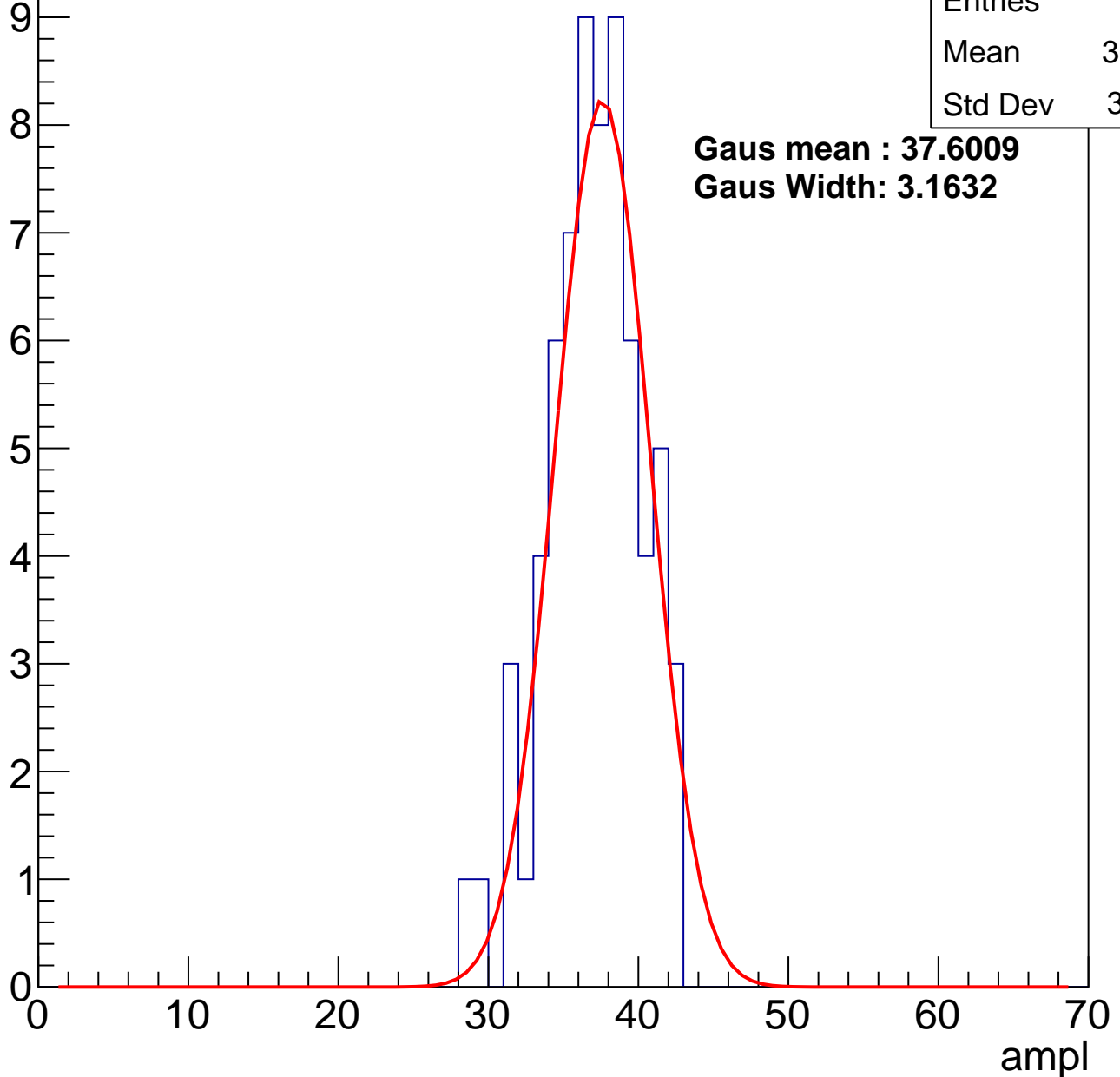
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.57 |
| Std Dev | 3.101 |

Gaus mean : 37.6009

Gaus Width: 3.1632



B0L001S, U7-ch0, adc2

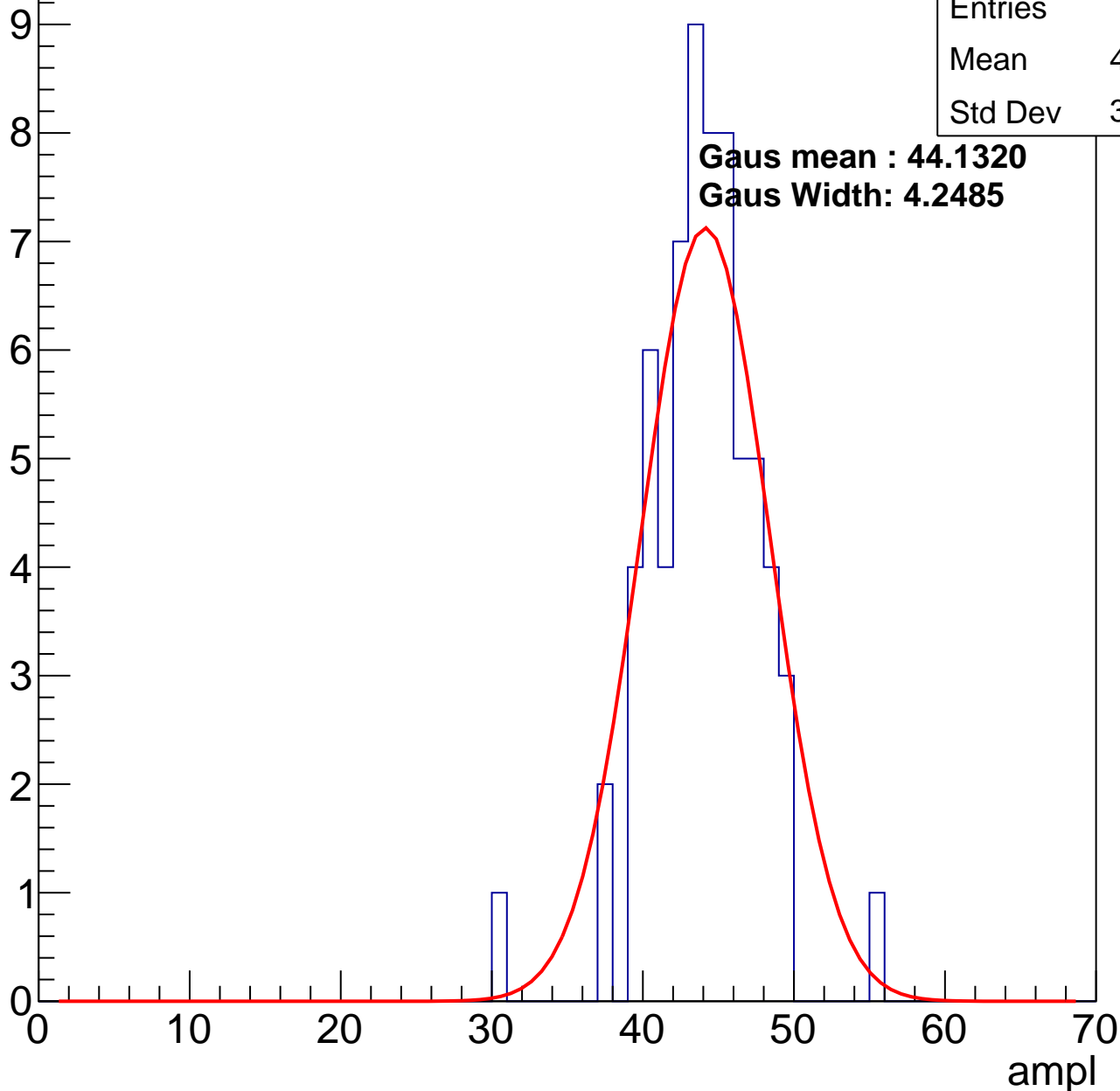
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.52 |
| Std Dev | 3.626 |

Gaus mean : 44.1320

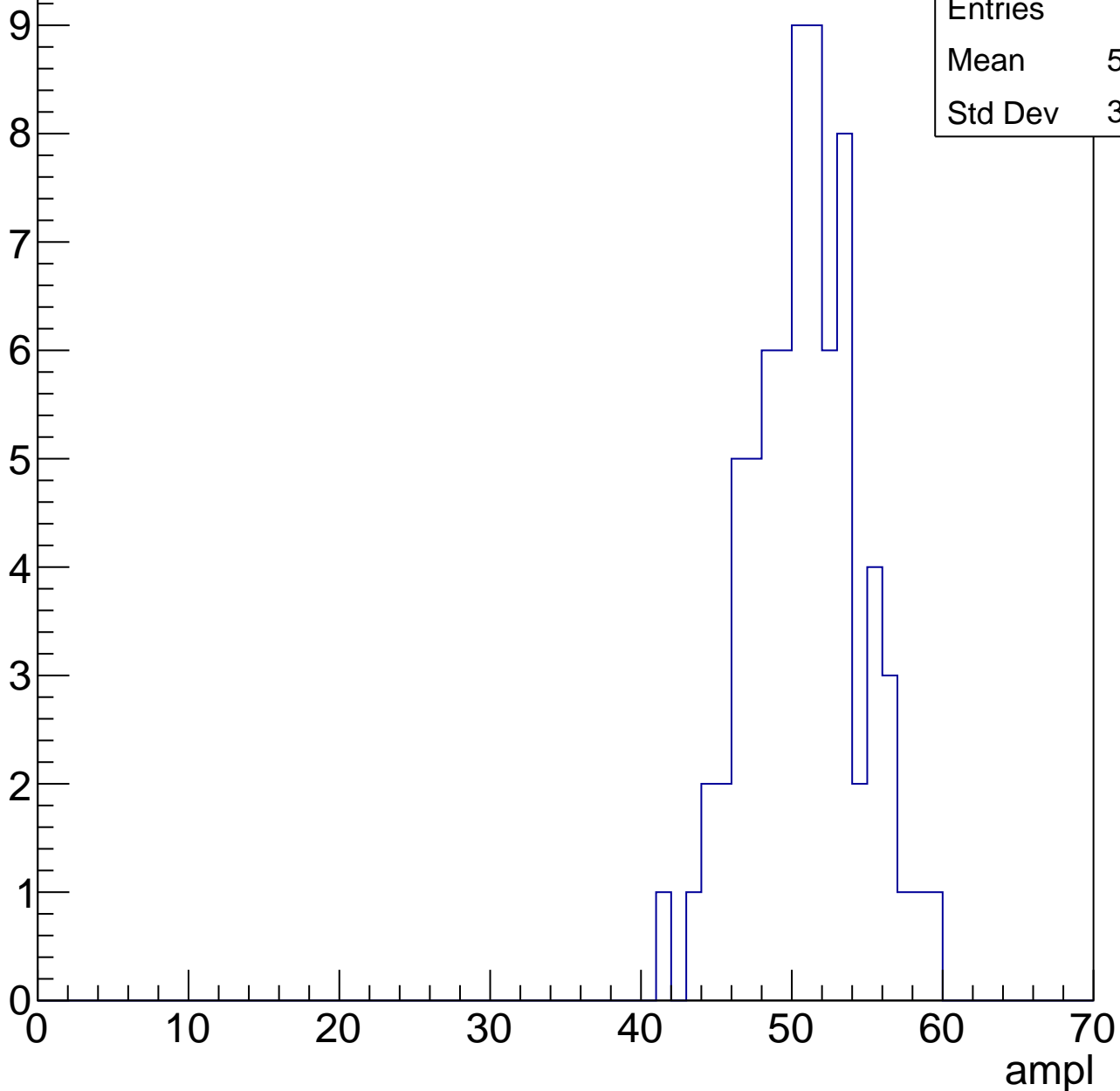
Gaus Width: 4.2485



B0L001S, U7-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

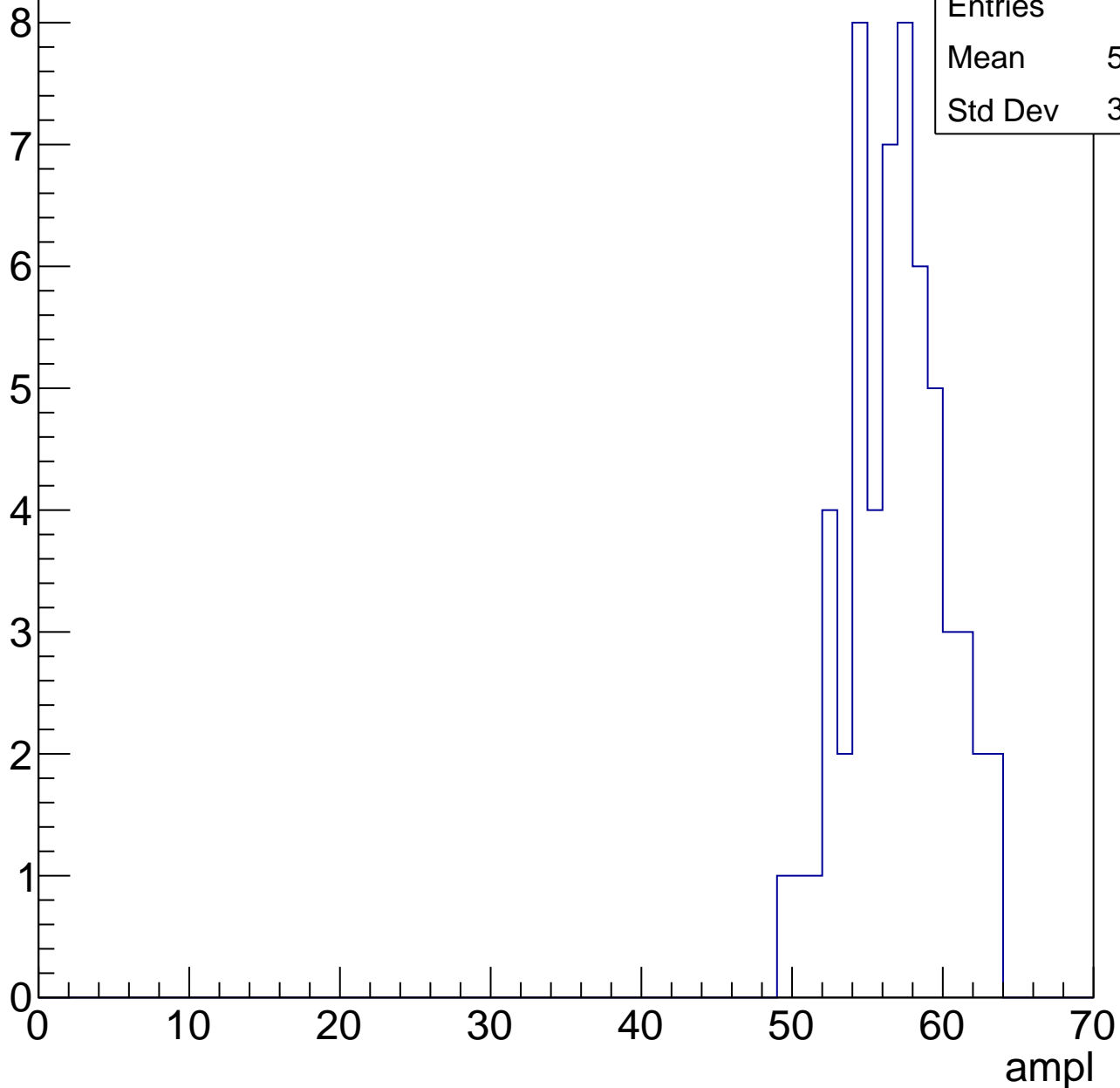


| | |
|---------|-------|
| Entries | 72 |
| Mean | 50.33 |
| Std Dev | 3.609 |

B0L001S, U7-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 57 |
| Mean | 56.49 |
| Std Dev | 3.202 |

B0L001S, U7-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

34

Mean

60.56

Std Dev

1.913

0

1

2

3

4

5

6

7

B0L001S, U7-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

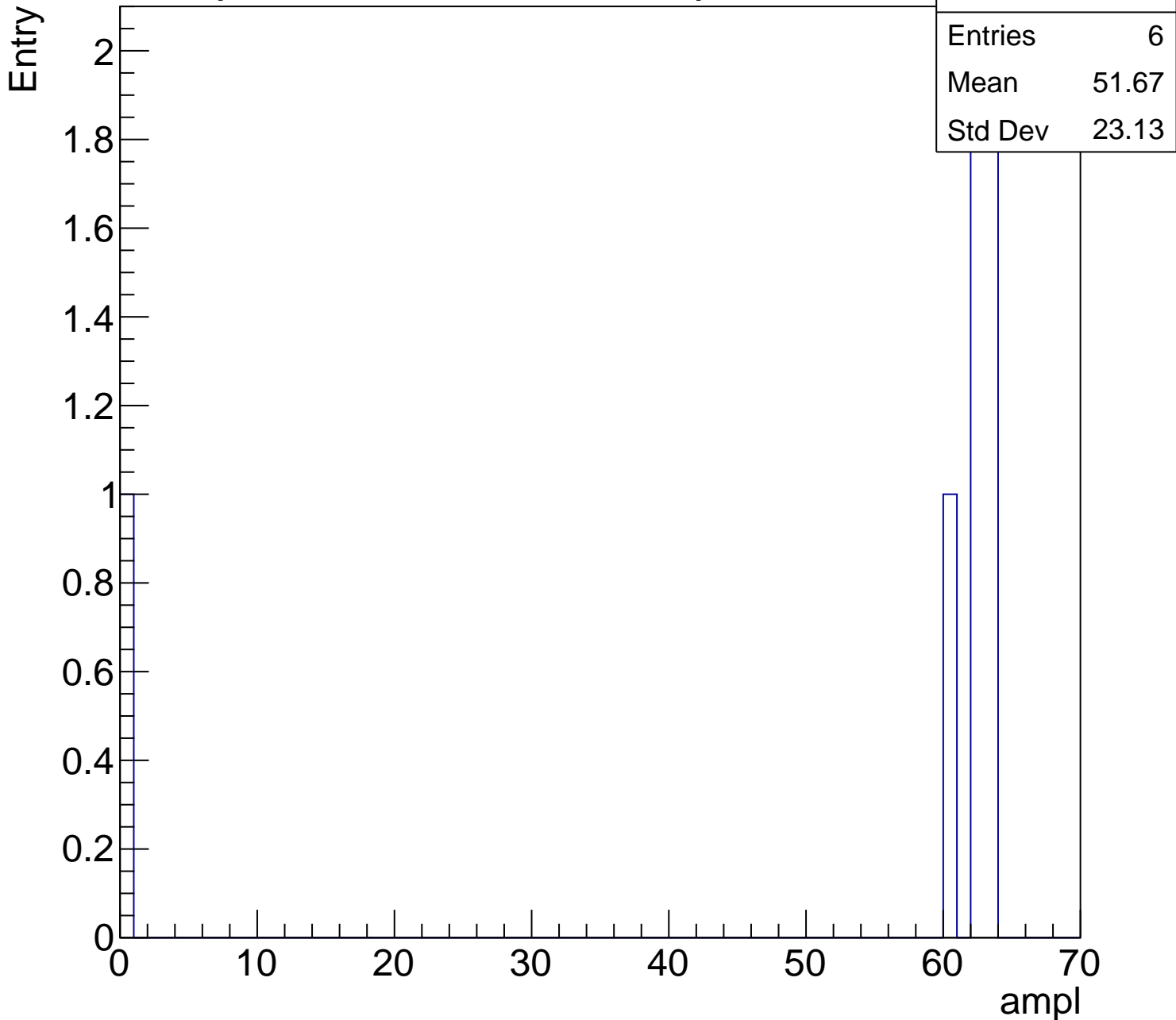
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.67 |
| Std Dev | 23.13 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch1, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 77 |
| Mean | 31.39 |
| Std Dev | 6.037 |

Gaus mean : 32.9216

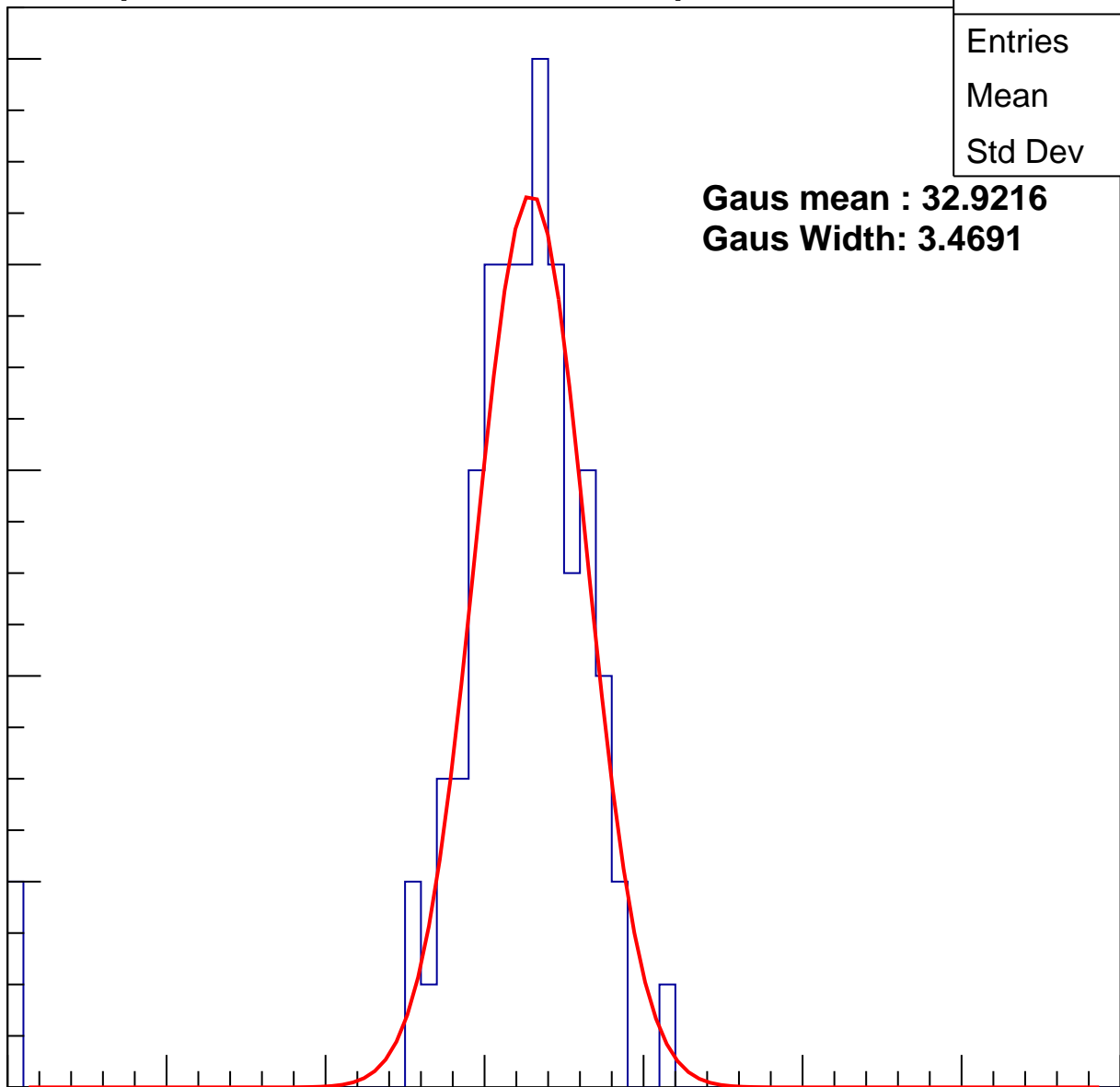
Gaus Width: 3.4691

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch1, adc1

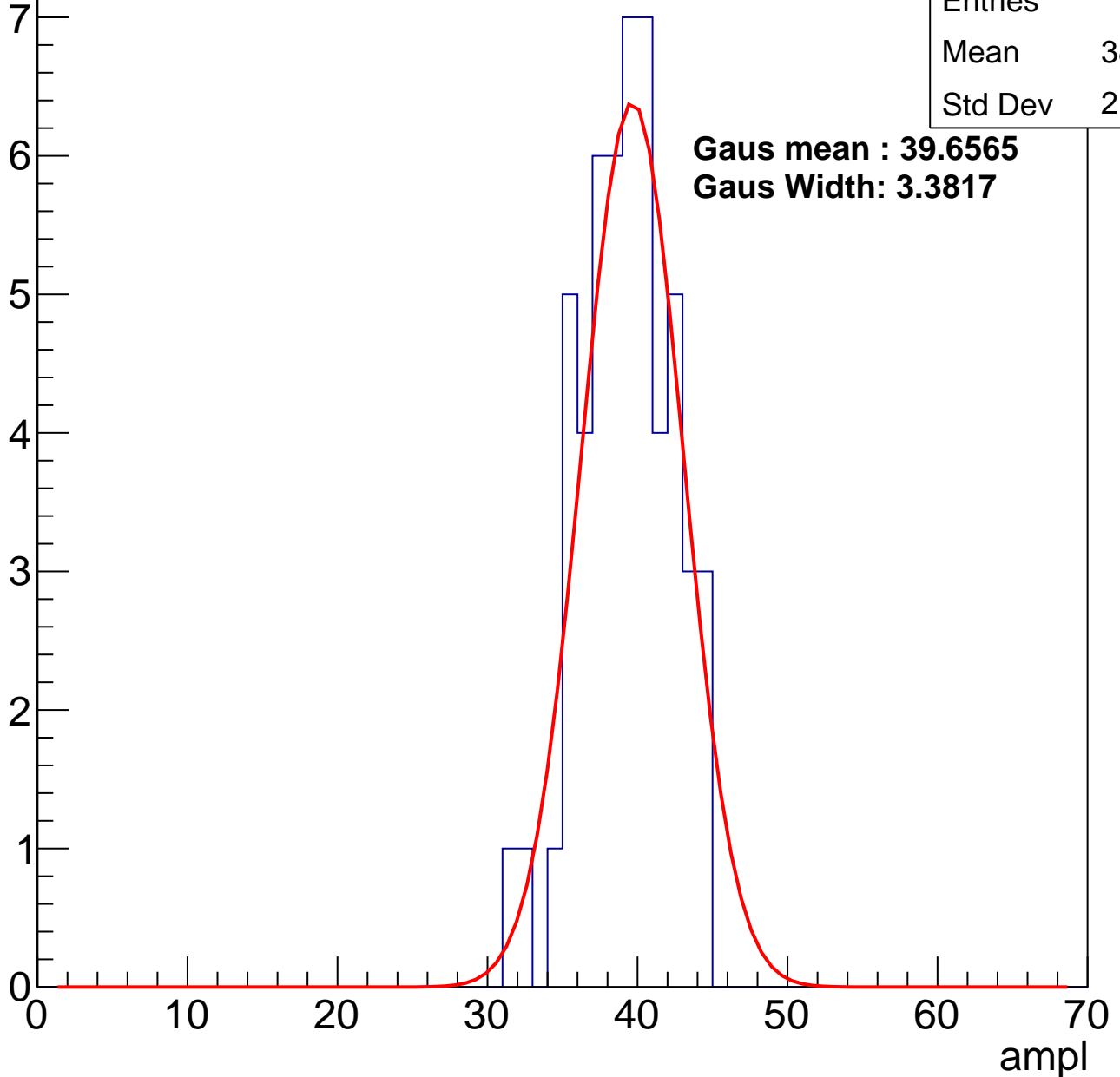
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 38.75 |
| Std Dev | 2.984 |

Gaus mean : 39.6565

Gaus Width: 3.3817



B0L001S, U7-ch1, adc2

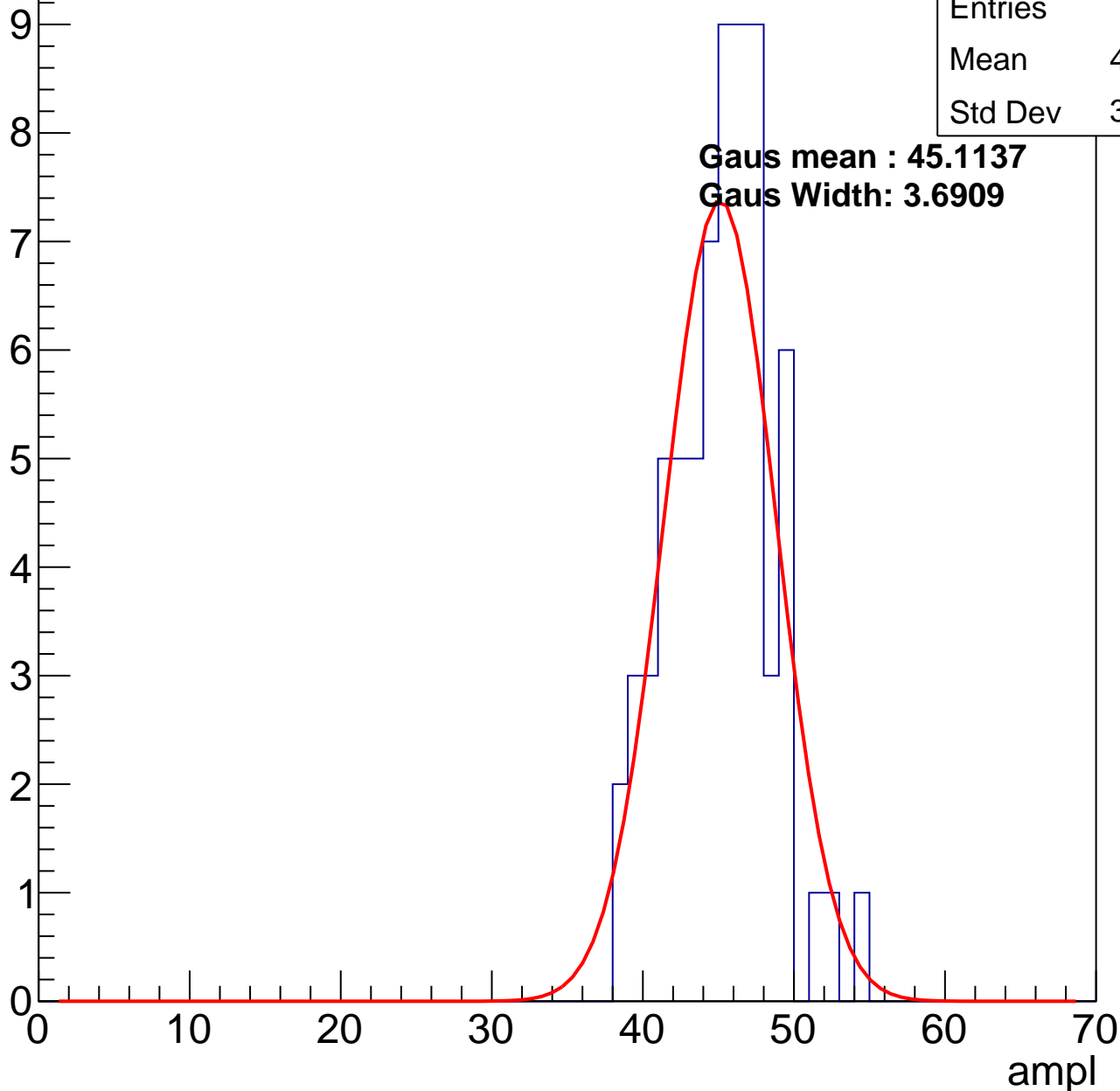
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 44.75 |
| Std Dev | 3.325 |

Gaus mean : 45.1137

Gaus Width: 3.6909



B0L001S, U7-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

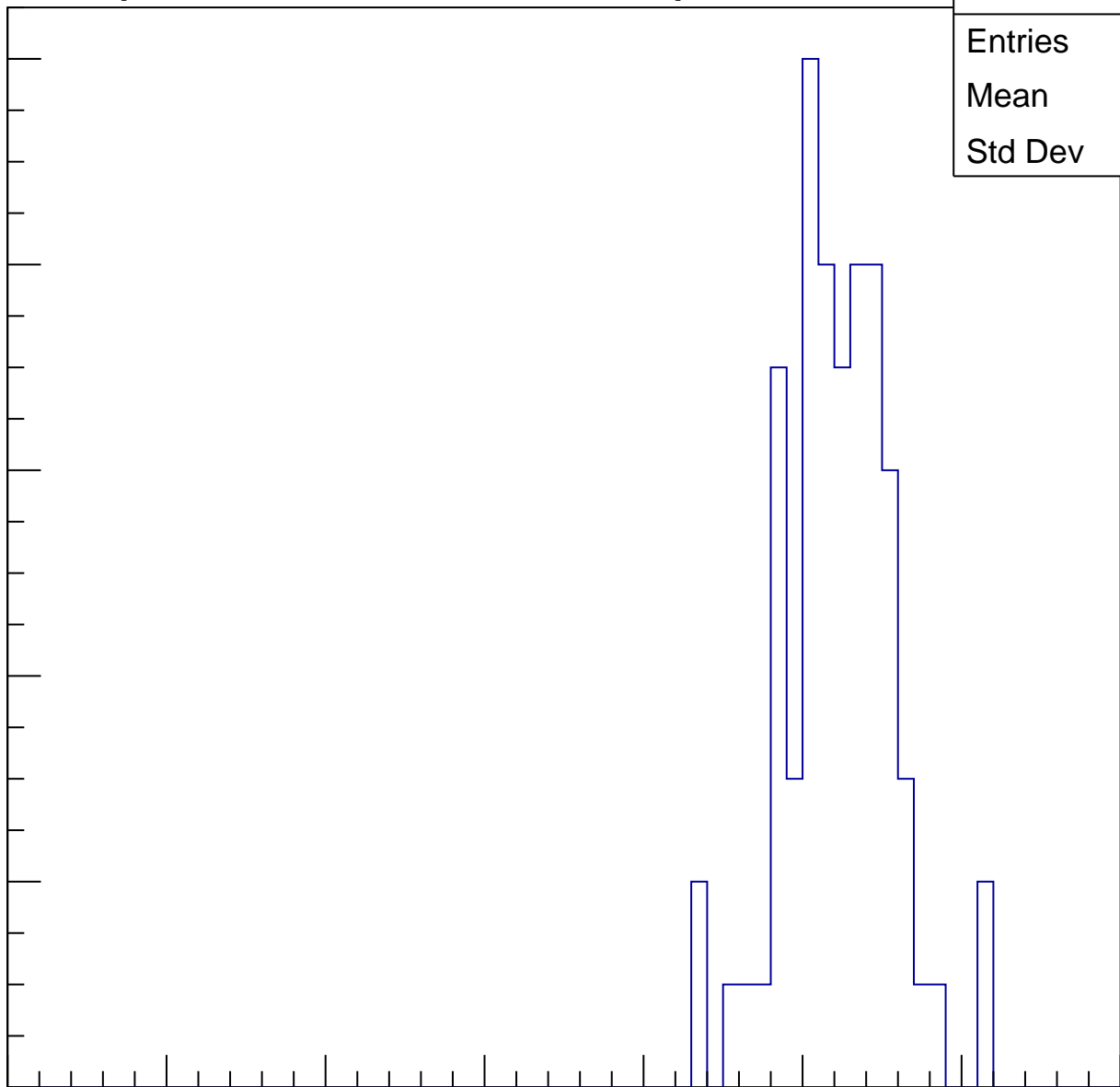
| | |
|---------|-------|
| Entries | 69 |
| Mean | 51.74 |
| Std Dev | 3.446 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

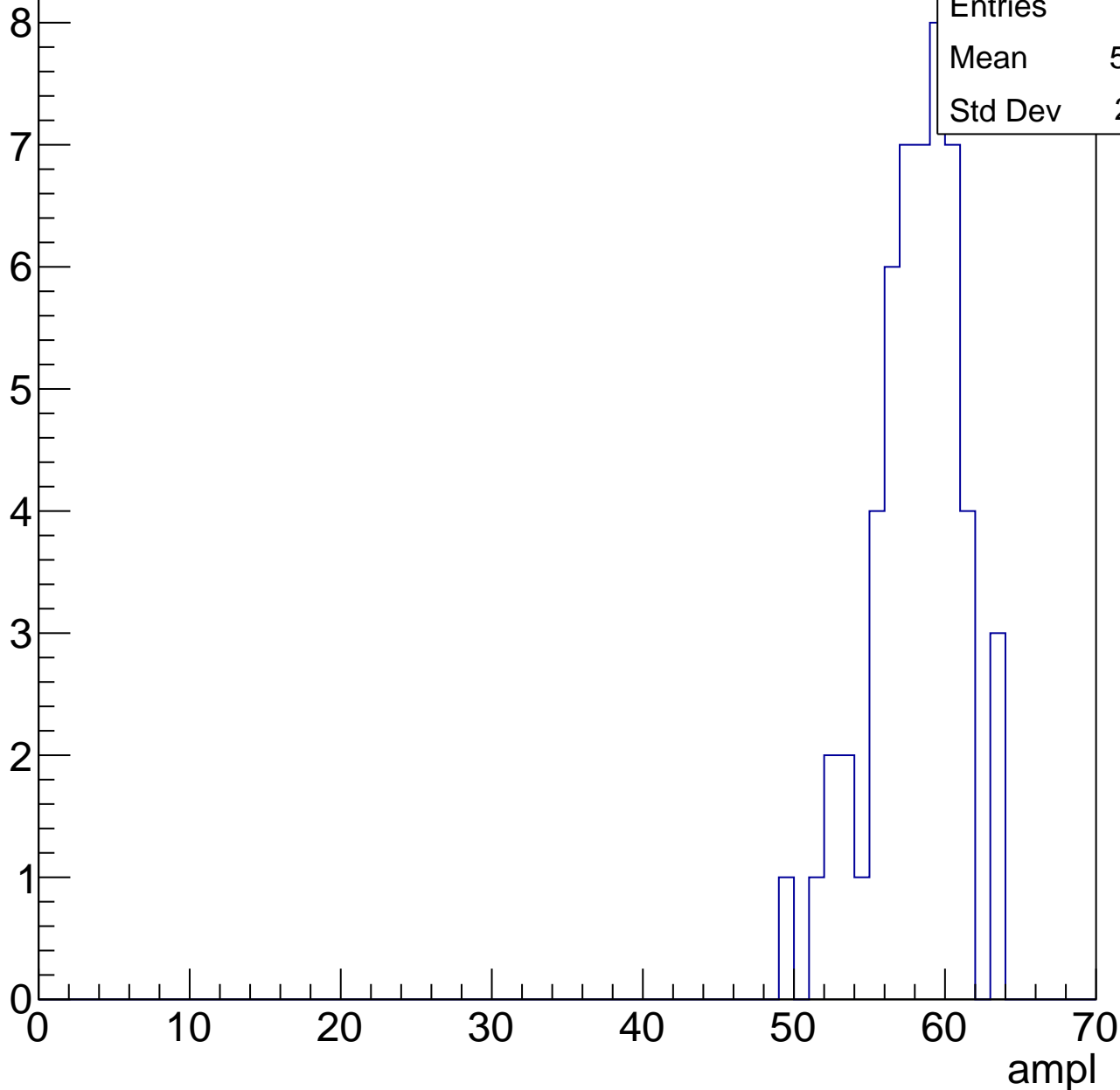


B0L001S, U7-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.55 |
| Std Dev | 2.981 |



B0L001S, U7-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 32 |
| Mean | 59.03 |
| Std Dev | 10.78 |

Entry

10

8

6

4

2

0

0

10

20

30

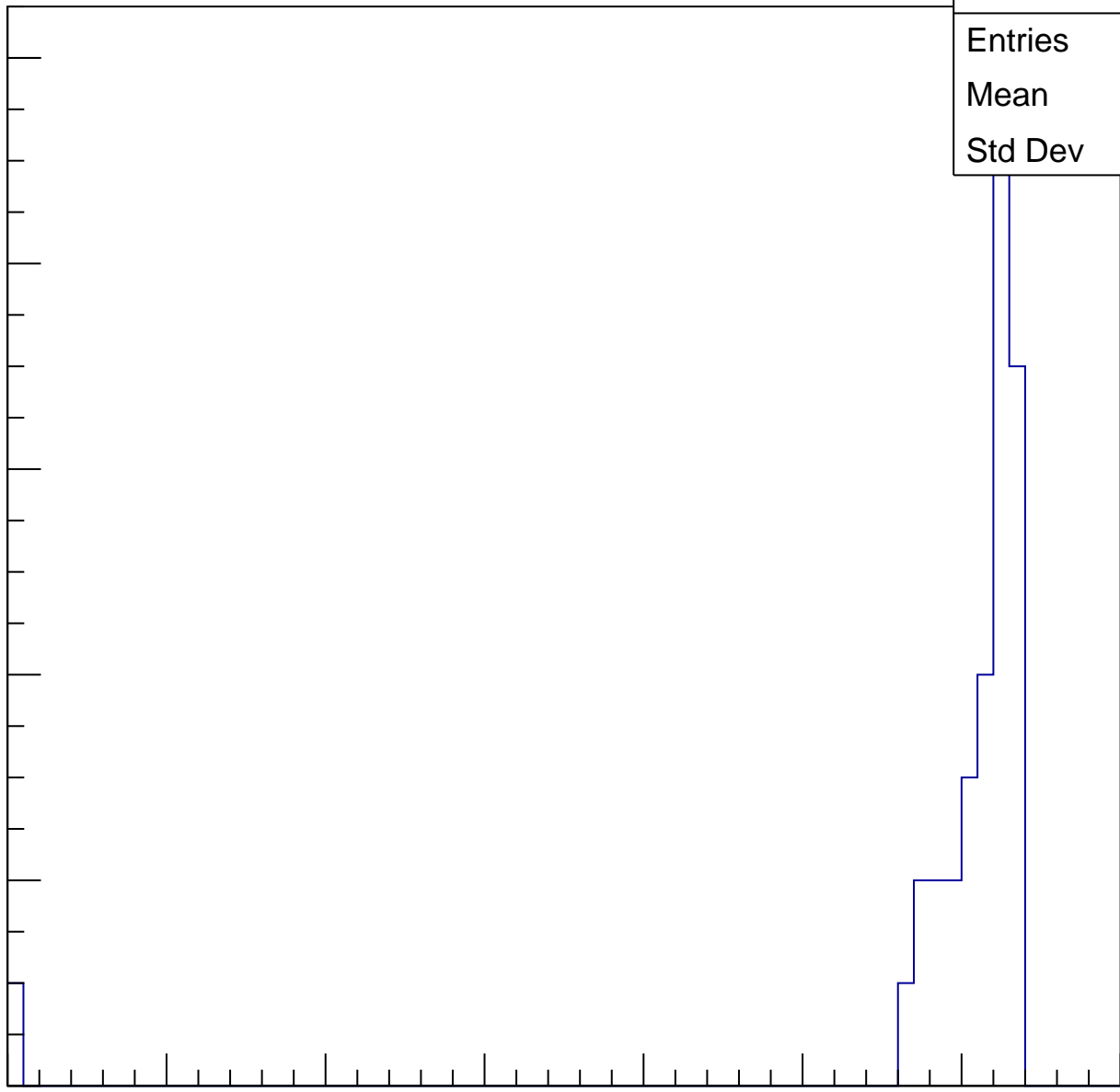
40

50

60

70

ampl



B0L001S, U7-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch2, adc0

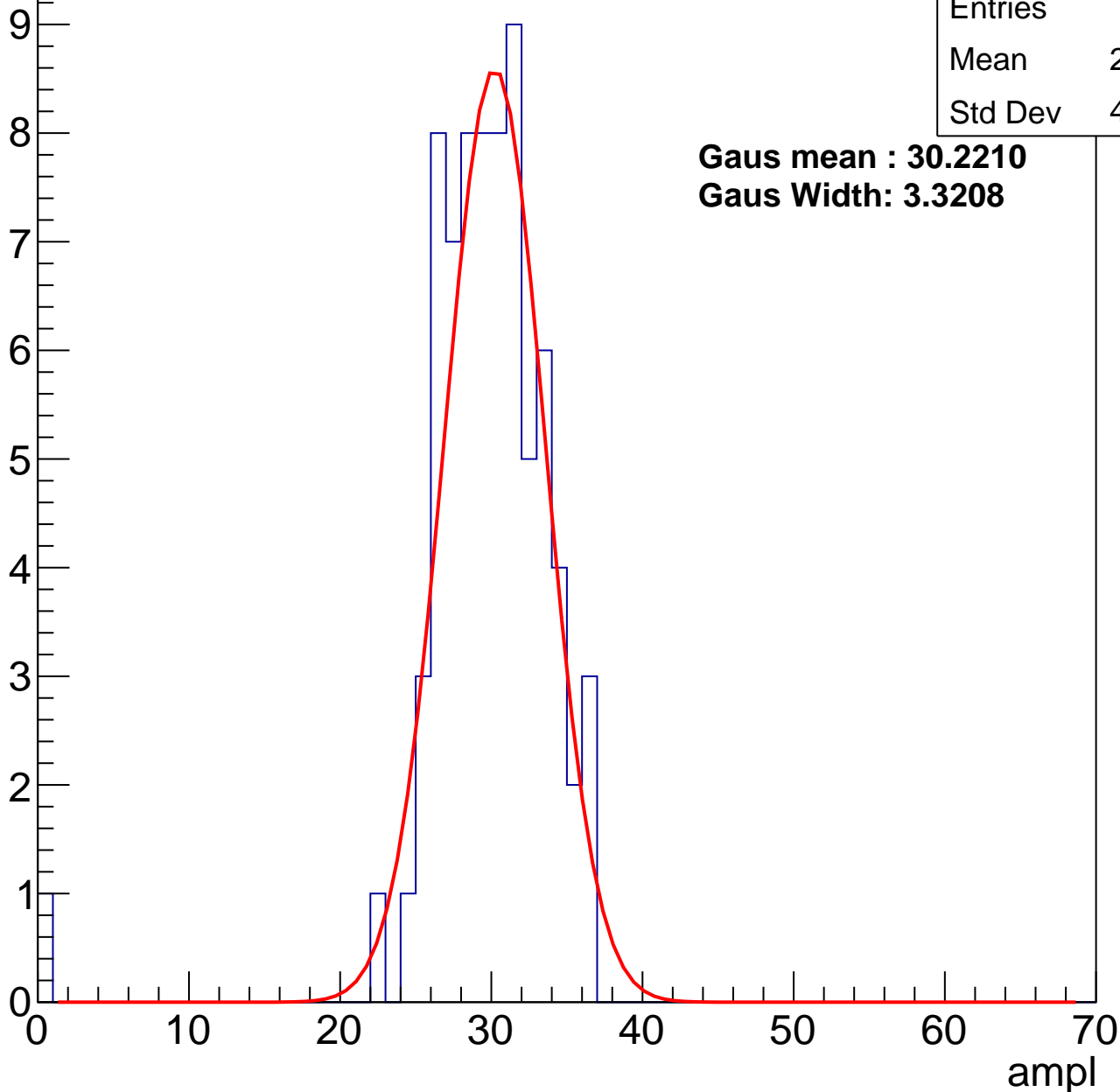
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.26 |
| Std Dev | 4.606 |

Gaus mean : 30.2210

Gaus Width: 3.3208



B0L001S, U7-ch2, adc1

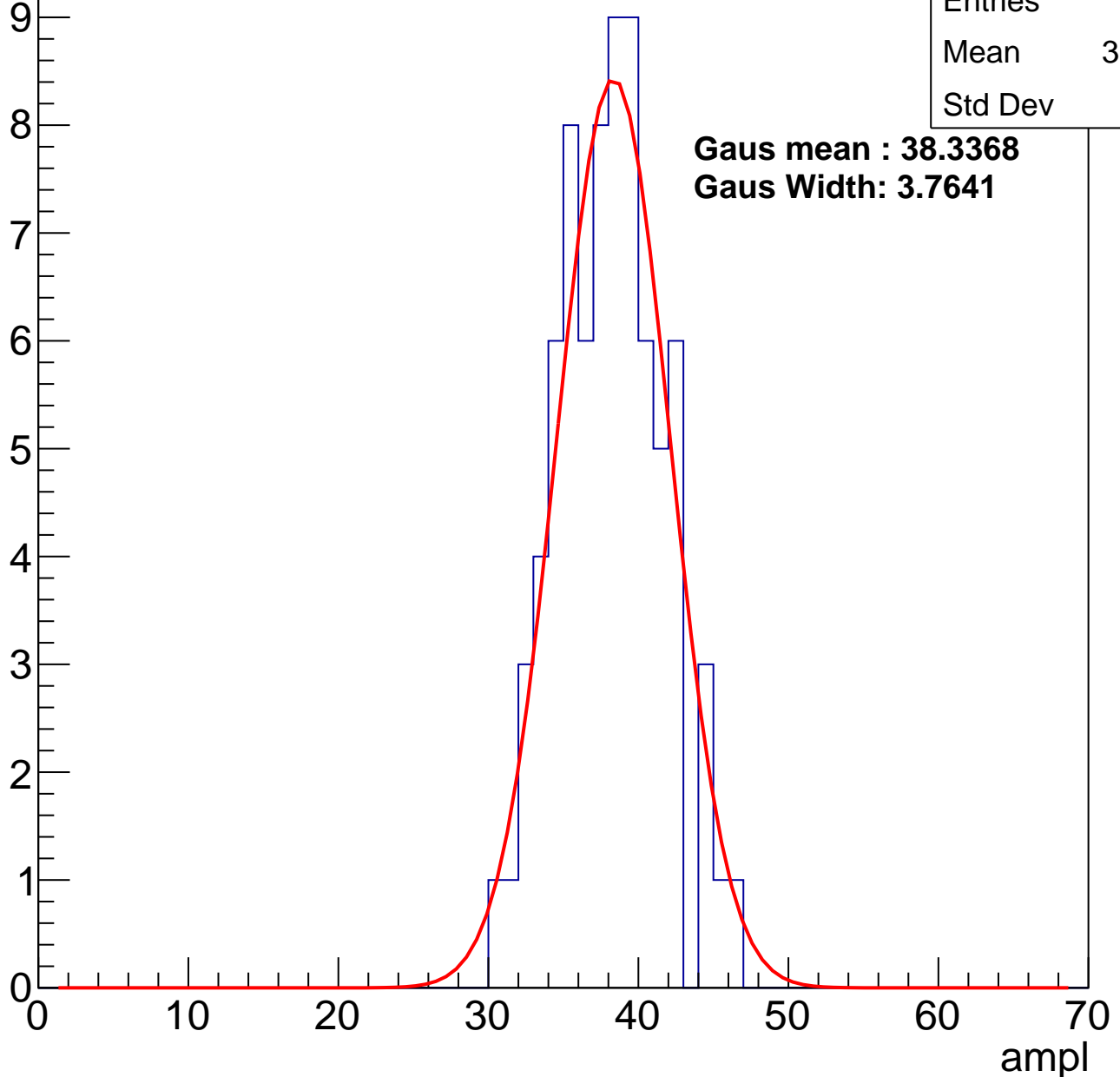
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.64 |
| Std Dev | 3.43 |

Gaus mean : 38.3368

Gaus Width: 3.7641



B0L001S, U7-ch2, adc2

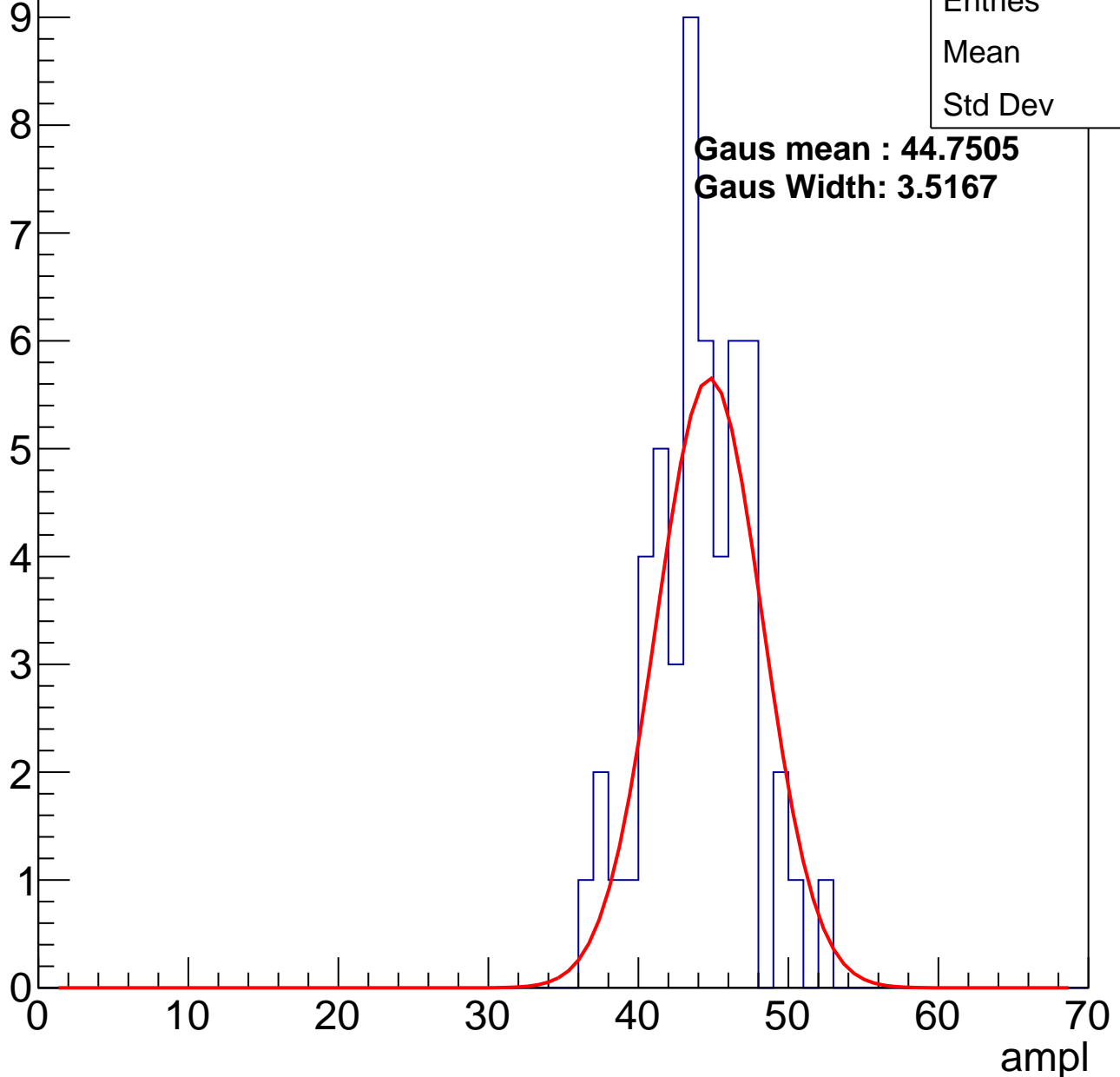
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 52 |
| Mean | 43.6 |
| Std Dev | 3.33 |

Gaus mean : 44.7505

Gaus Width: 3.5167



B0L001S, U7-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

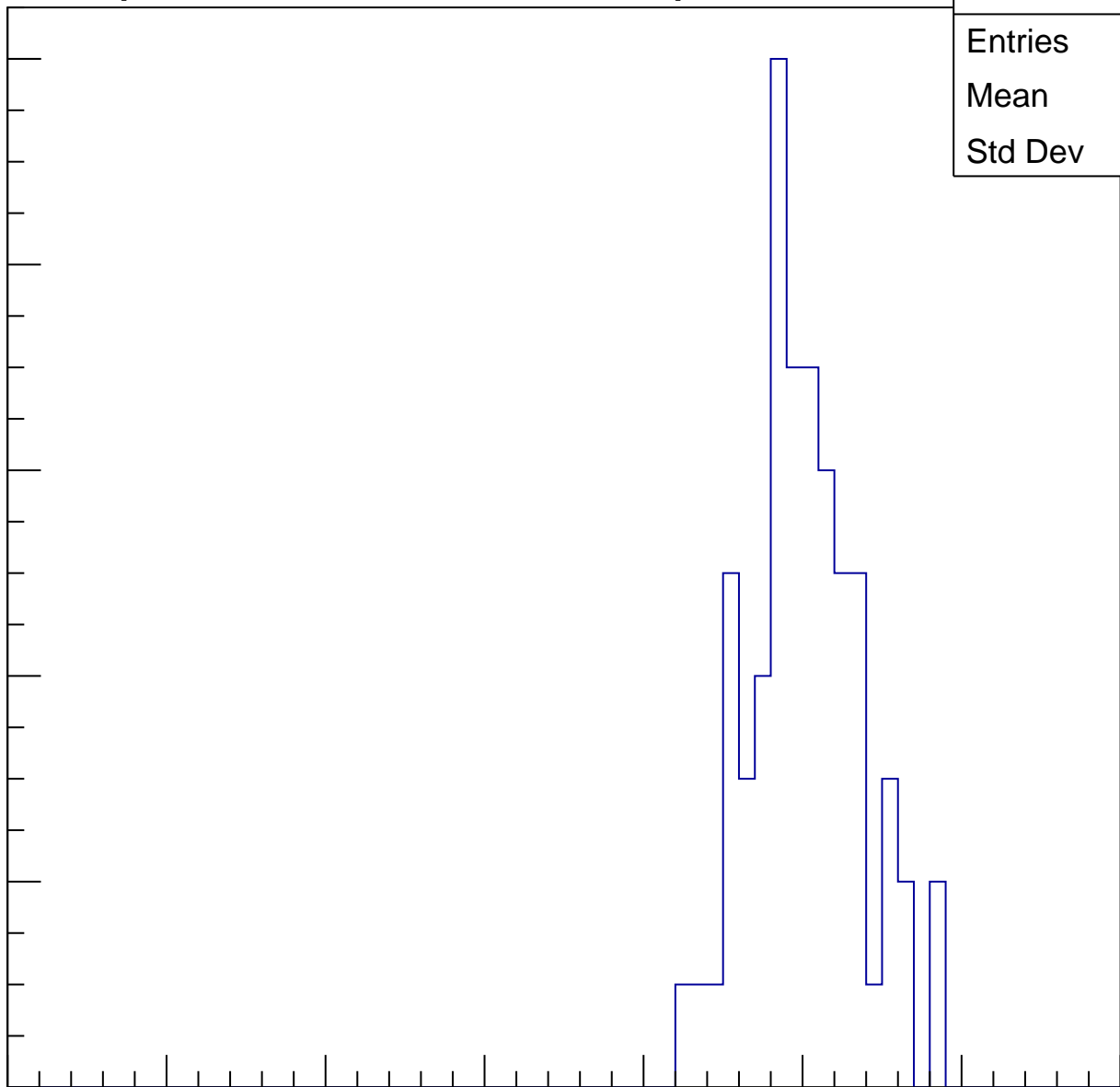
| | |
|---------|-------|
| Entries | 63 |
| Mean | 49.7 |
| Std Dev | 3.476 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

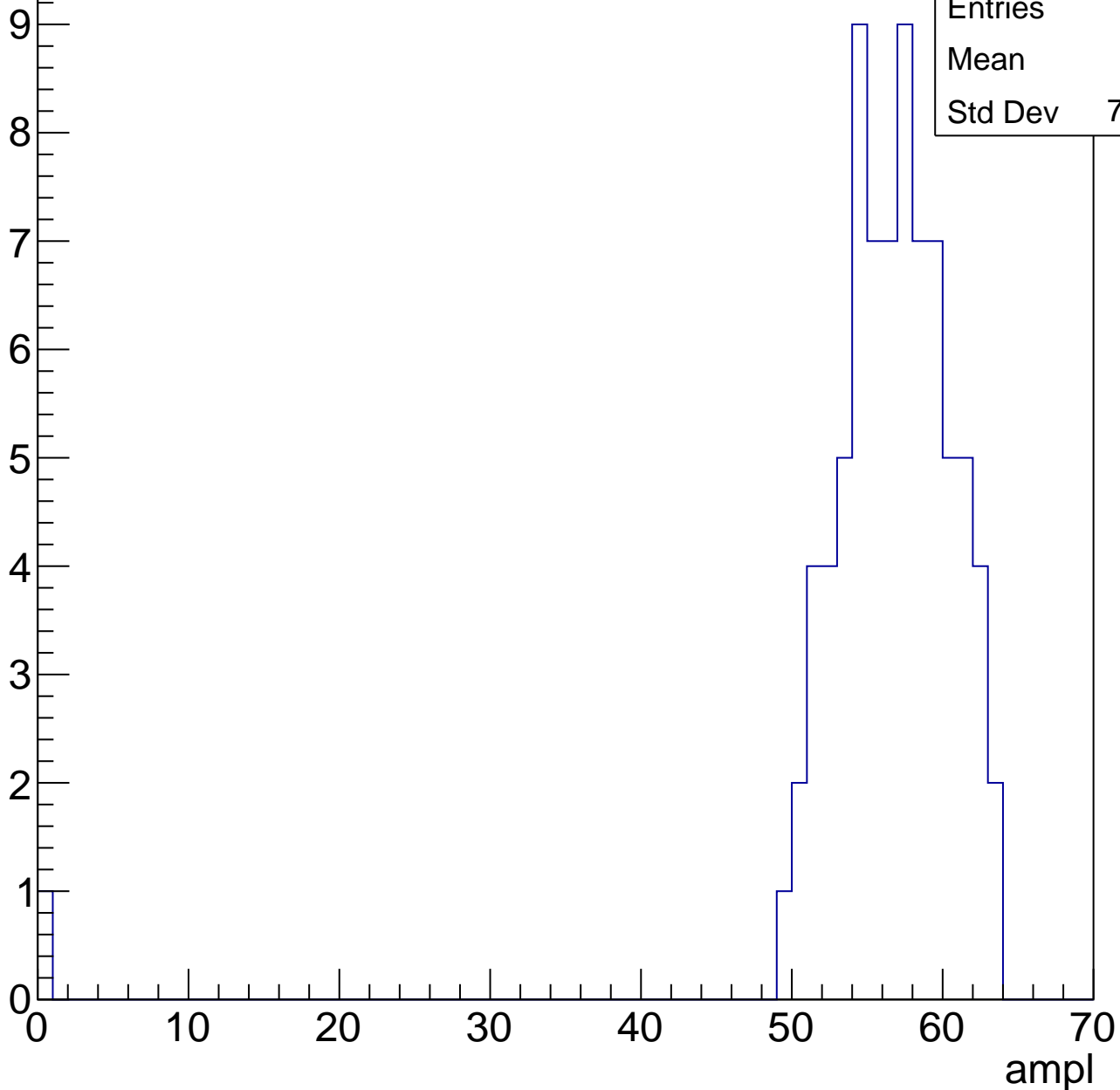


B0L001S, U7-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 55.7 |
| Std Dev | 7.159 |

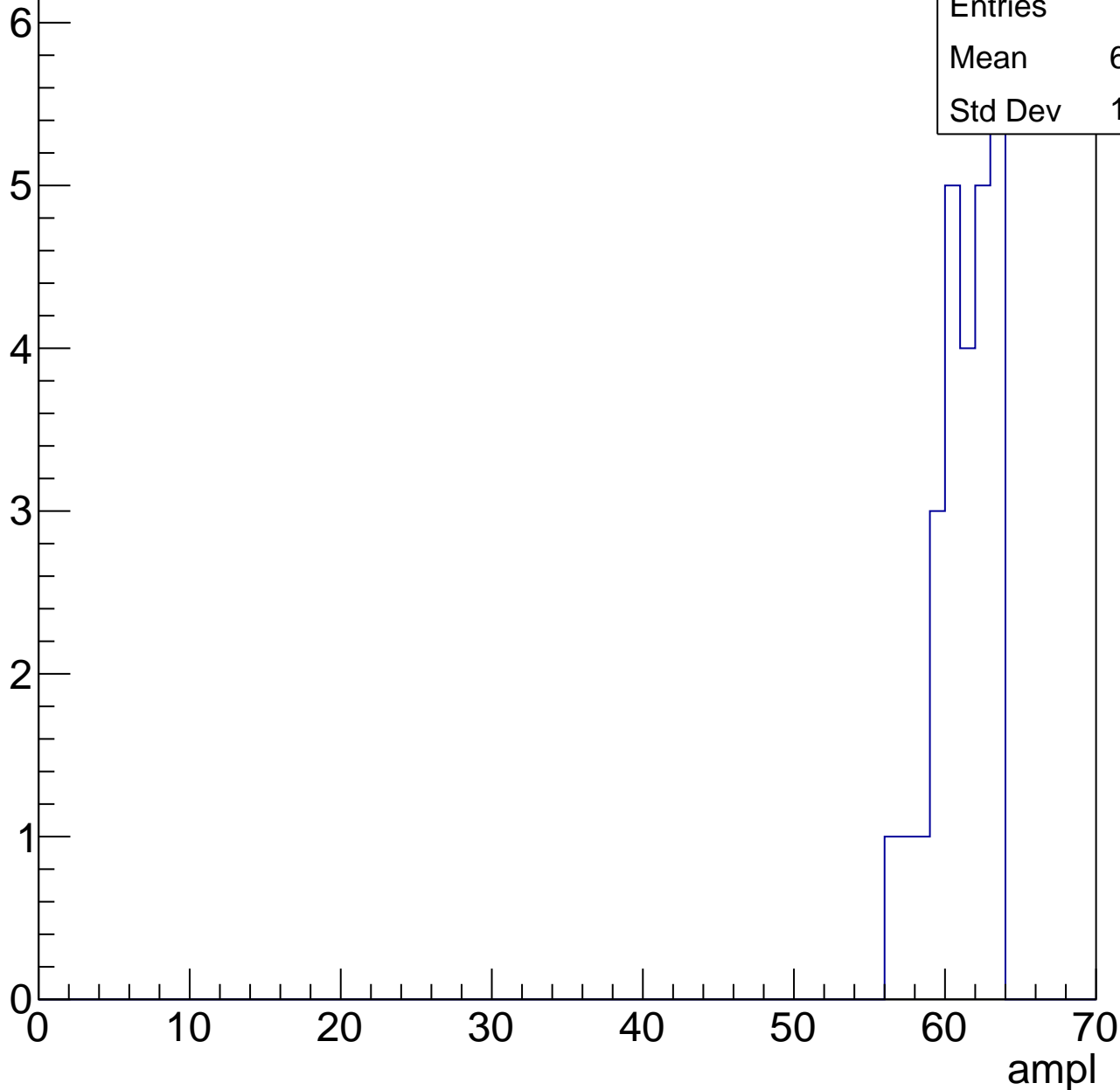


B0L001S, U7-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 60.77 |
| Std Dev | 1.908 |



B0L001S, U7-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

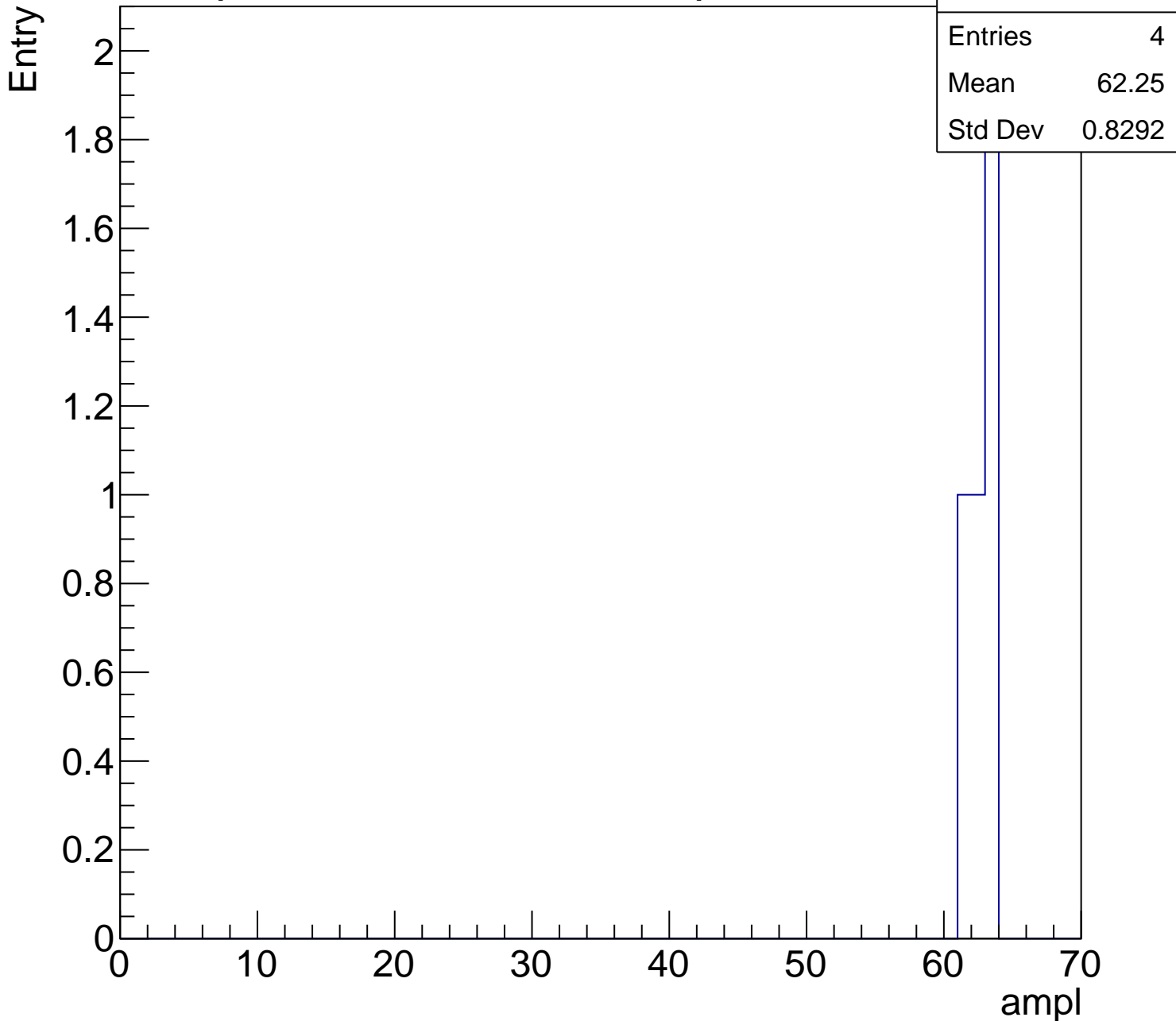
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 4 |
| Mean | 62.25 |
| Std Dev | 0.8292 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch3, adc0

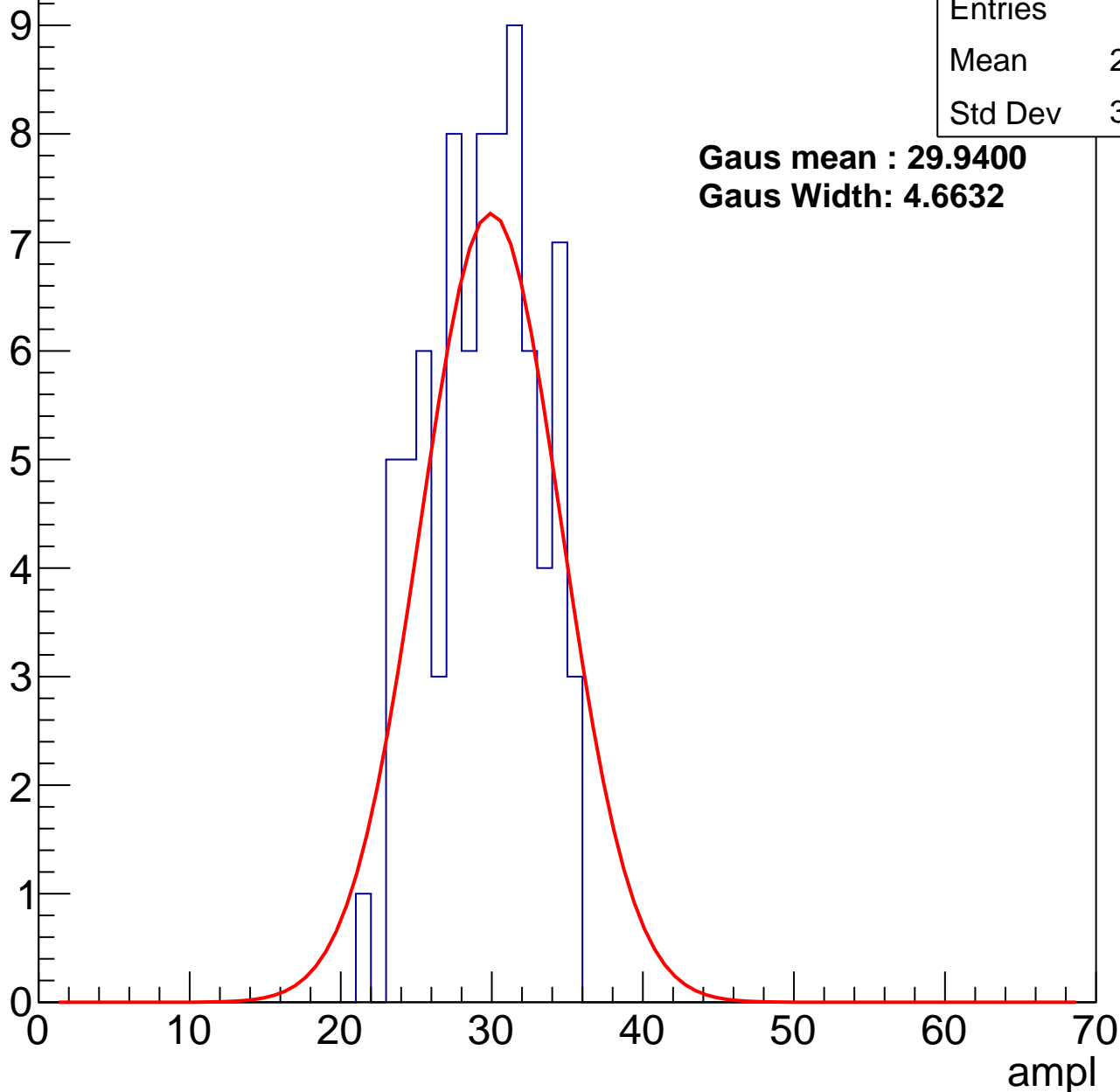
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.94 |
| Std Dev | 3.513 |

Gaus mean : 29.9400

Gaus Width: 4.6632



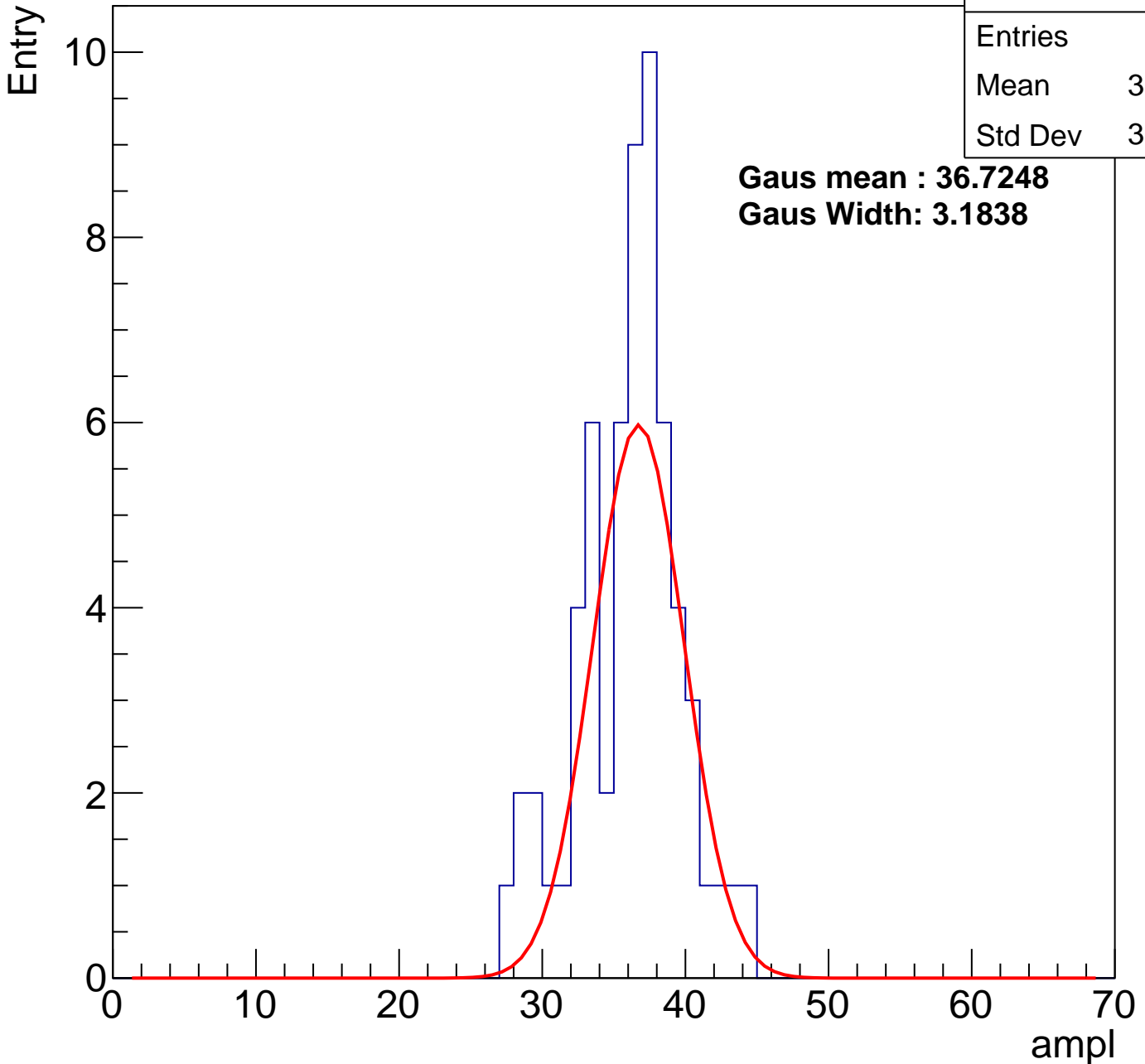
B0L001S, U7-ch3, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.64 |
| Std Dev | 3.585 |

Gaus mean : 36.7248

Gaus Width: 3.1838



B0L001S, U7-ch3, adc2

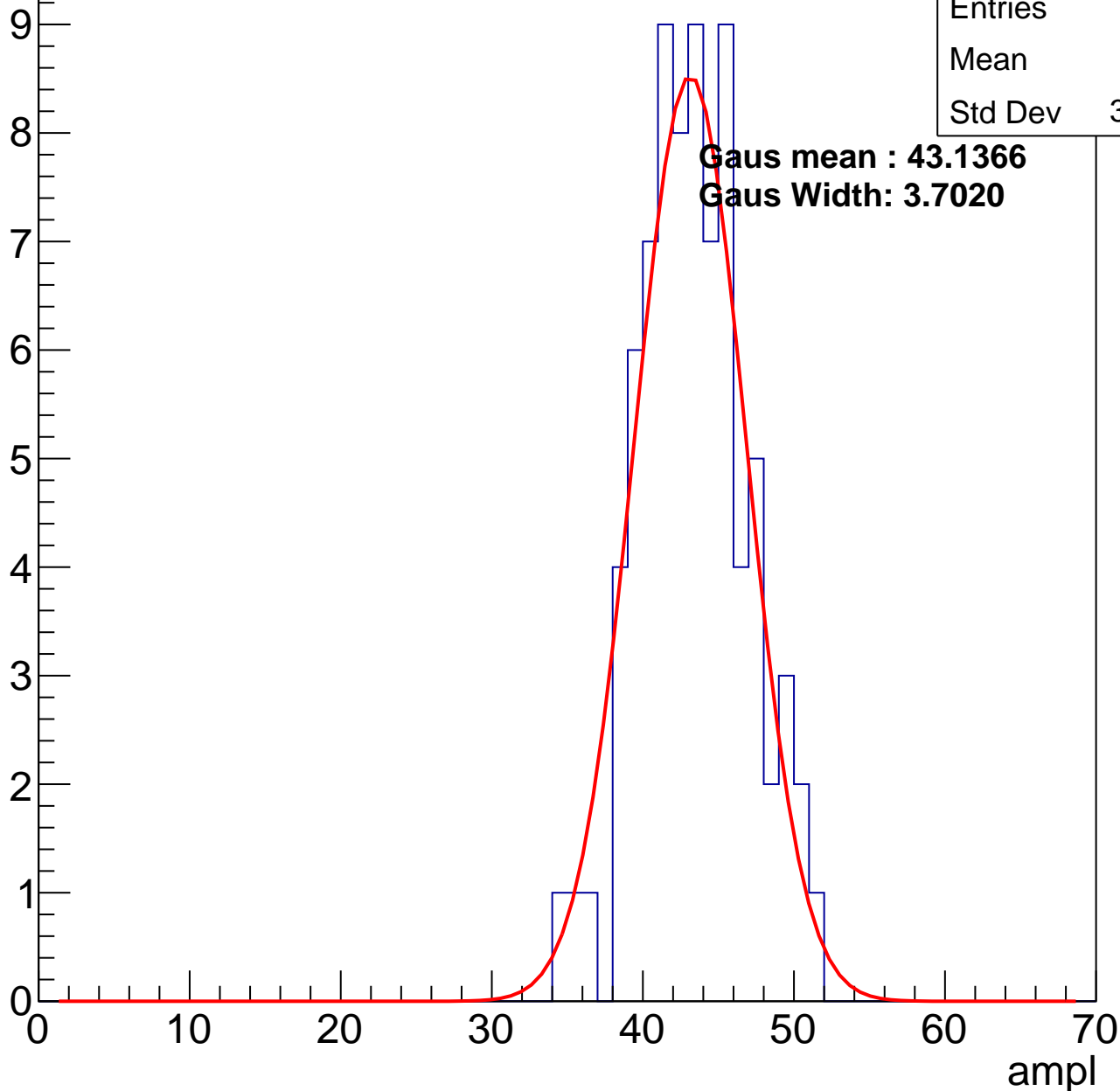
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 42.9 |
| Std Dev | 3.513 |

Gaus mean : 43.1366

Gaus Width: 3.7020

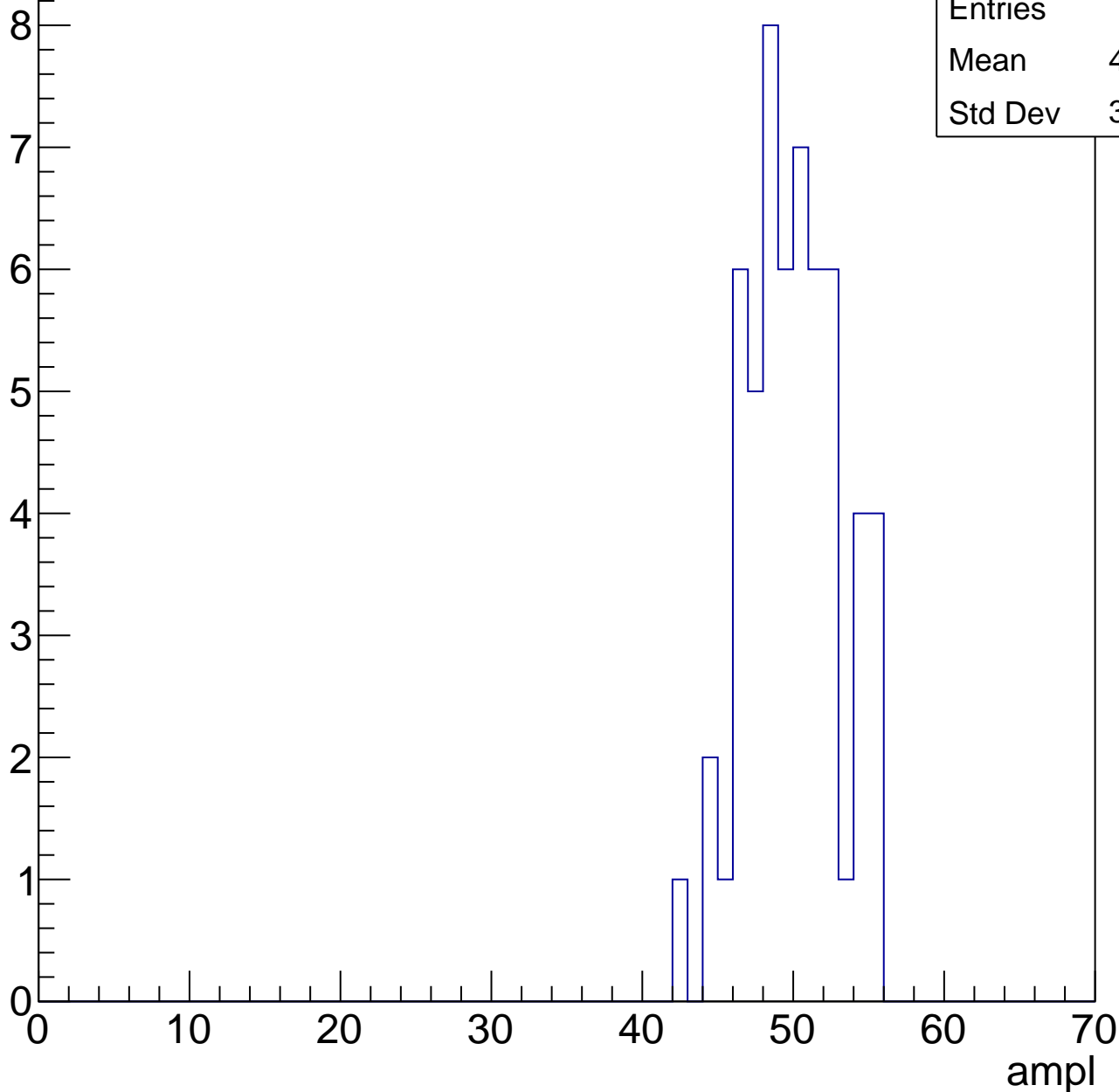


B0L001S, U7-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.49 |
| Std Dev | 3.044 |



B0L001S, U7-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

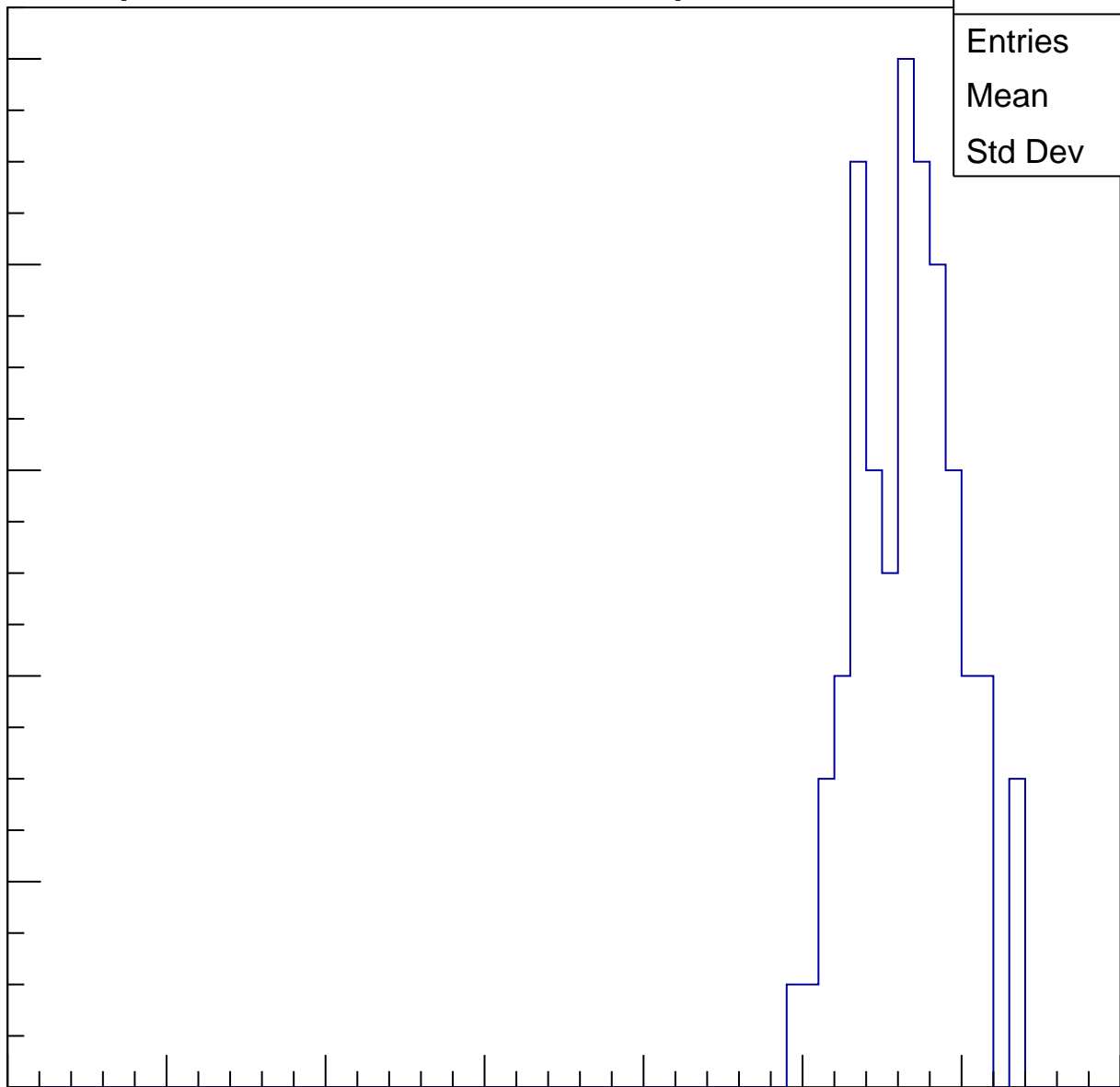
| | |
|---------|-------|
| Entries | 73 |
| Mean | 56.16 |
| Std Dev | 3.18 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

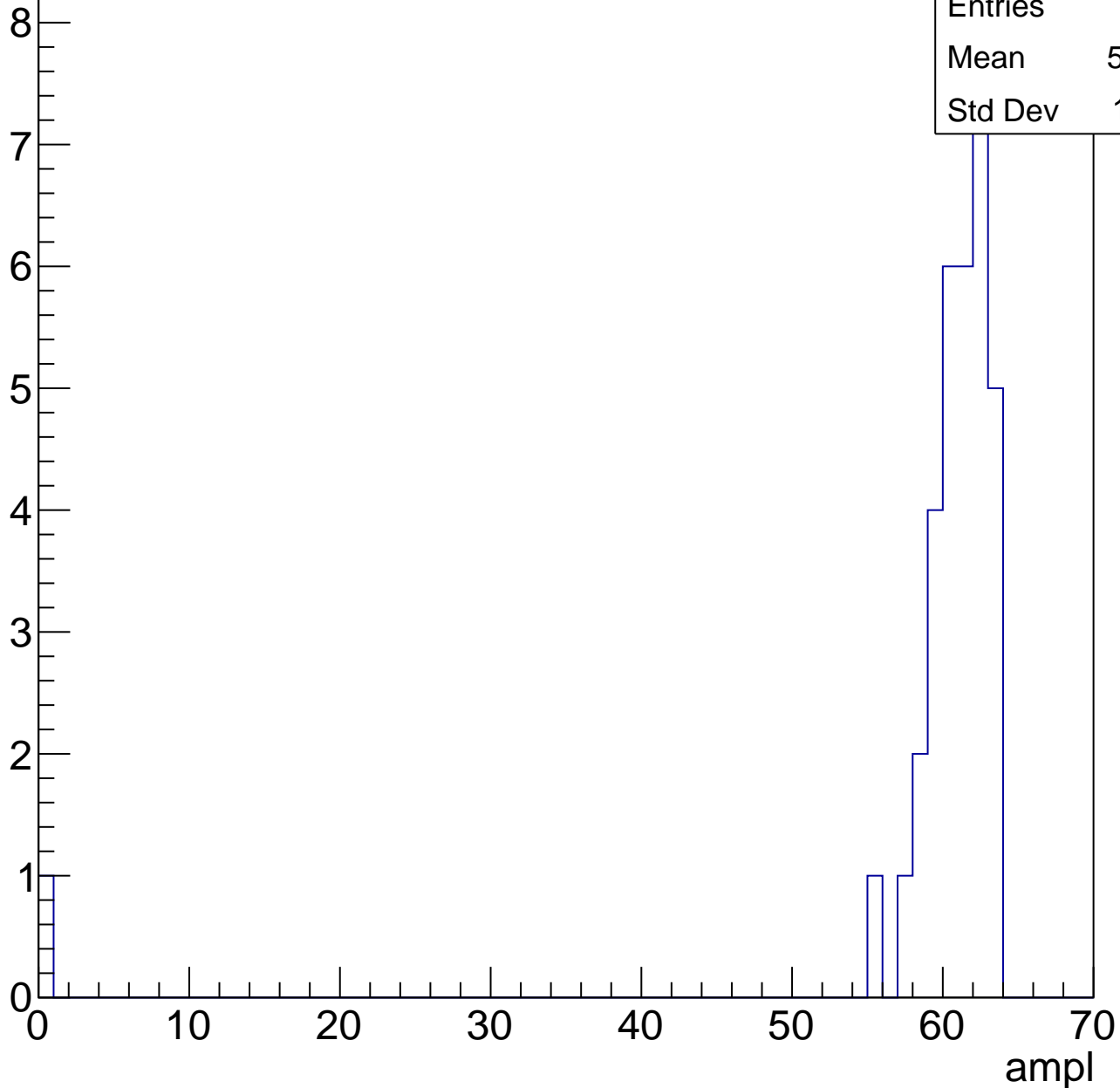


B0L001S, U7-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 58.85 |
| Std Dev | 10.41 |



B0L001S, U7-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 4 |
| Mean | 62.5 |
| Std Dev | 0.5 |

ampl

B0L001S, U7-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch4, adc0

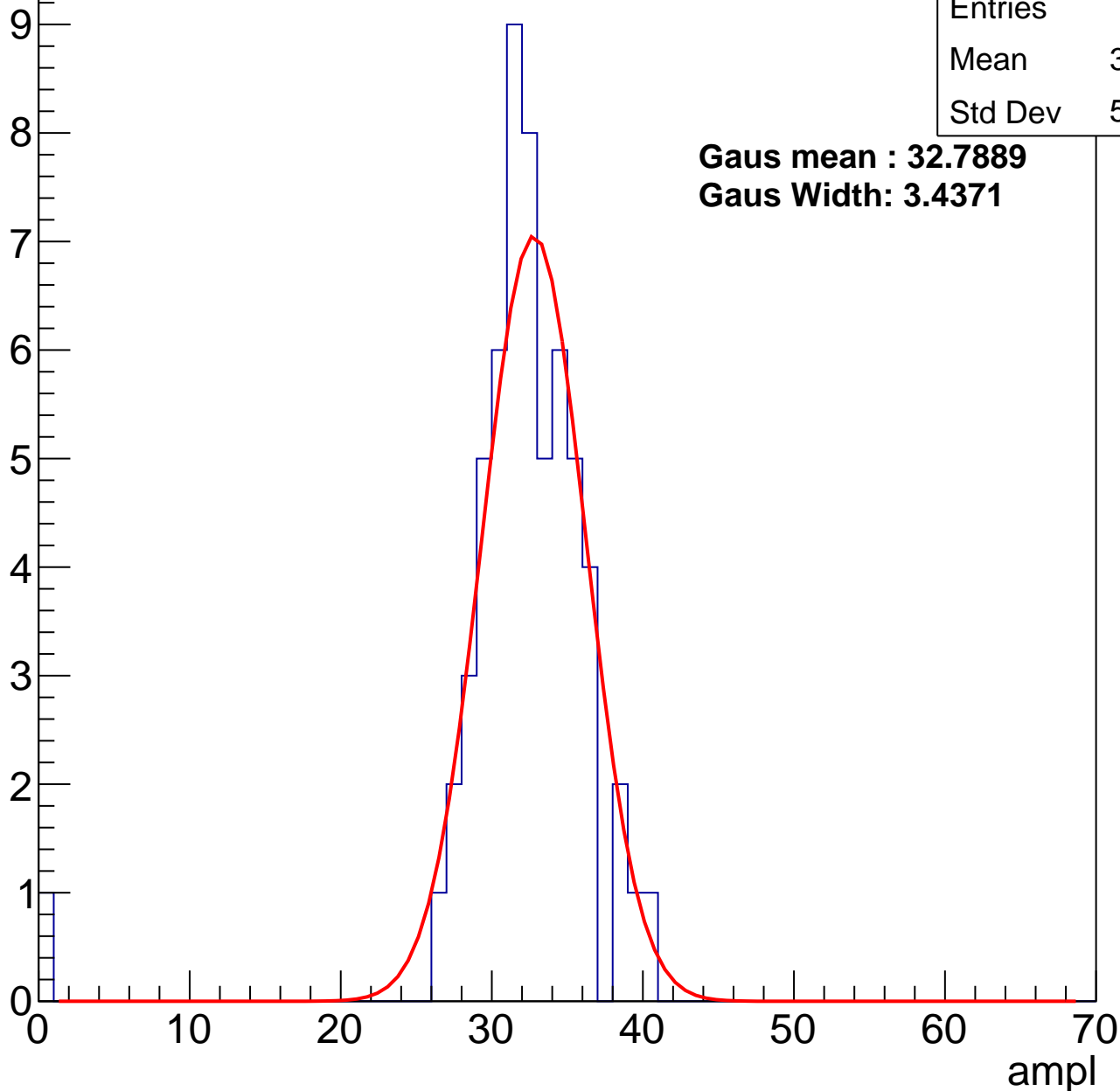
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 31.64 |
| Std Dev | 5.128 |

Gaus mean : 32.7889

Gaus Width: 3.4371



B0L001S, U7-ch4, adc1

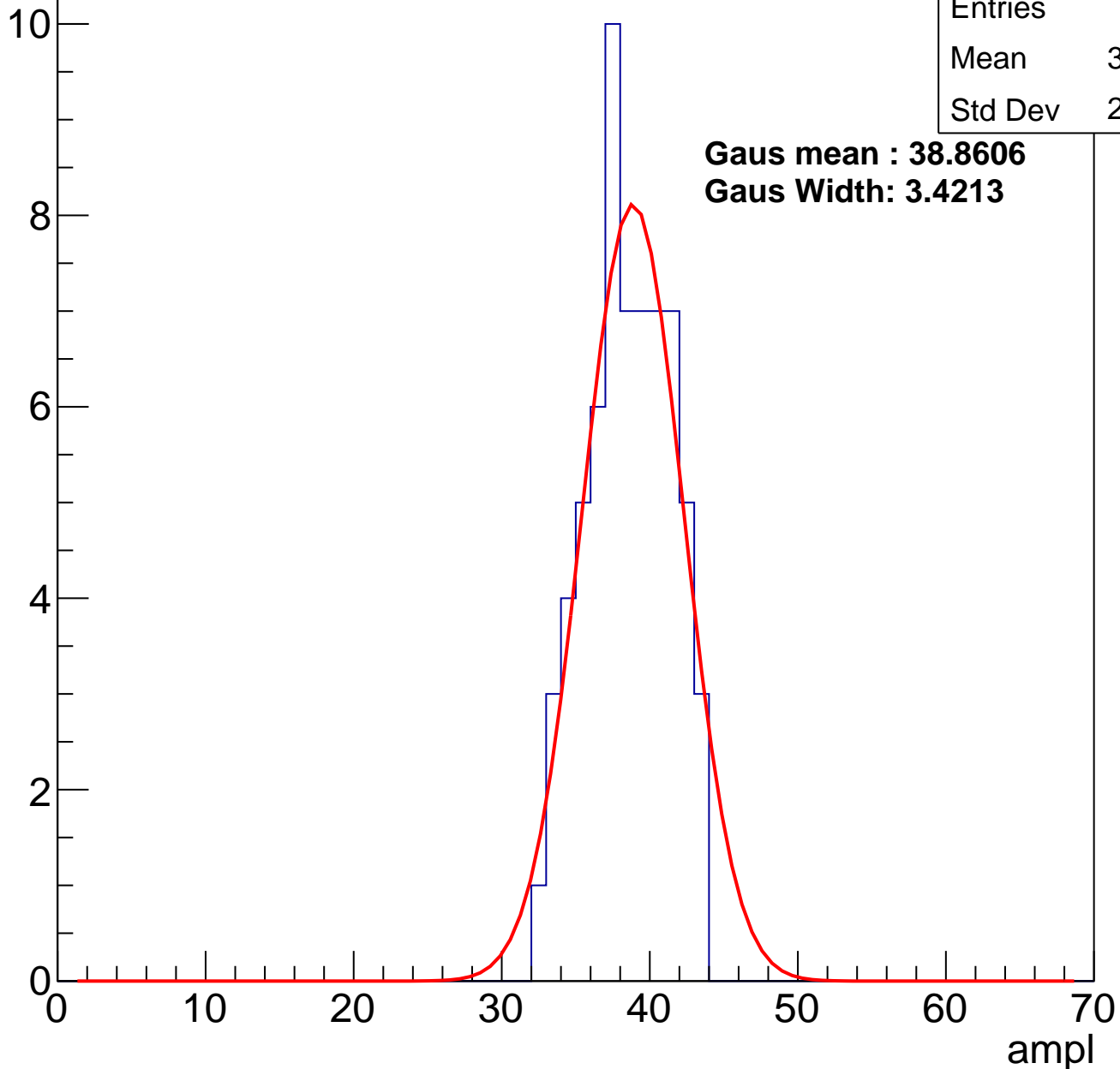
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 65 |
| Mean | 38.05 |
| Std Dev | 2.792 |

Gaus mean : 38.8606

Gaus Width: 3.4213

Entry



B0L001S, U7-ch4, adc2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.73 |
| Std Dev | 3.244 |

Gaus mean : 45.3042

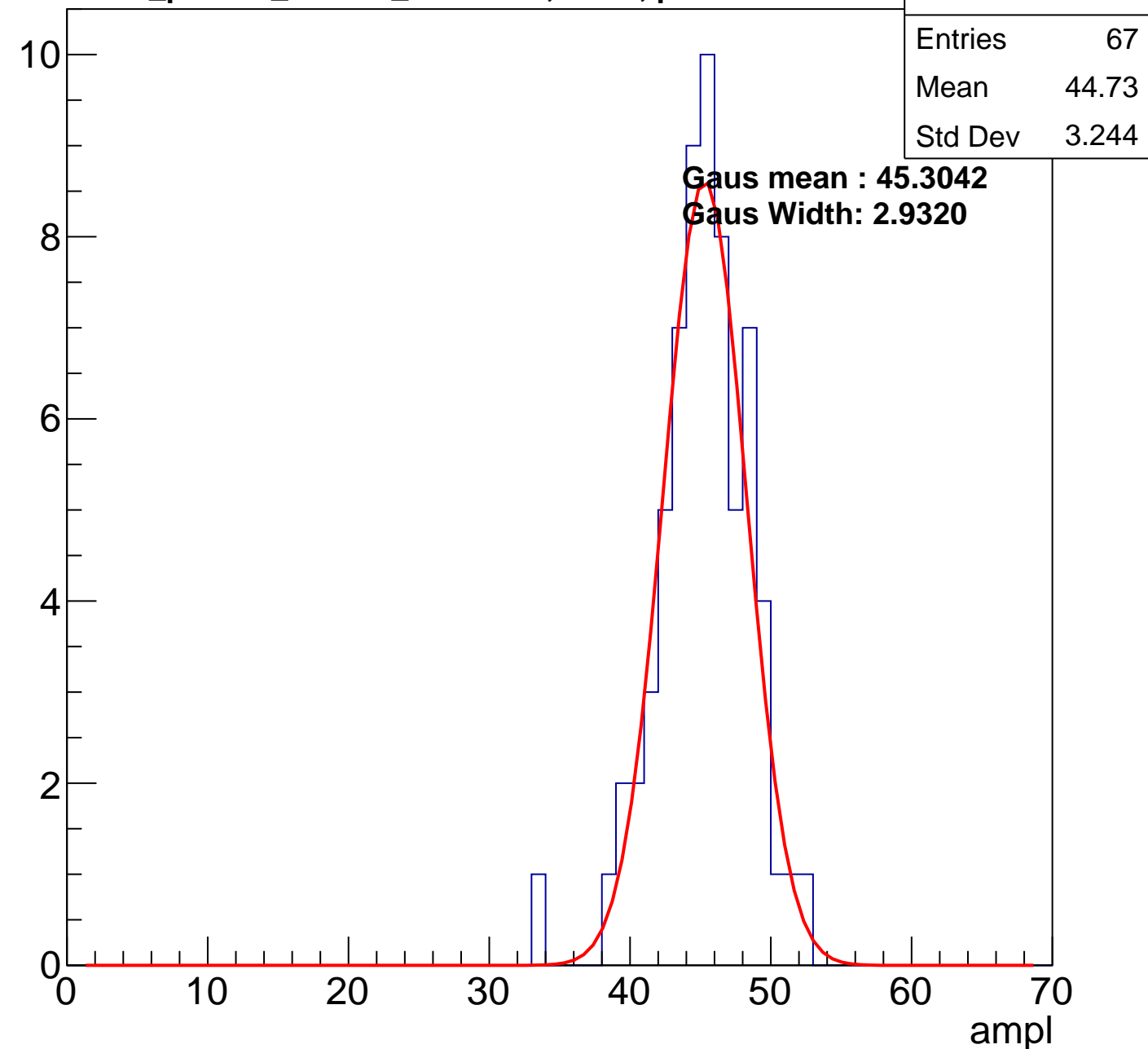
Gaus Width: 2.9320

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

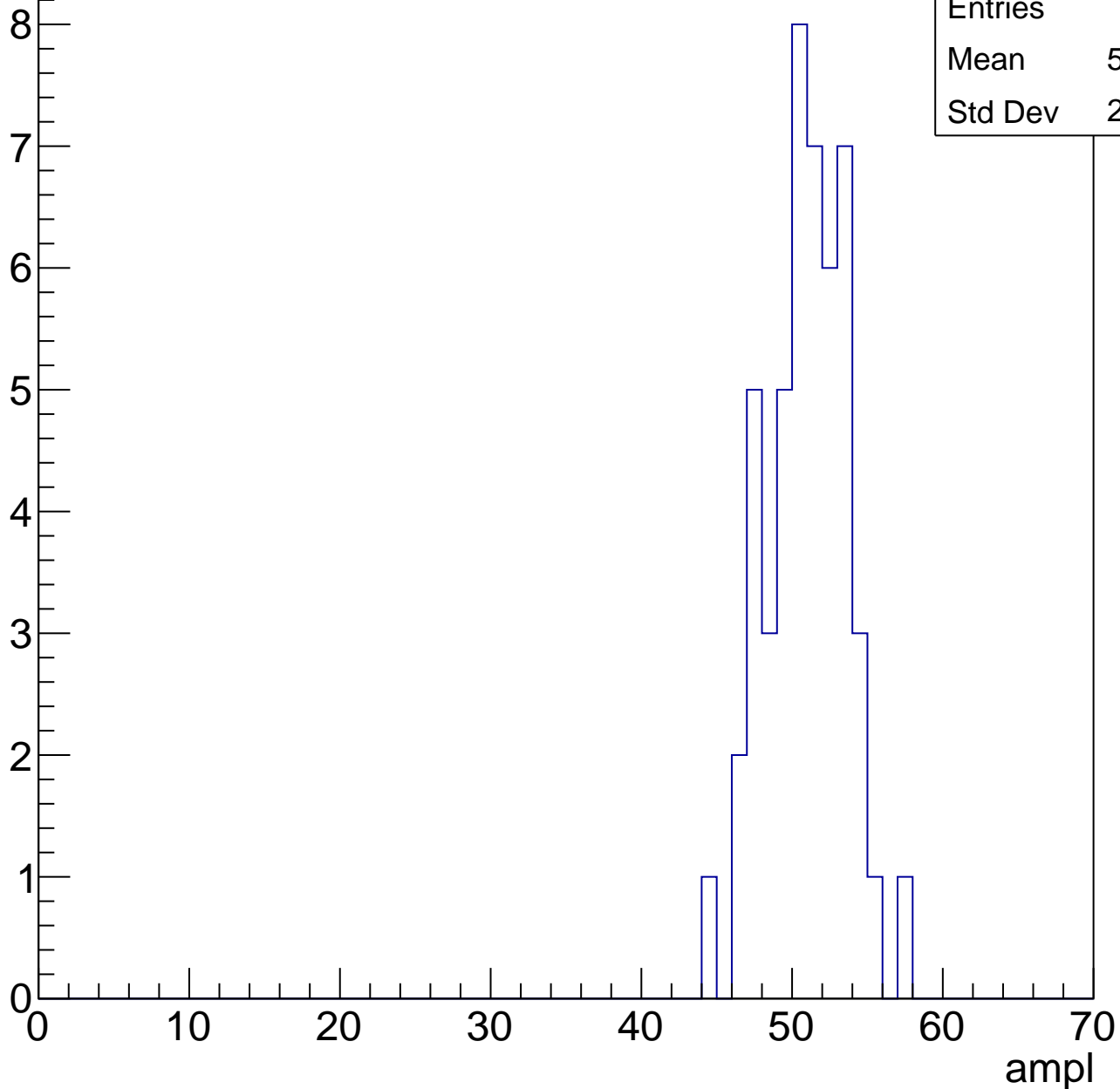


B0L001S, U7-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 50.49 |
| Std Dev | 2.612 |

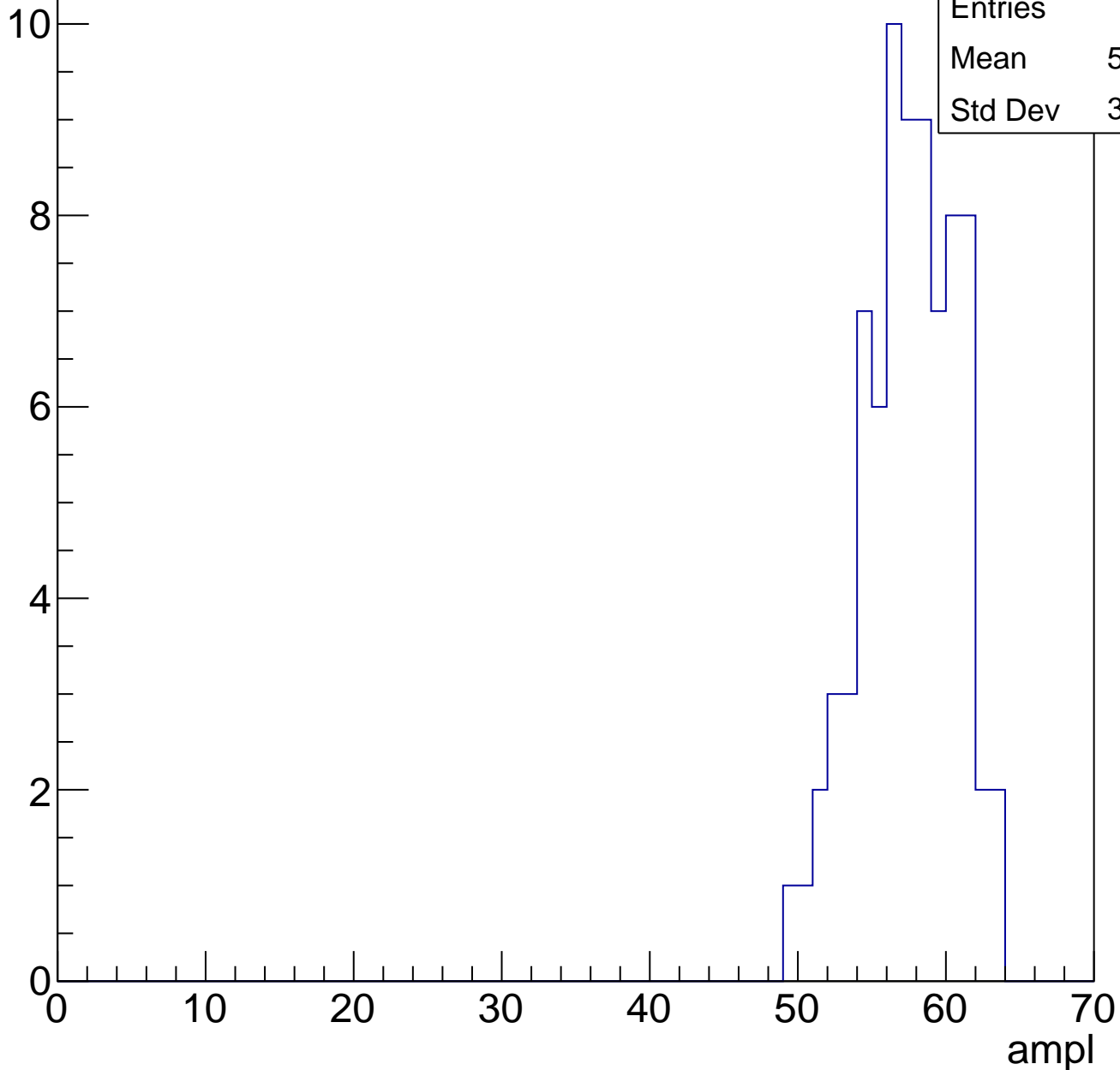


B0L001S, U7-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 78 |
| Mean | 57.05 |
| Std Dev | 3.129 |

Entry

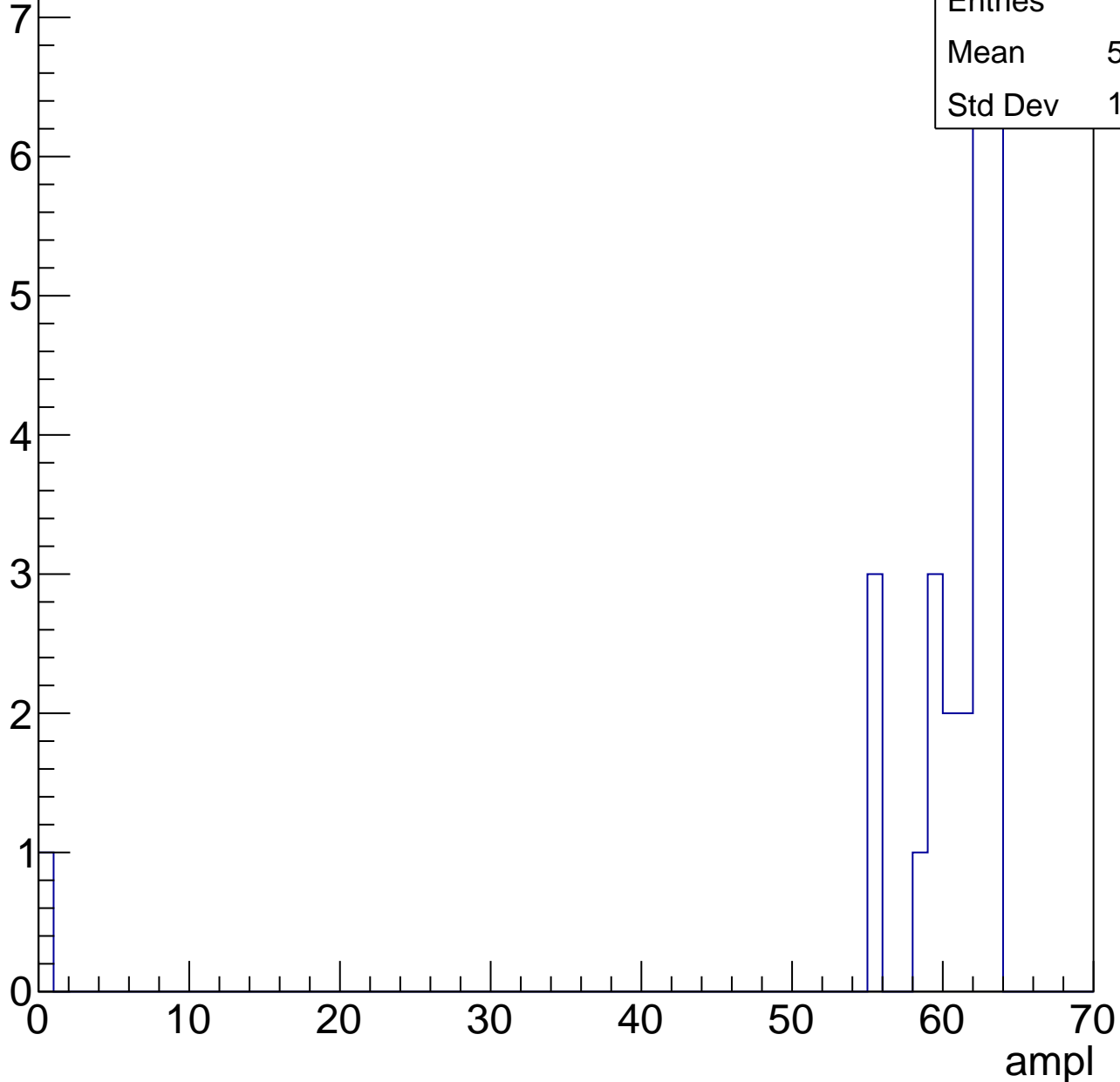


B0L001S, U7-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 58.35 |
| Std Dev | 11.94 |



B0L001S, U7-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch5, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 31.84 |
| Std Dev | 3.325 |

Gaus mean : 32.7735

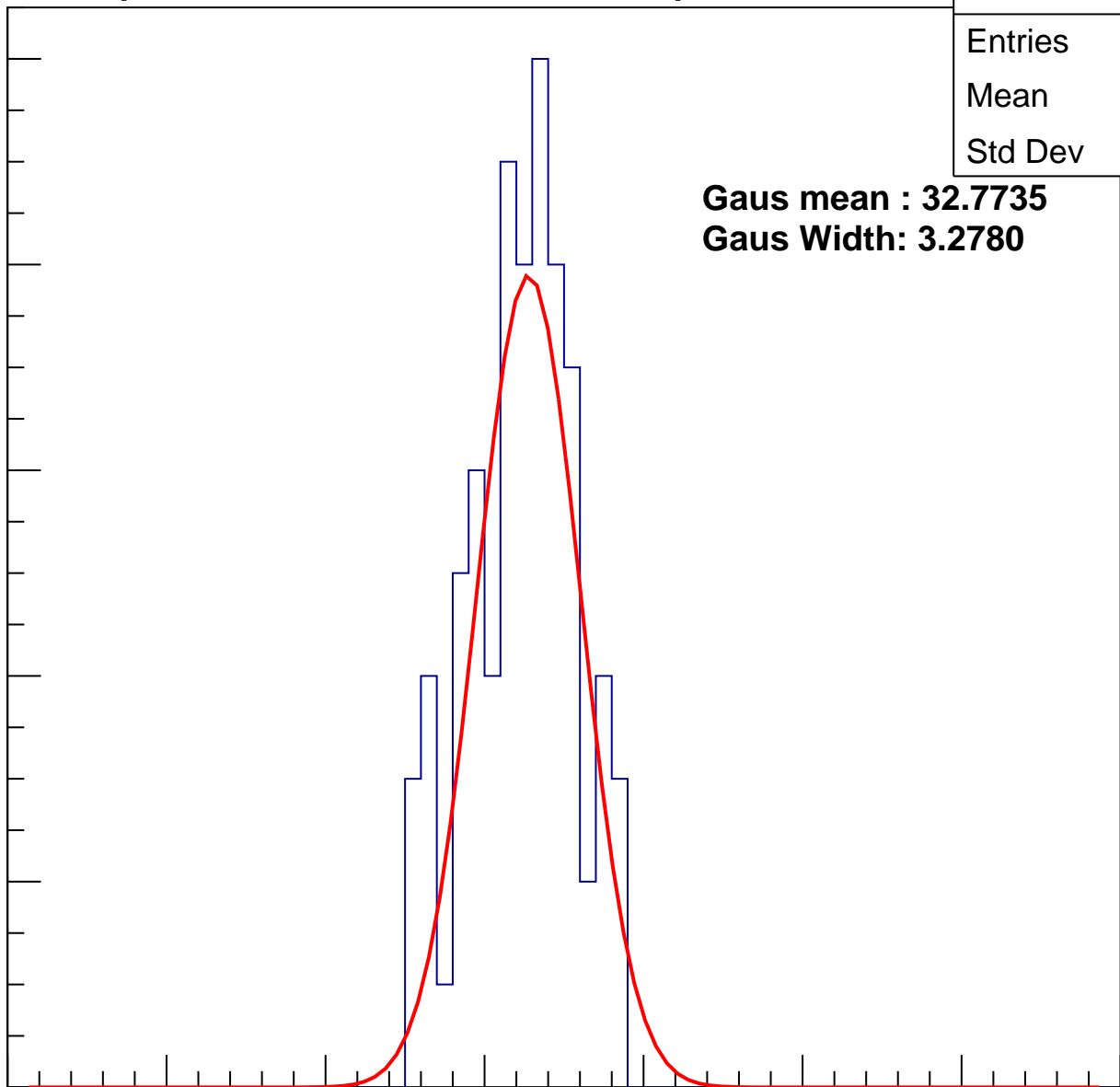
Gaus Width: 3.2780

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch5, adc1

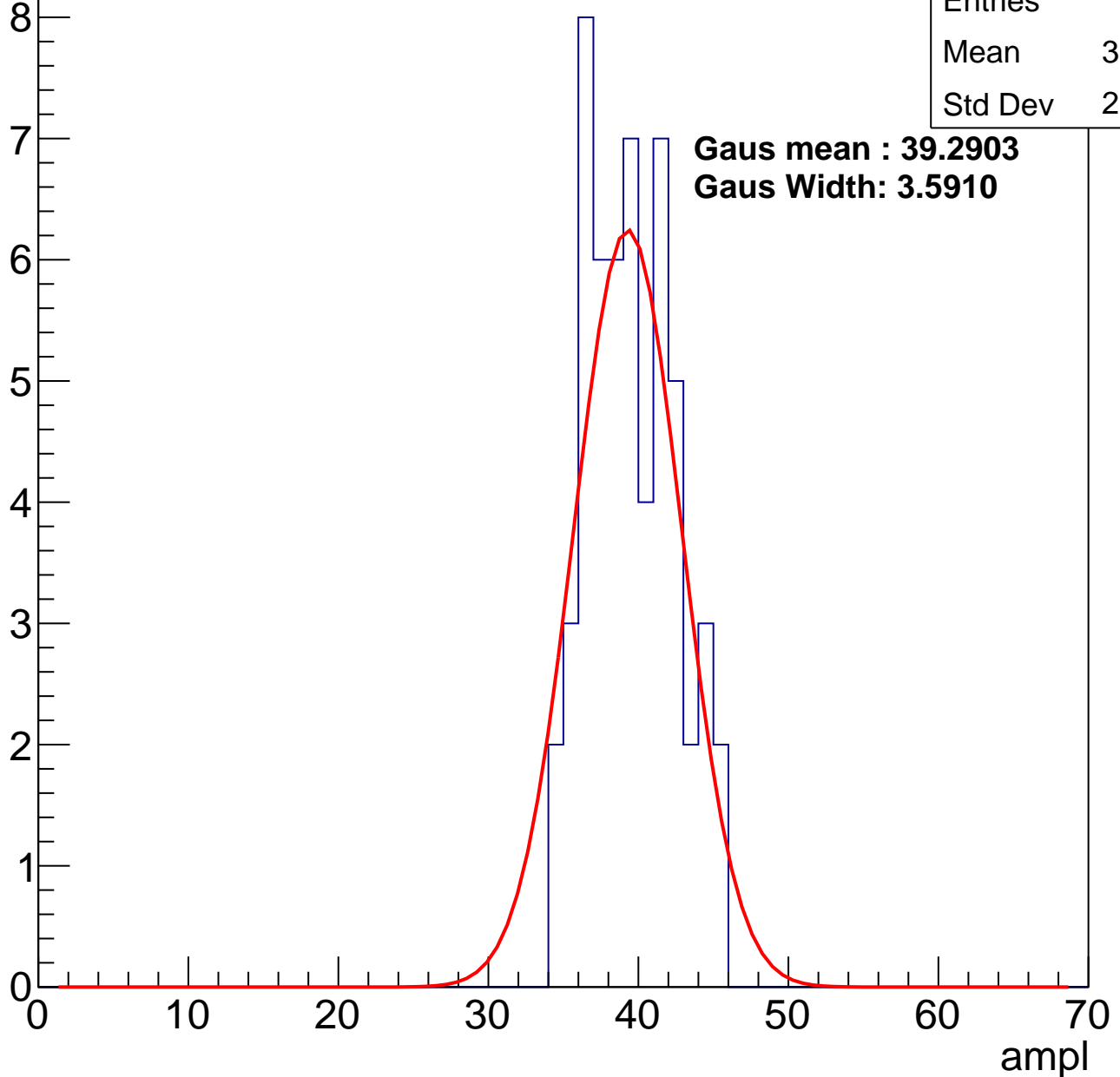
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 39.07 |
| Std Dev | 2.878 |

Gaus mean : 39.2903

Gaus Width: 3.5910



B0L001S, U7-ch5, adc2

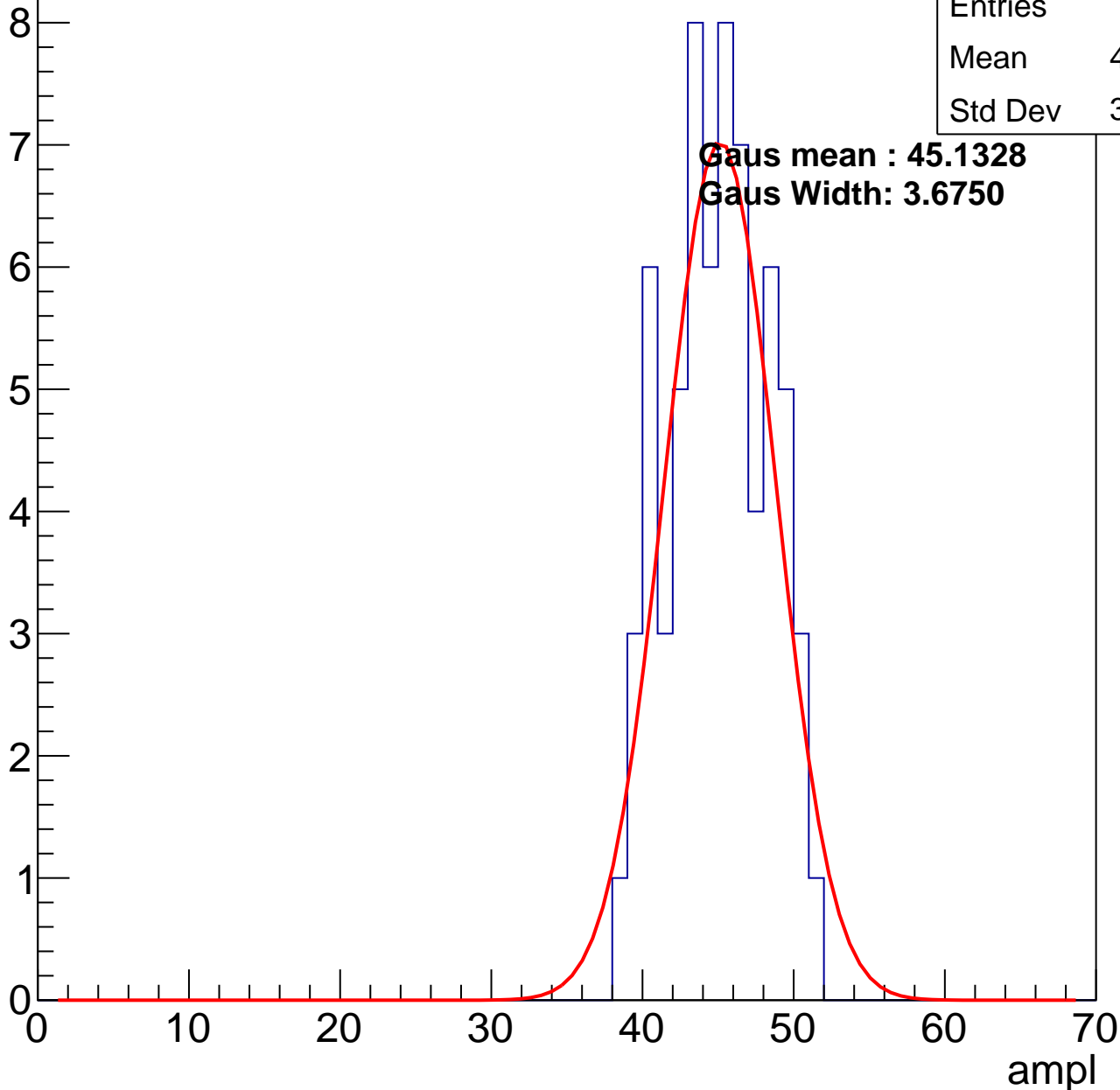
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44.55 |
| Std Dev | 3.239 |

Gaus mean : 45.1328

Gaus Width: 3.6750

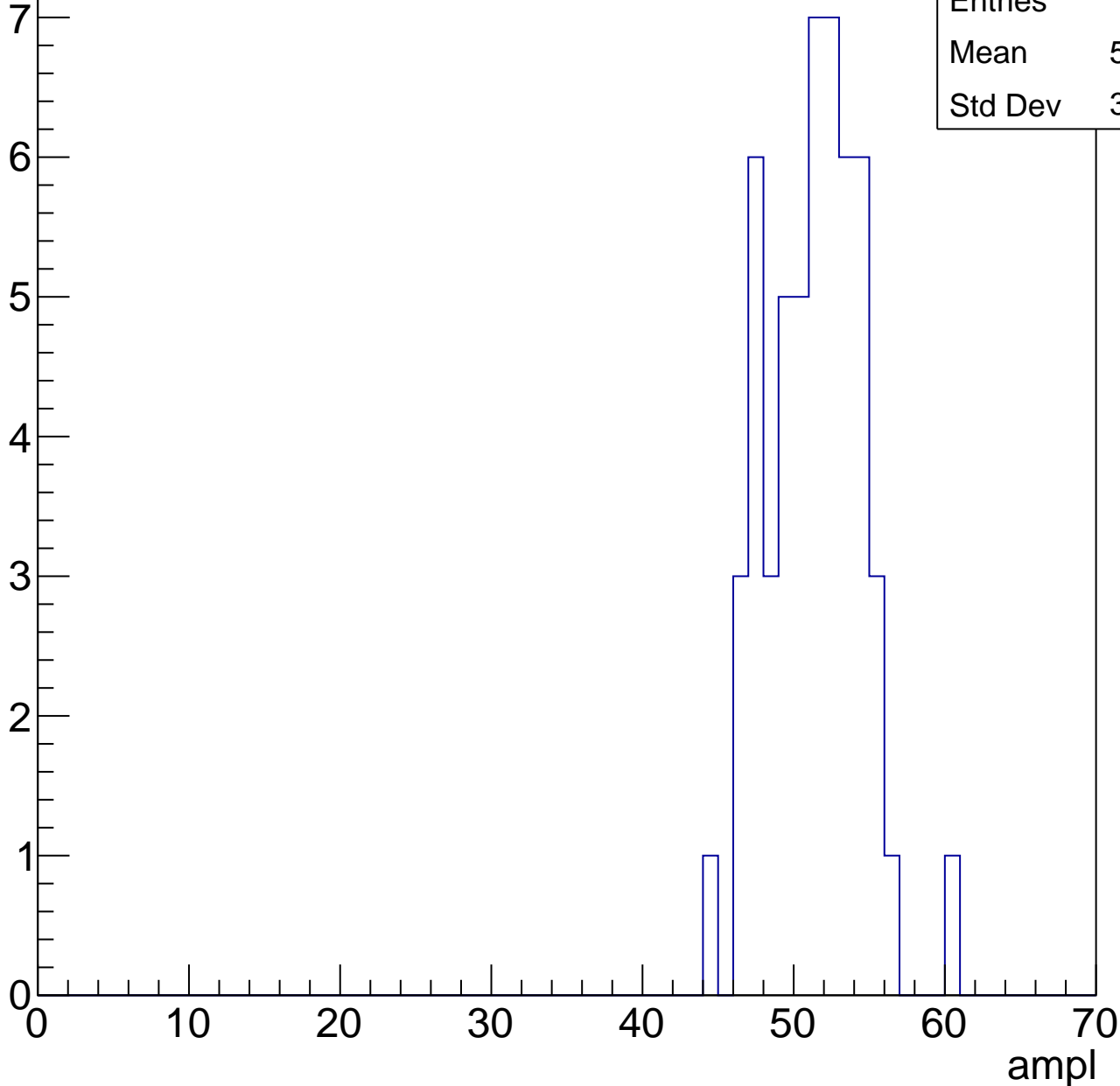


B0L001S, U7-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

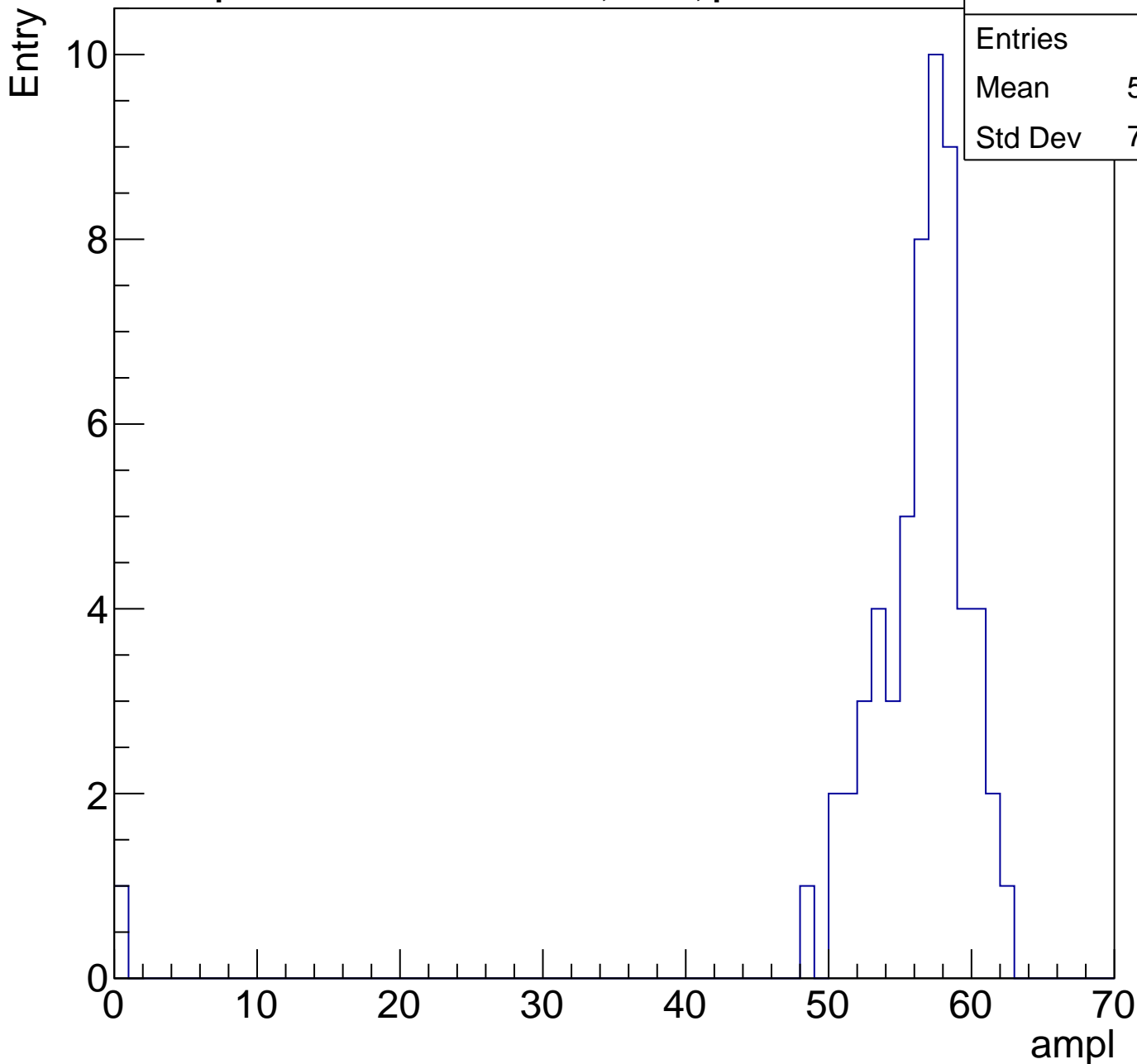
| | |
|---------|-------|
| Entries | 54 |
| Mean | 50.87 |
| Std Dev | 3.073 |



B0L001S, U7-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

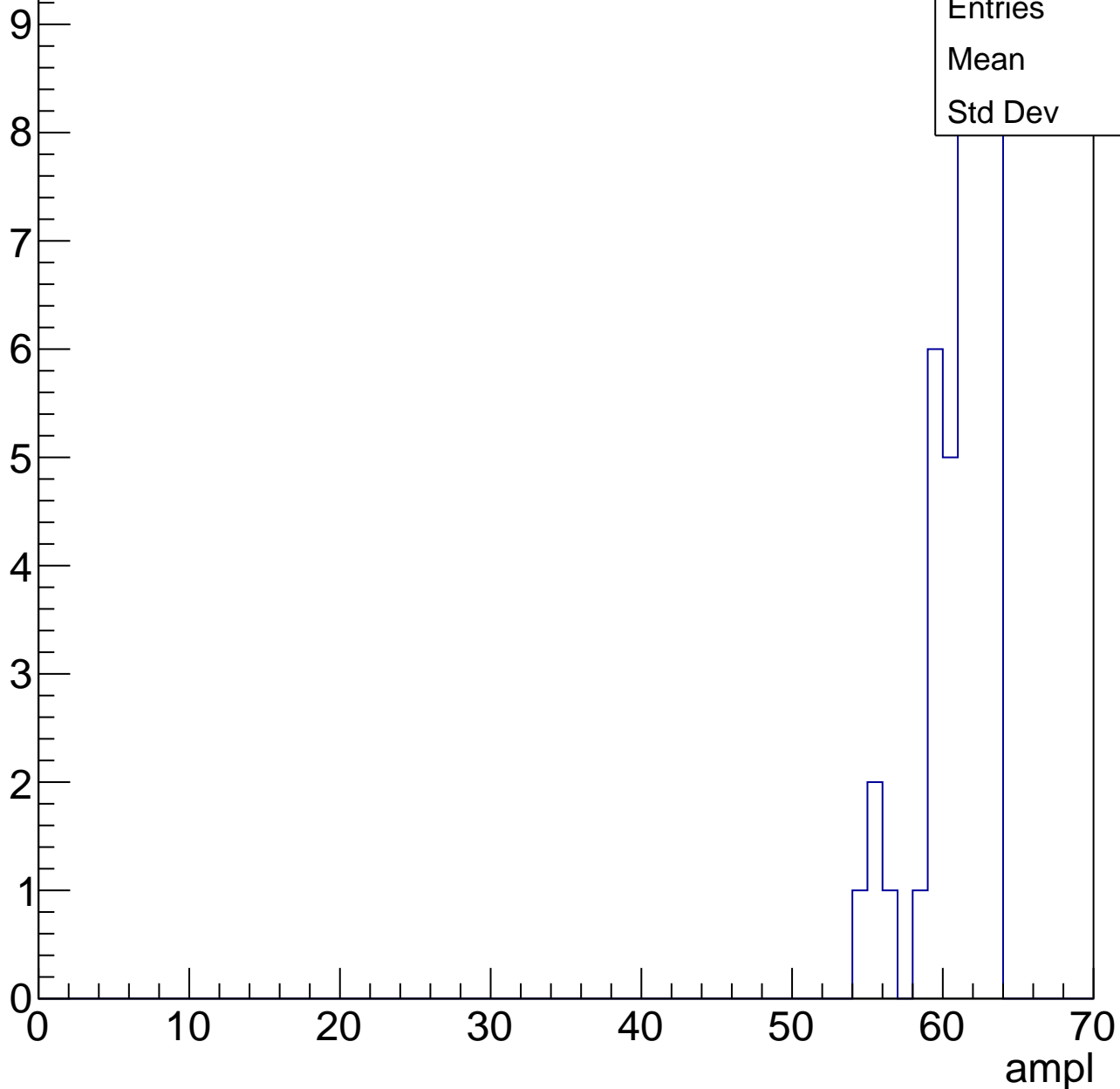
| | |
|---------|-------|
| Entries | 59 |
| Mean | 55.17 |
| Std Dev | 7.827 |



B0L001S, U7-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch6, adc0

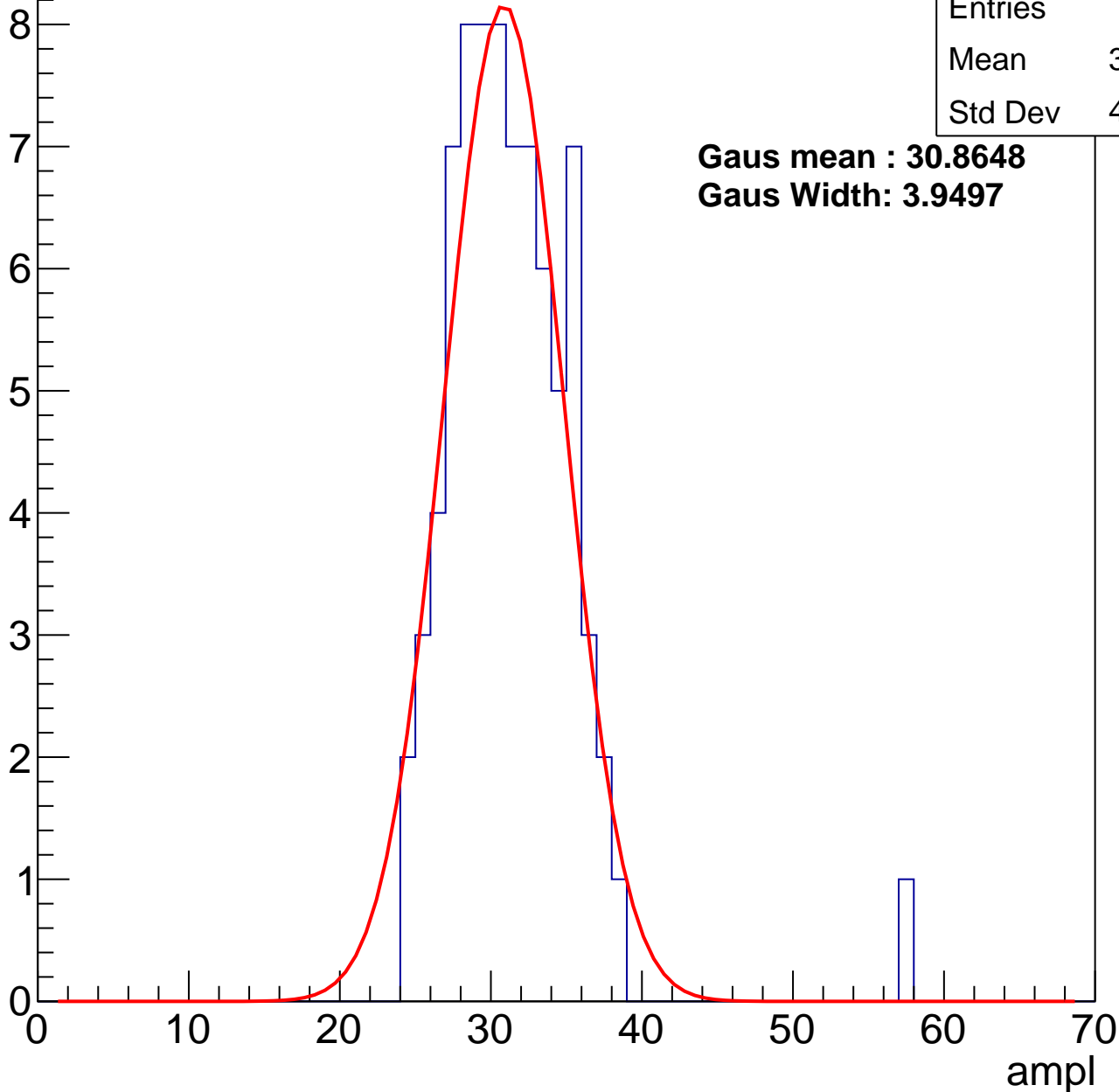
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 30.92 |
| Std Dev | 4.486 |

Gaus mean : 30.8648

Gaus Width: 3.9497



B0L001S, U7-ch6, adc1

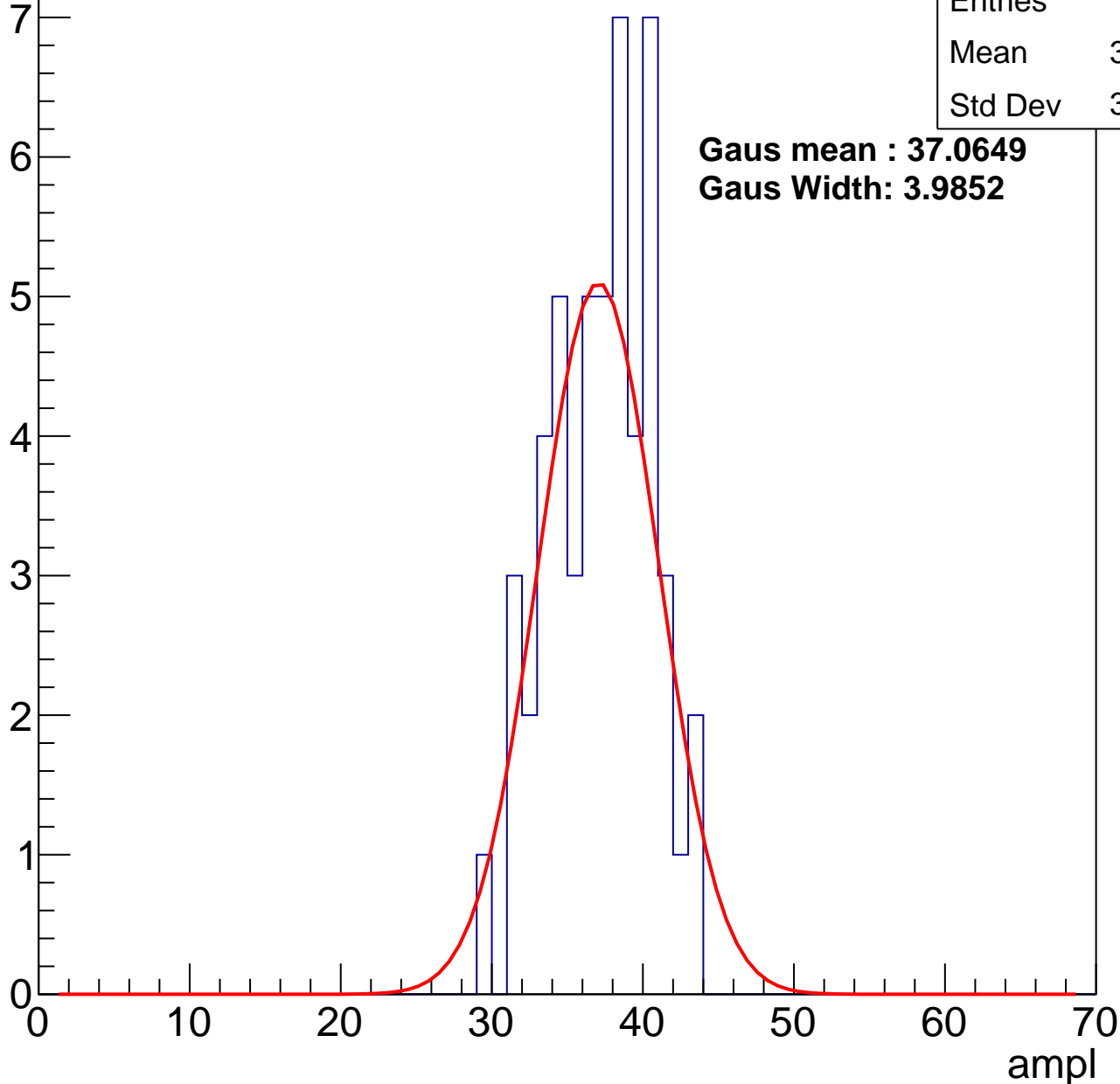
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 36.75 |
| Std Dev | 3.327 |

Gaus mean : 37.0649

Gaus Width: 3.9852



B0L001S, U7-ch6, adc2

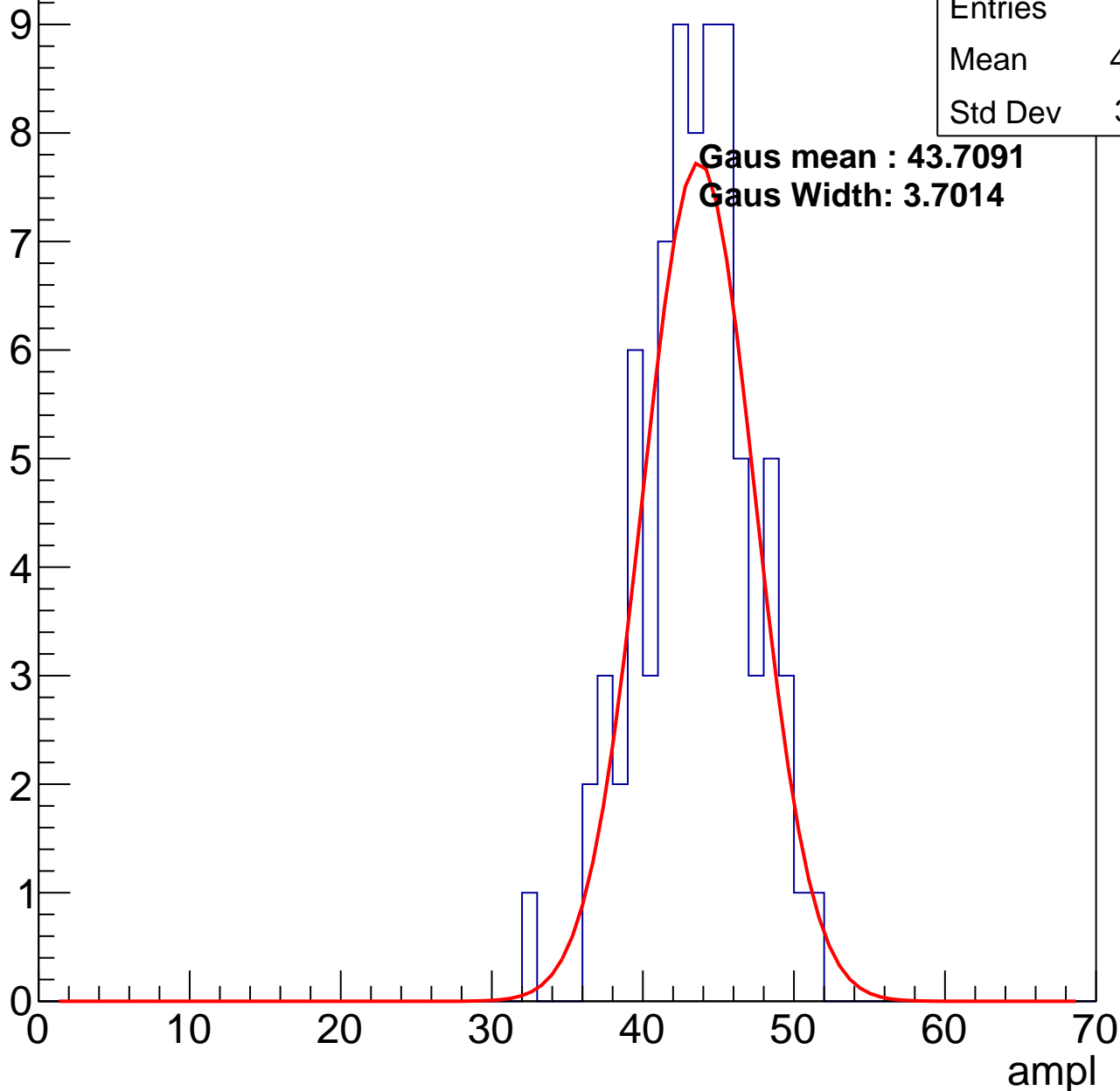
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 43.04 |
| Std Dev | 3.641 |

Gaus mean : 43.7091

Gaus Width: 3.7014

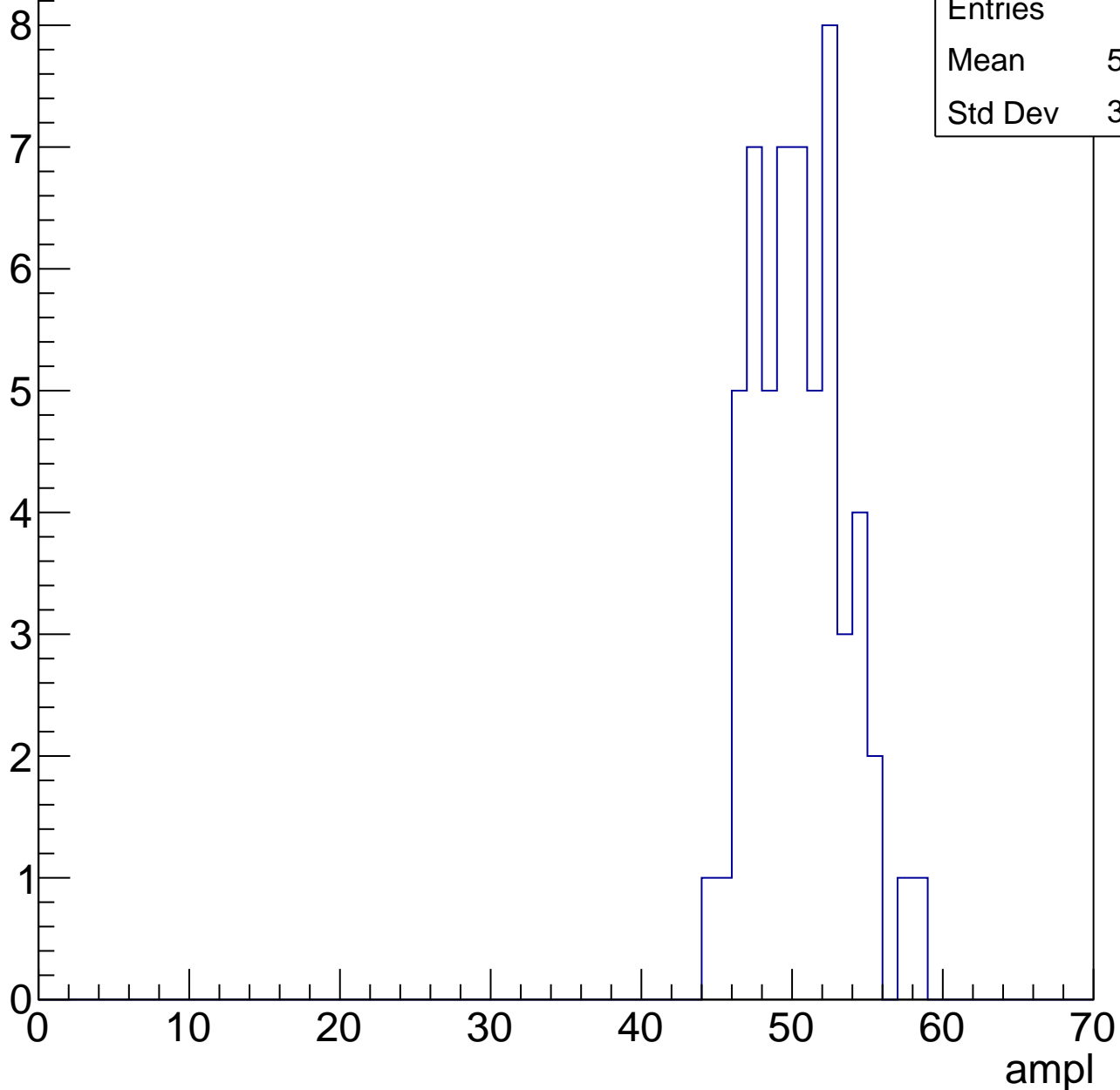


B0L001S, U7-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

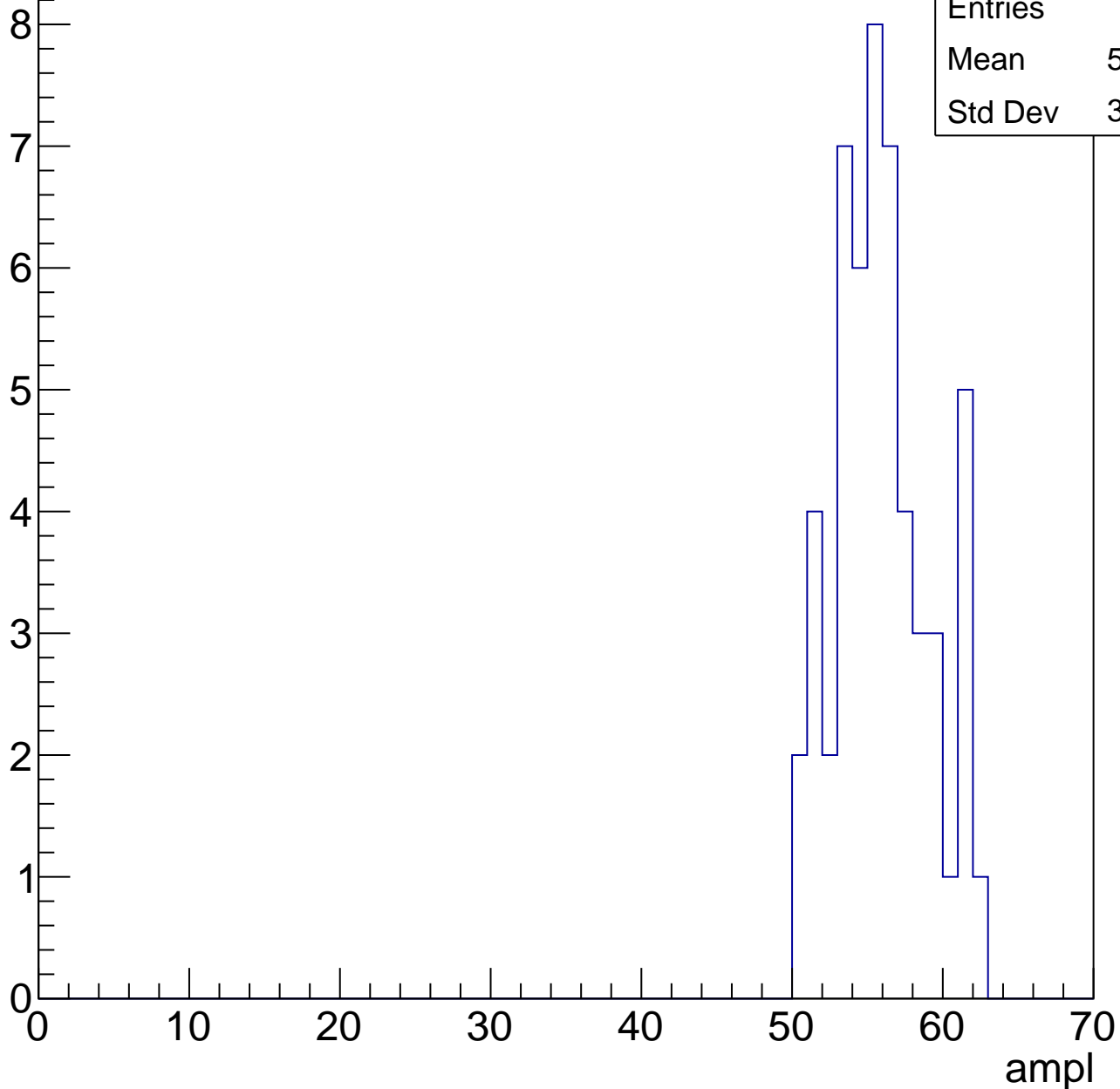
| | |
|---------|-------|
| Entries | 57 |
| Mean | 50.04 |
| Std Dev | 3.026 |



B0L001S, U7-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.49 |
| Std Dev | 3.088 |

B0L001S, U7-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|------|
| Entries | 52 |
| Mean | 59 |
| Std Dev | 8.54 |

Entry

10

8

6

4

2

0

0

10

20

30

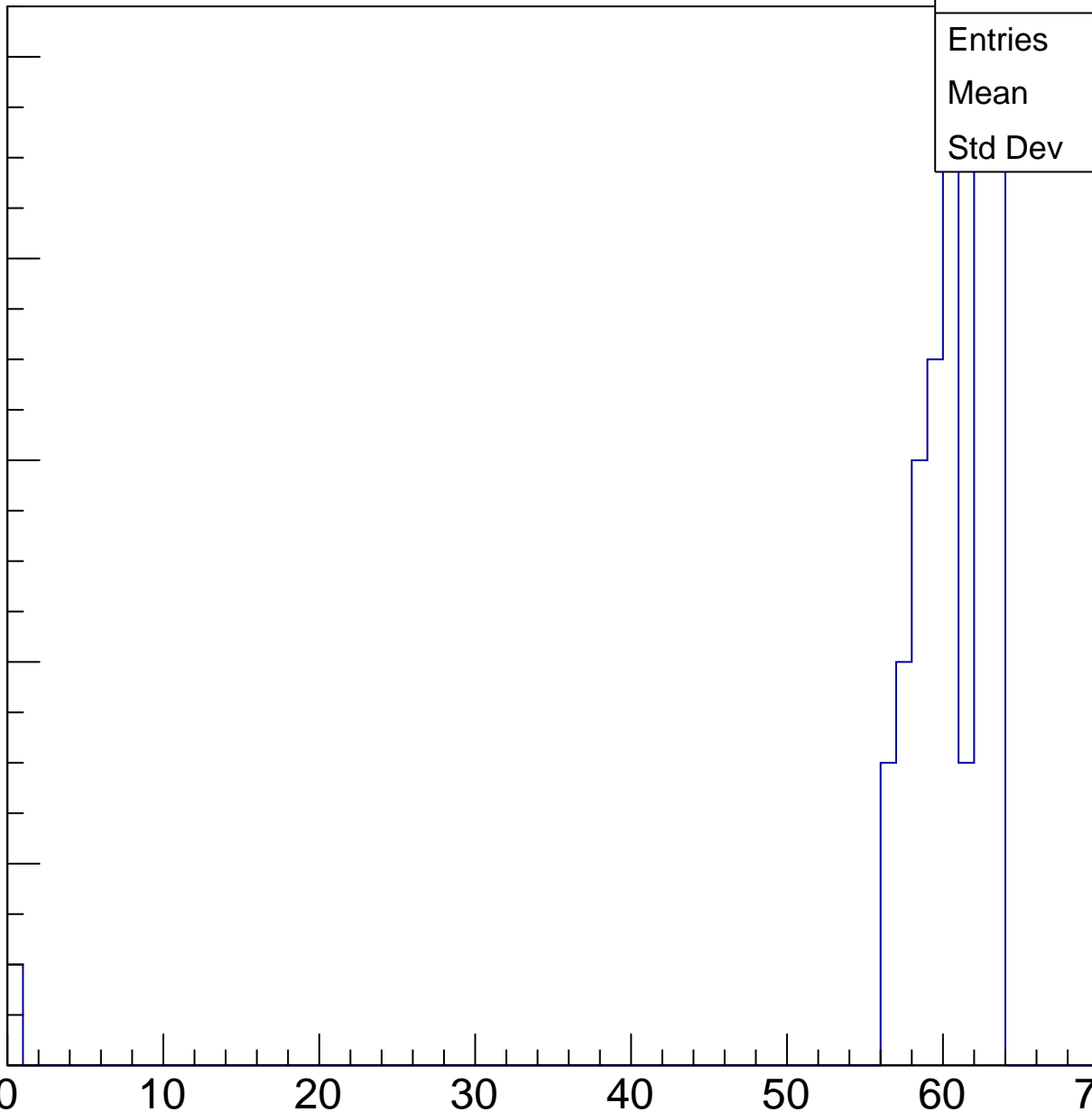
40

50

60

70

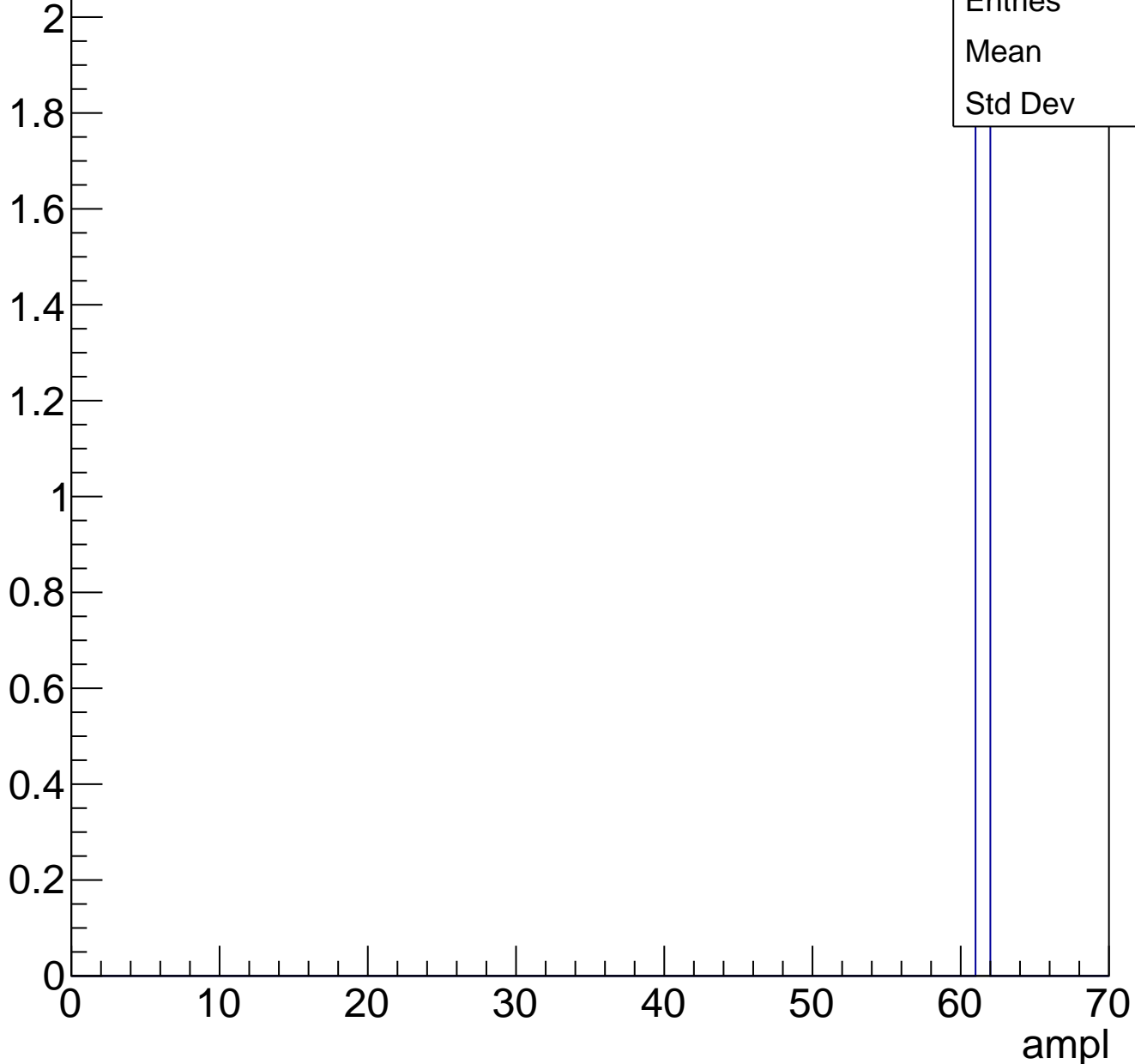
ampl



B0L001S, U7-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 61 |
| Std Dev | 0 |

B0L001S, U7-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch7, adc0

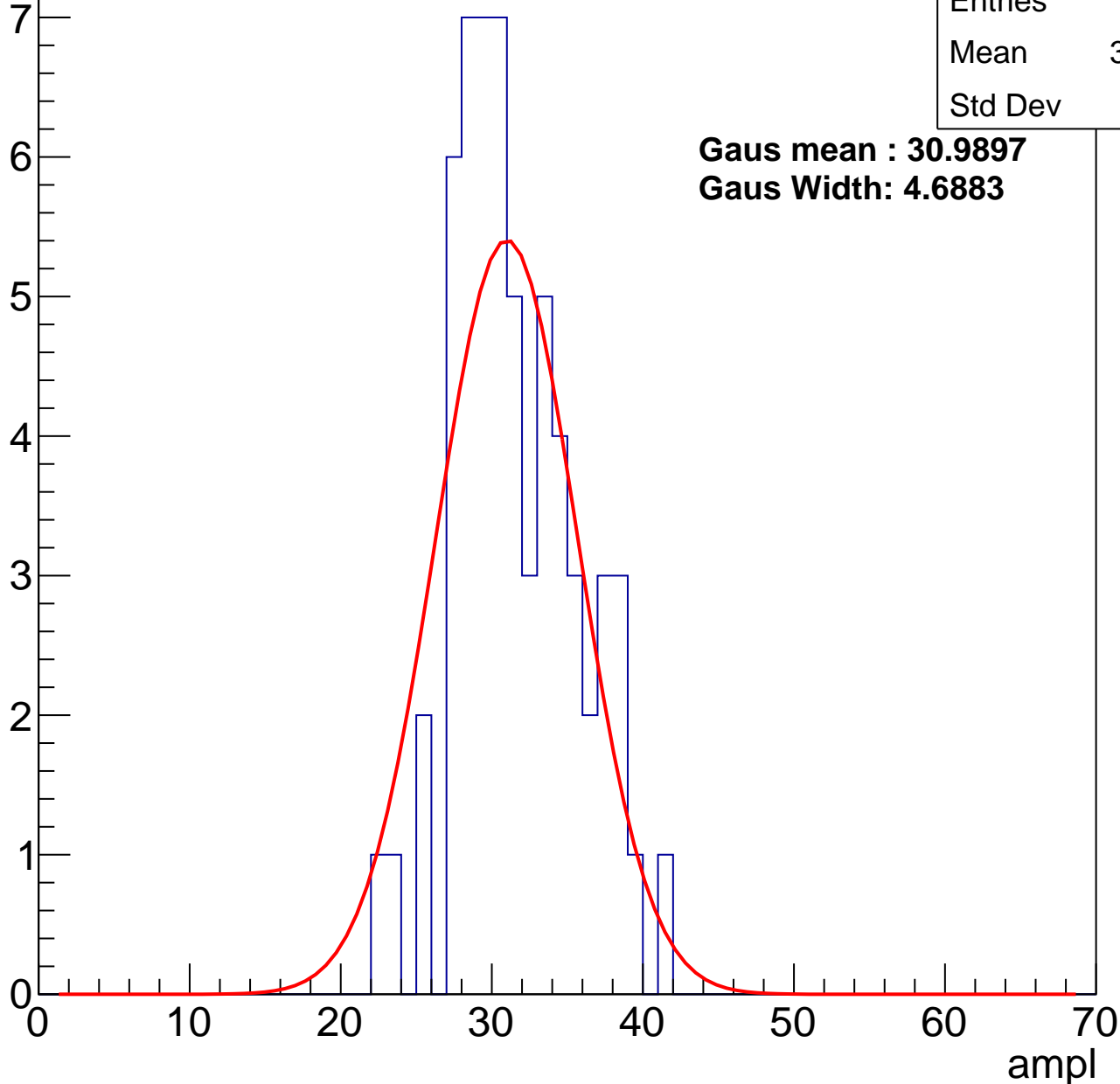
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 31.15 |
| Std Dev | 4.02 |

Gaus mean : 30.9897

Gaus Width: 4.6883



B0L001S, U7-ch7, adc1

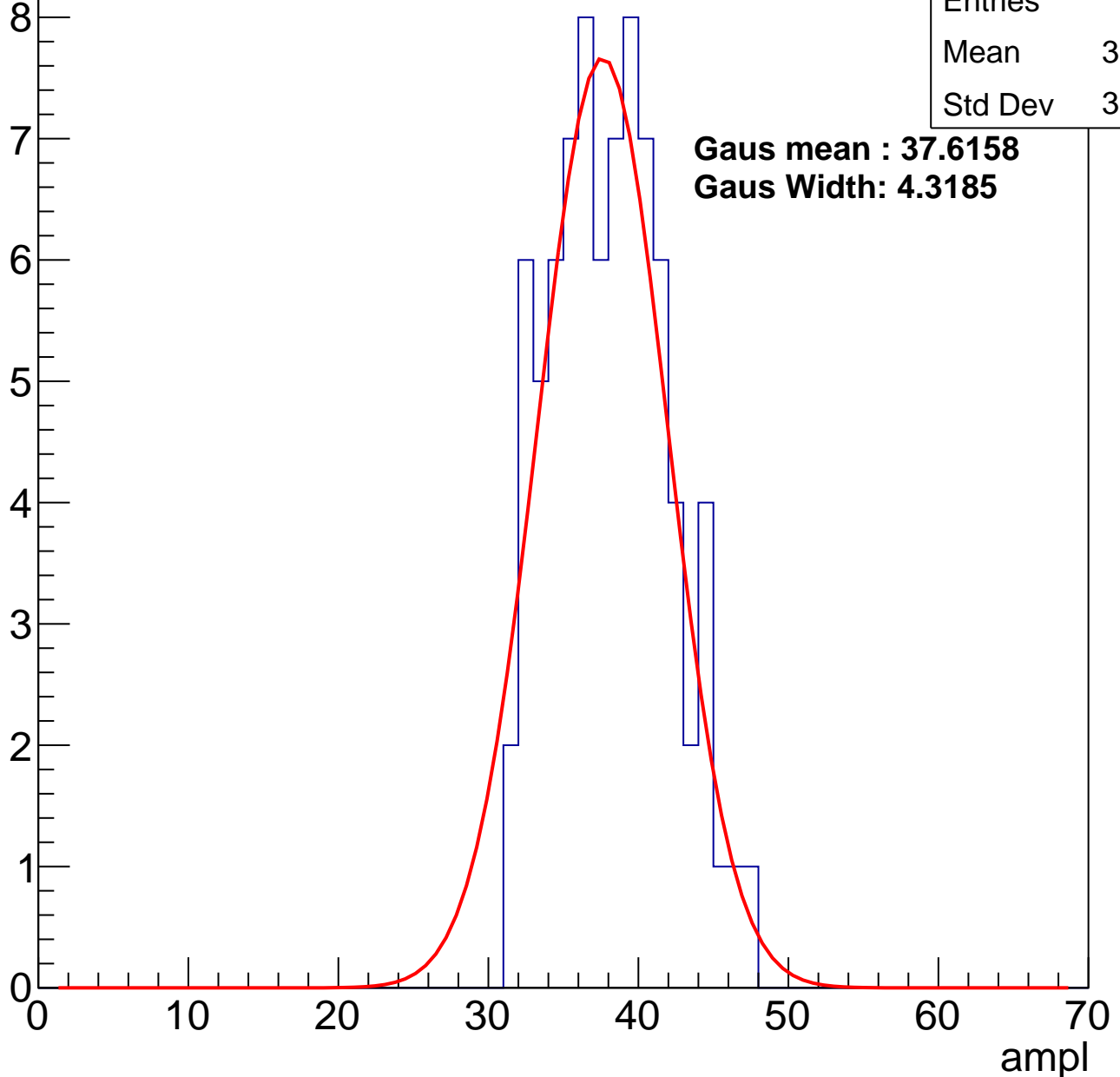
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 37.65 |
| Std Dev | 3.798 |

Gaus mean : 37.6158

Gaus Width: 4.3185



B0L001S, U7-ch7, adc2

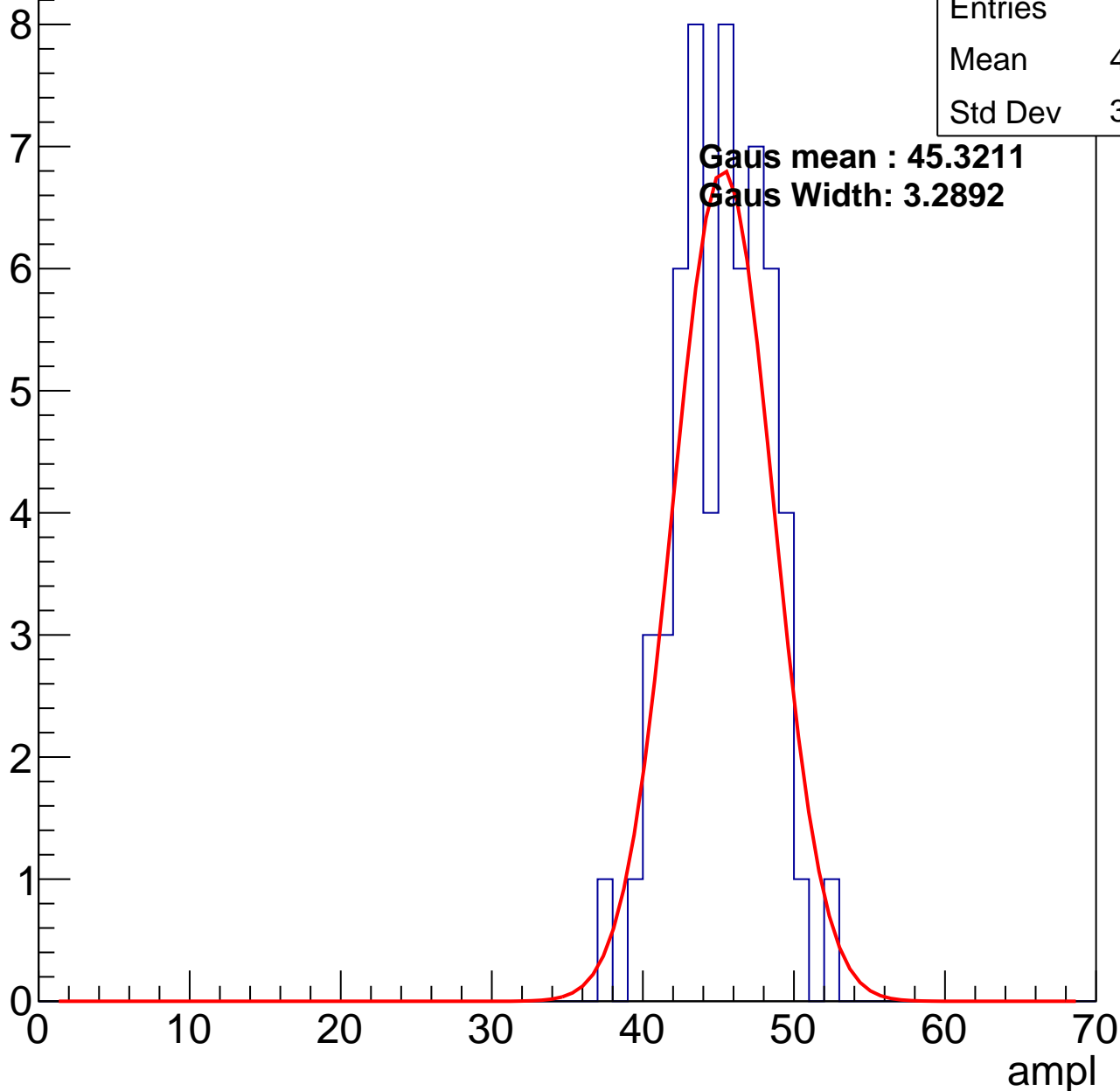
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 44.78 |
| Std Dev | 3.014 |

Gaus mean : 45.3211

Gaus Width: 3.2892

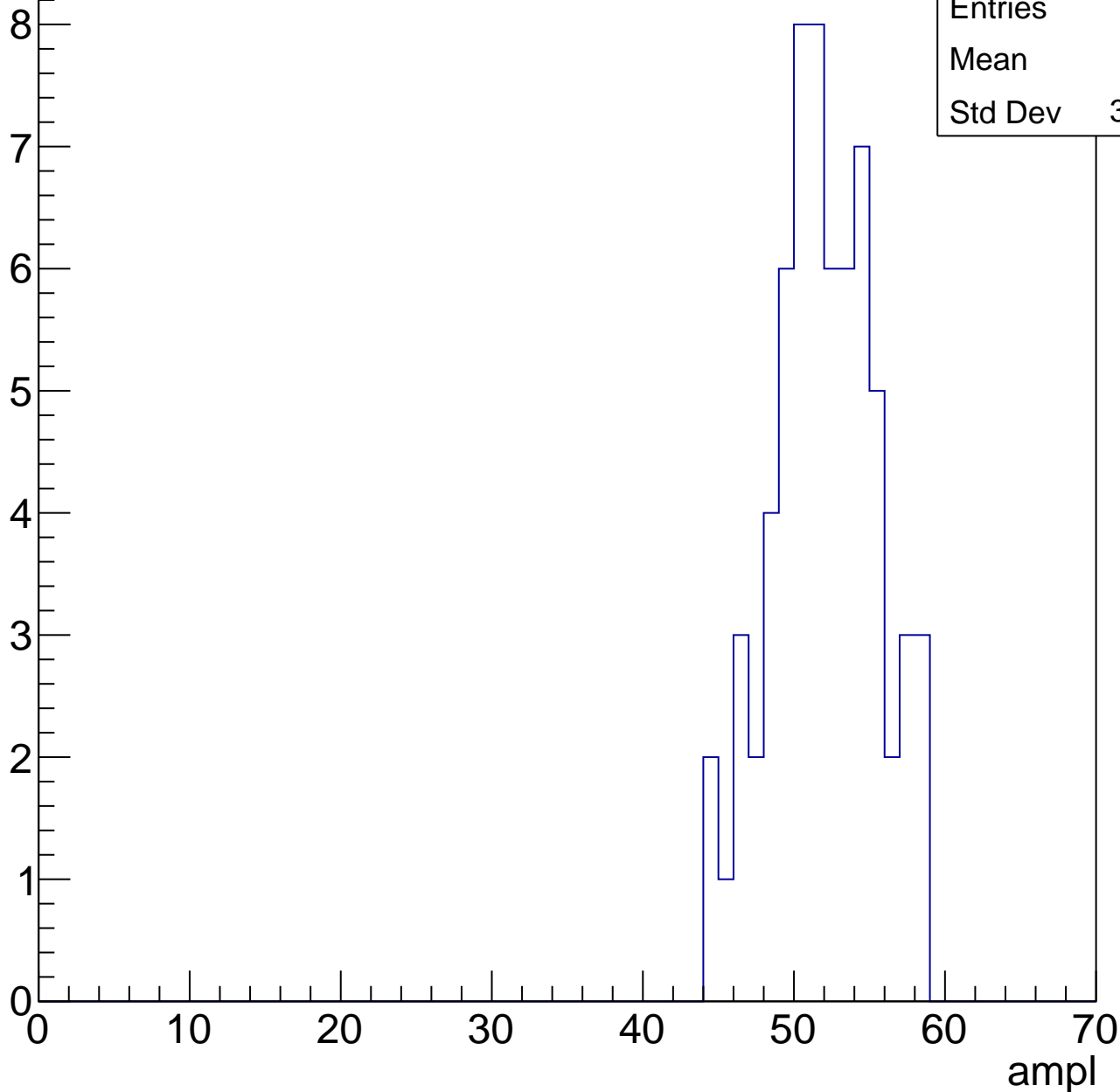


B0L001S, U7-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 51.5 |
| Std Dev | 3.417 |

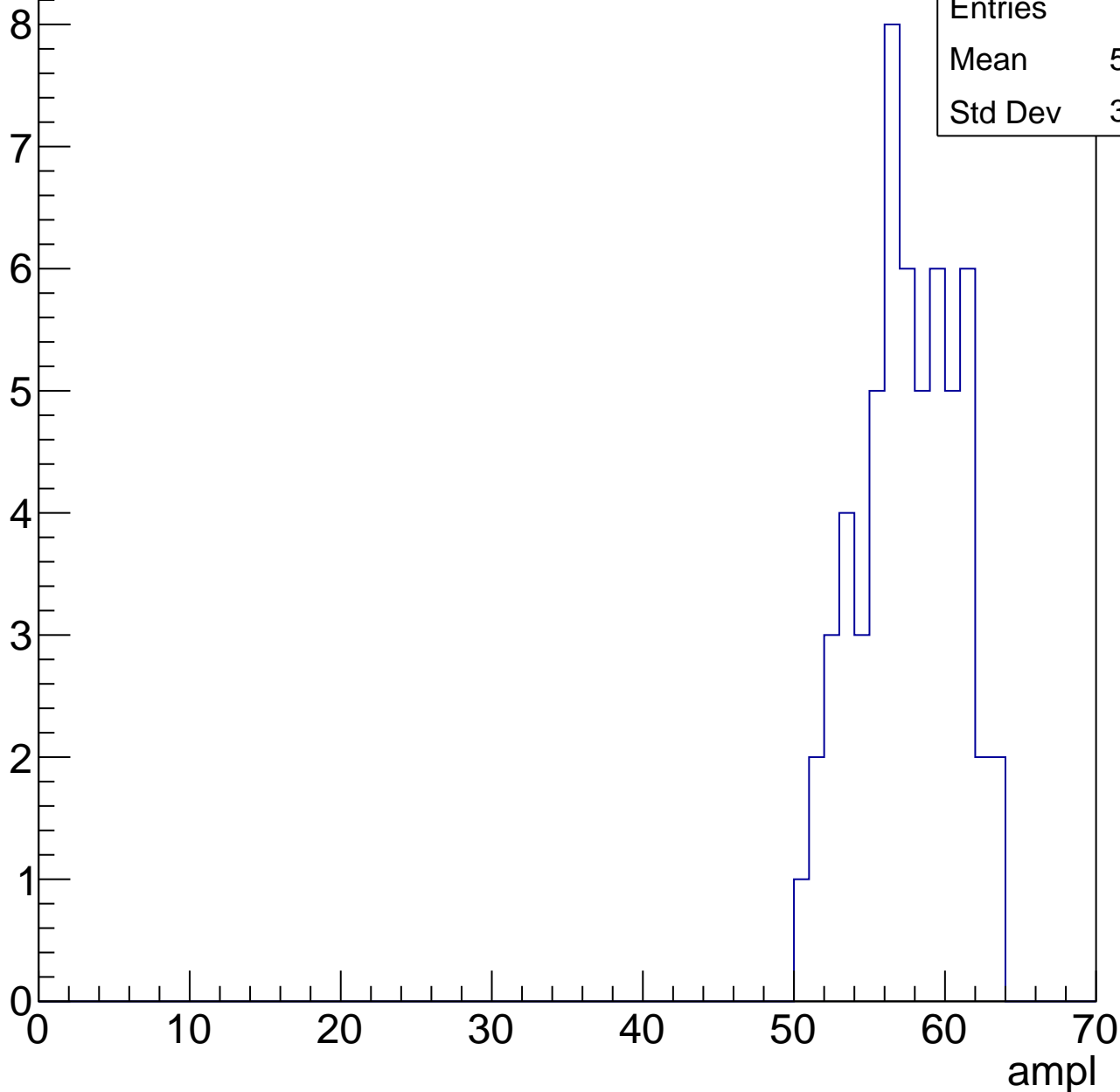


B0L001S, U7-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 57.02 |
| Std Dev | 3.235 |

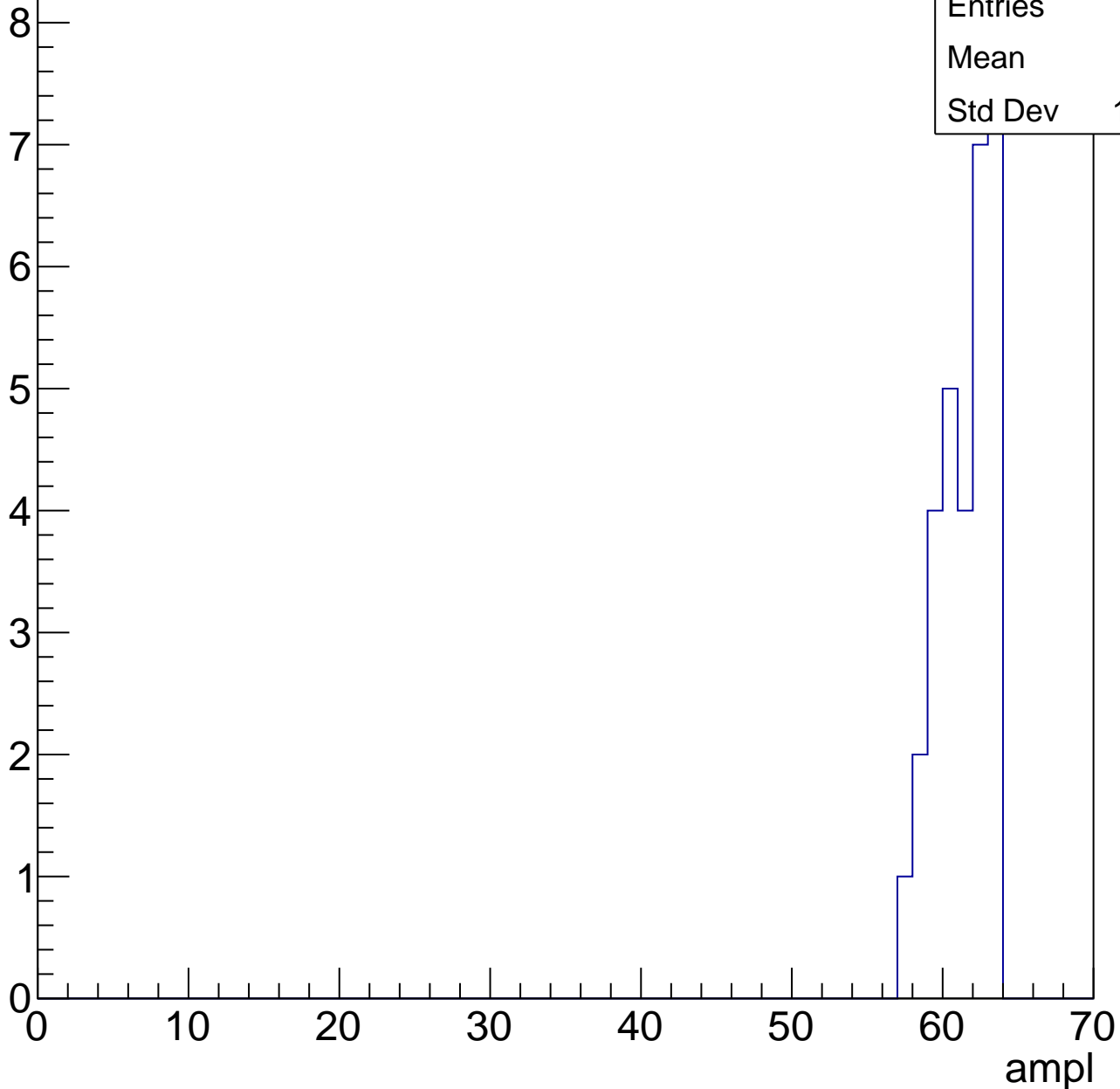


B0L001S, U7-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

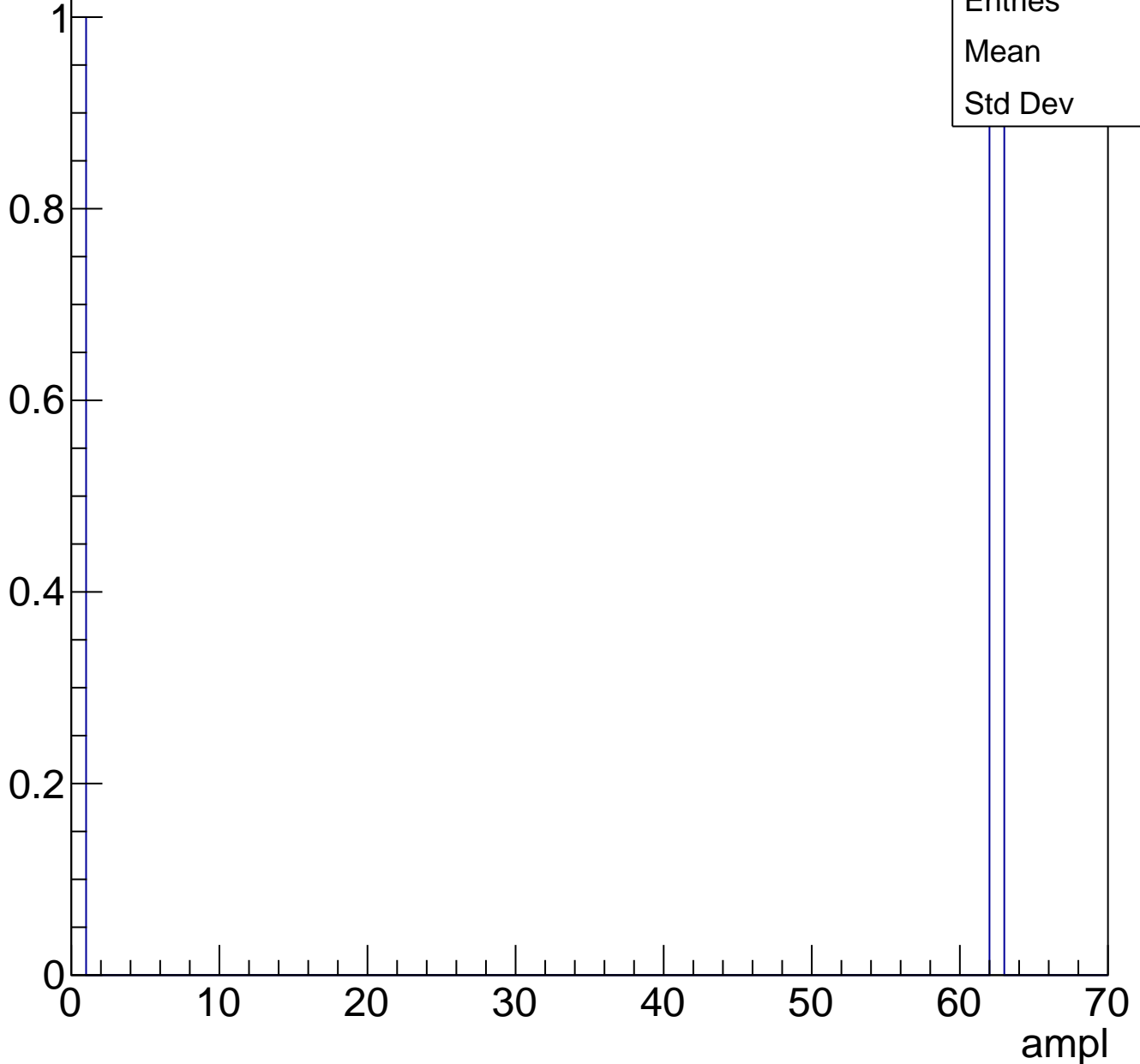
| | |
|---------|-------|
| Entries | 31 |
| Mean | 61 |
| Std Dev | 1.741 |



B0L001S, U7-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch8, adc0

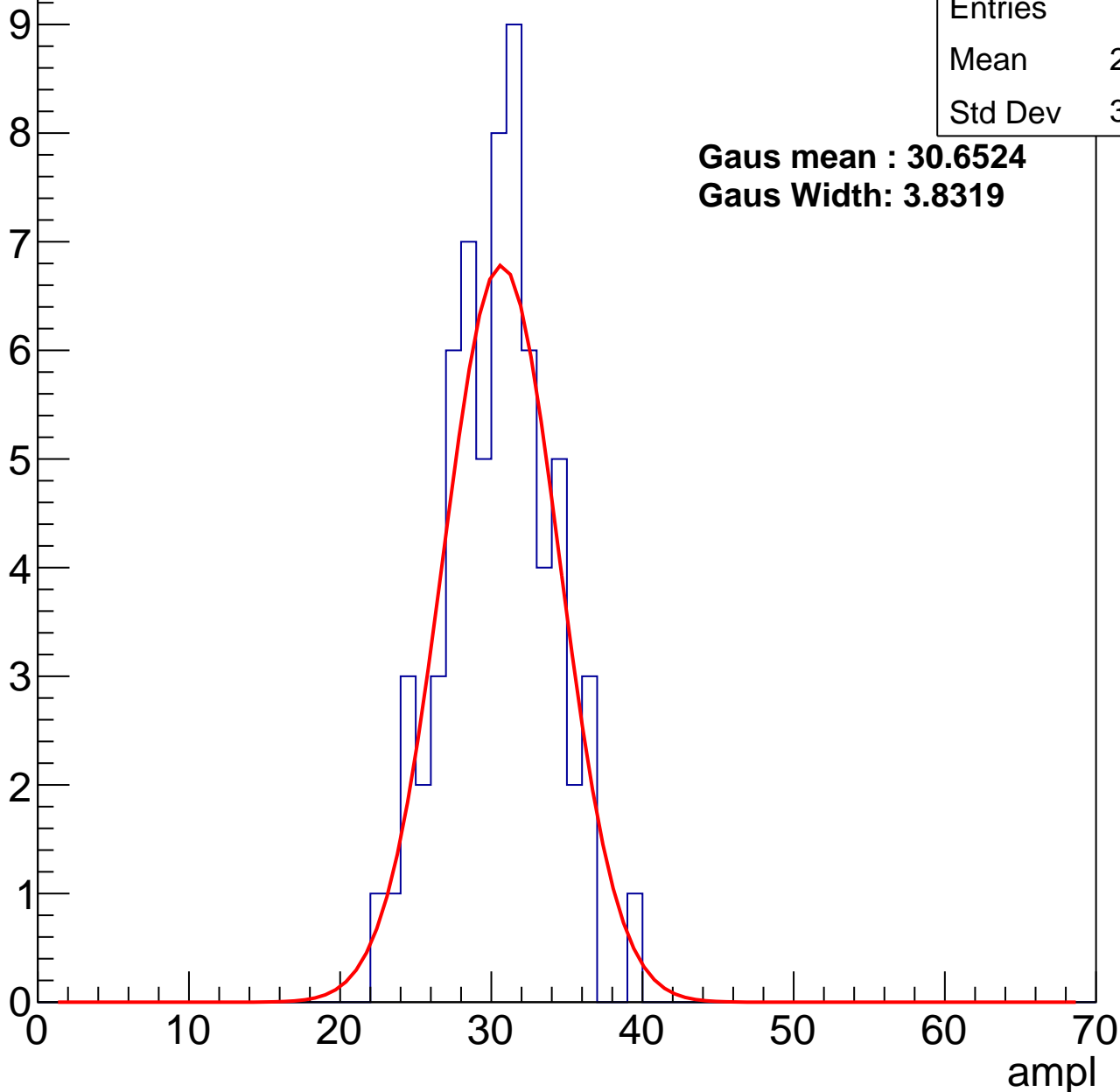
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 29.97 |
| Std Dev | 3.468 |

Gaus mean : 30.6524

Gaus Width: 3.8319



B0L001S, U7-ch8, adc1

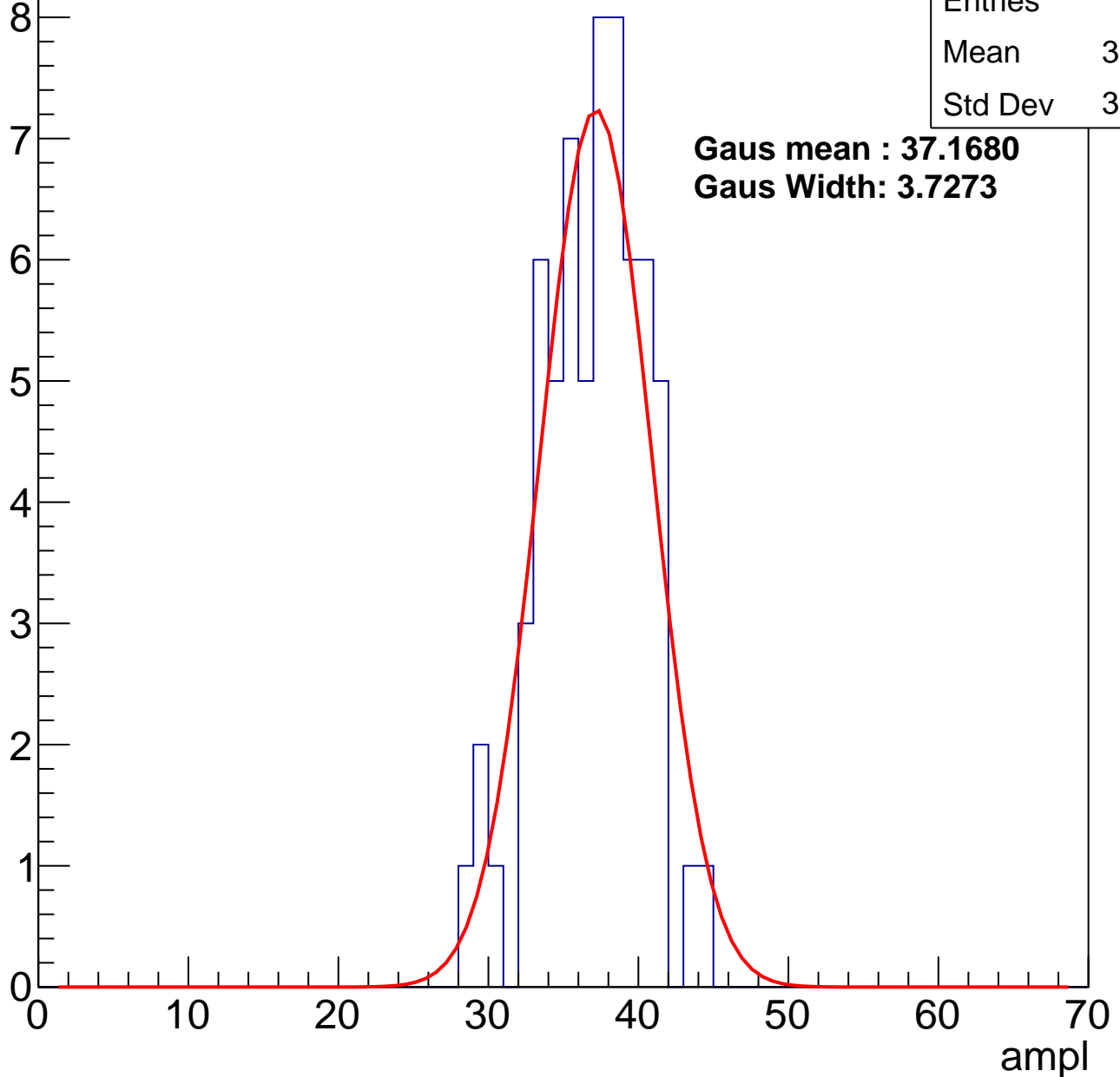
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.48 |
| Std Dev | 3.375 |

Gaus mean : 37.1680

Gaus Width: 3.7273



B0L001S, U7-ch8, adc2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 57 |
| Mean | 42.35 |
| Std Dev | 3.165 |

Gaus mean : 42.9957

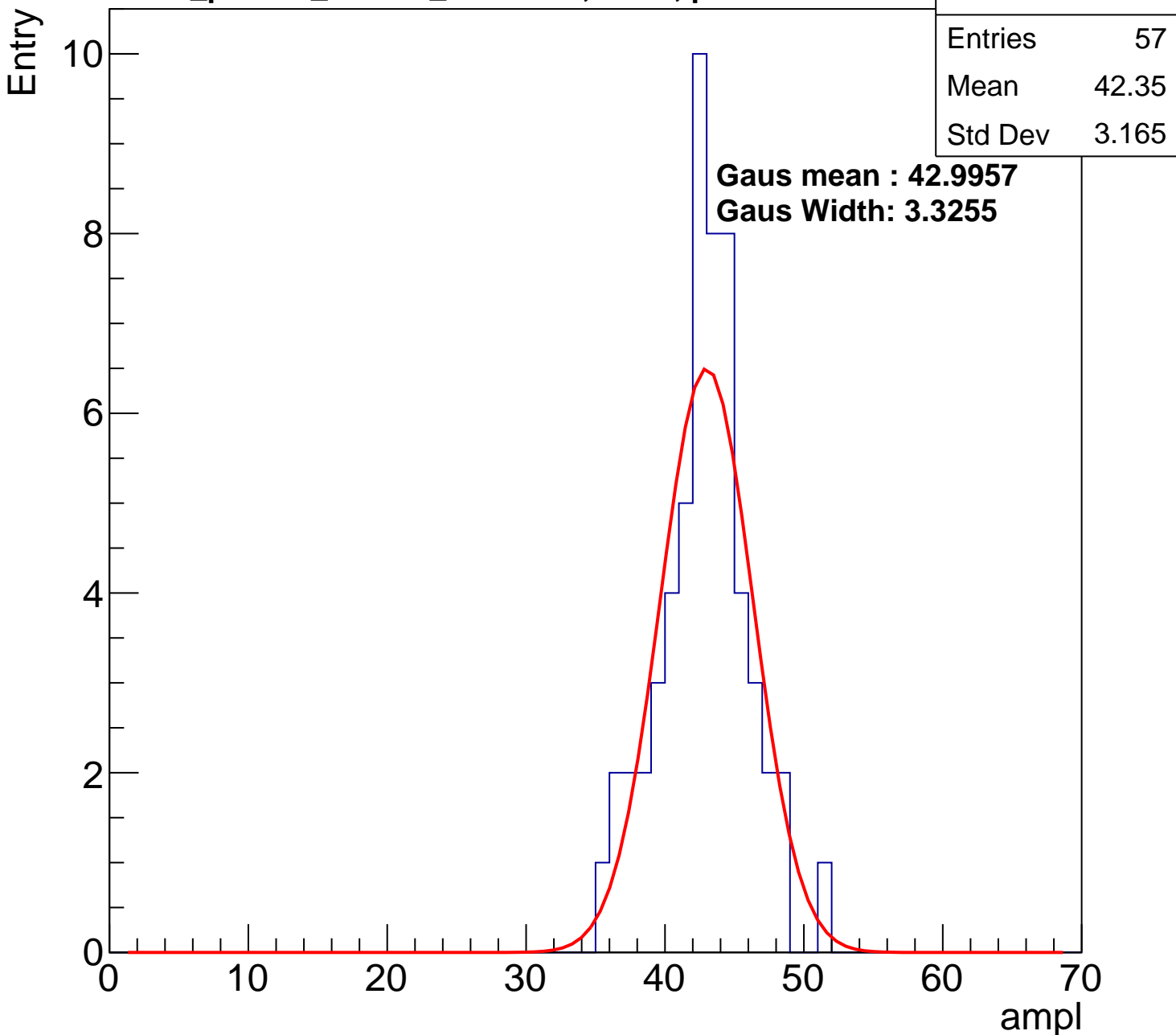
Gaus Width: 3.3255

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

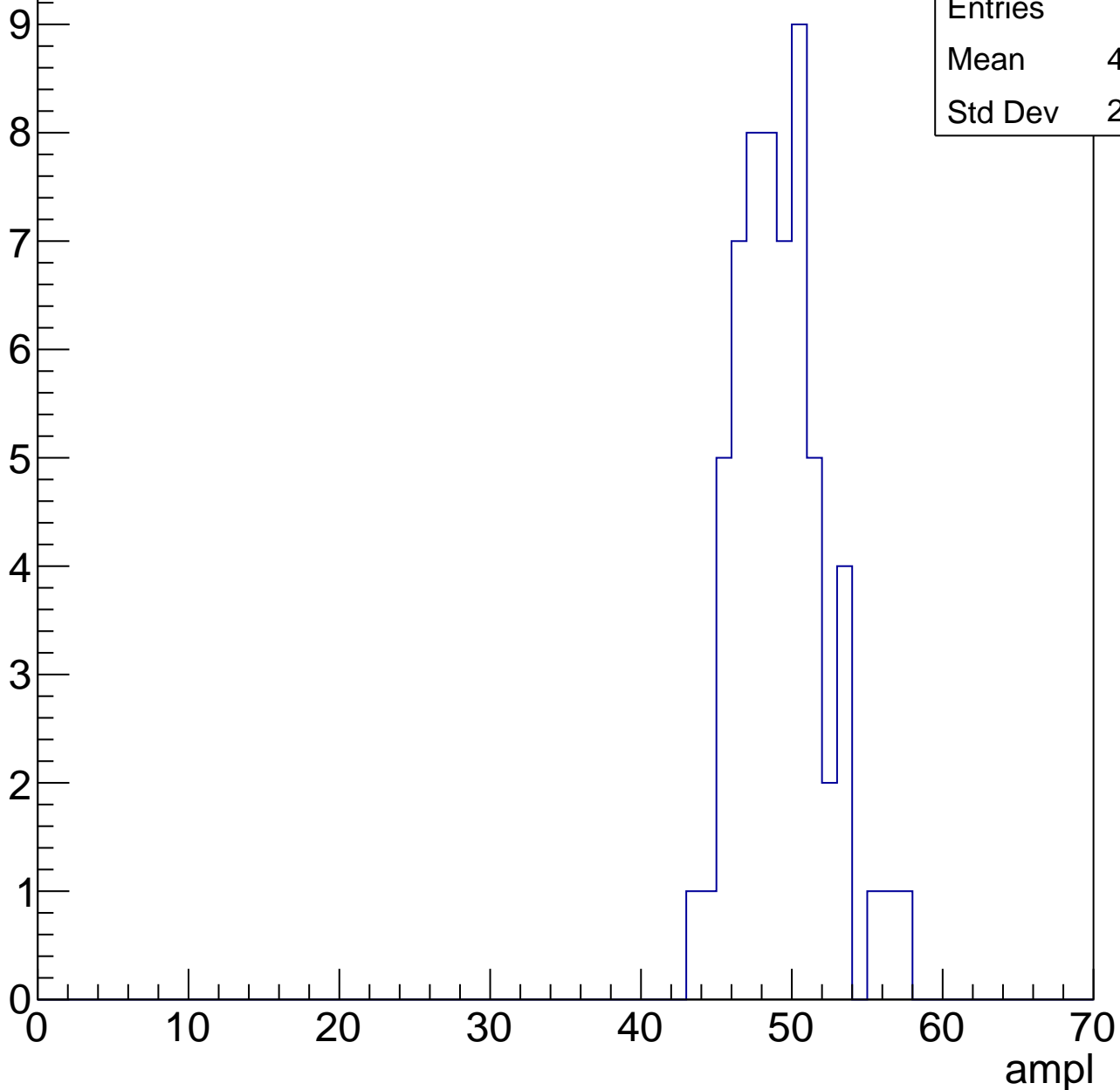


B0L001S, U7-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

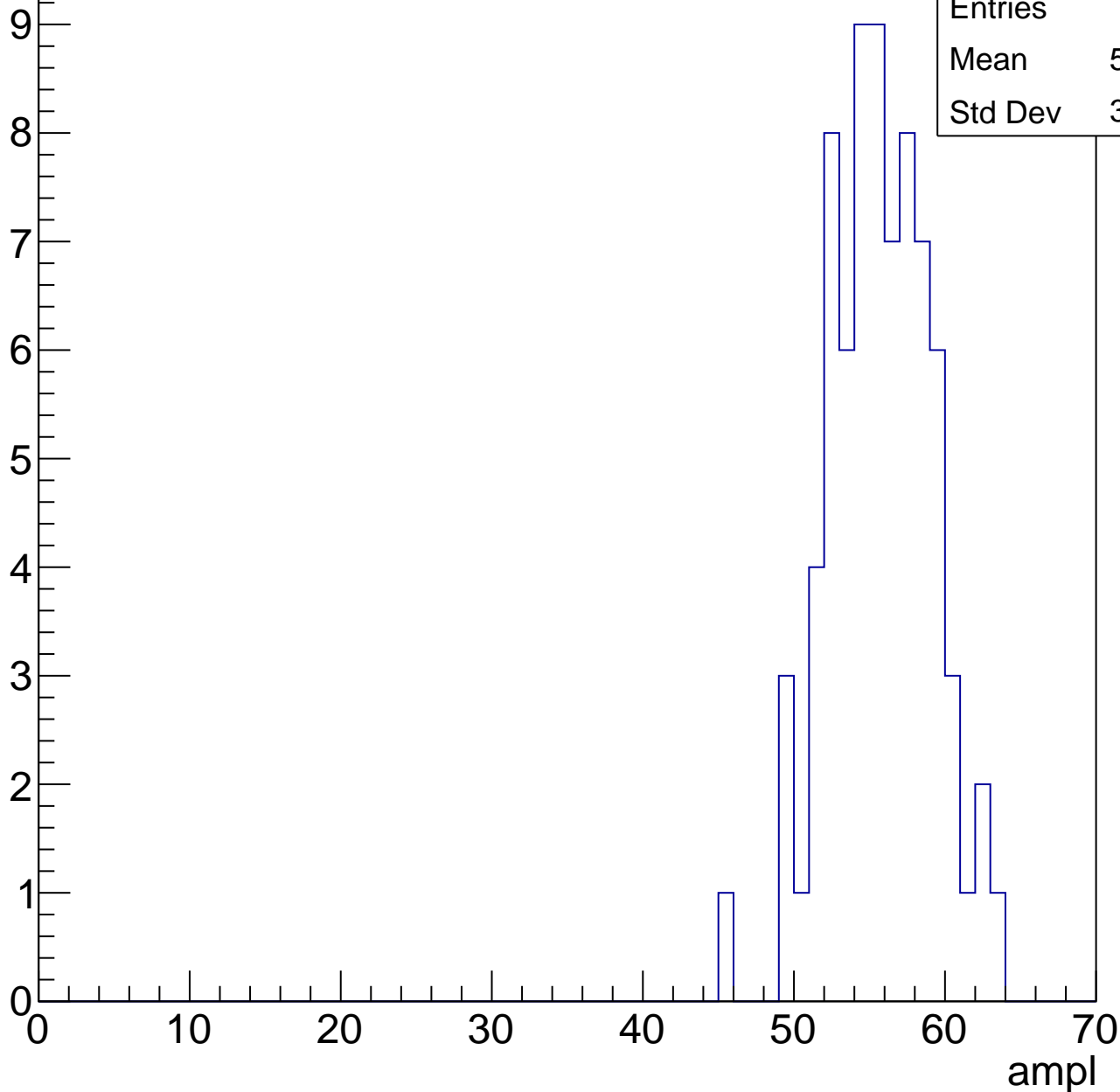
| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.77 |
| Std Dev | 2.889 |



B0L001S, U7-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

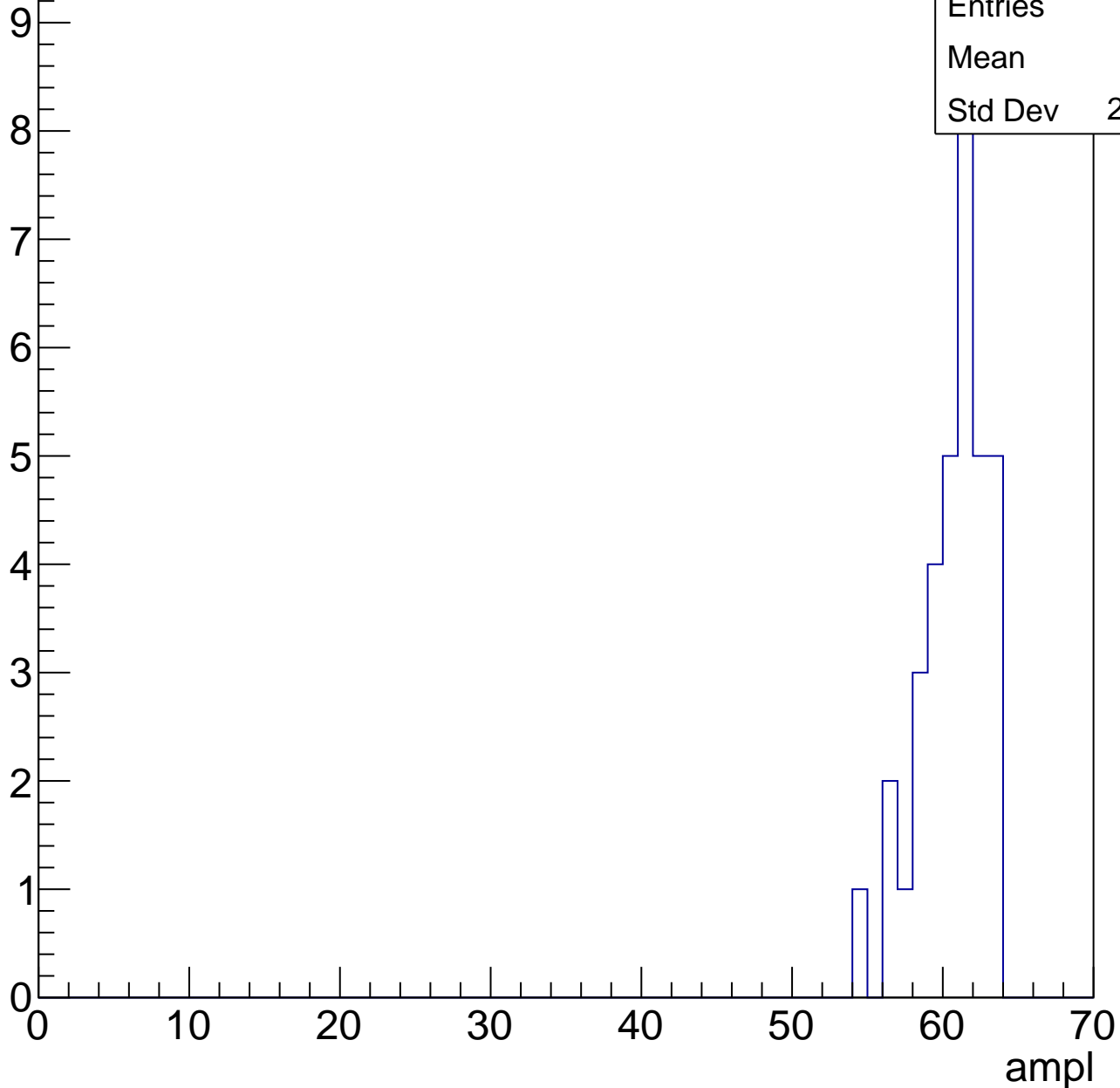


| | |
|---------|-------|
| Entries | 76 |
| Mean | 55.22 |
| Std Dev | 3.382 |

B0L001S, U7-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

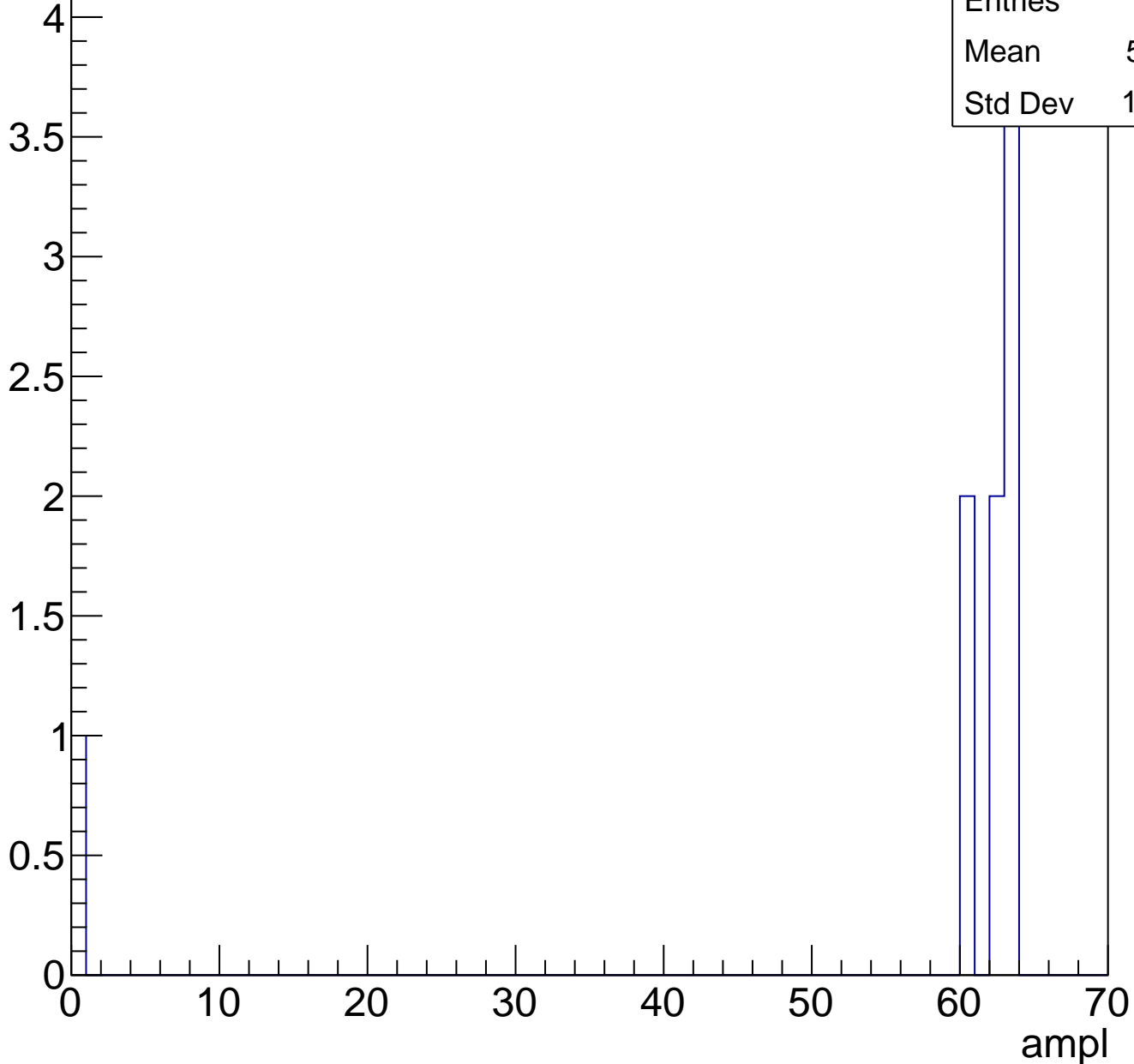
Entry



B0L001S, U7-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 9 |
| Mean | 55.11 |
| Std Dev | 19.52 |

B0L001S, U7-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch9, adc0

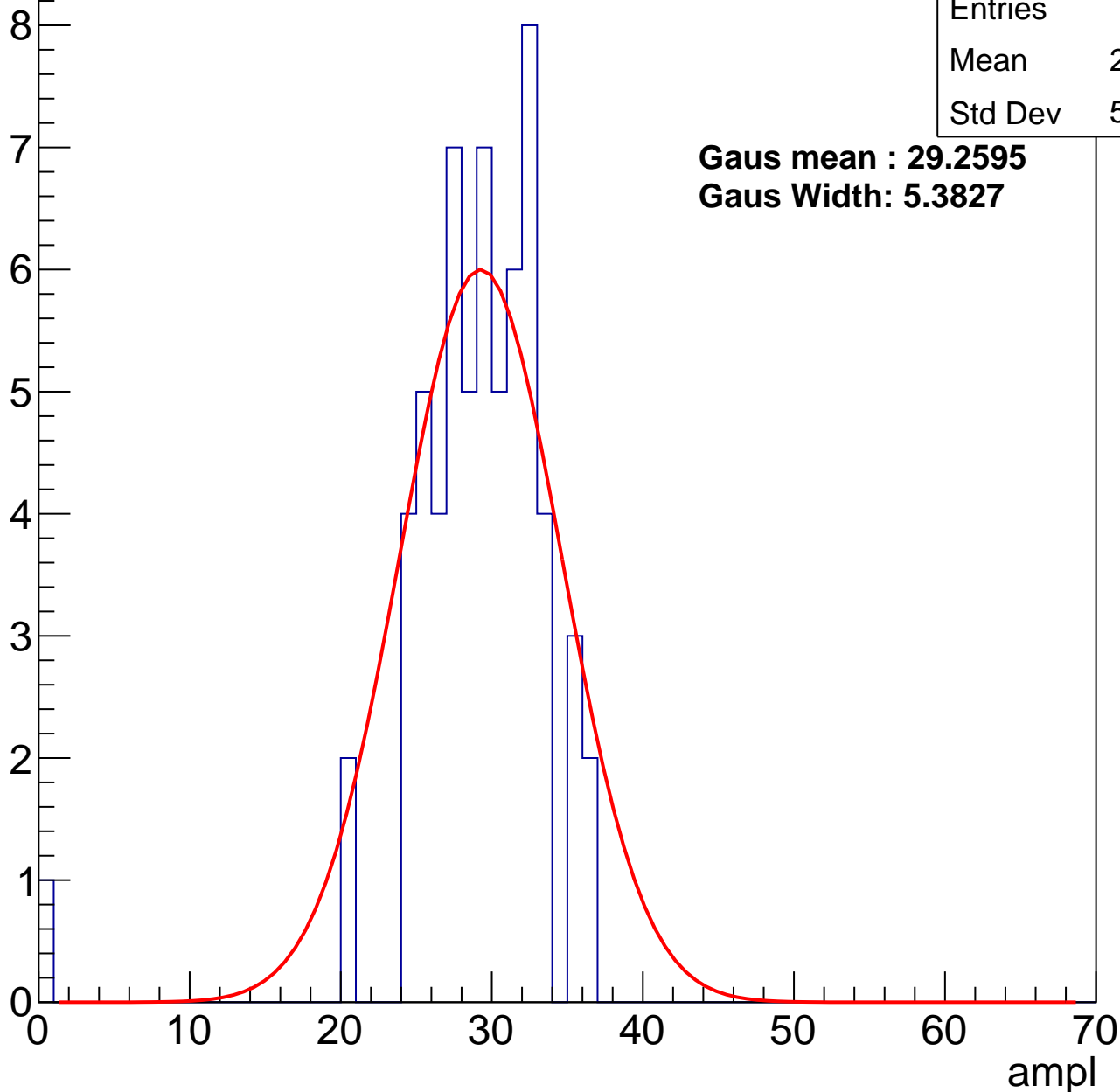
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.54 |
| Std Dev | 5.055 |

Gaus mean : 29.2595

Gaus Width: 5.3827



B0L001S, U7-ch9, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 36.19 |
| Std Dev | 3.758 |

Gaus mean : 36.5005

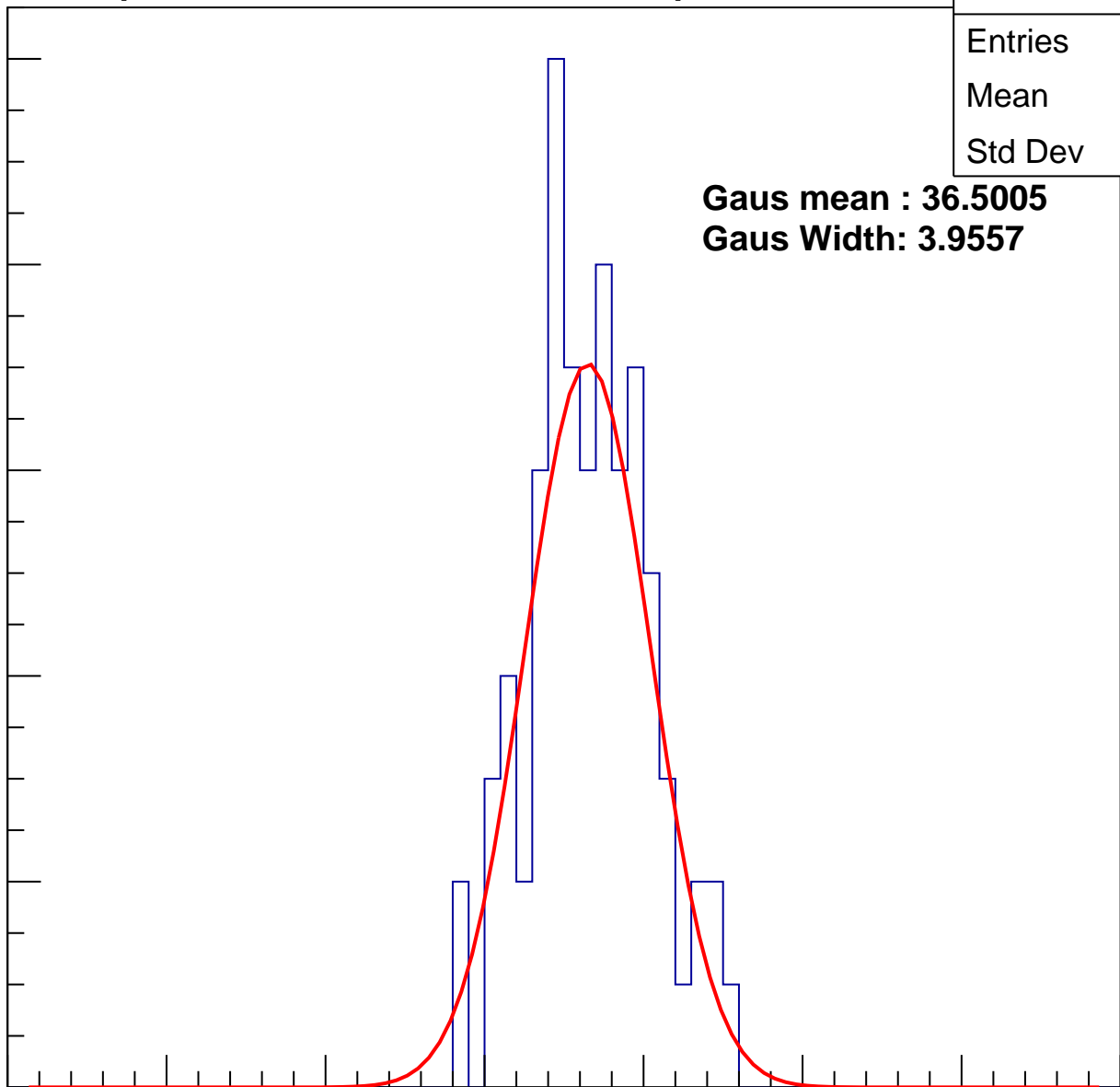
Gaus Width: 3.9557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch9, adc2

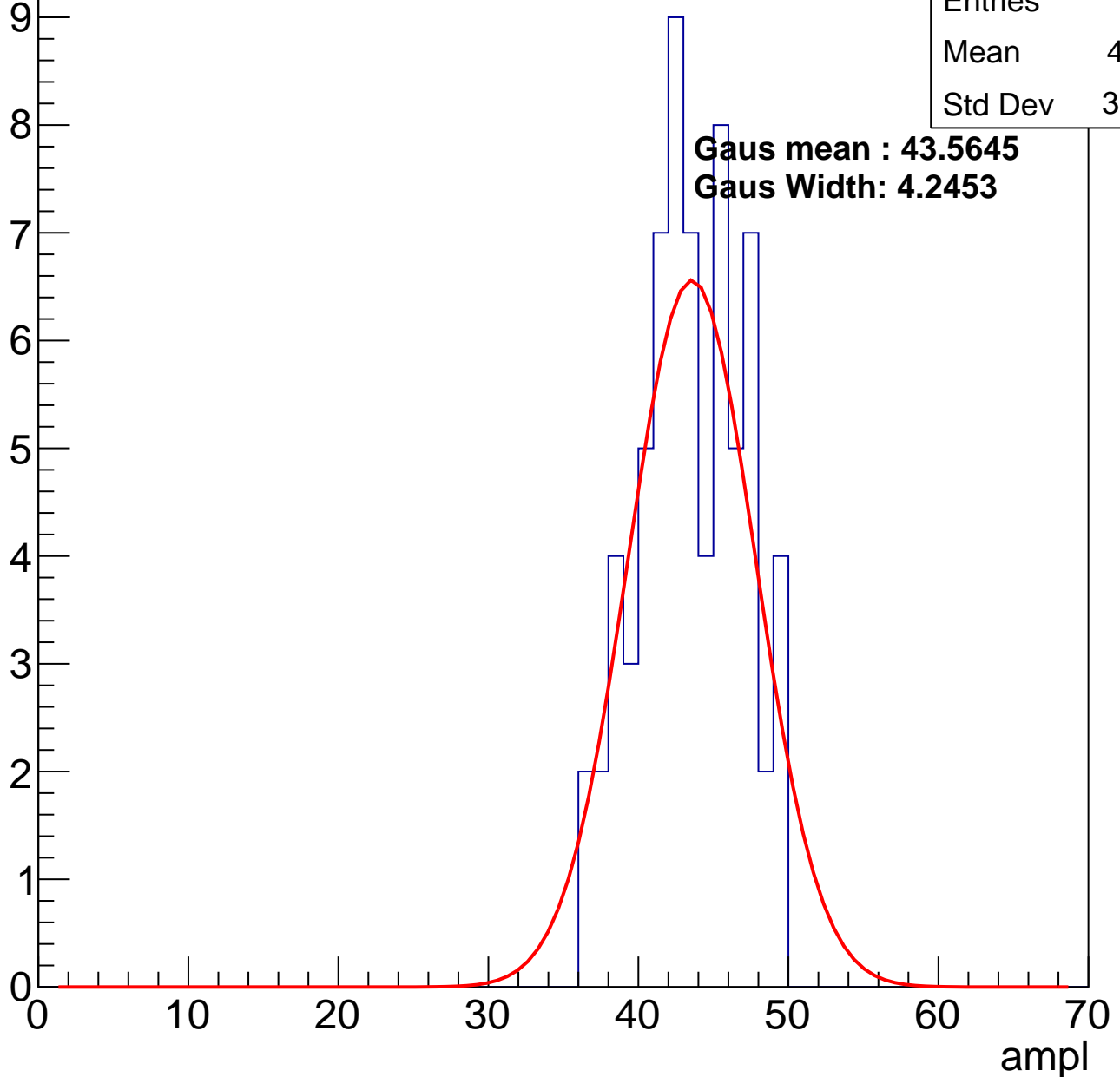
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.01 |
| Std Dev | 3.377 |

Gaus mean : 43.5645

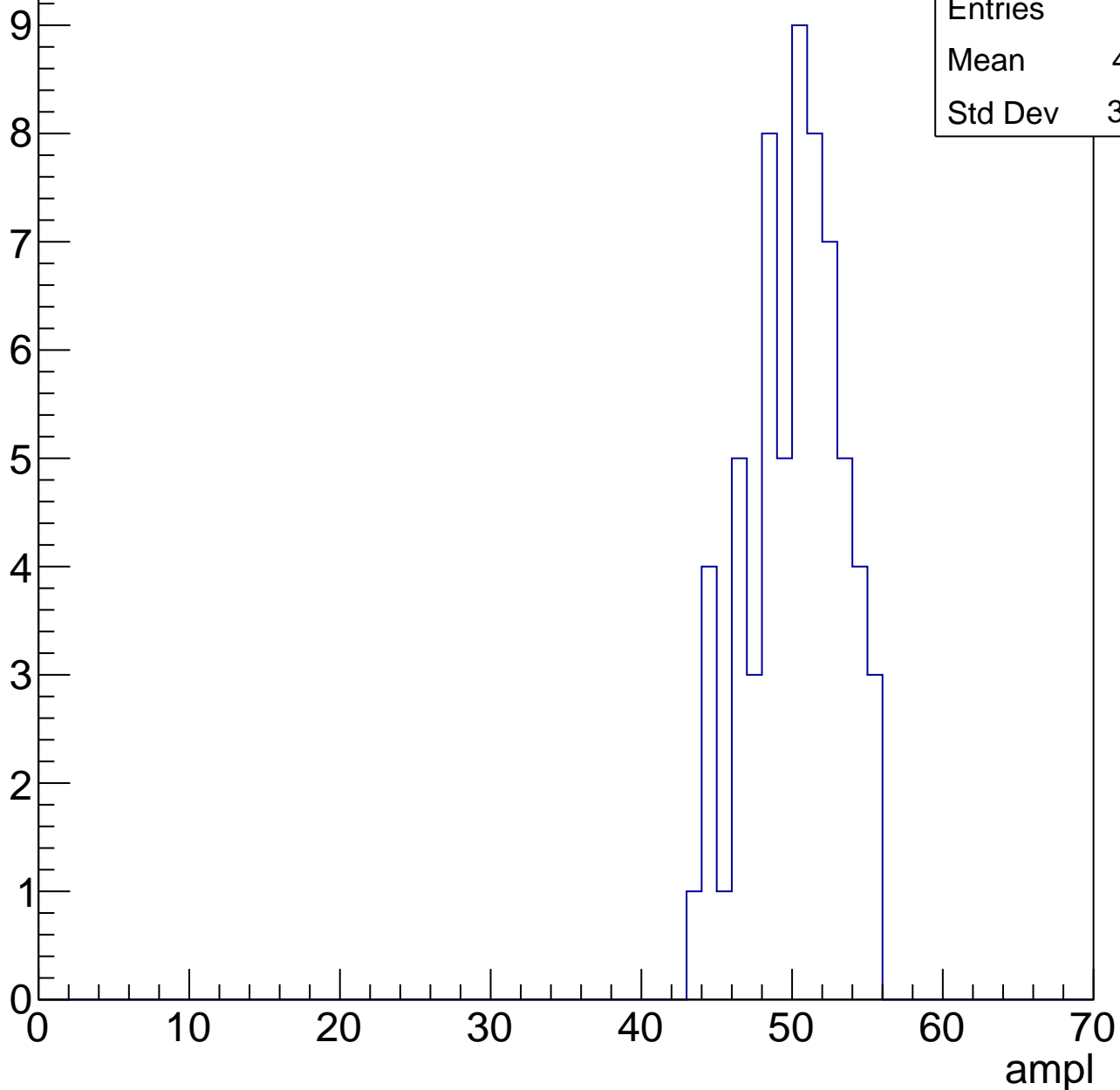
Gaus Width: 4.2453



B0L001S, U7-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

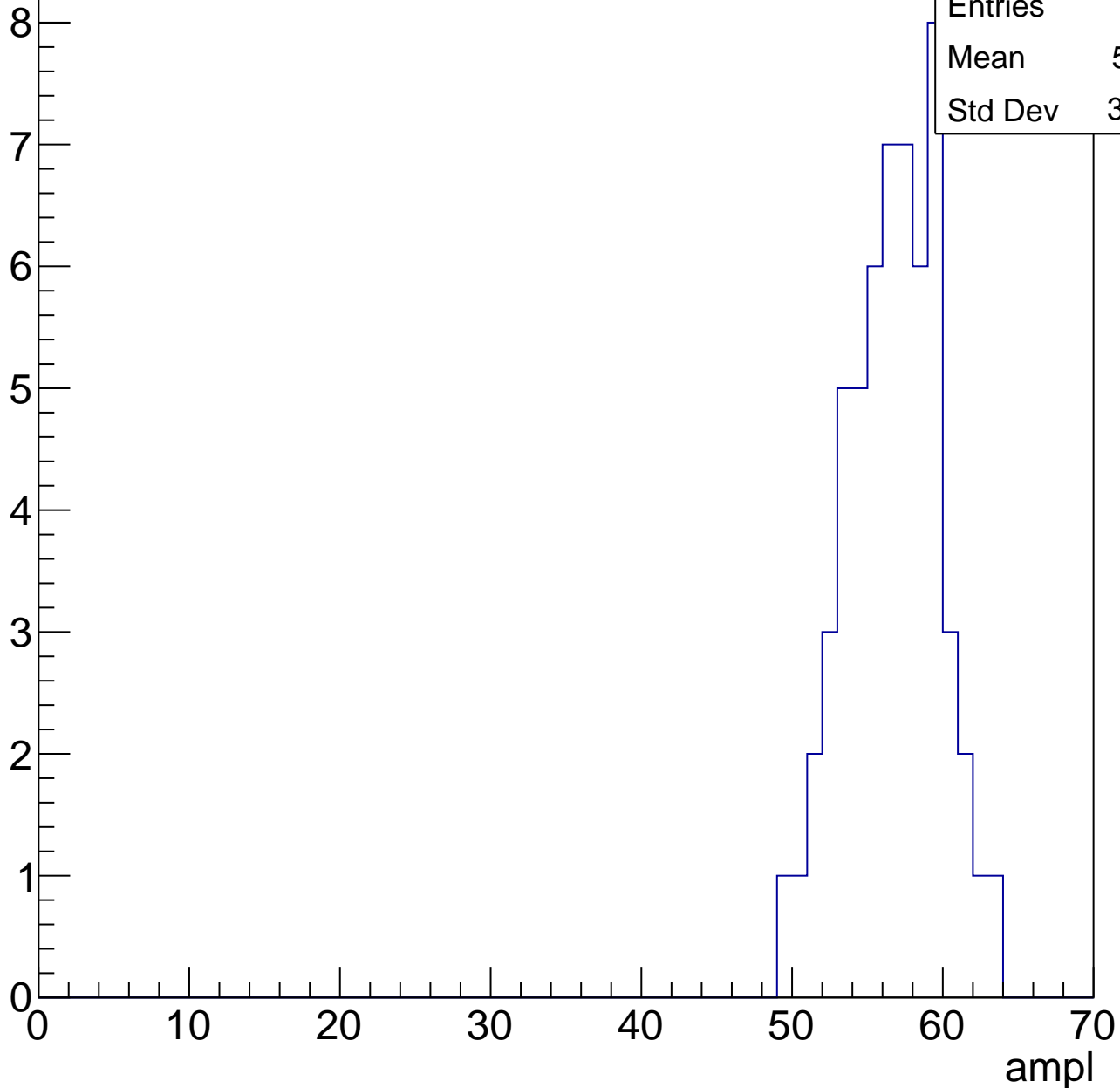


B0L001S, U7-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

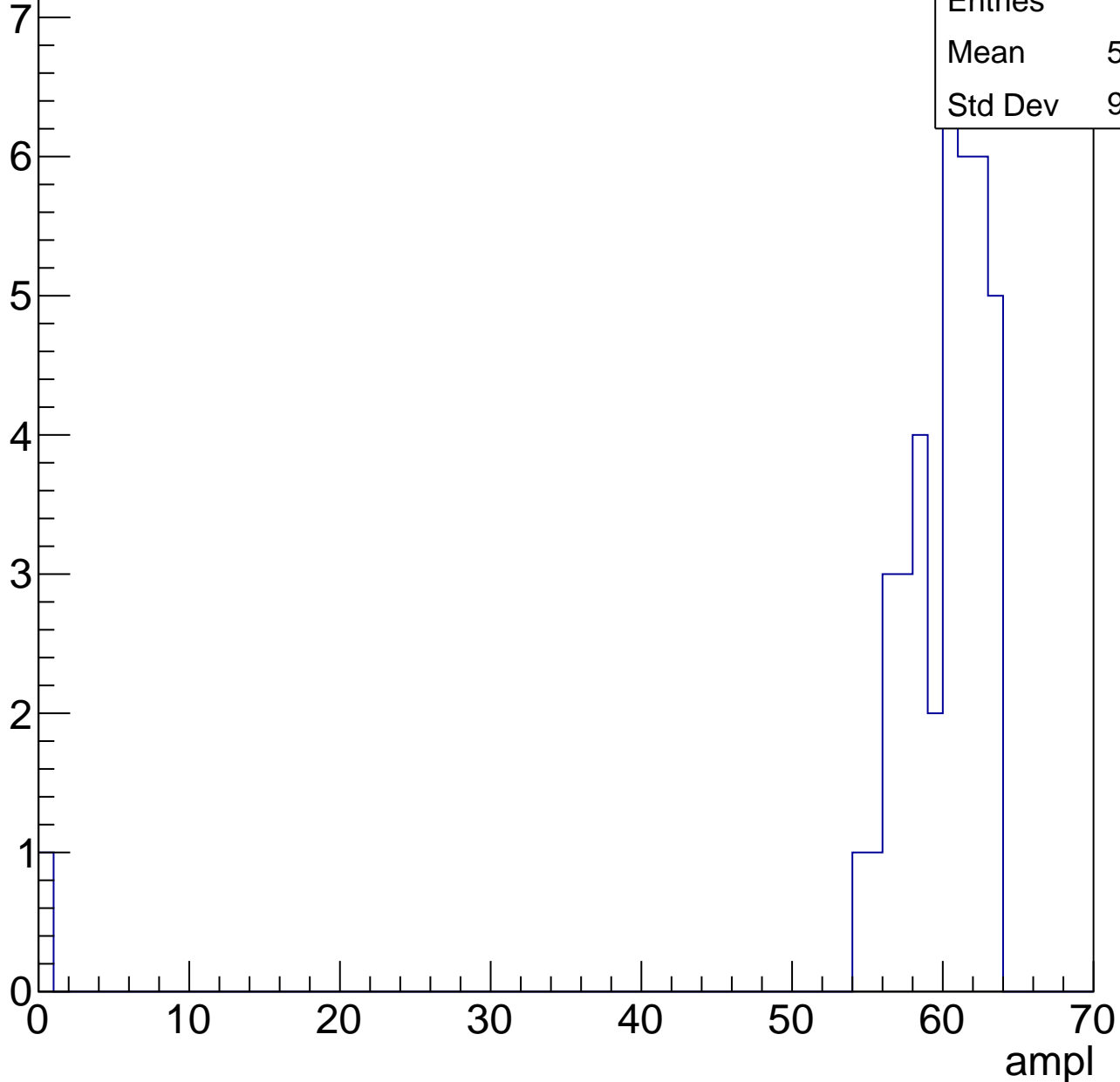
| | |
|---------|-------|
| Entries | 58 |
| Mean | 56.21 |
| Std Dev | 3.044 |



B0L001S, U7-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch10, adc0

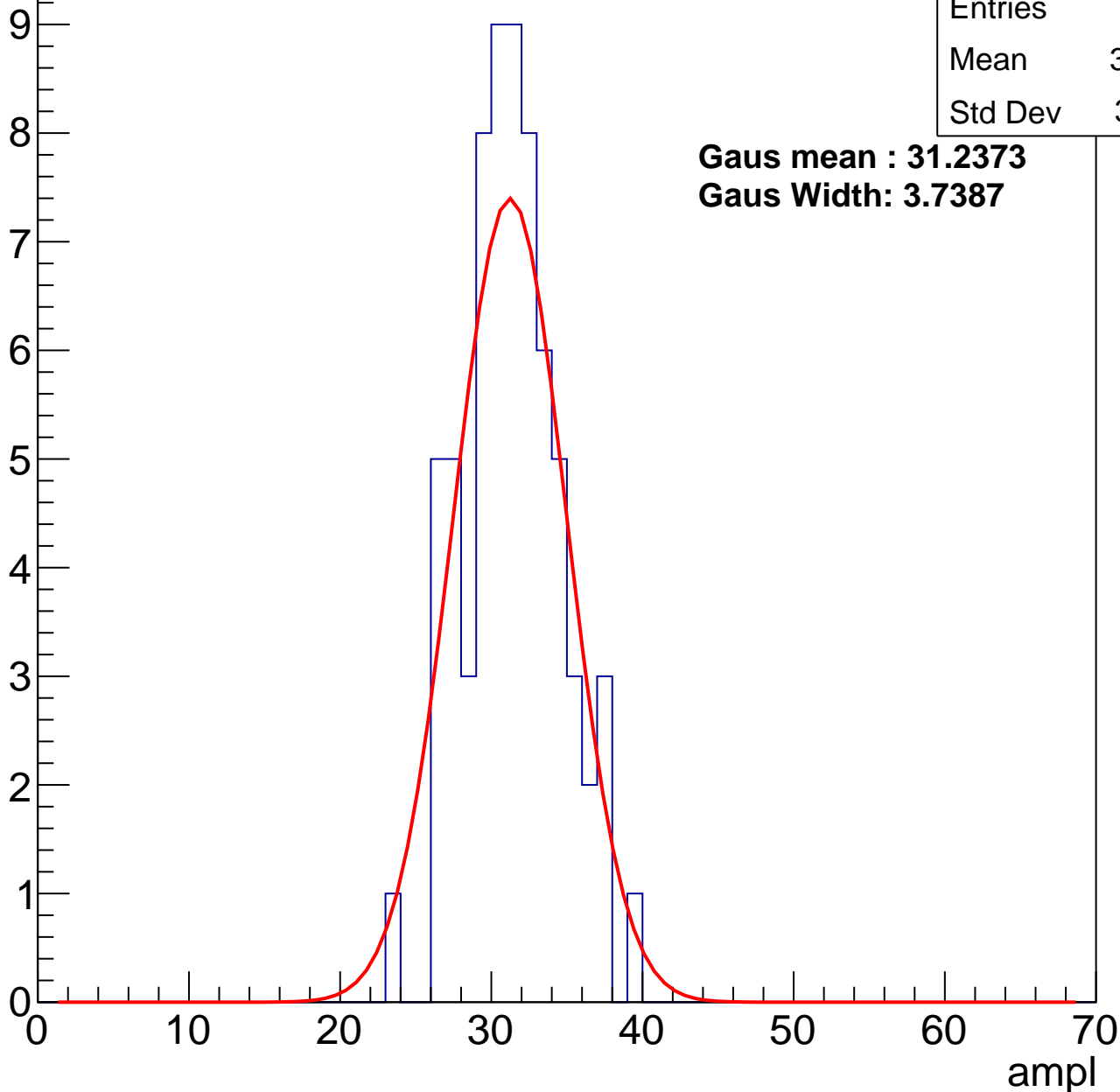
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 30.94 |
| Std Dev | 3.171 |

Gaus mean : 31.2373

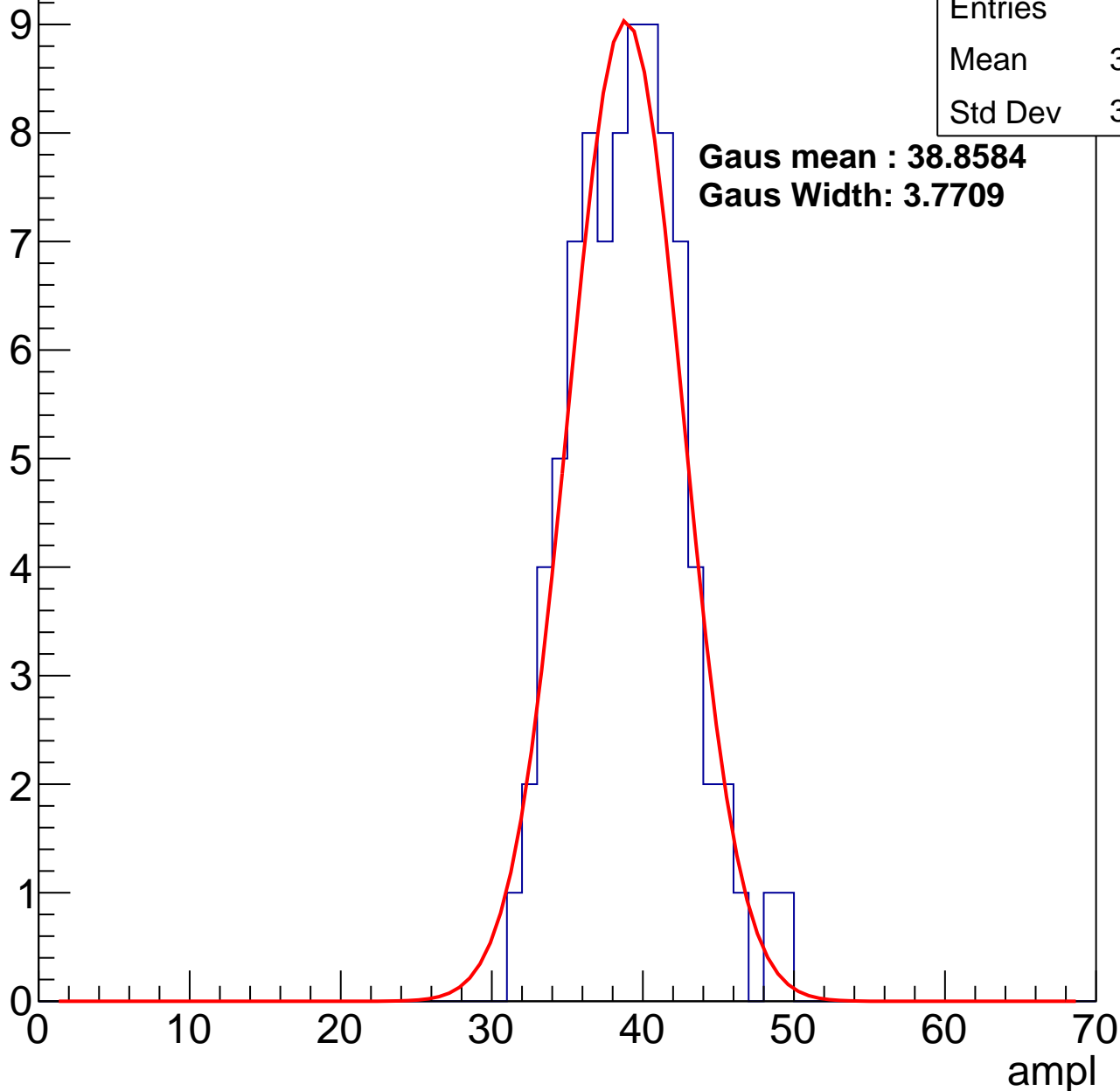
Gaus Width: 3.7387



B0L001S, U7-ch10, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch10, adc2

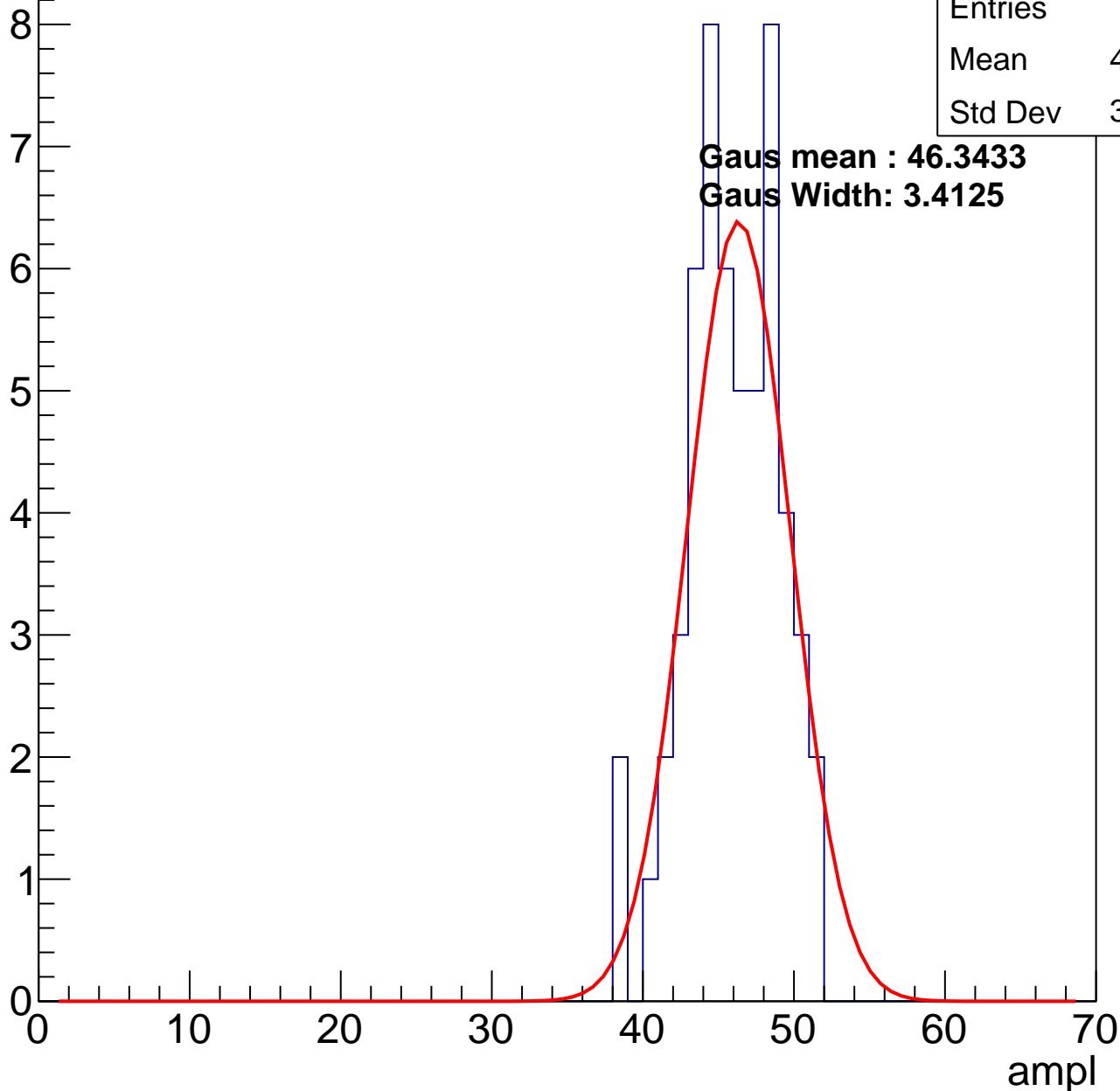
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 45.47 |
| Std Dev | 3.044 |

Gaus mean : 46.3433

Gaus Width: 3.4125

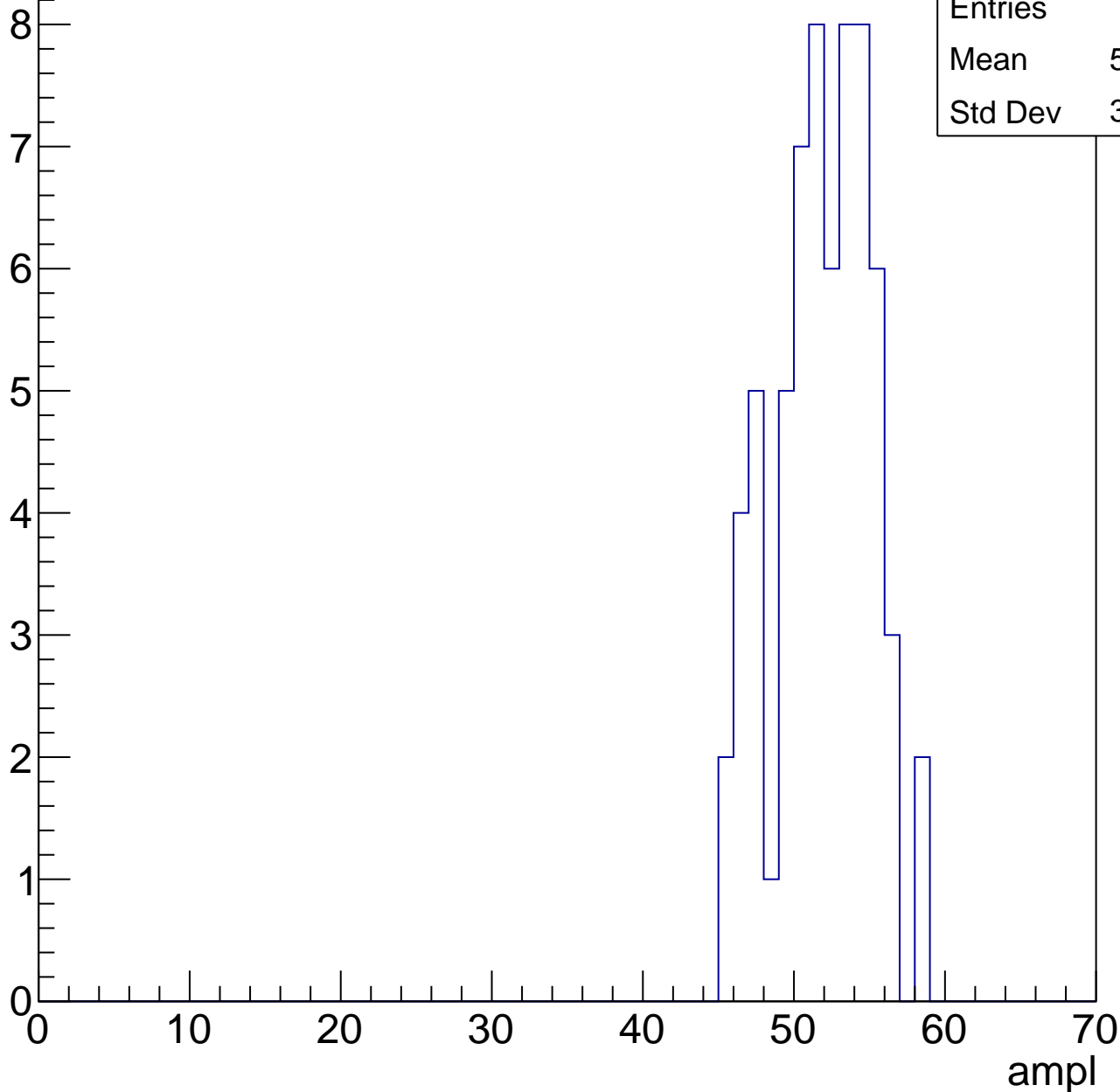


B0L001S, U7-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.42 |
| Std Dev | 3.176 |

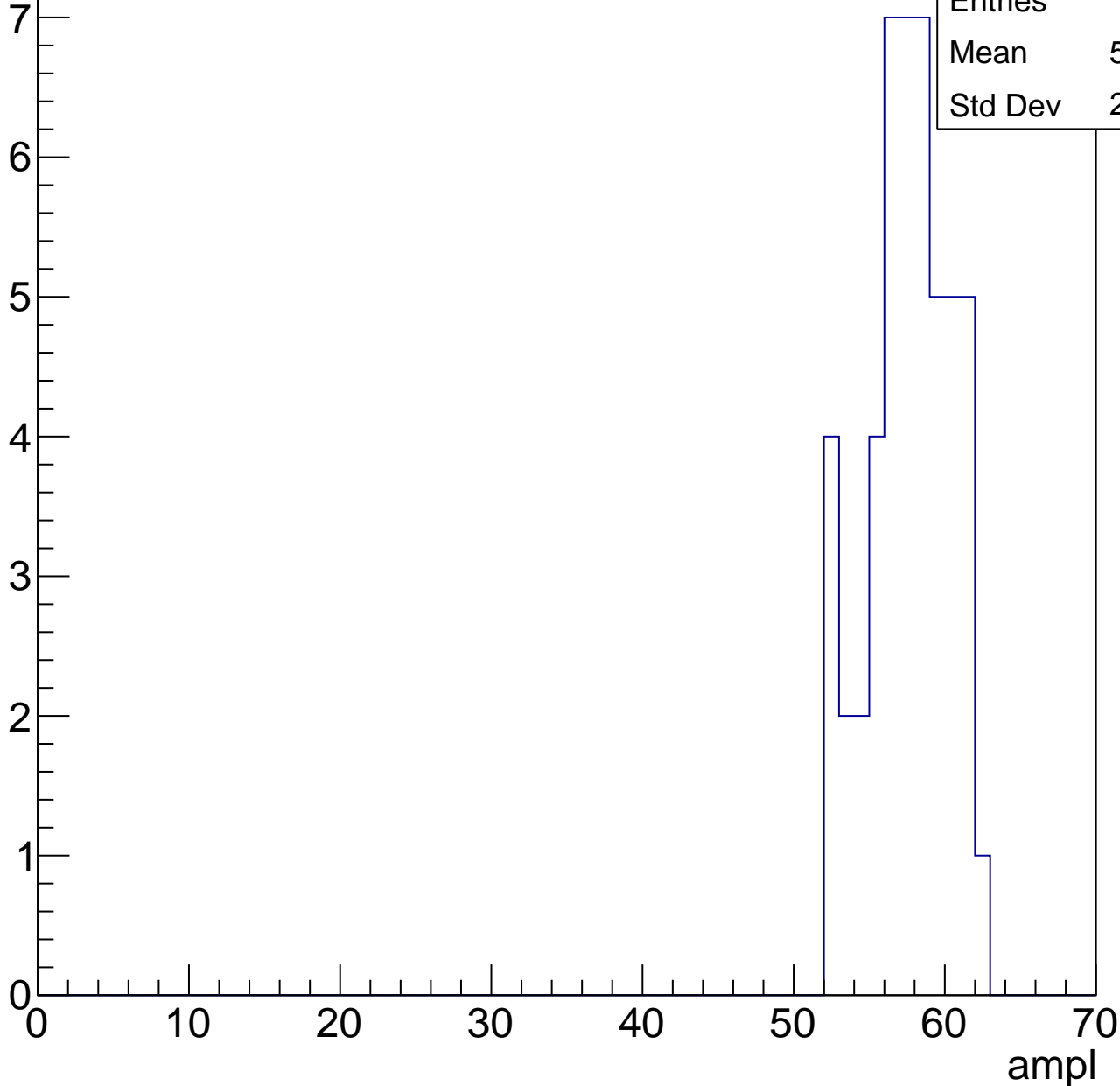


B0L001S, U7-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.16 |
| Std Dev | 2.668 |

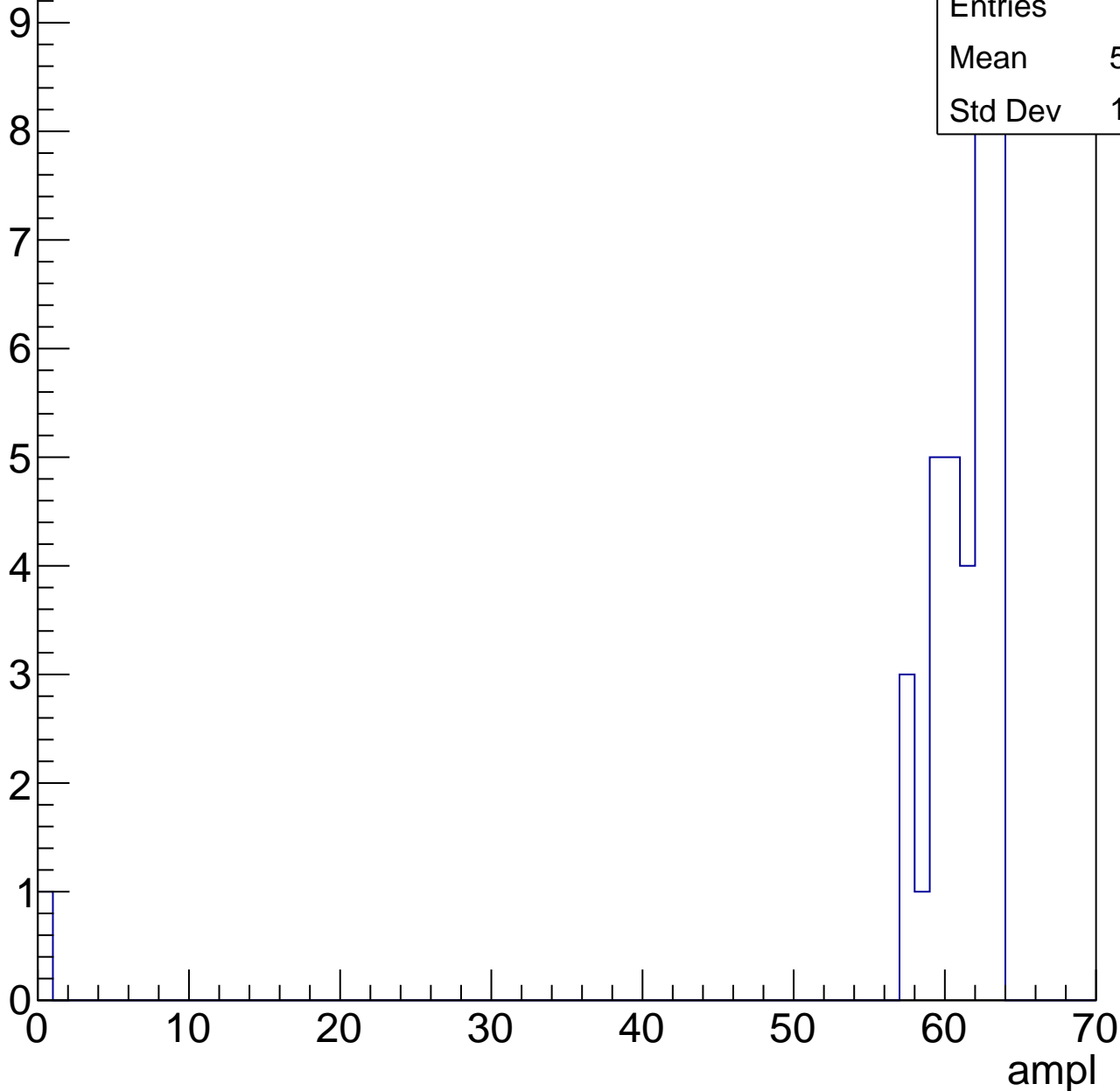


B0L001S, U7-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

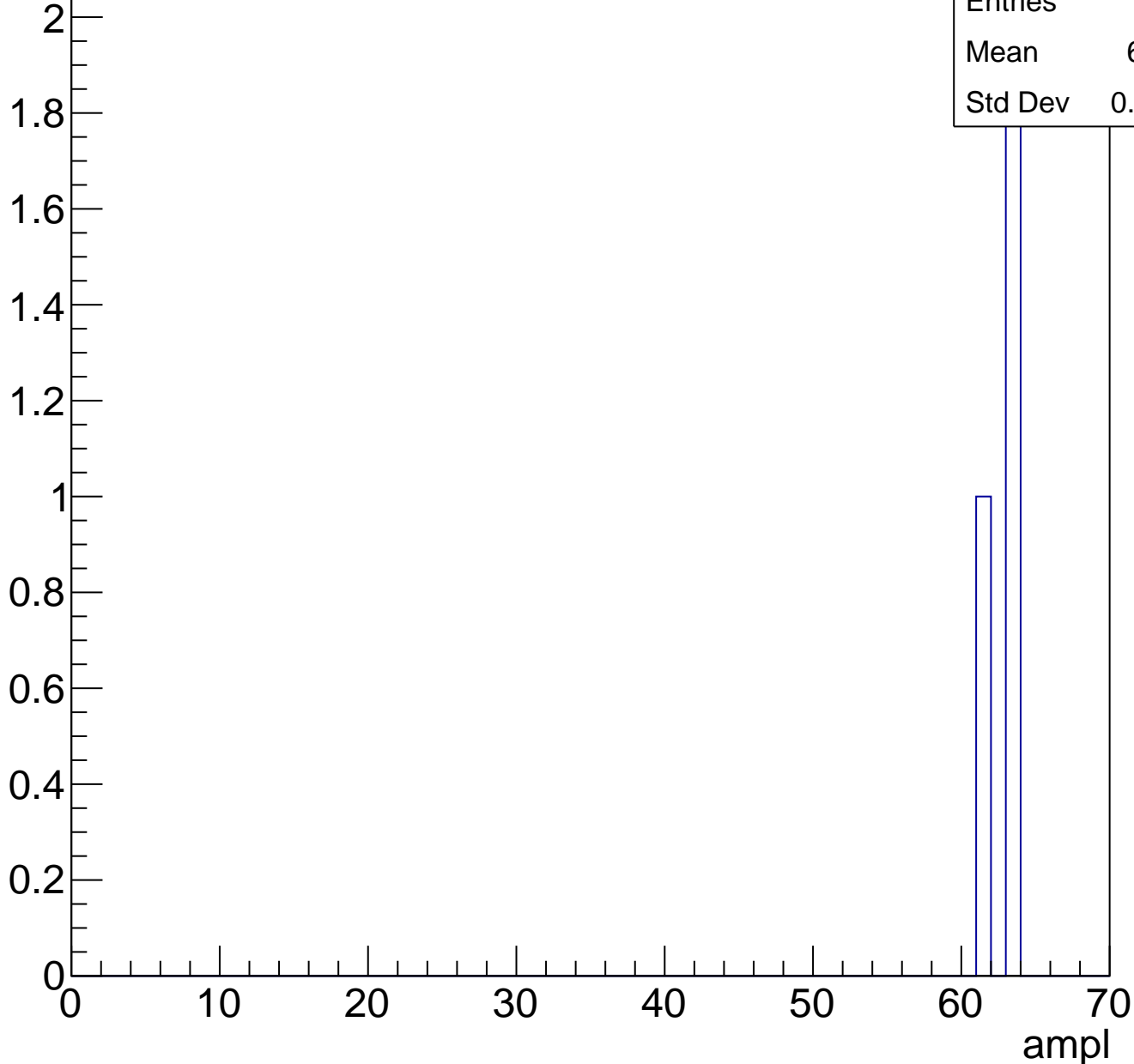
| | |
|---------|-------|
| Entries | 36 |
| Mean | 59.17 |
| Std Dev | 10.17 |



B0L001S, U7-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch11, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 29.94 |
| Std Dev | 4.768 |

Gaus mean : 31.2302

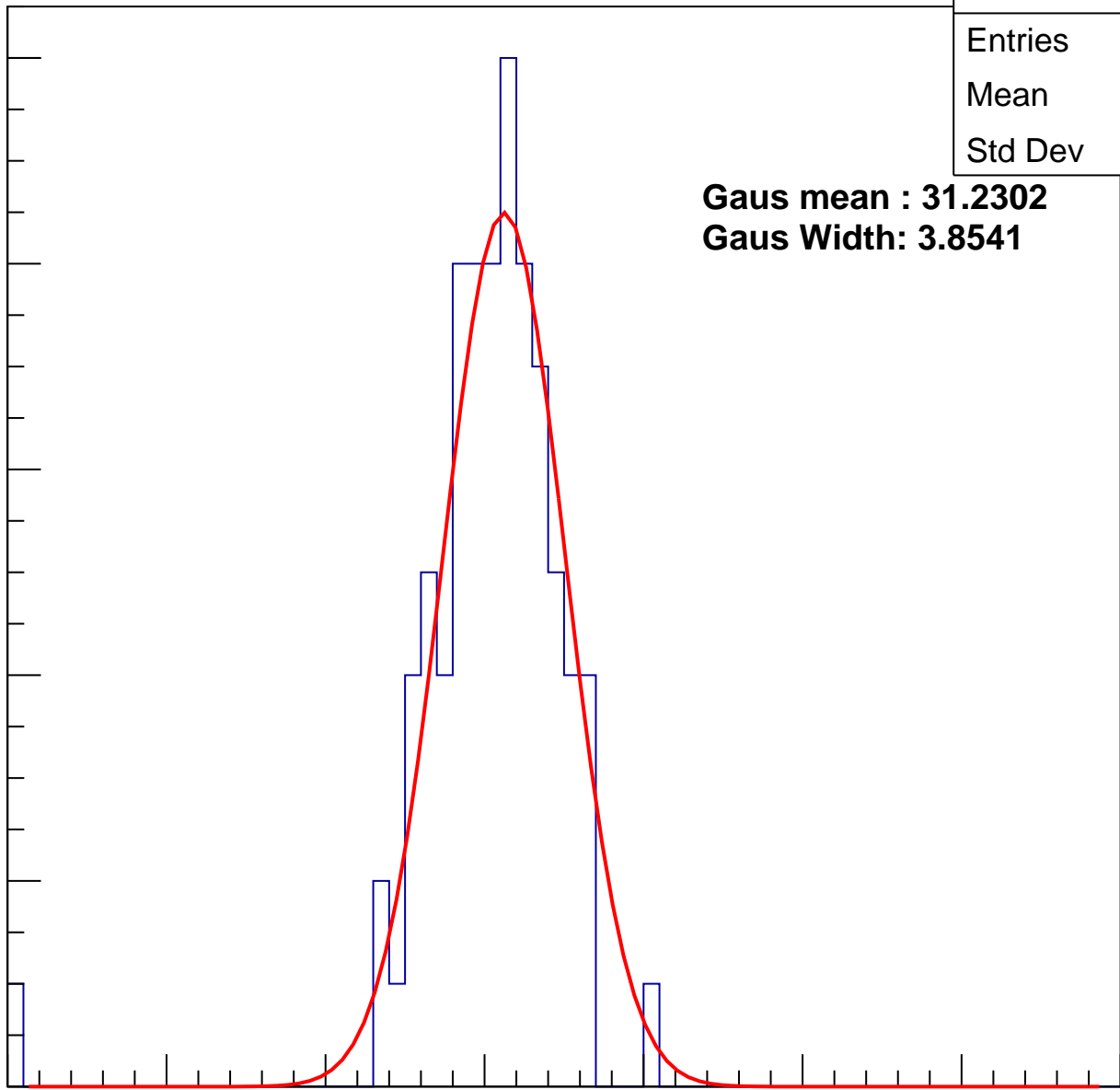
Gaus Width: 3.8541

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch11, adc1

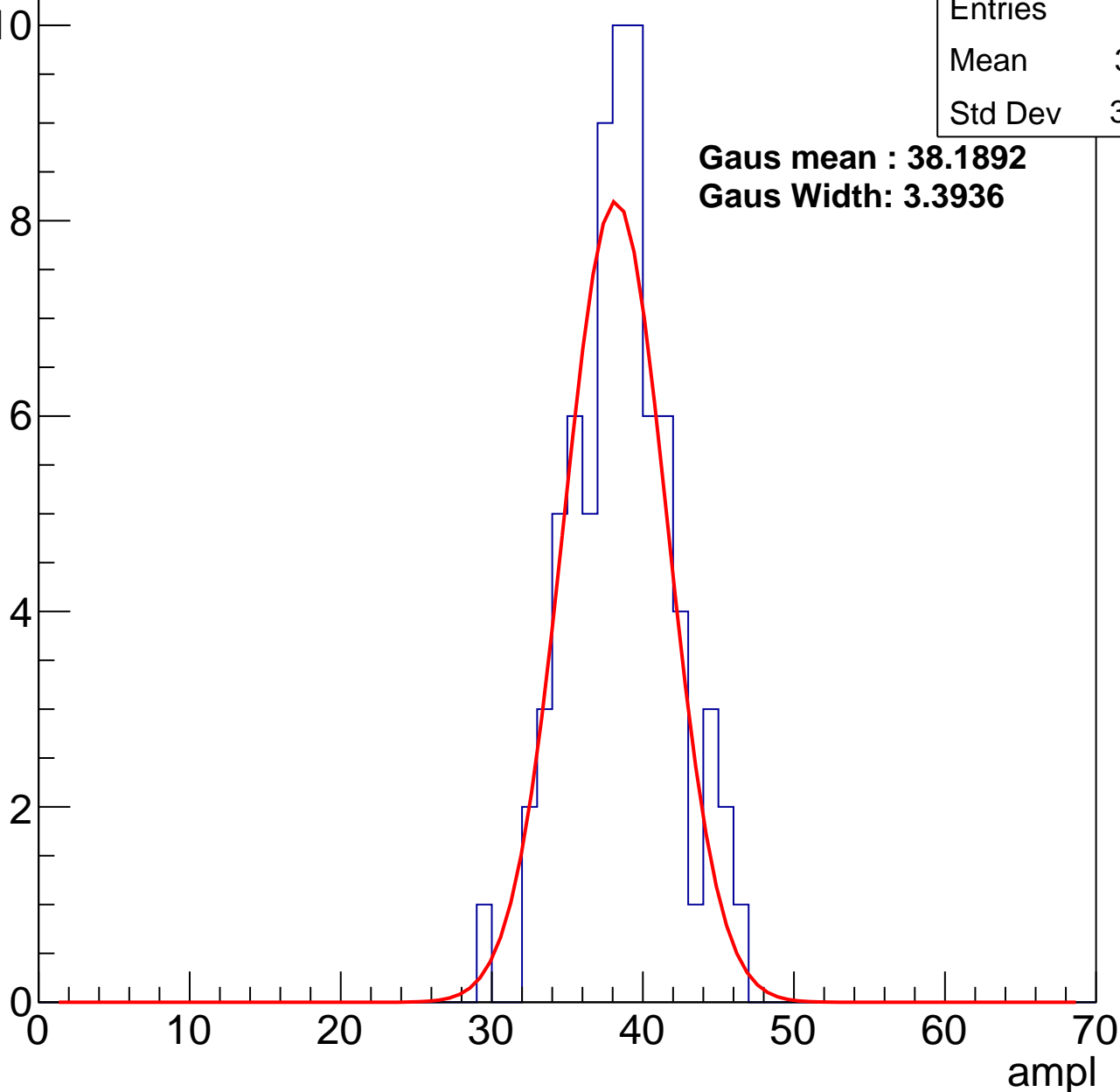
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 38.11 |
| Std Dev | 3.363 |

Gaus mean : 38.1892

Gaus Width: 3.3936



B0L001S, U7-ch11, adc2

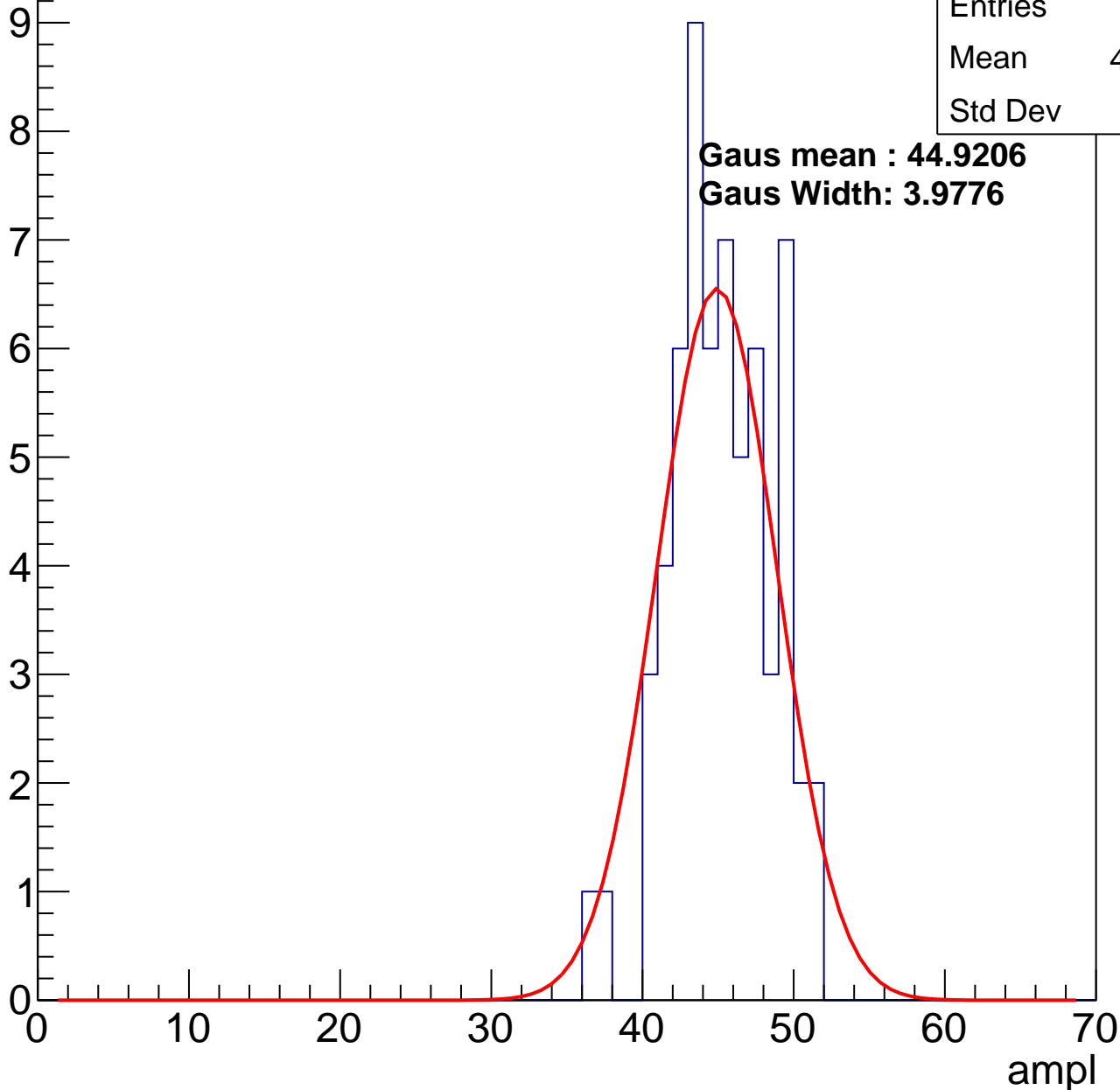
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 44.77 |
| Std Dev | 3.28 |

Gaus mean : 44.9206

Gaus Width: 3.9776

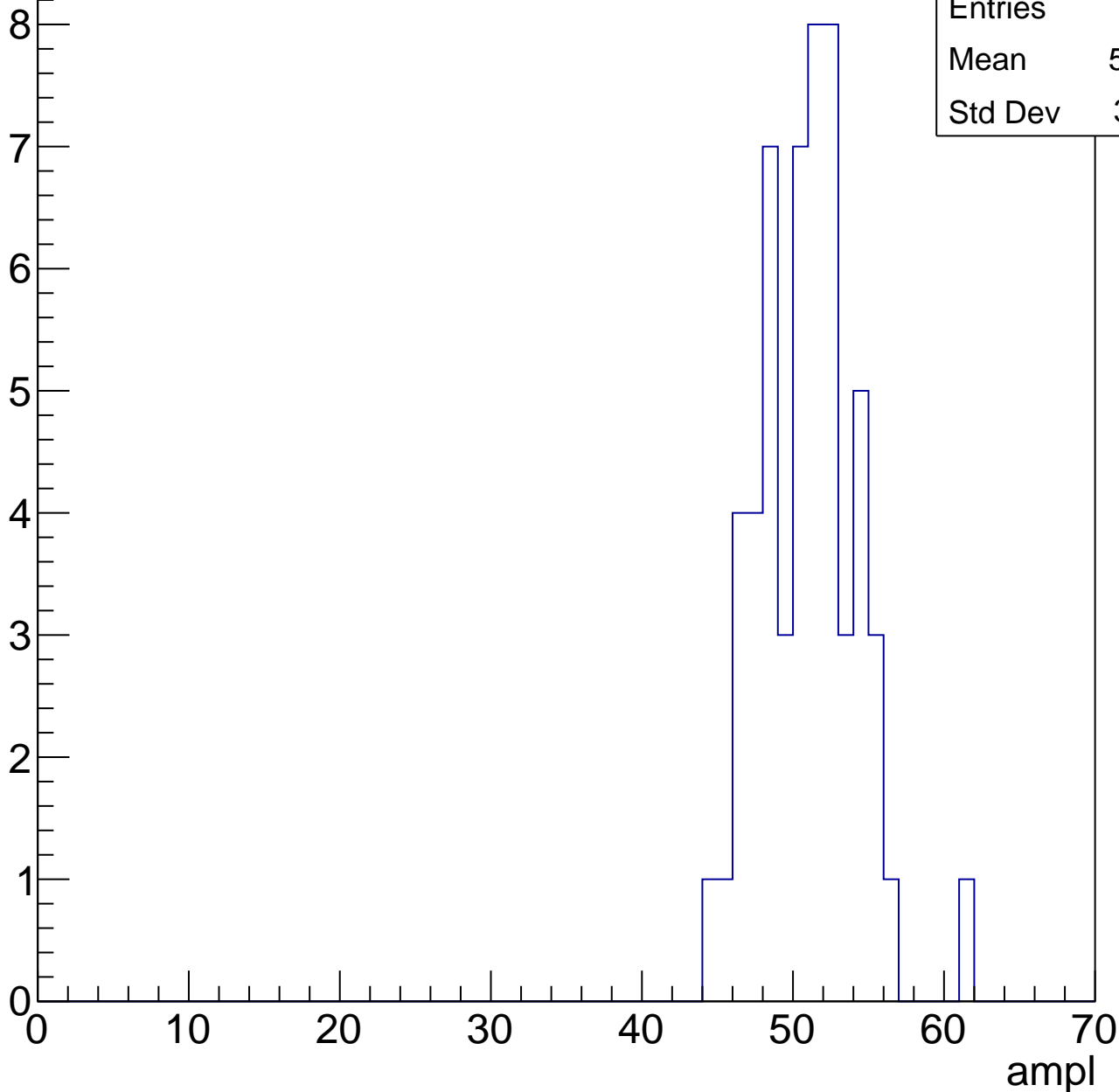


B0L001S, U7-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 50.52 |
| Std Dev | 3.151 |

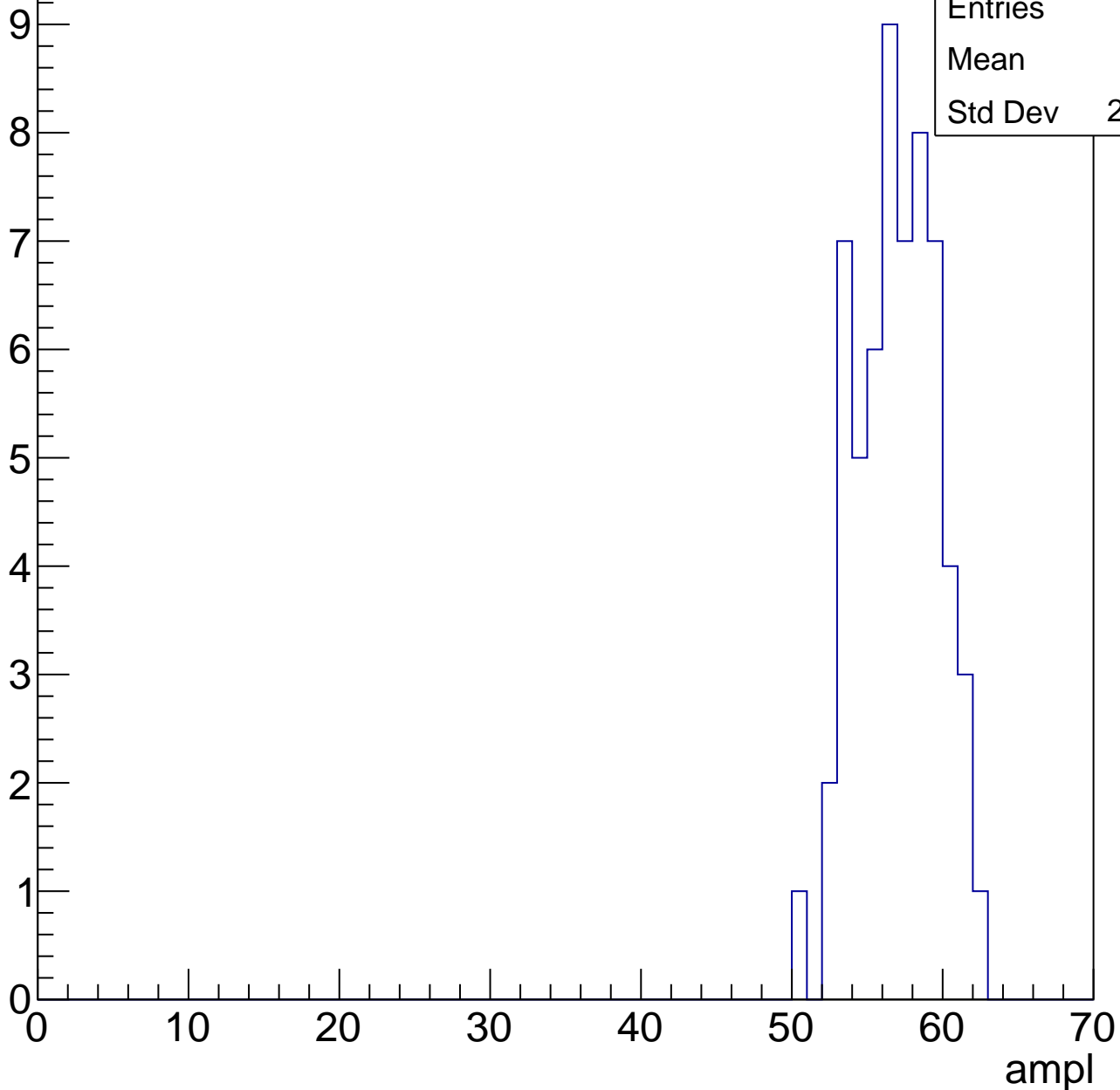


B0L001S, U7-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 56.5 |
| Std Dev | 2.643 |

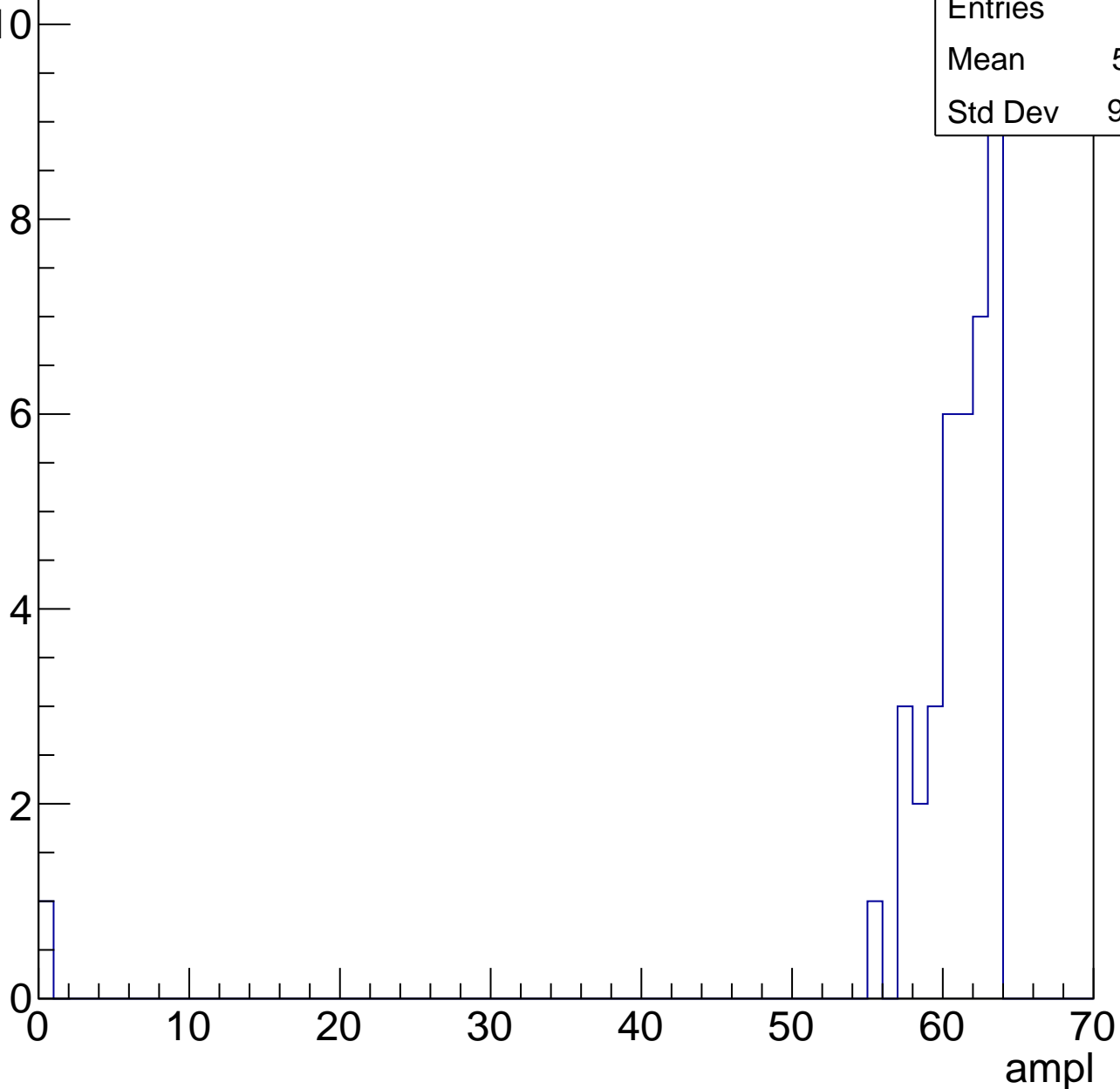


B0L001S, U7-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 59.21 |
| Std Dev | 9.822 |



B0L001S, U7-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 2 |
| Mean | 62 |
| Std Dev | 0 |

ampl

B0L001S, U7-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch12, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 79 |
| Mean | 31.11 |
| Std Dev | 3.338 |

Gaus mean : 31.7390

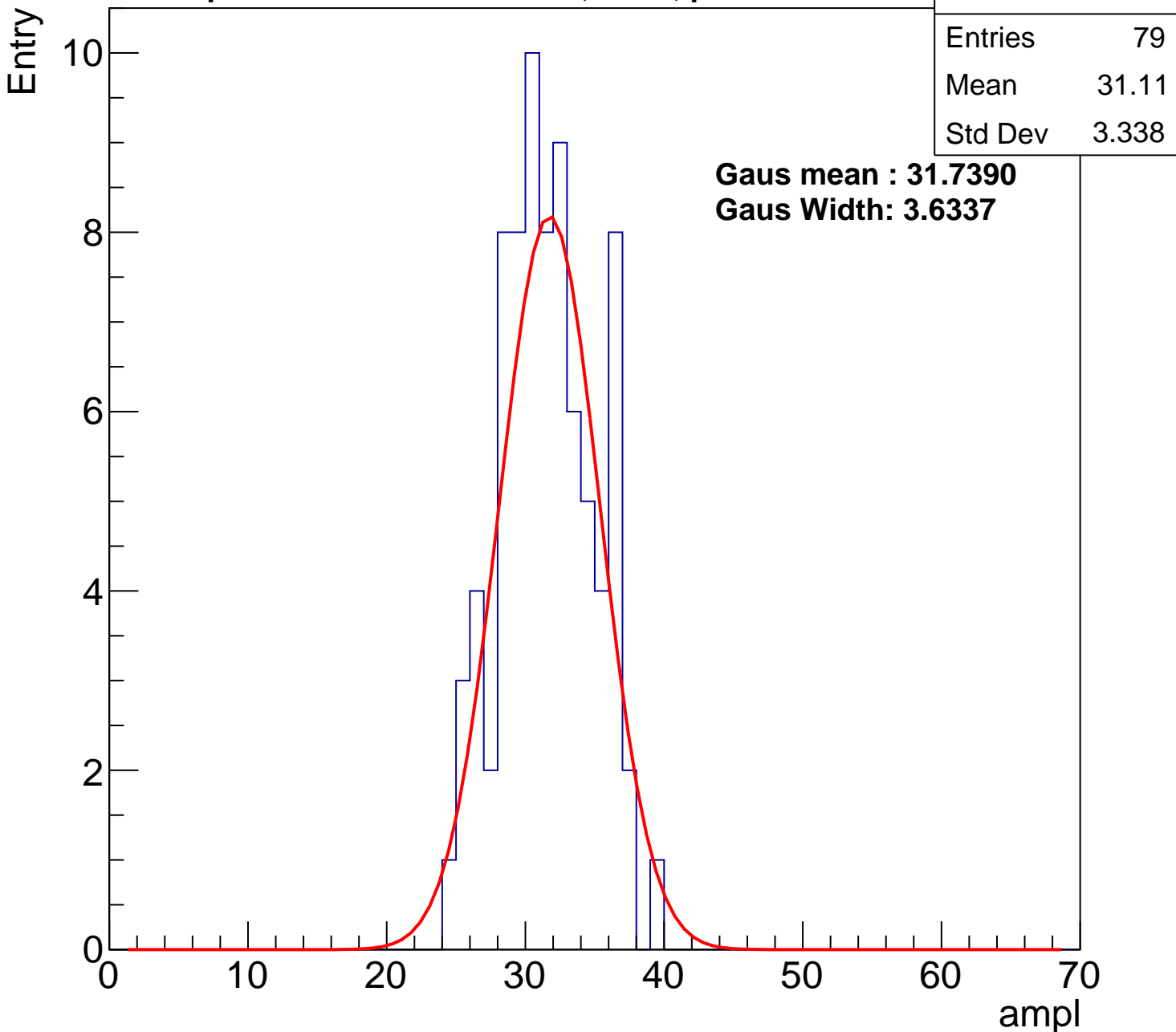
Gaus Width: 3.6337

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch12, adc1

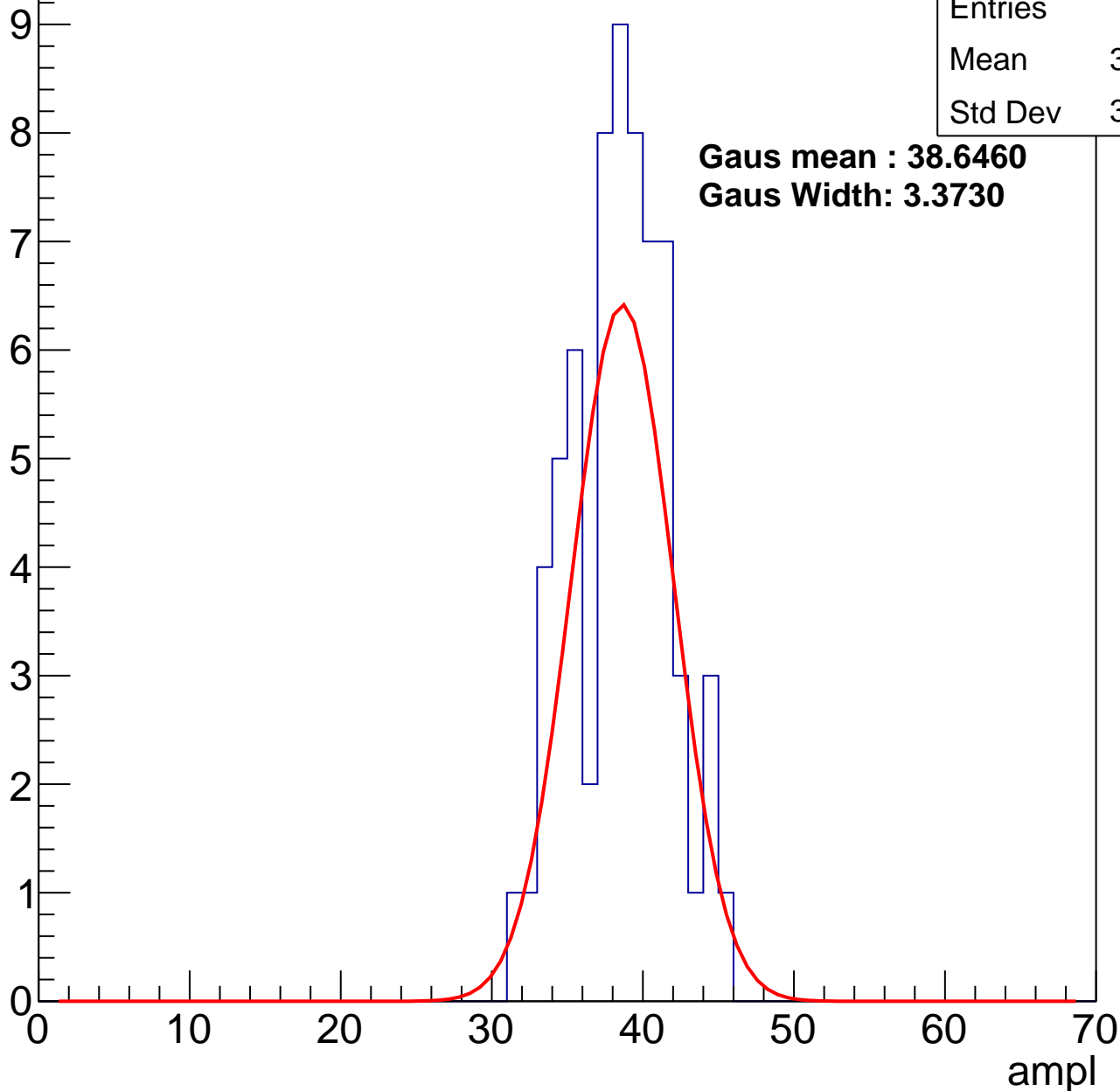
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 38.03 |
| Std Dev | 3.172 |

Gaus mean : 38.6460

Gaus Width: 3.3730



B0L001S, U7-ch12, adc2

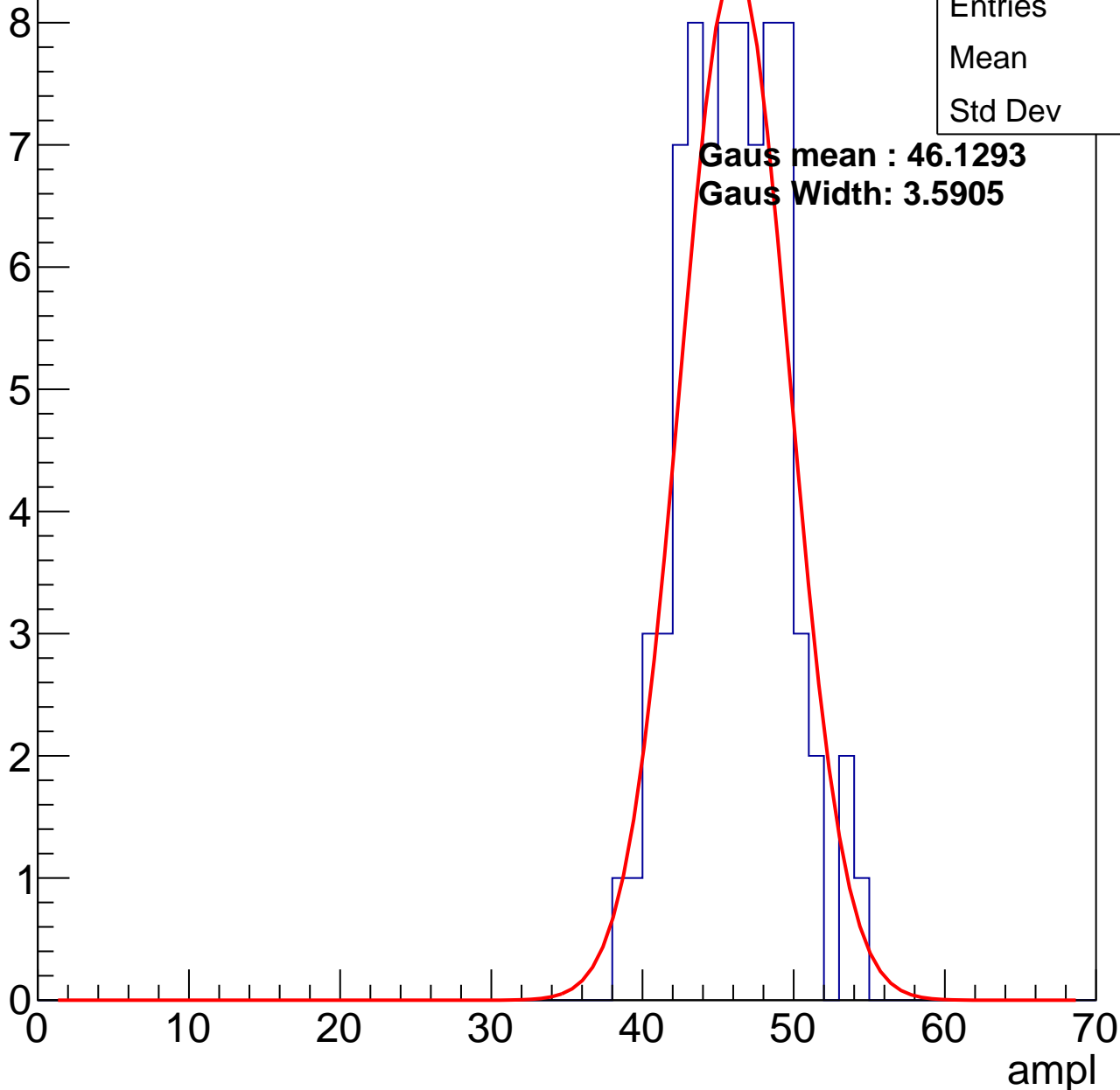
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 77 |
| Mean | 45.6 |
| Std Dev | 3.37 |

Gaus mean : 46.1293

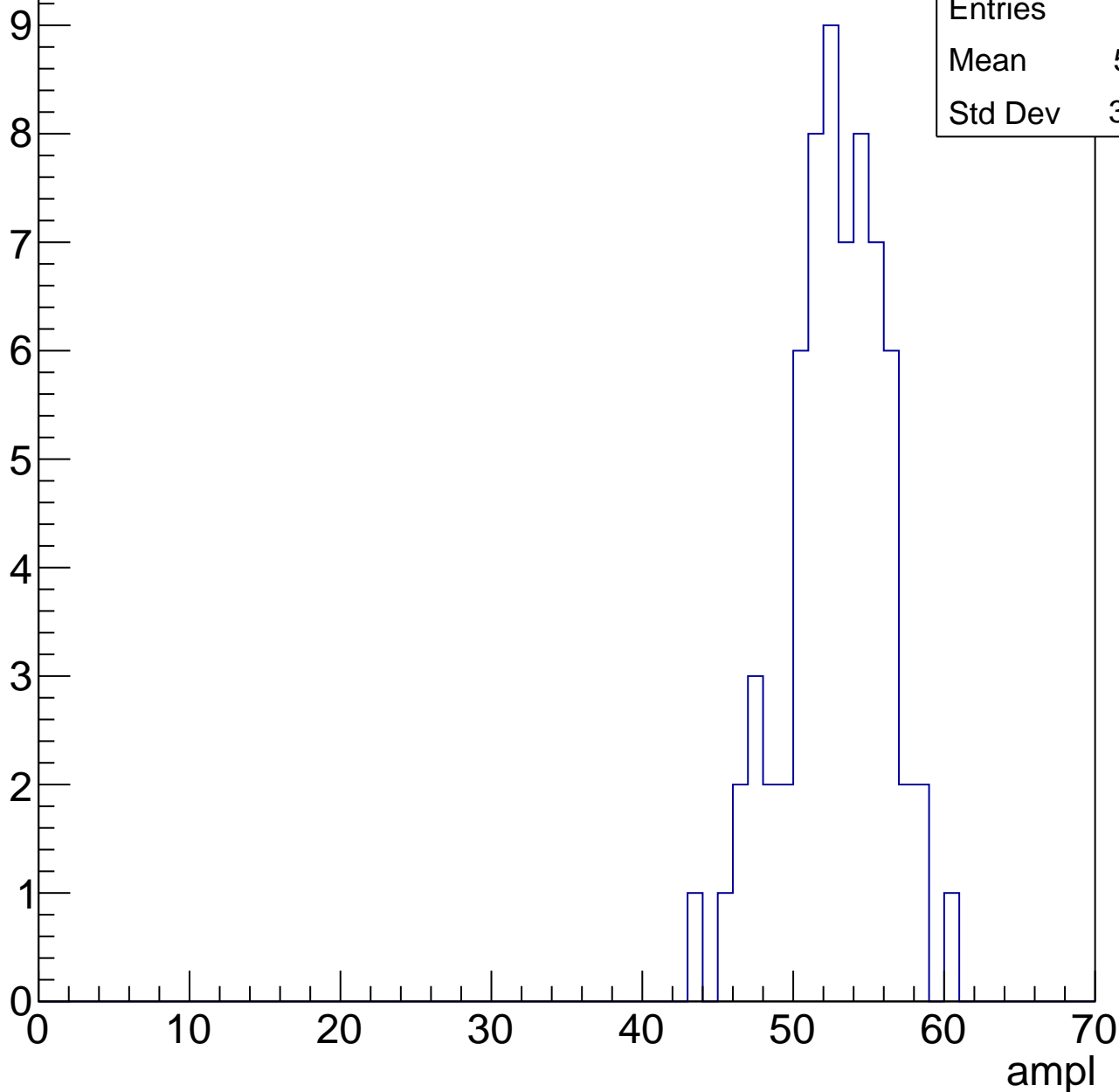
Gaus Width: 3.5905



B0L001S, U7-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



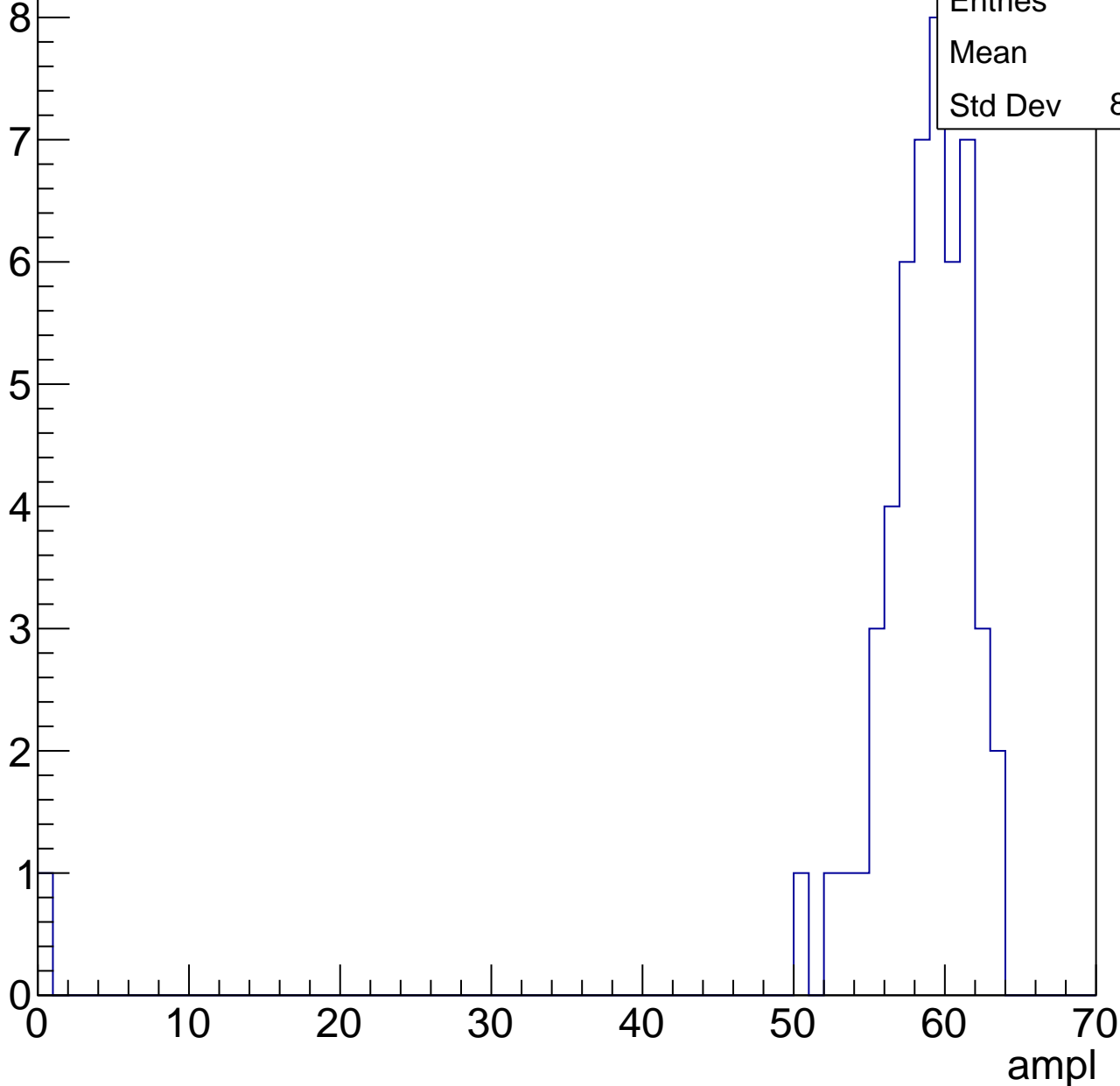
| | |
|---------|-------|
| Entries | 67 |
| Mean | 52.31 |
| Std Dev | 3.315 |

B0L001S, U7-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.2 |
| Std Dev | 8.534 |

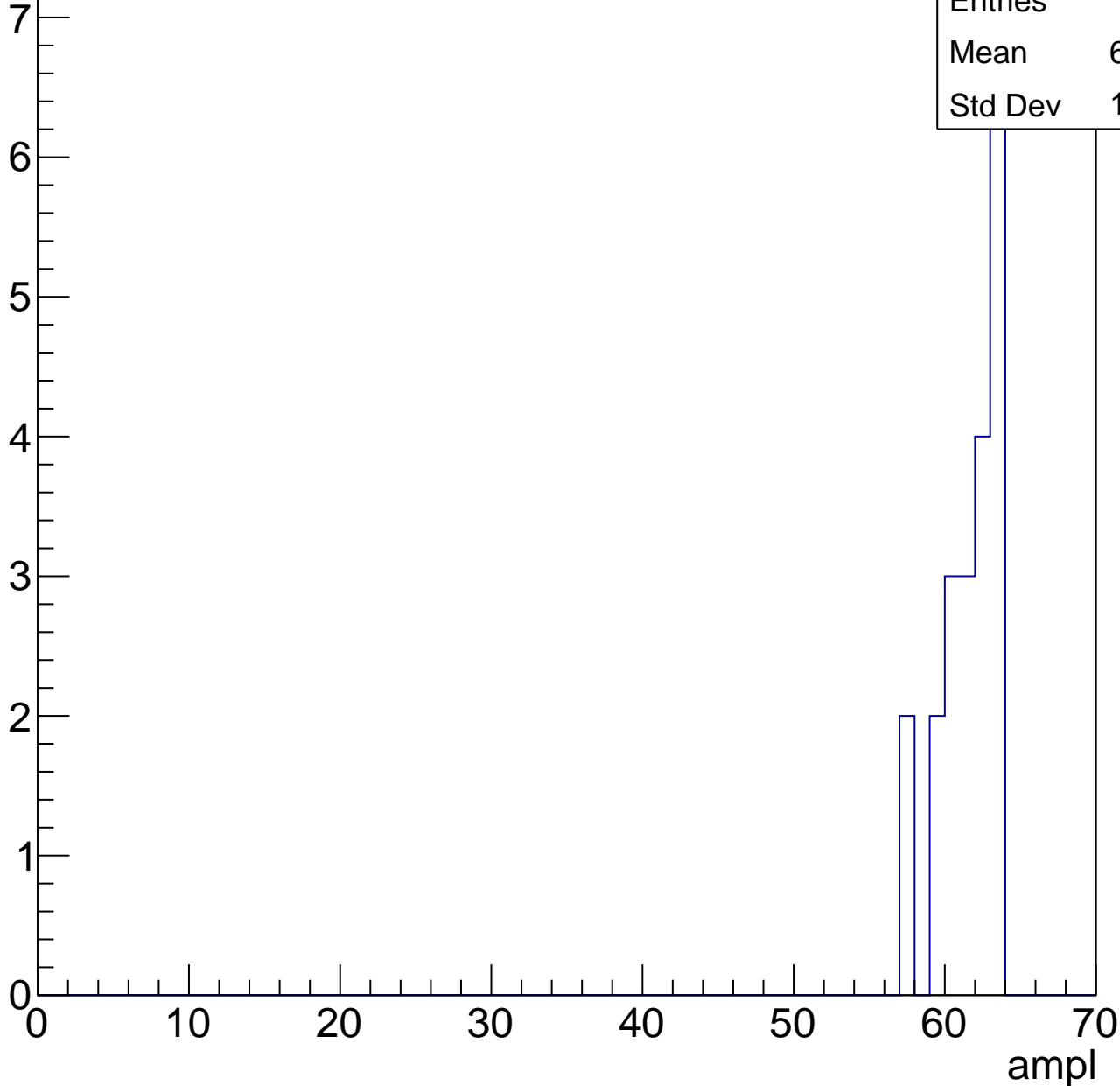


B0L001S, U7-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

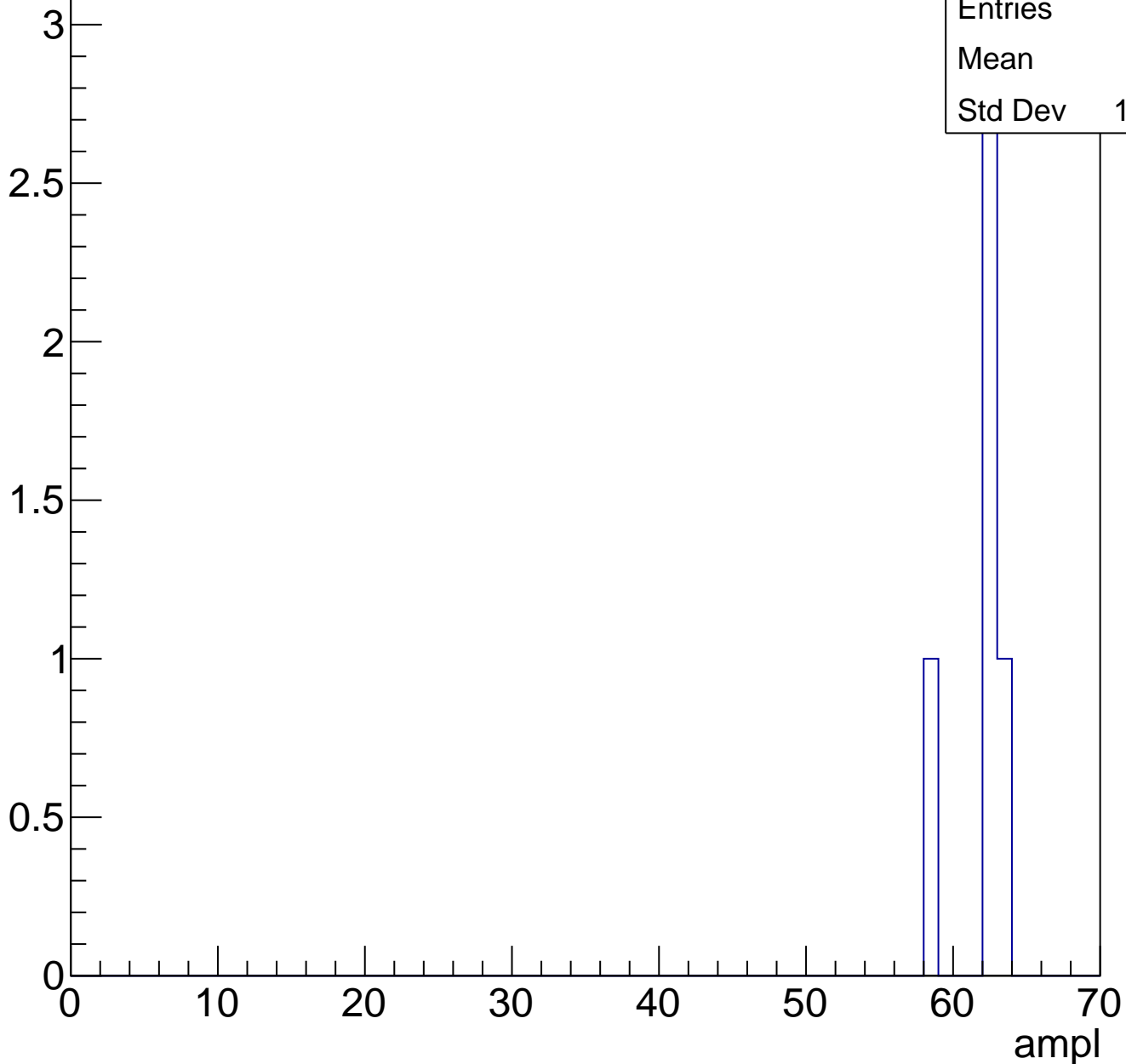
| | |
|---------|-------|
| Entries | 21 |
| Mean | 61.14 |
| Std Dev | 1.884 |



B0L001S, U7-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch13, adc0

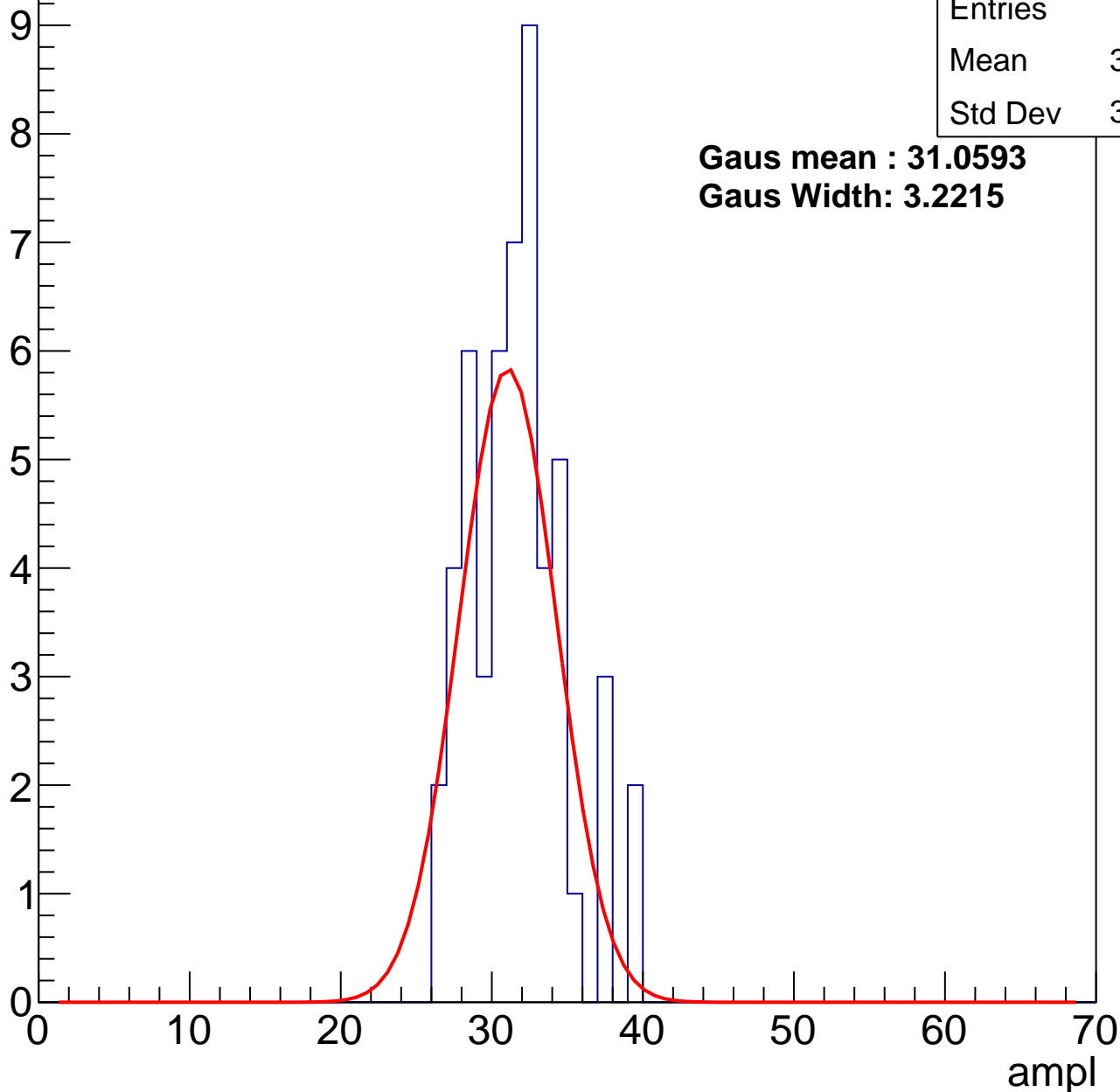
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 31.27 |
| Std Dev | 3.114 |

Gaus mean : 31.0593

Gaus Width: 3.2215



B0L001S, U7-ch13, adc1

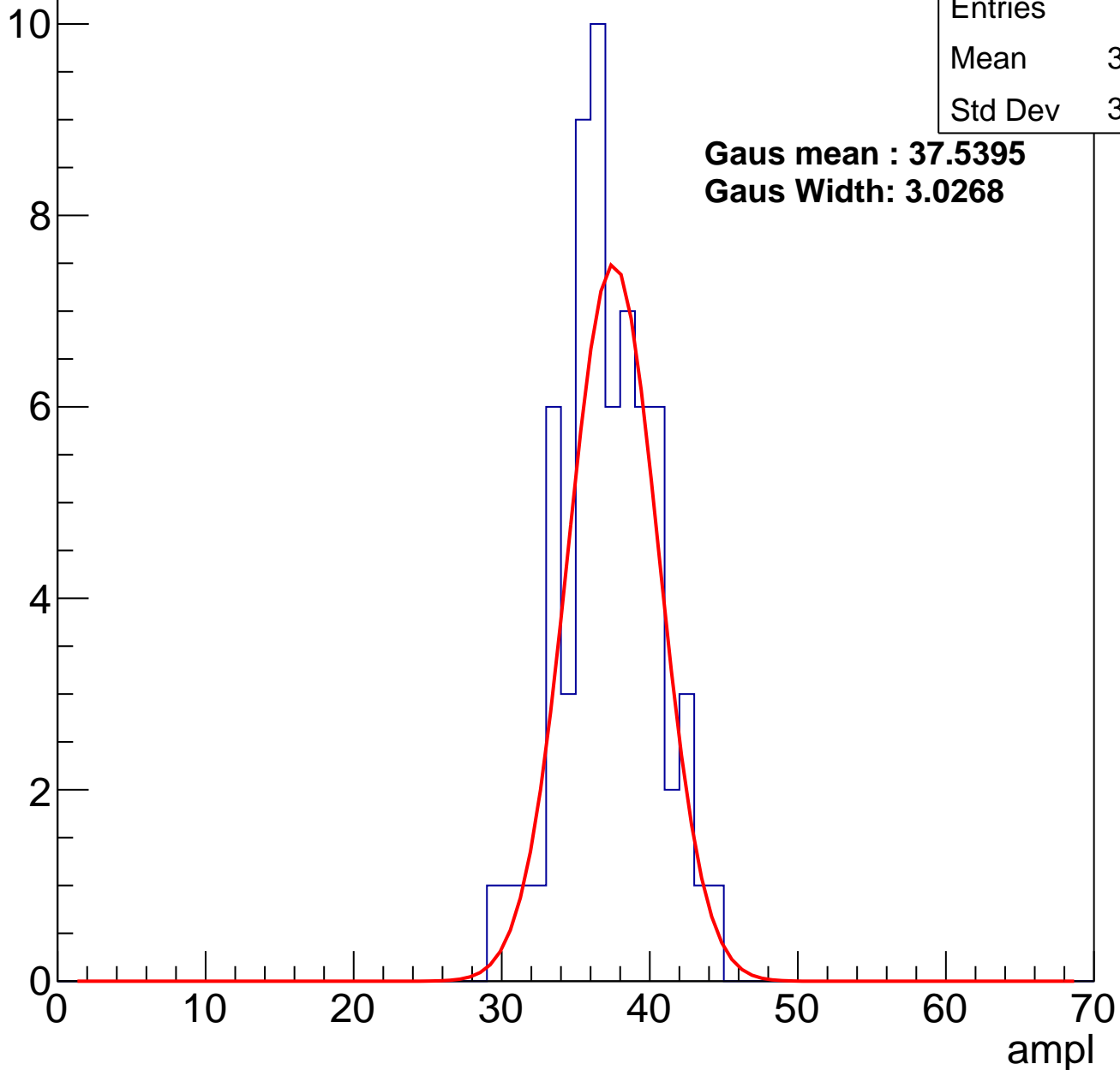
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.78 |
| Std Dev | 3.105 |

Gaus mean : 37.5395

Gaus Width: 3.0268

Entry



B0L001S, U7-ch13, adc2

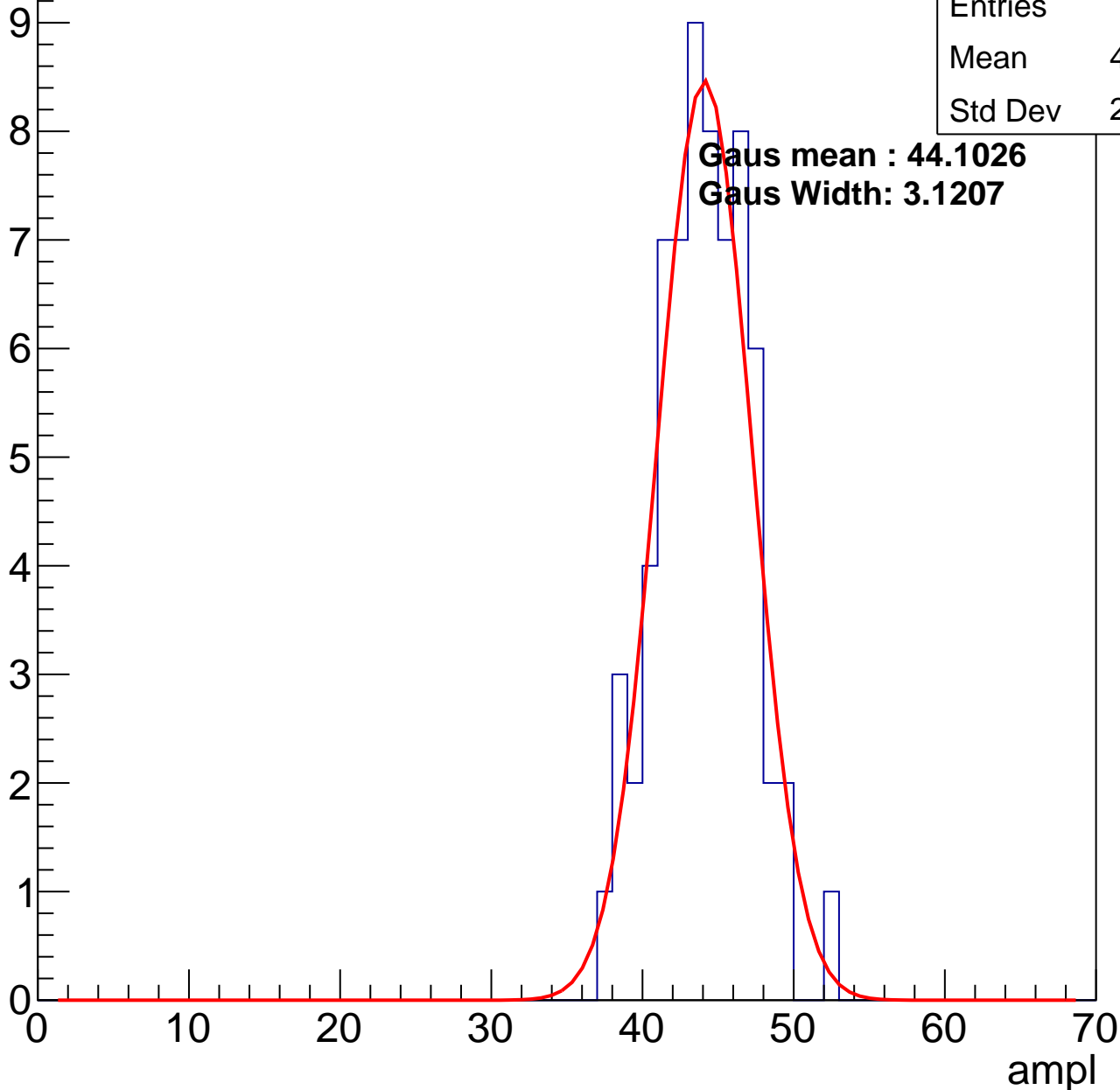
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.58 |
| Std Dev | 2.988 |

Gaus mean : 44.1026

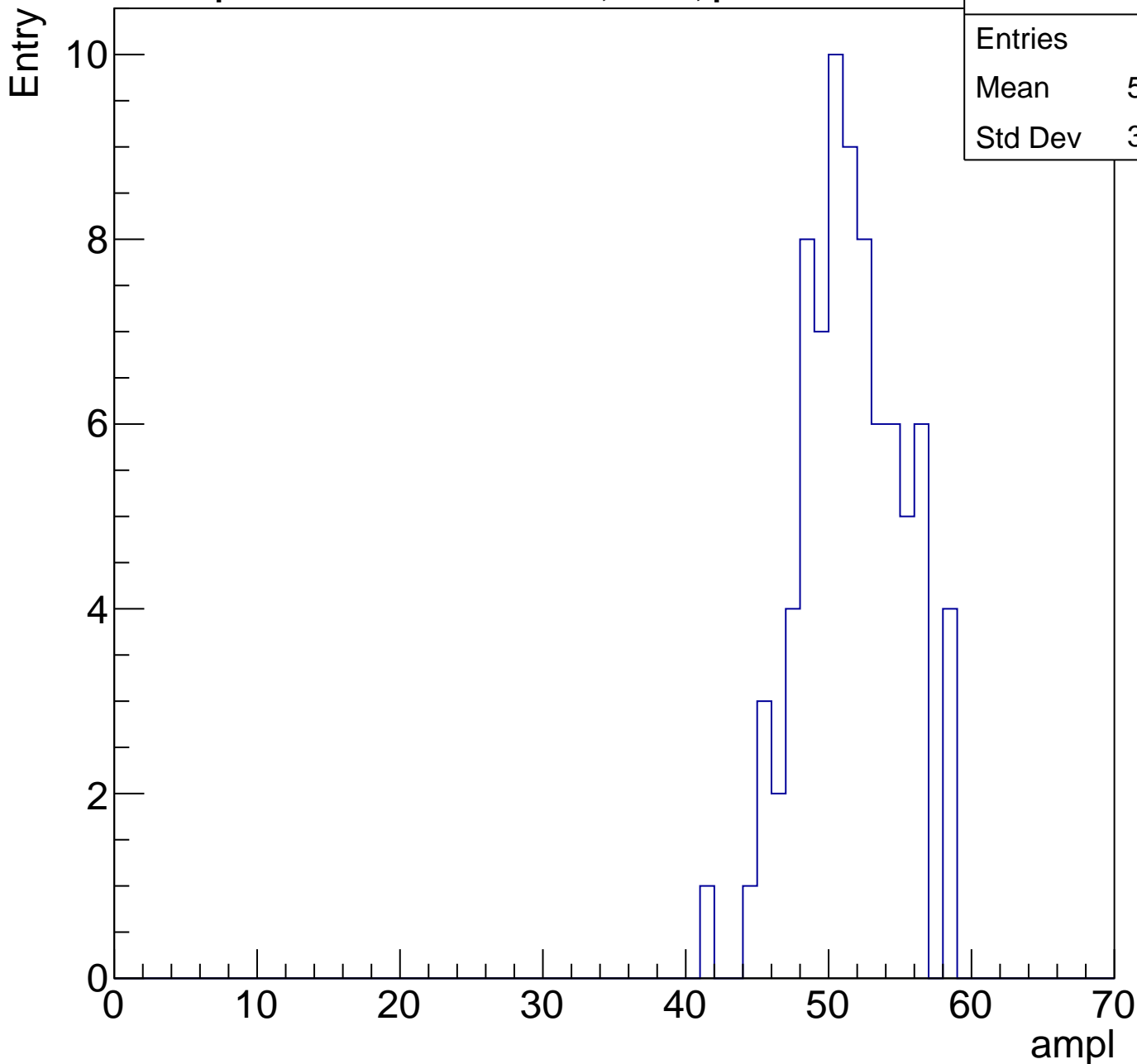
Gaus Width: 3.1207



B0L001S, U7-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

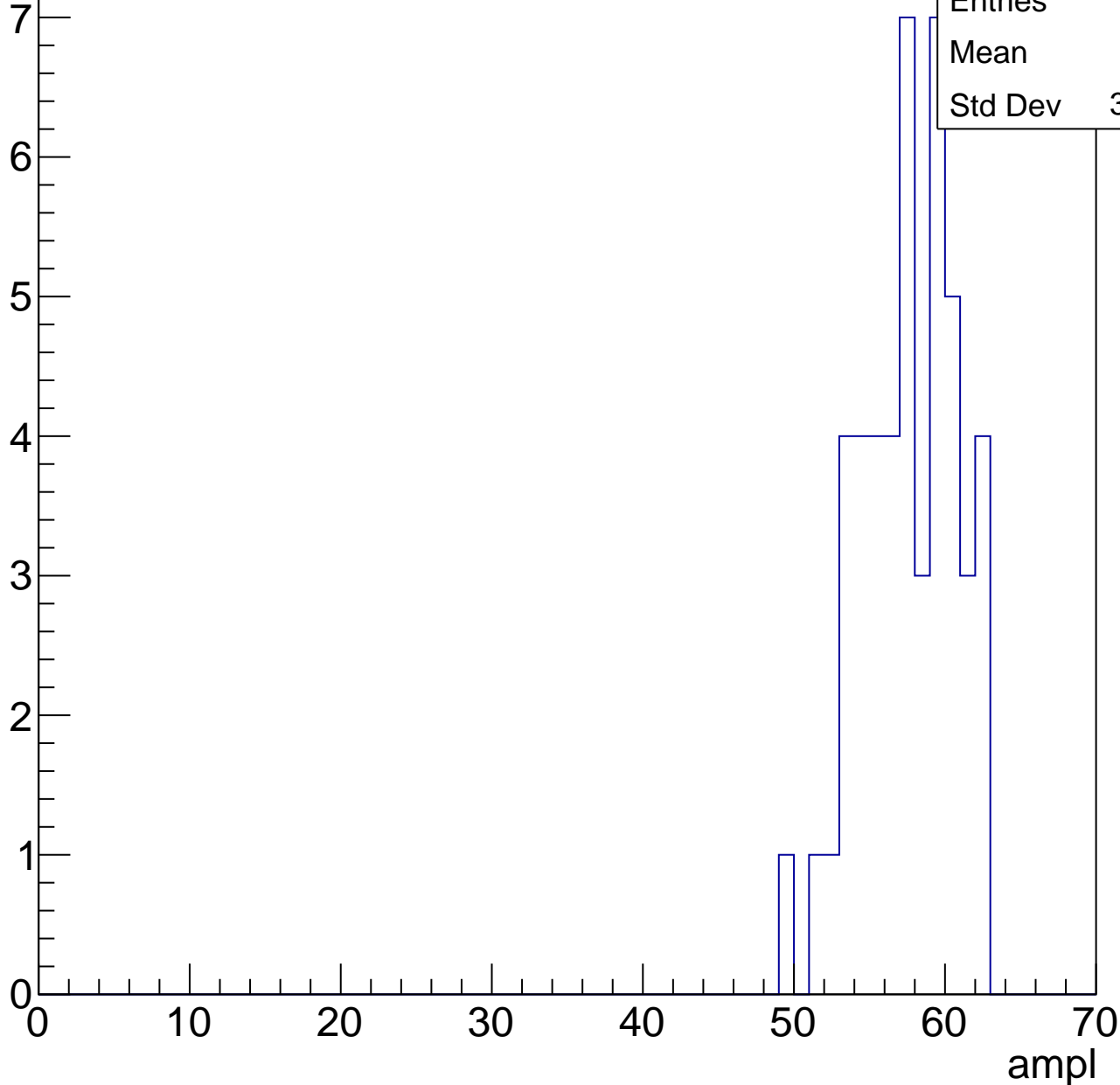
| | |
|---------|-------|
| Entries | 80 |
| Mean | 51.09 |
| Std Dev | 3.522 |



B0L001S, U7-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

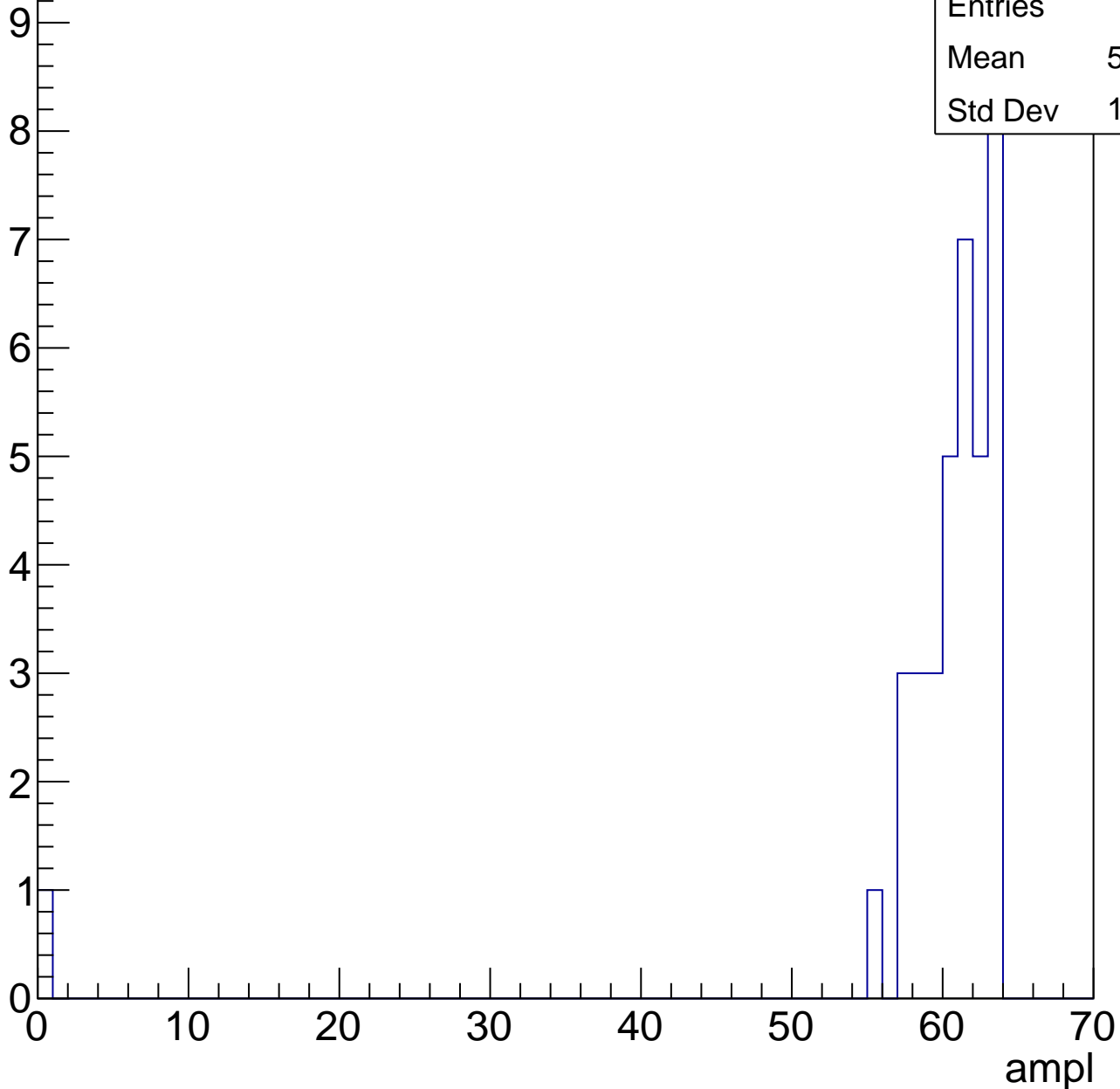


B0L001S, U7-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 58.95 |
| Std Dev | 10.05 |



B0L001S, U7-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch14, adc0

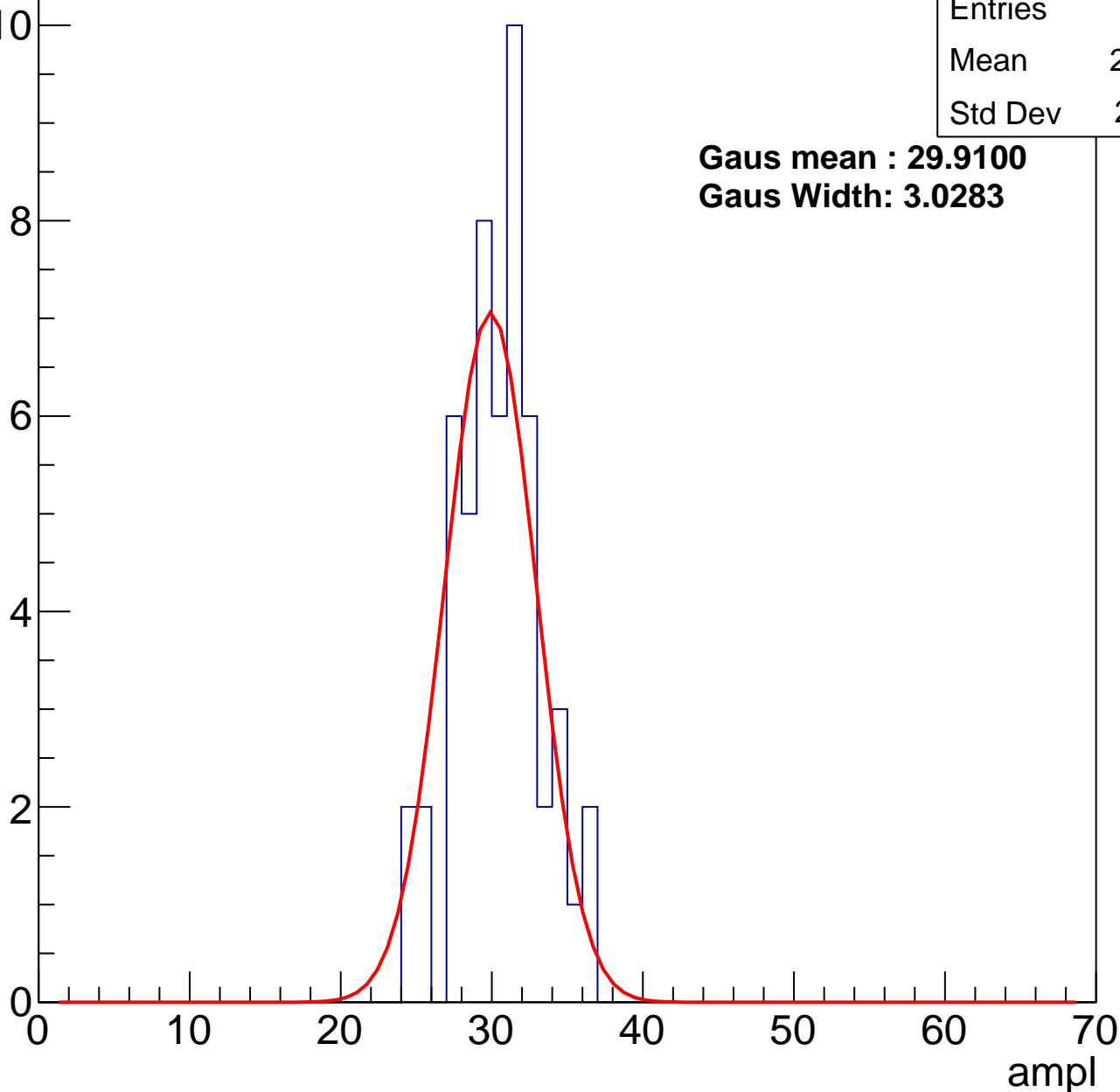
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 29.98 |
| Std Dev | 2.751 |

Gaus mean : 29.9100

Gaus Width: 3.0283



B0L001S, U7-ch14, adc1

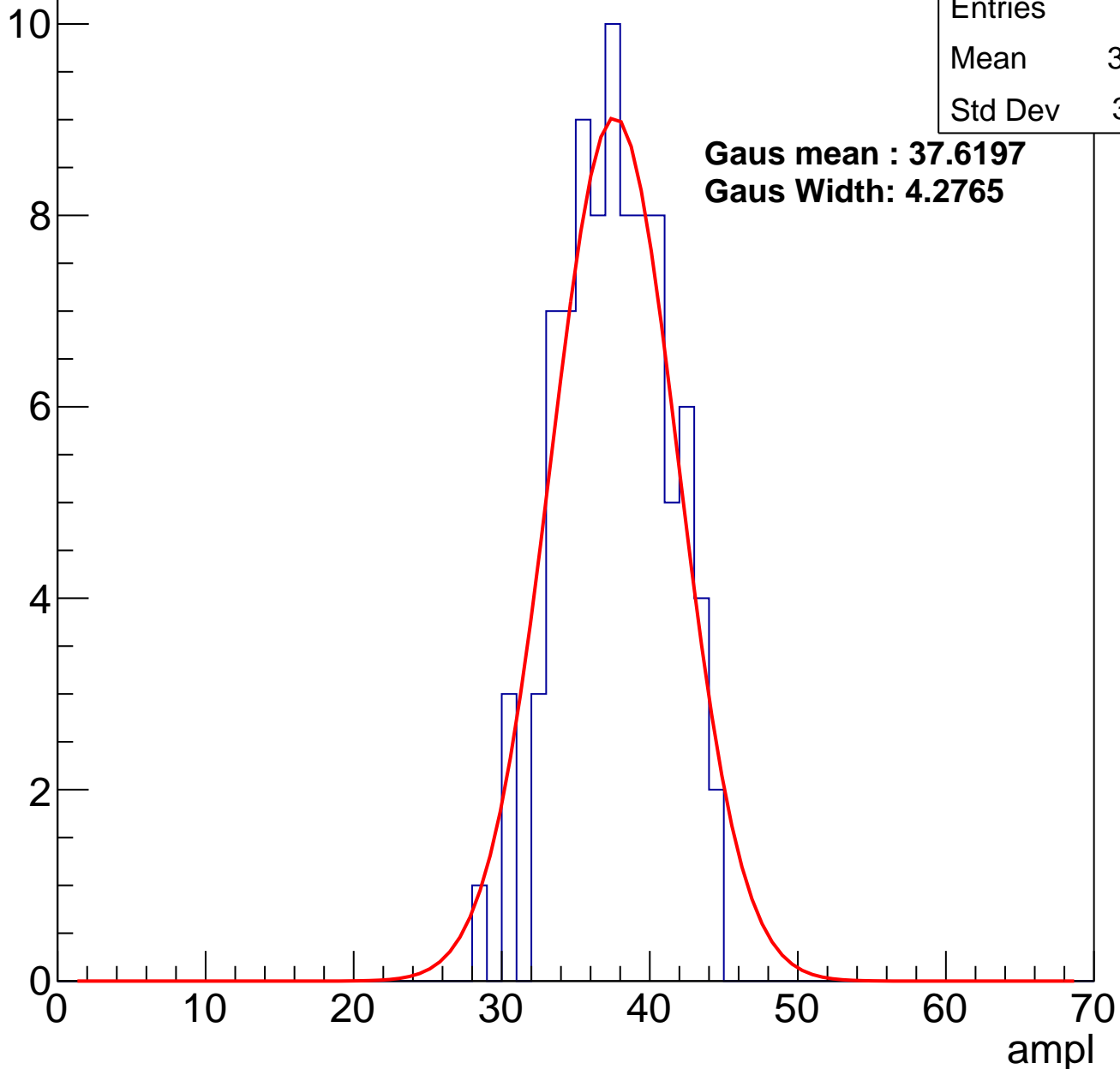
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 89 |
| Mean | 37.18 |
| Std Dev | 3.521 |

Gaus mean : 37.6197

Gaus Width: 4.2765

Entry



B0L001S, U7-ch14, adc2

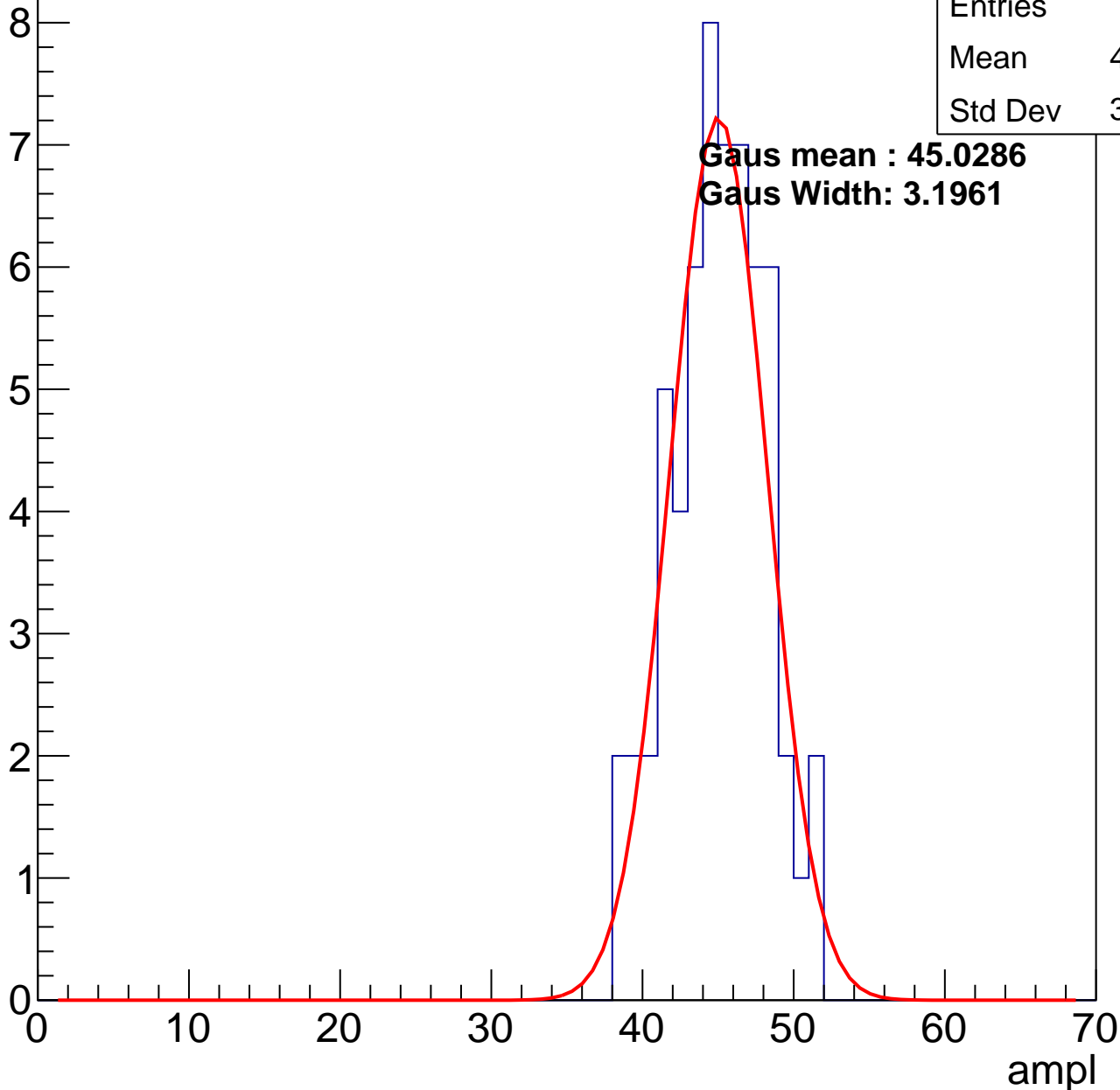
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 44.57 |
| Std Dev | 3.084 |

Gaus mean : 45.0286

Gaus Width: 3.1961

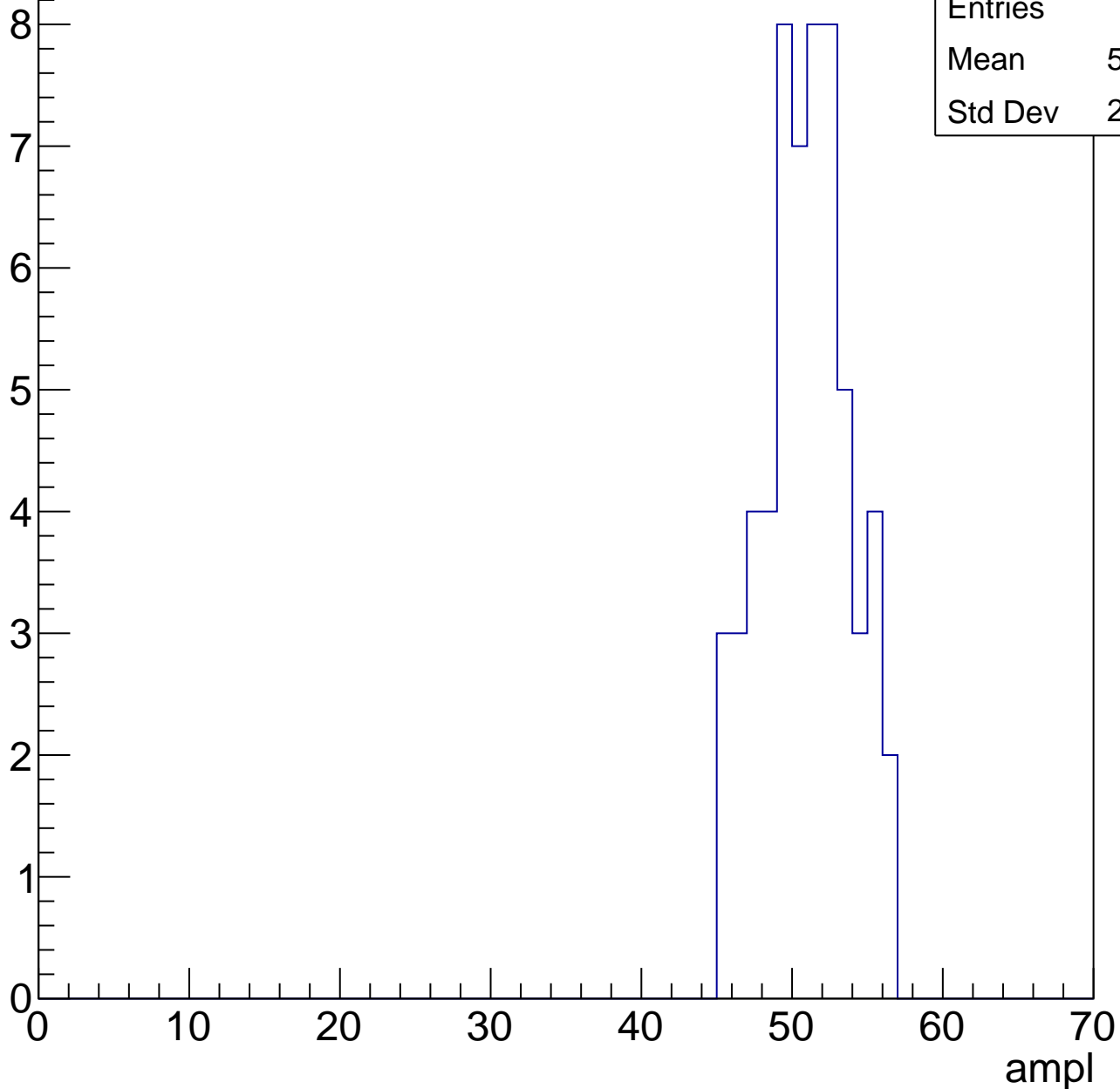


B0L001S, U7-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 50.47 |
| Std Dev | 2.837 |

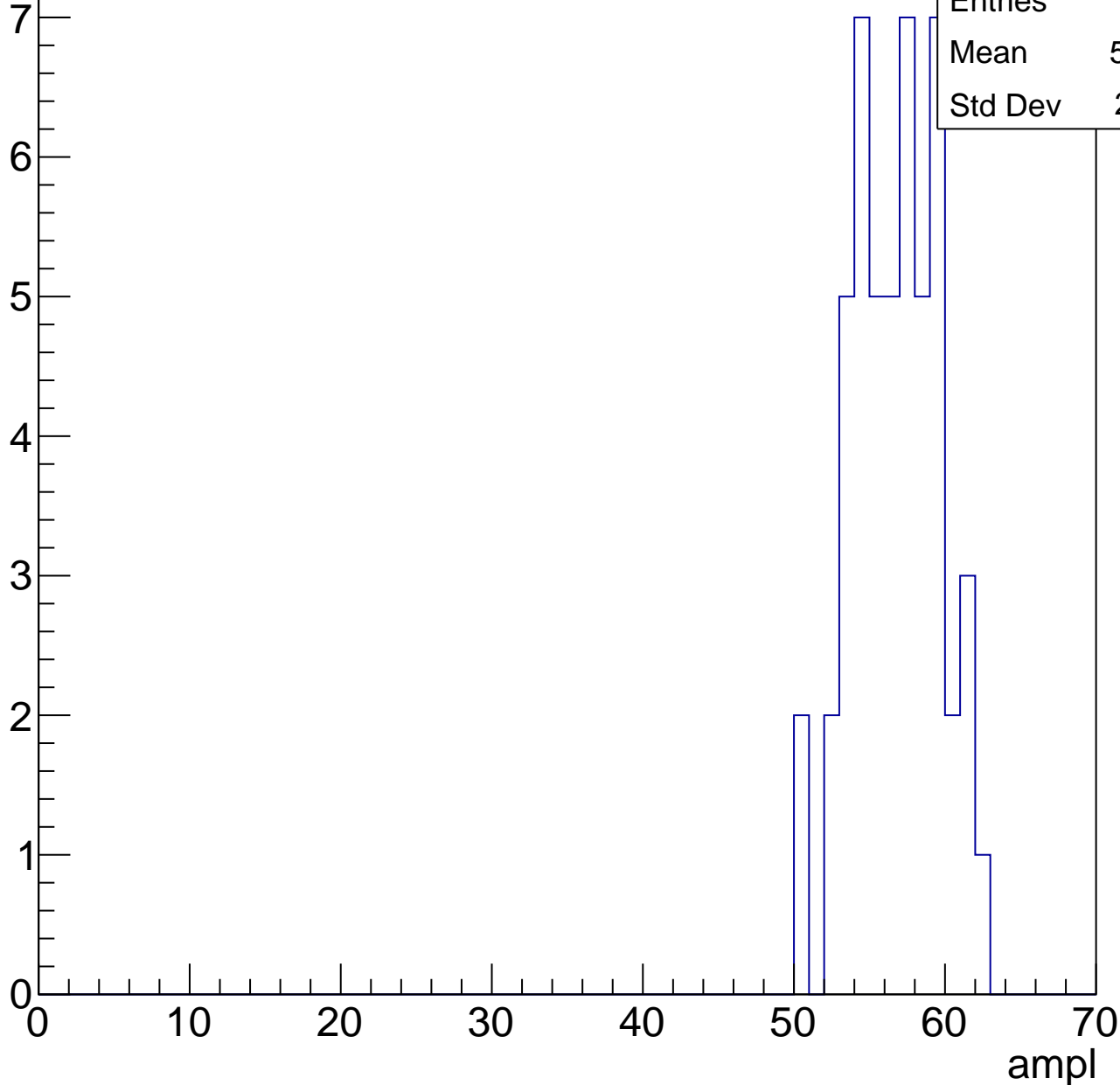


B0L001S, U7-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

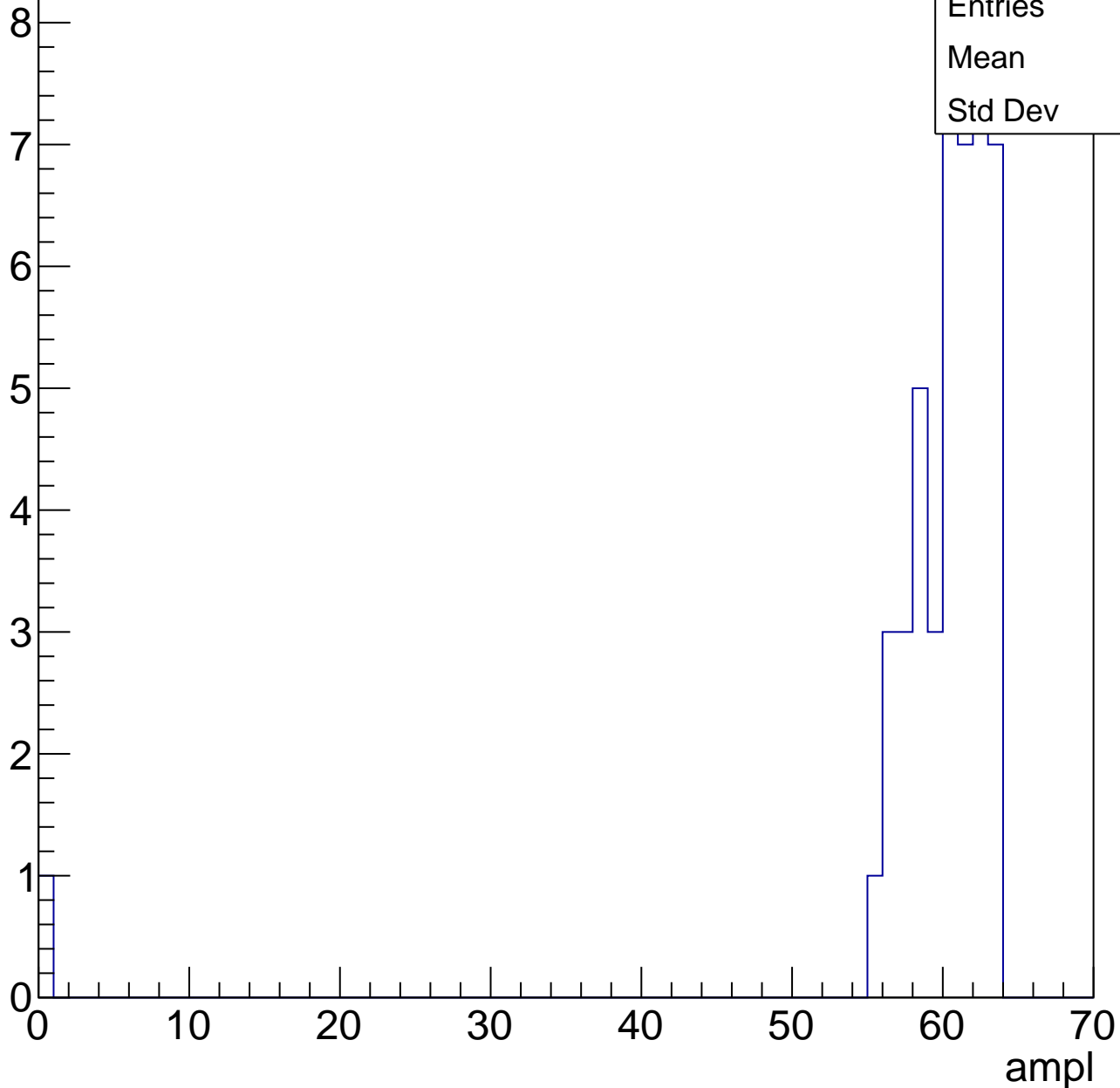
| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.25 |
| Std Dev | 2.841 |



B0L001S, U7-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch15, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 65 |
| Mean | 29.49 |
| Std Dev | 3.604 |

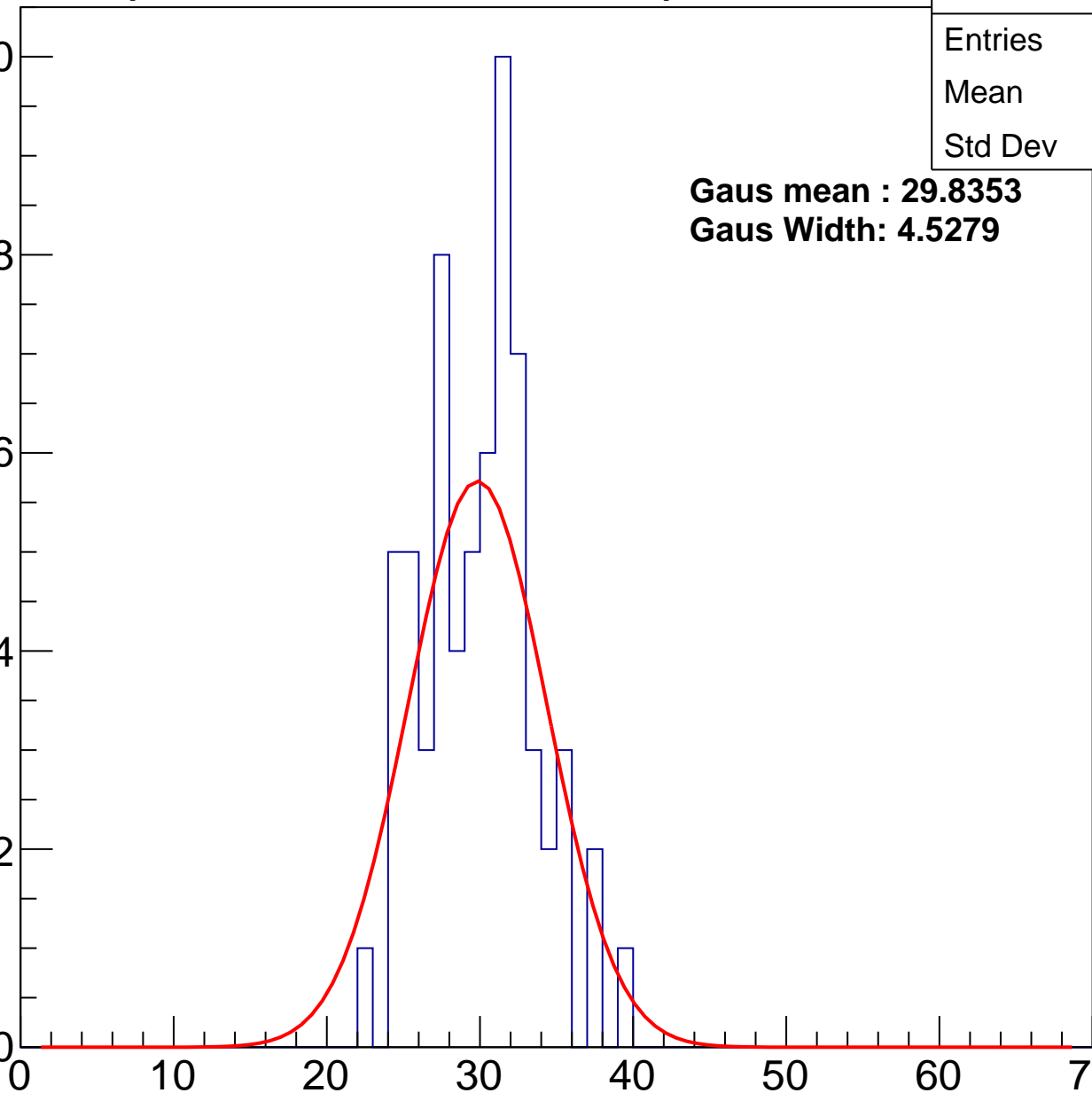
Gaus mean : 29.8353

Gaus Width: 4.5279

Entry

10
8
6
4
2
0

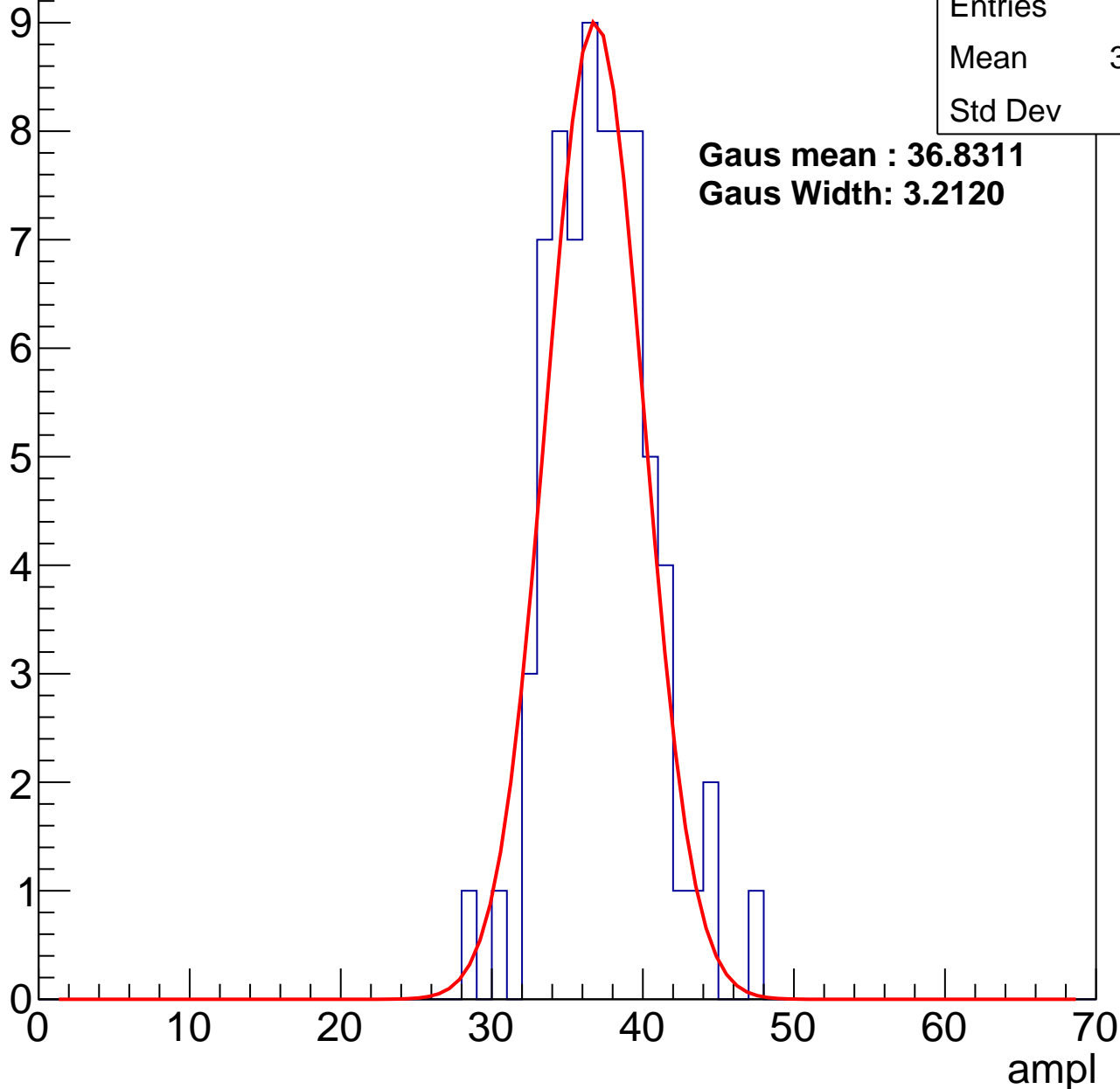
ampl



B0L001S, U7-ch15, adc1

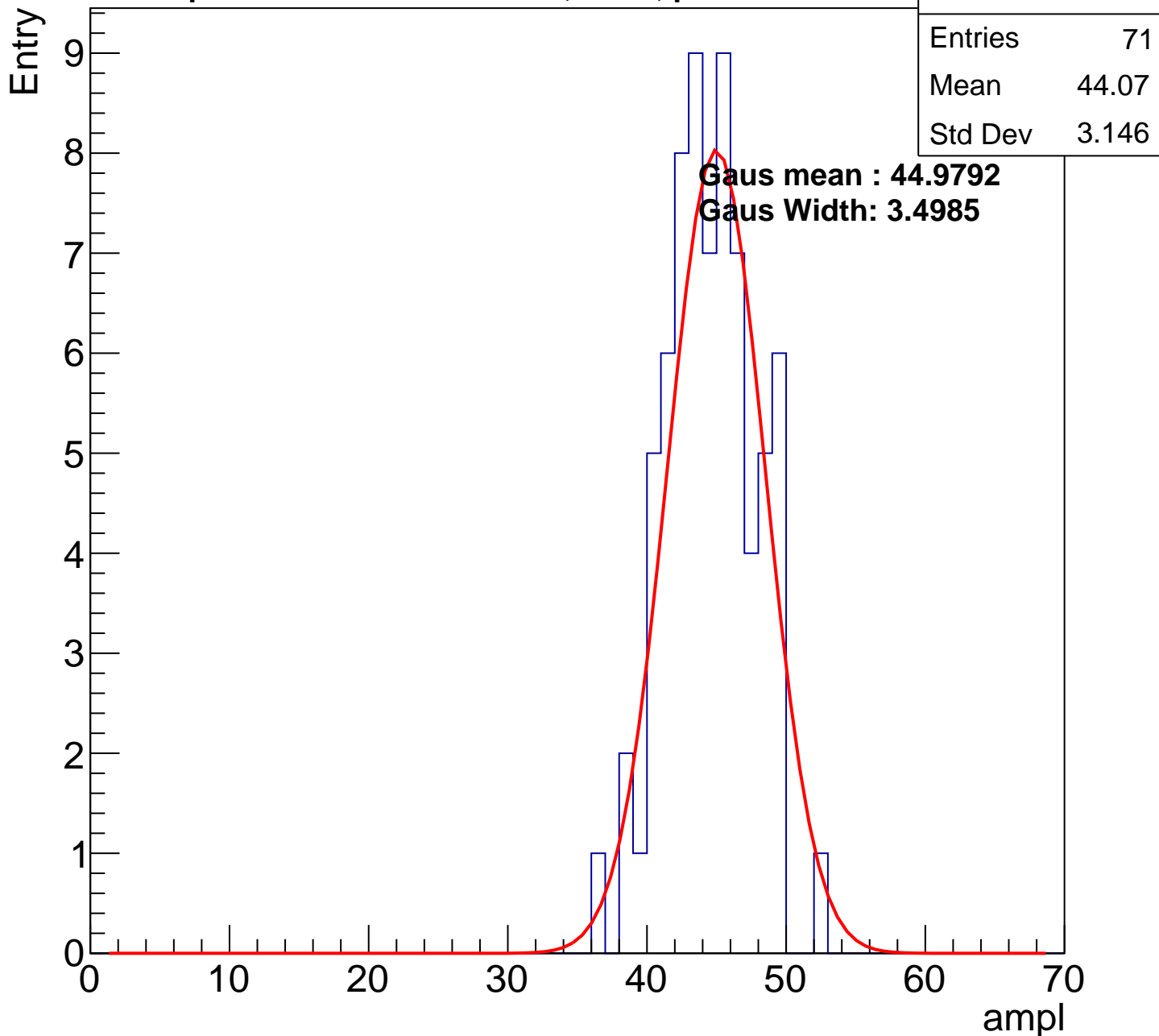
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch15, adc2

calib_packv5_042523_0143.root, FC#9, port A1

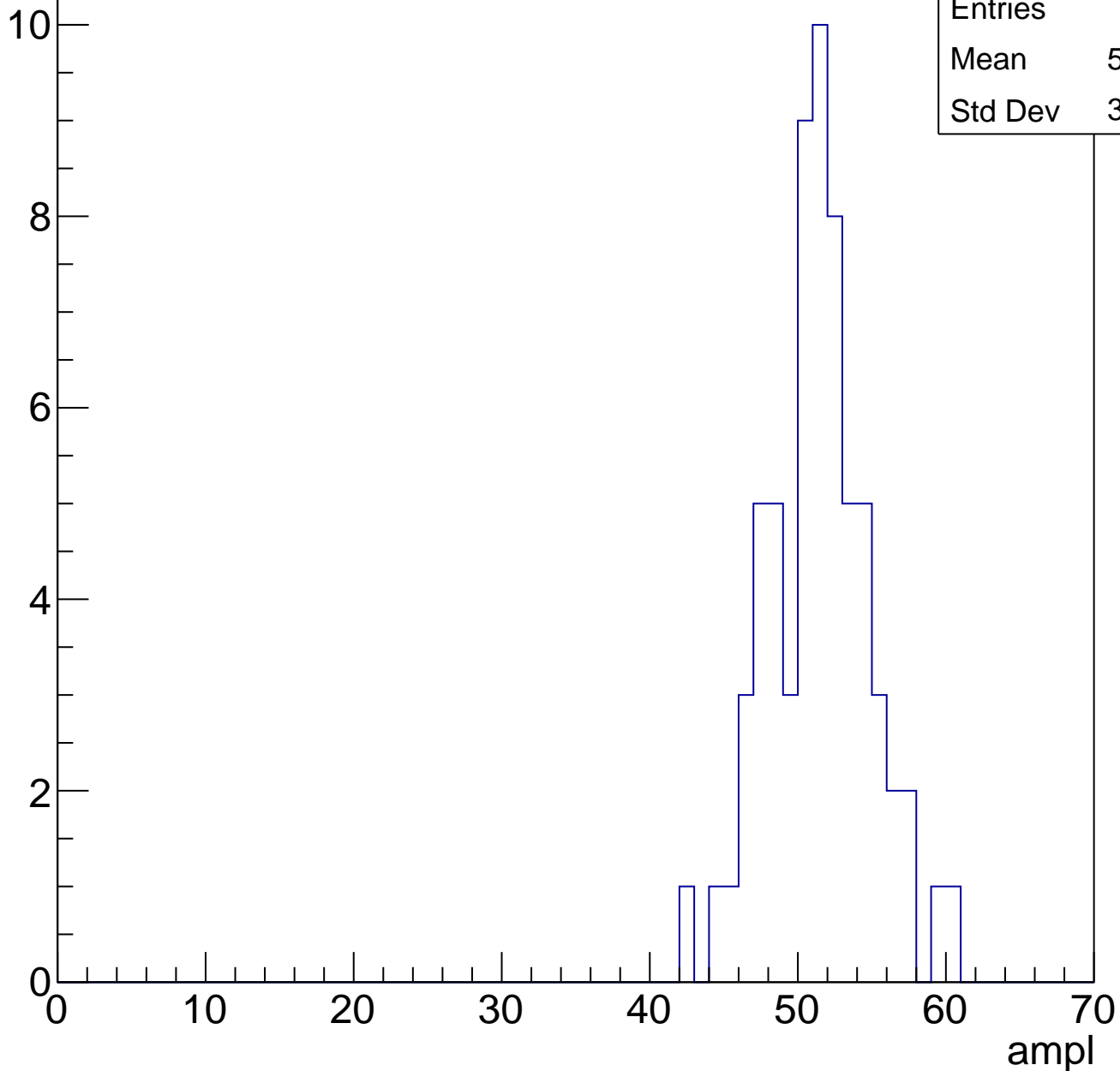


B0L001S, U7-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.95 |
| Std Dev | 3.453 |

Entry

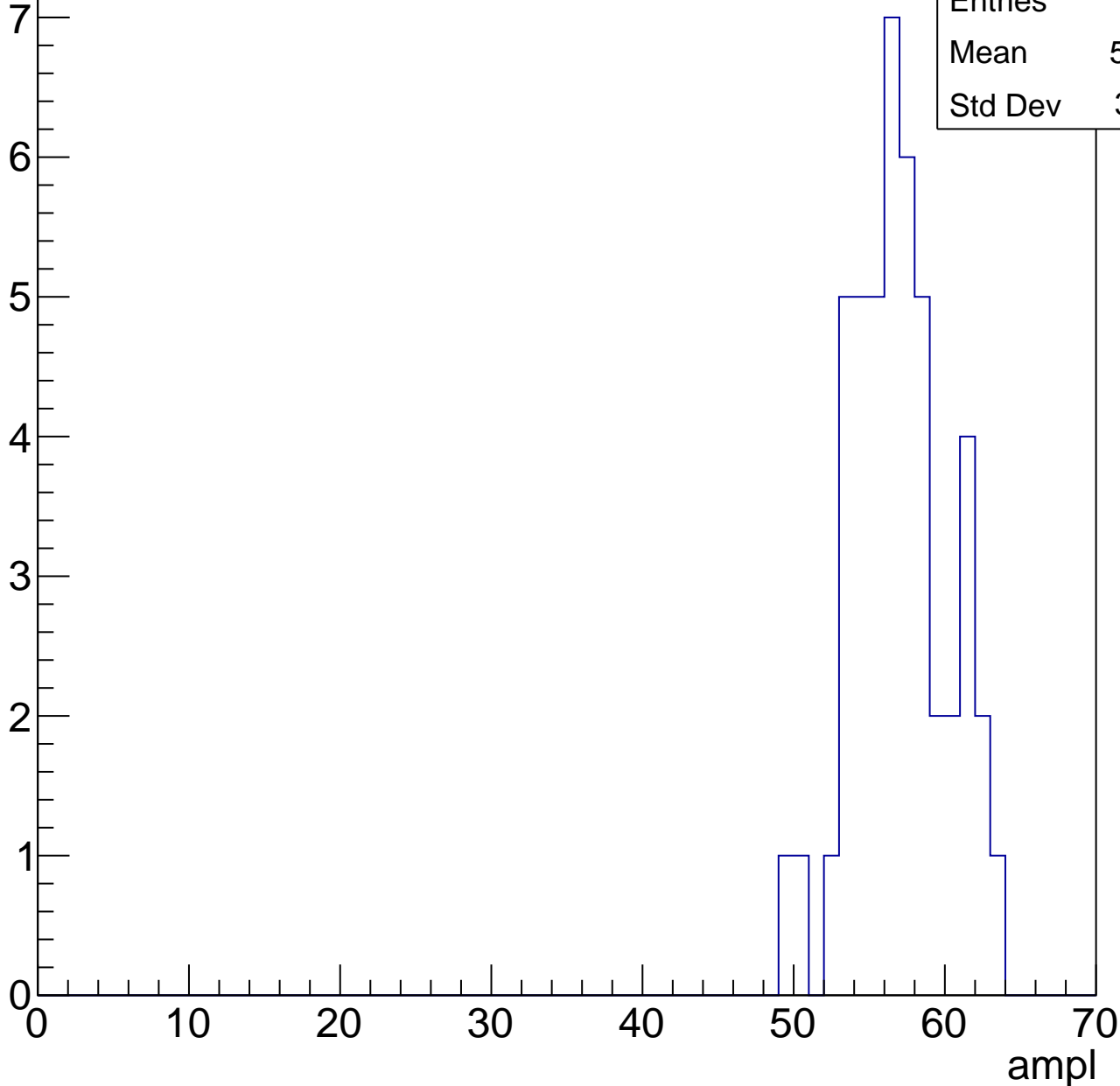


B0L001S, U7-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 56.47 |
| Std Dev | 3.121 |

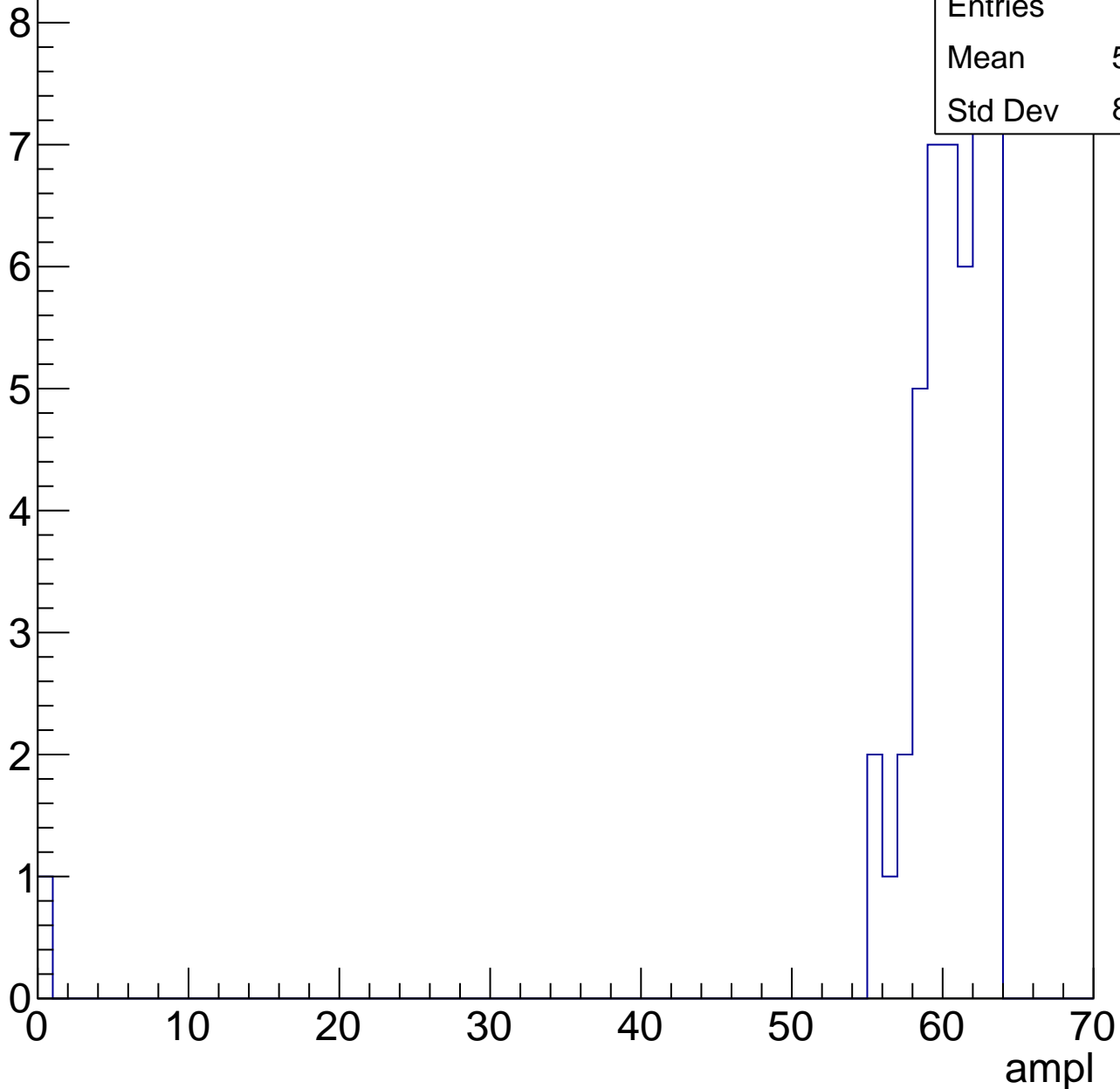


B0L001S, U7-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.91 |
| Std Dev | 8.951 |



B0L001S, U7-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

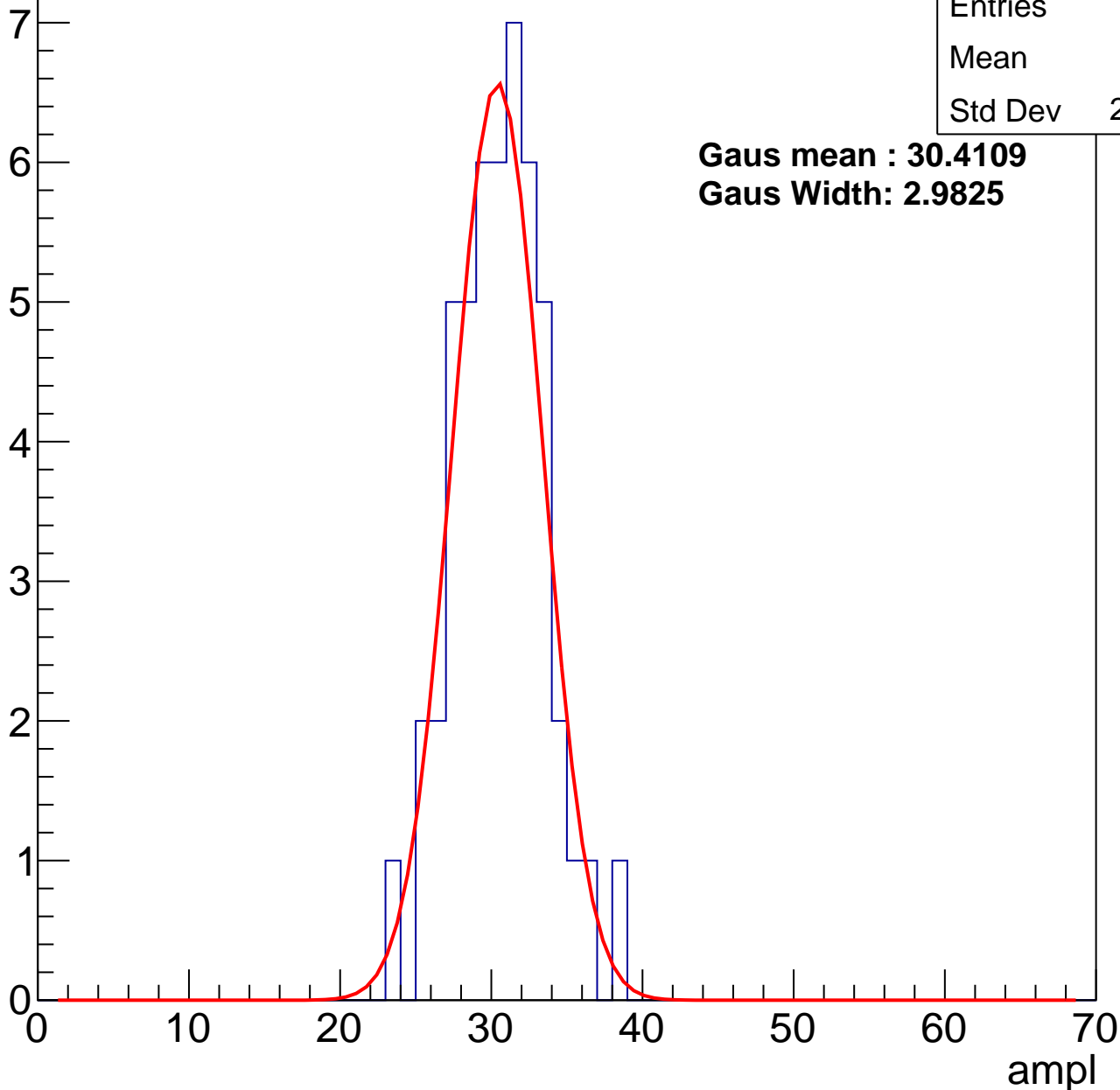
B0L001S, U7-ch16, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 30.1 |
| Std Dev | 2.948 |

Gaus mean : 30.4109
Gaus Width: 2.9825



B0L001S, U7-ch16, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 84 |
| Mean | 36.5 |
| Std Dev | 3.759 |

Gaus mean : 37.1098

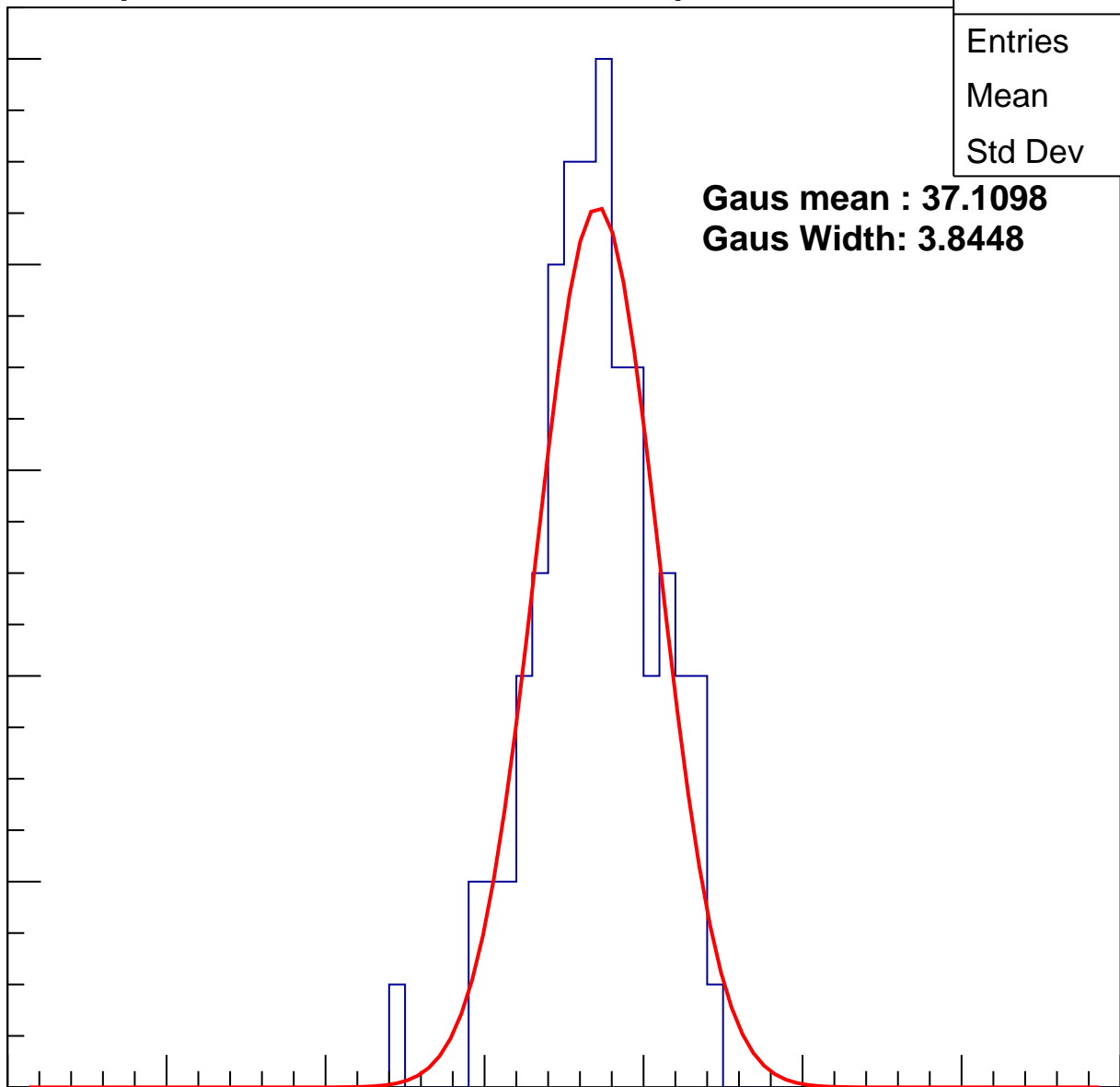
Gaus Width: 3.8448

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch16, adc2

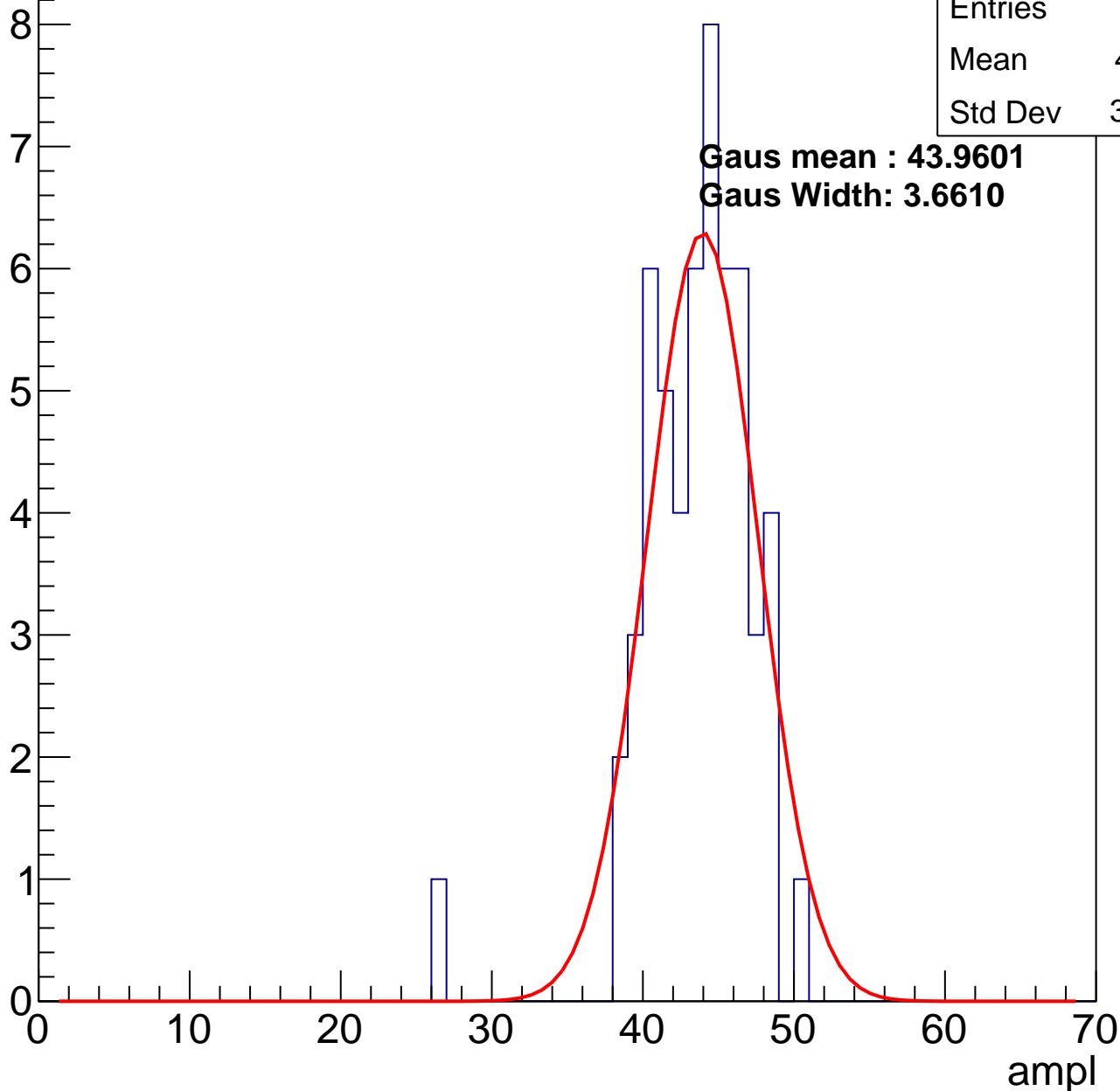
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.11 |
| Std Dev | 3.686 |

Gaus mean : 43.9601

Gaus Width: 3.6610



B0L001S, U7-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

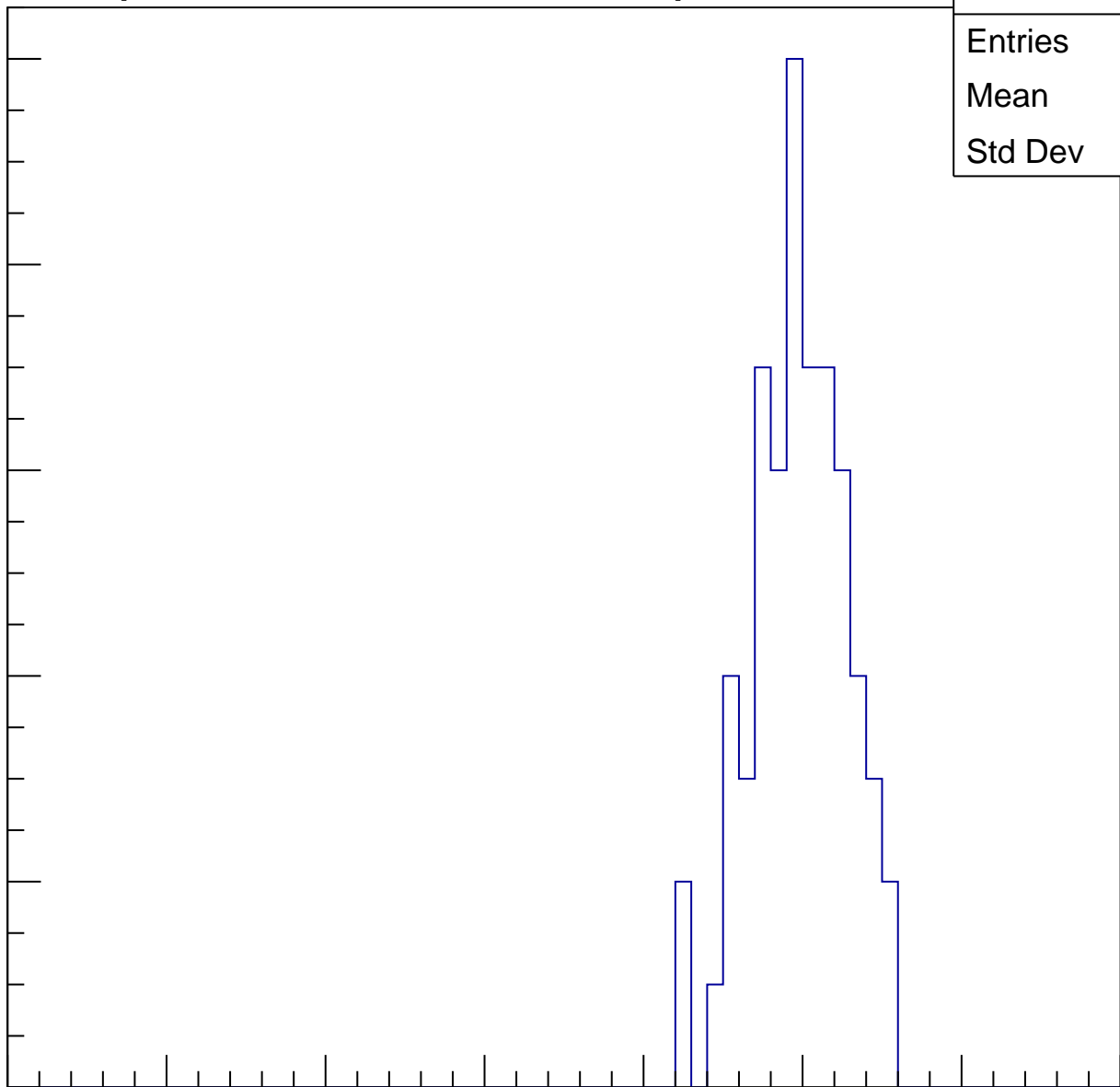
| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.29 |
| Std Dev | 2.959 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

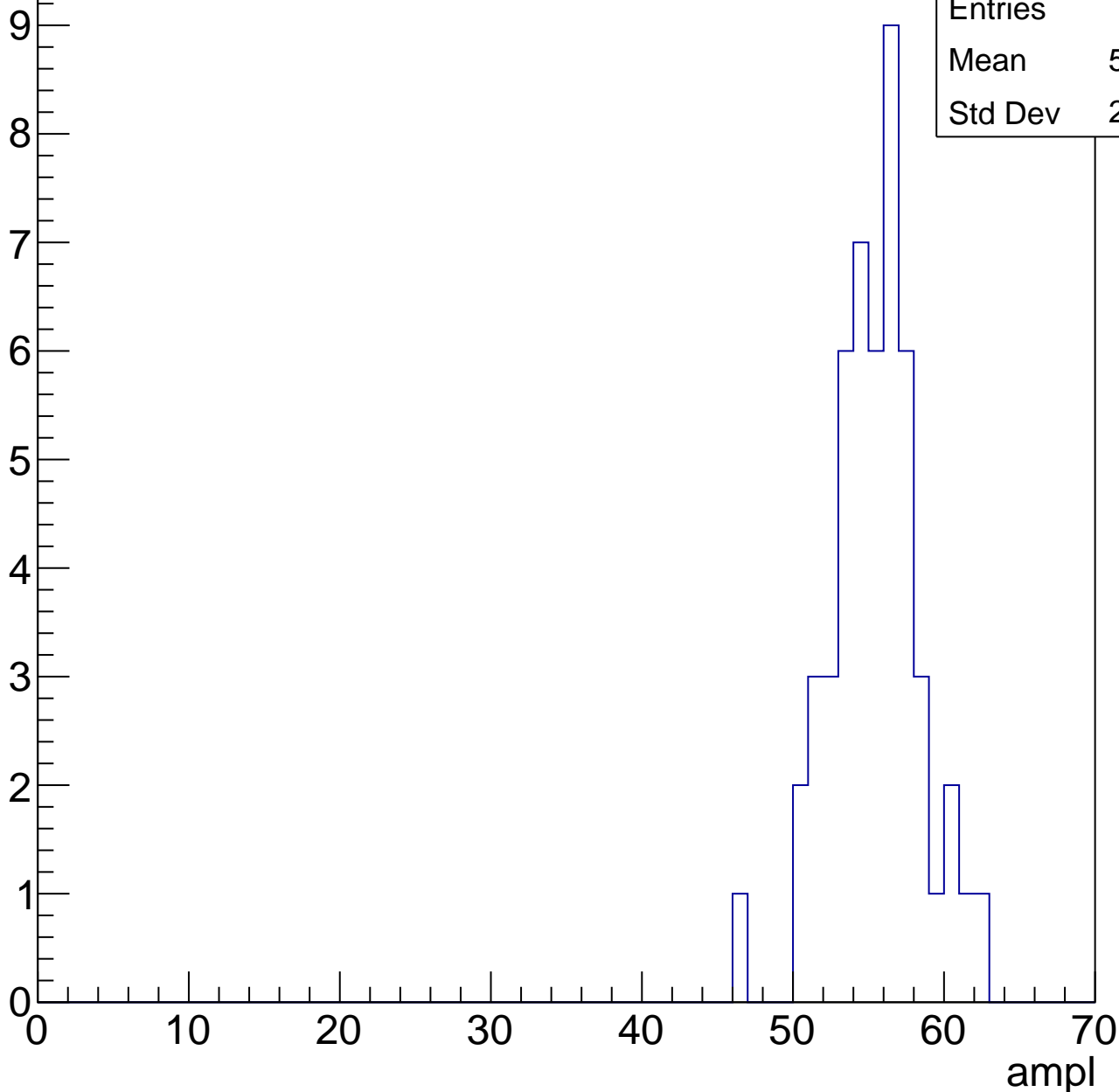


B0L001S, U7-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

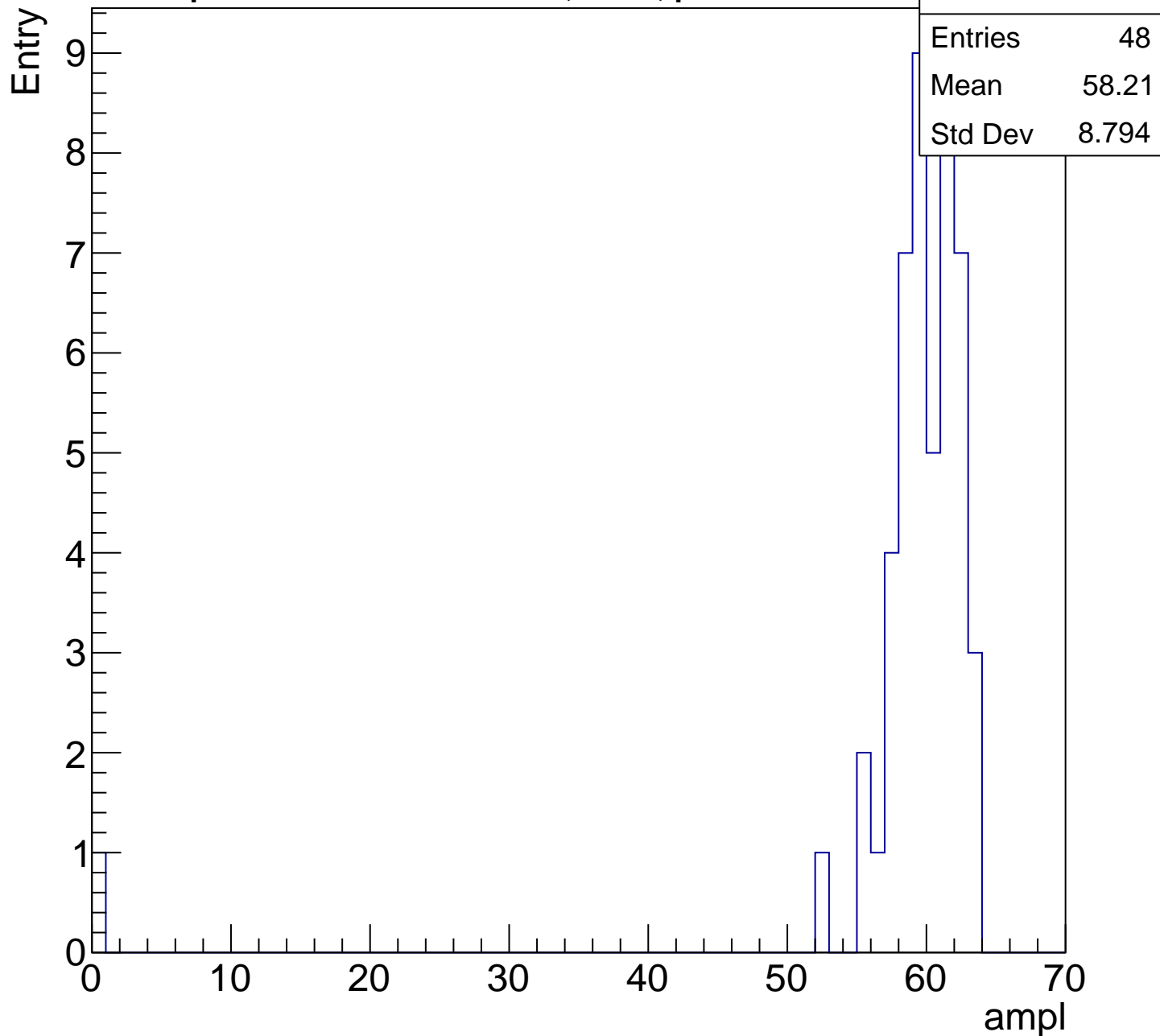
Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.96 |
| Std Dev | 2.964 |



B0L001S, U7-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

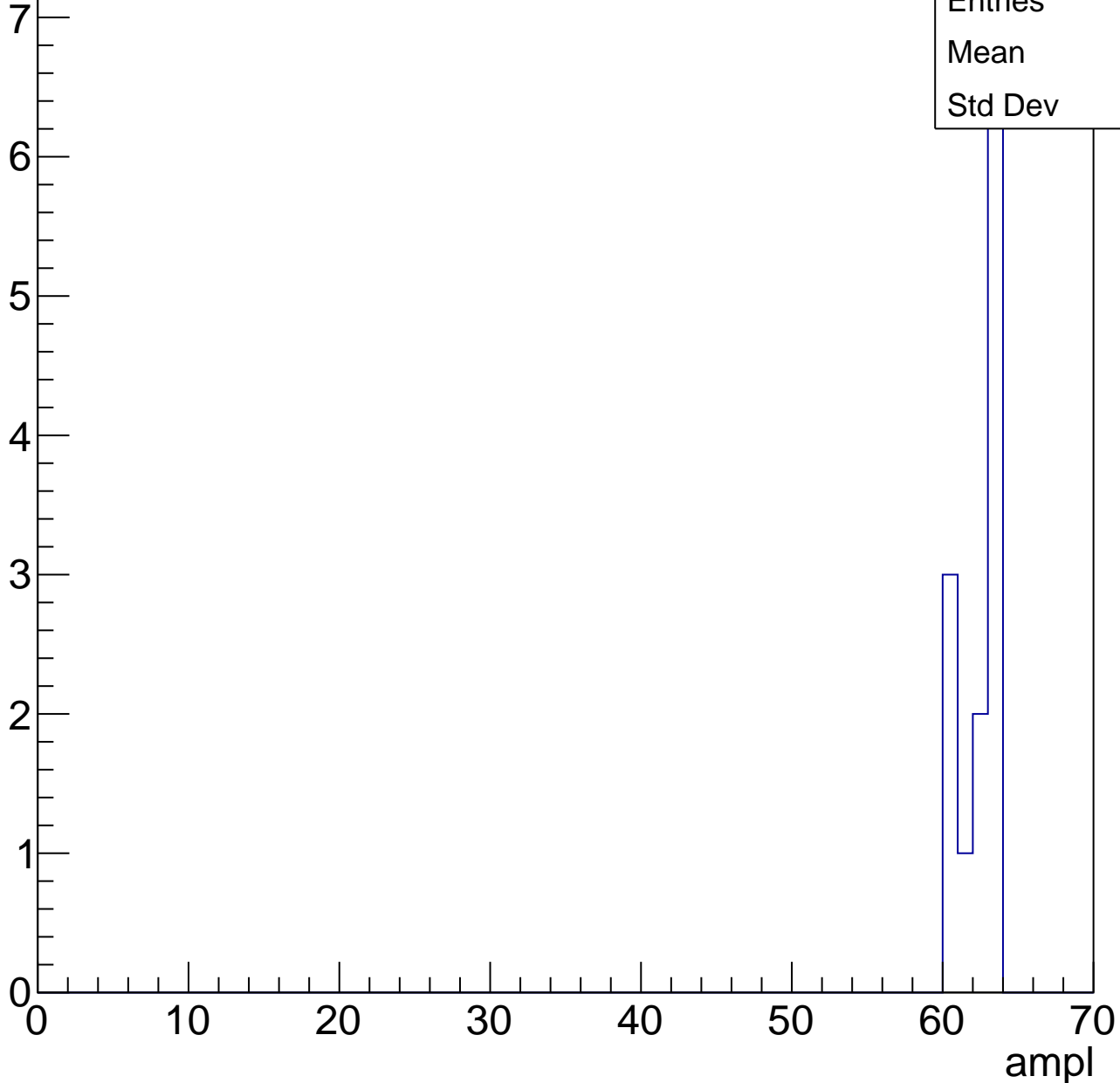


B0L001S, U7-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 13 |
| Mean | 62 |
| Std Dev | 1.24 |



B0L001S, U7-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch17, adc0

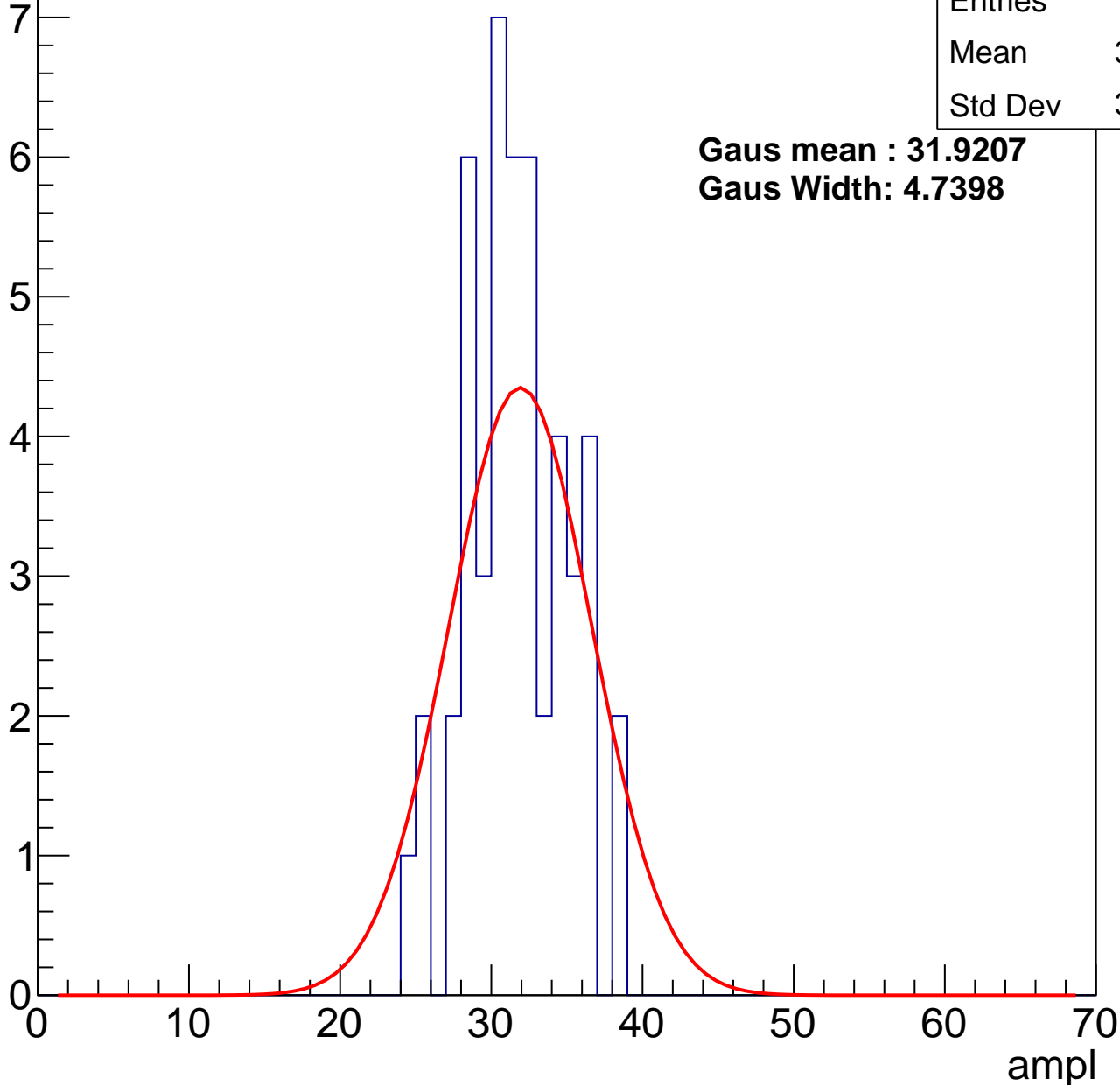
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 31.21 |
| Std Dev | 3.291 |

Gaus mean : 31.9207

Gaus Width: 4.7398



B0L001S, U7-ch17, adc1

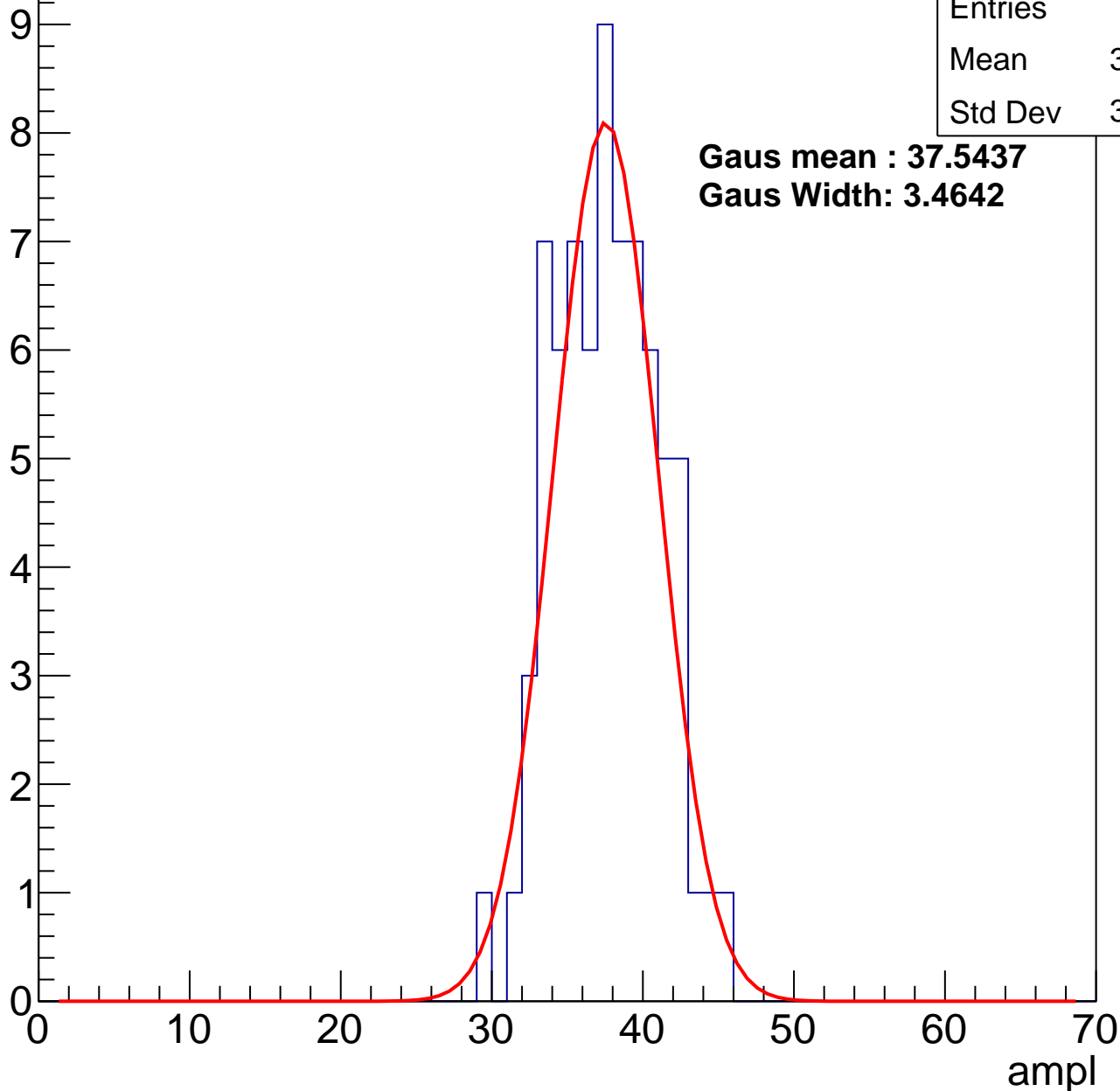
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 37.14 |
| Std Dev | 3.345 |

Gaus mean : 37.5437

Gaus Width: 3.4642



B0L001S, U7-ch17, adc2

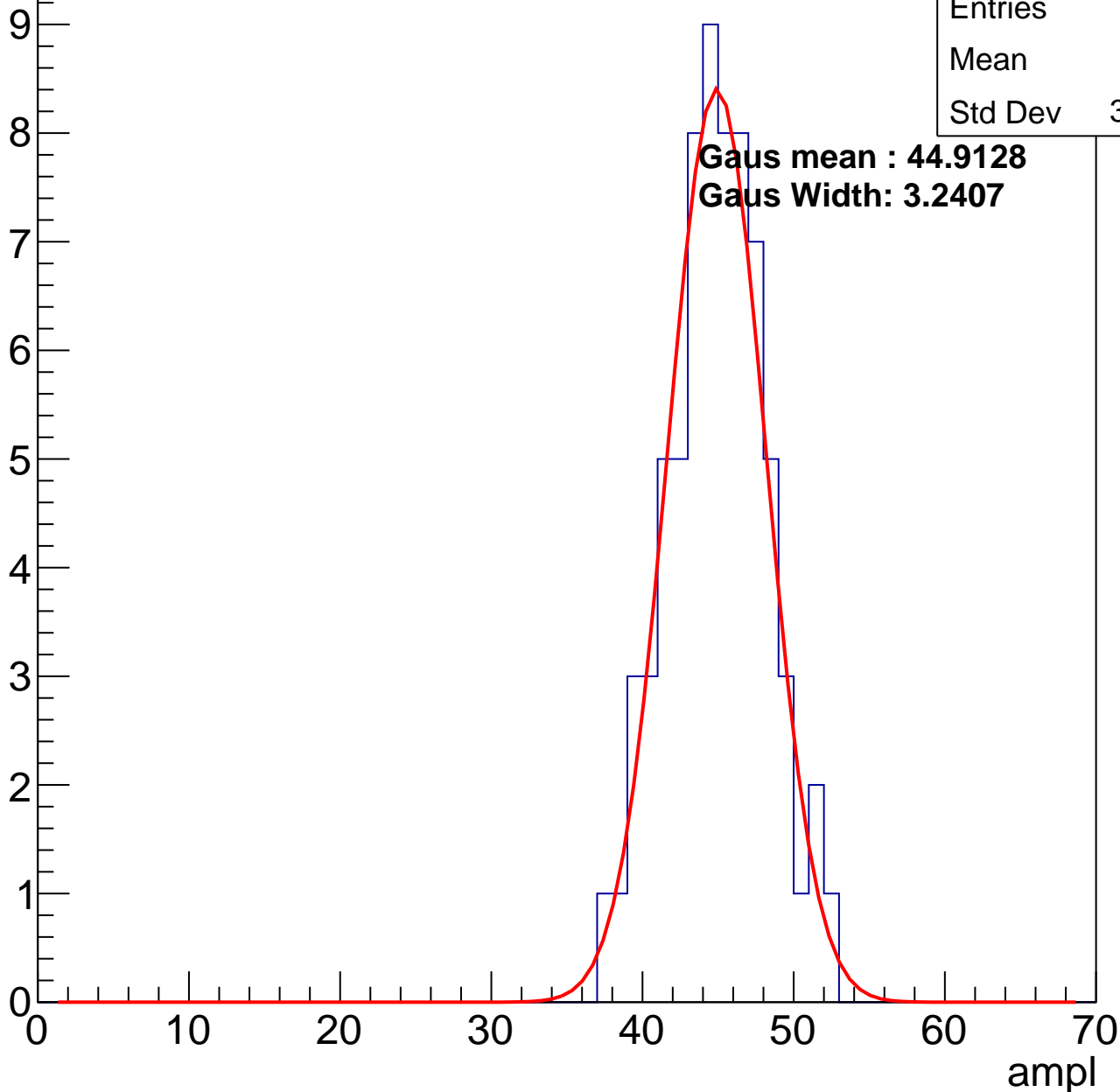
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.5 |
| Std Dev | 3.206 |

Gaus mean : 44.9128

Gaus Width: 3.2407

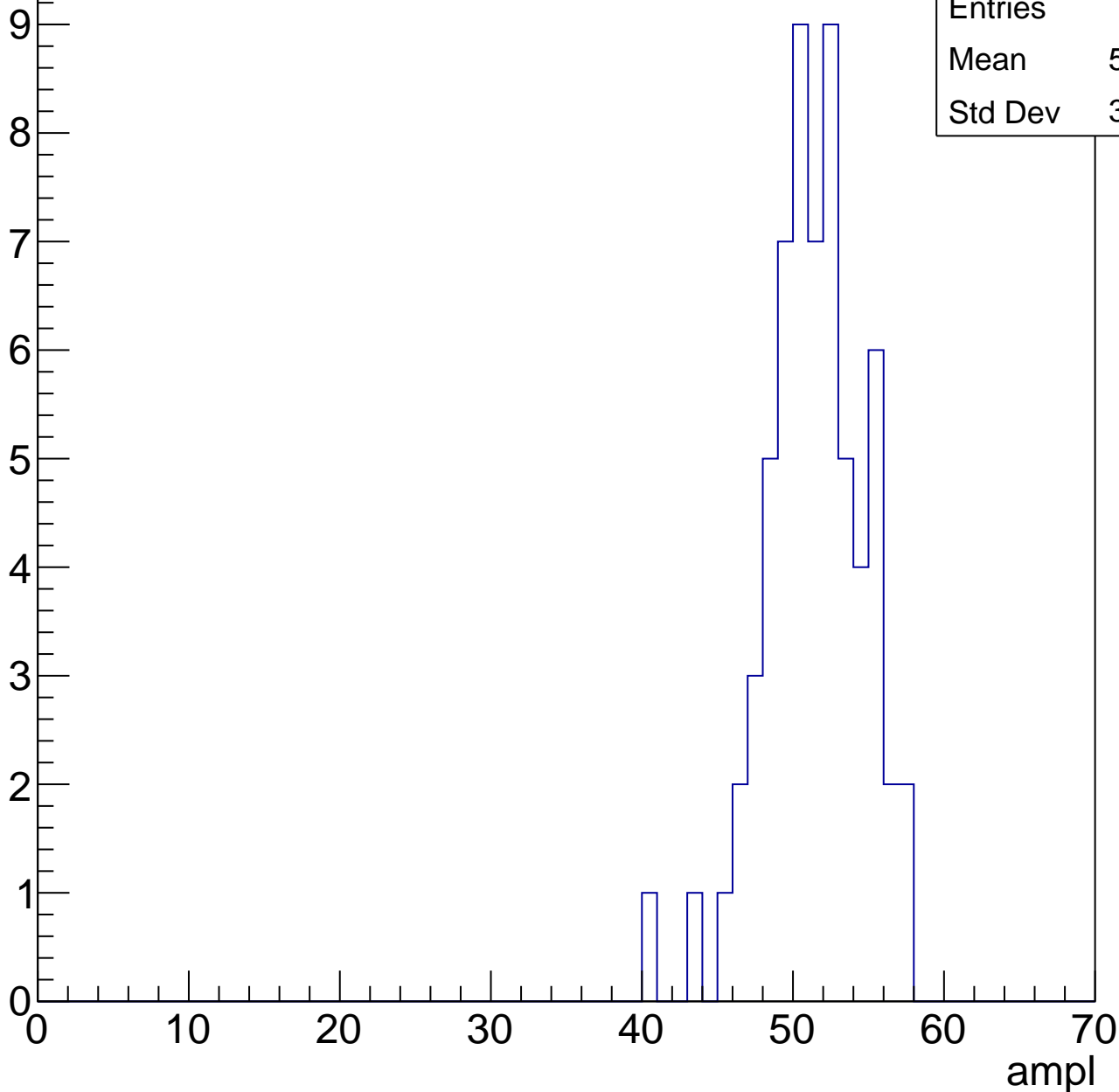


B0L001S, U7-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.88 |
| Std Dev | 3.267 |

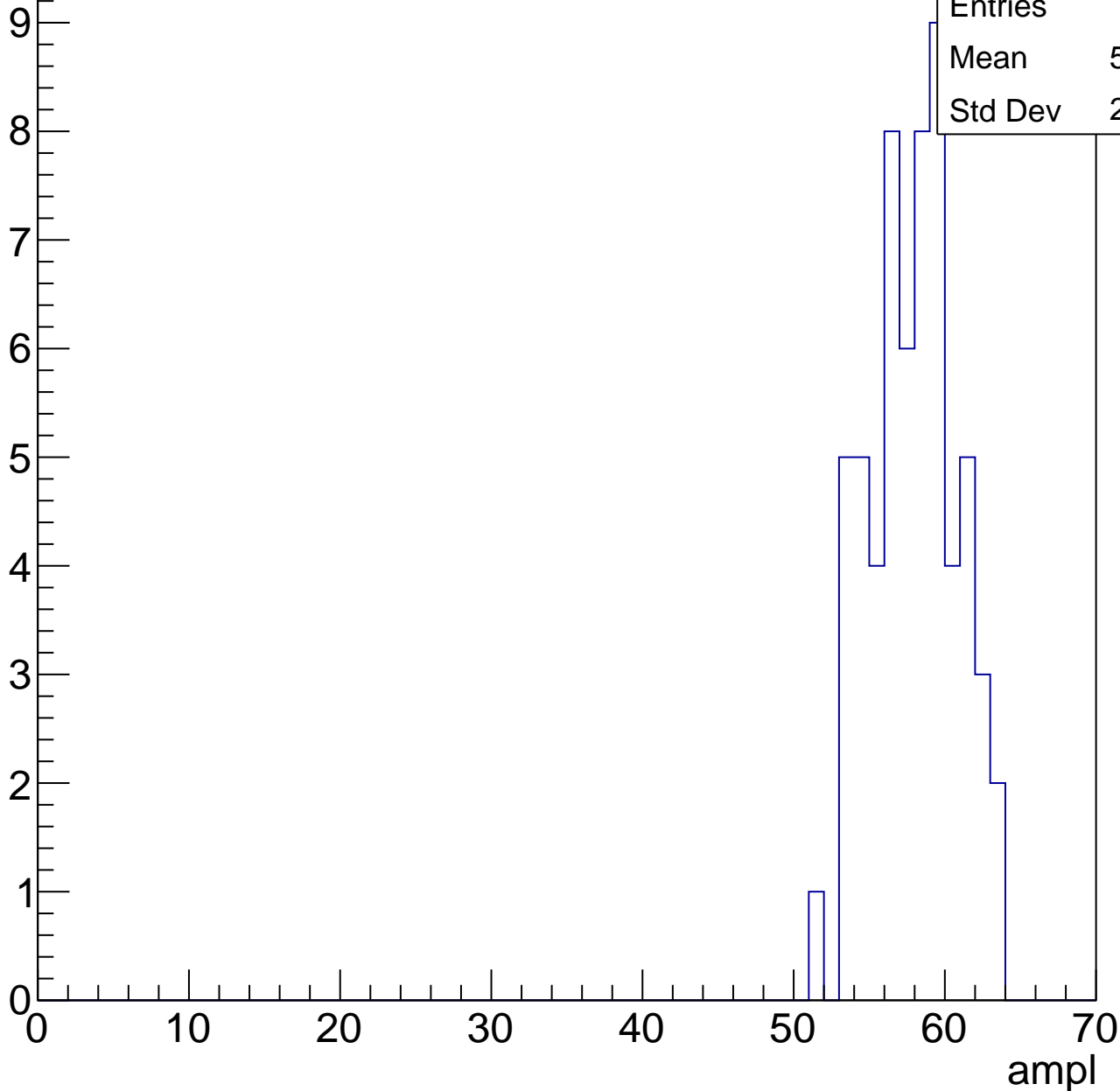


B0L001S, U7-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 57.47 |
| Std Dev | 2.825 |

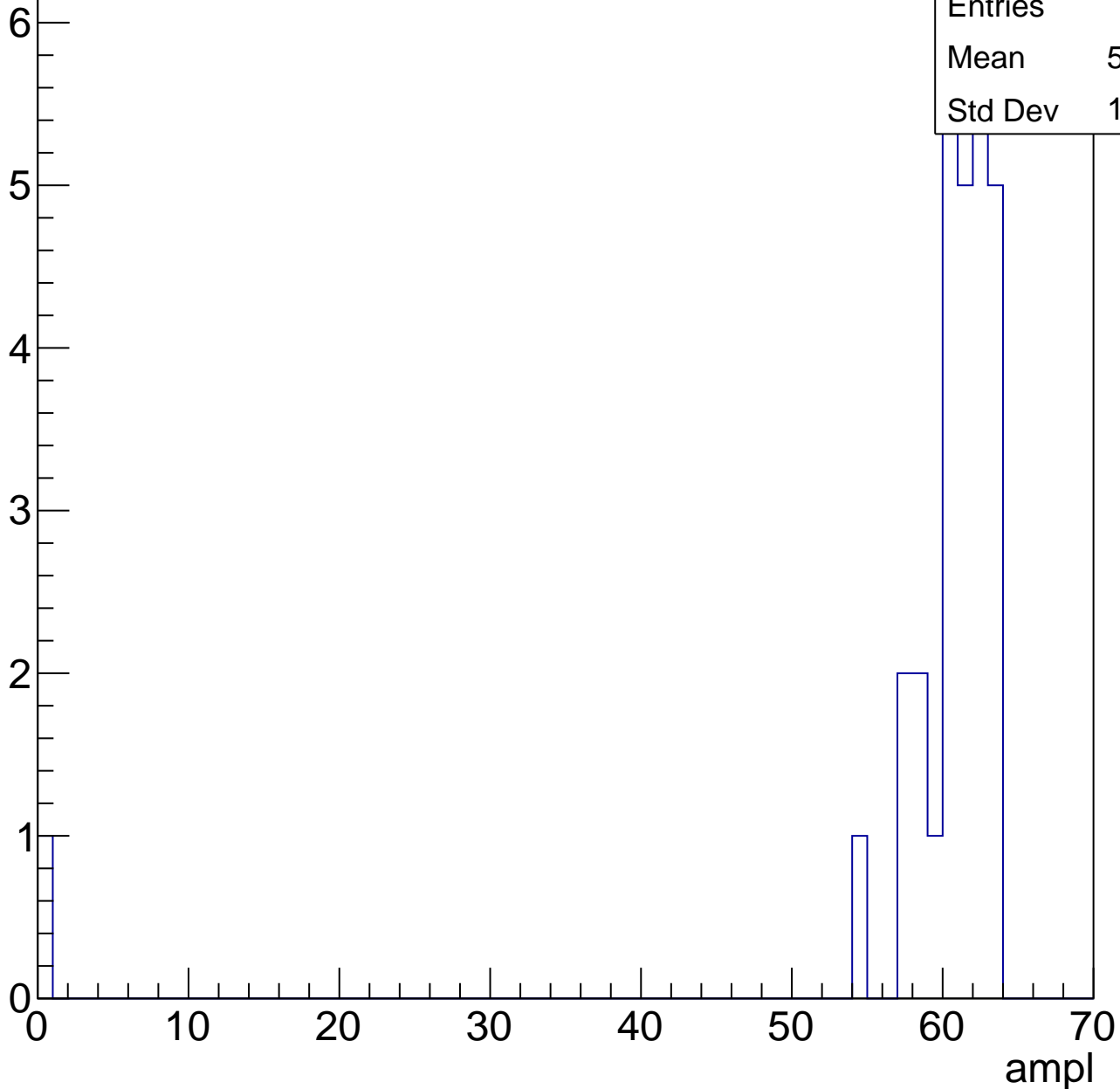


B0L001S, U7-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.45 |
| Std Dev | 11.25 |



B0L001S, U7-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 62.75 |
| Std Dev | 0.433 |

B0L001S, U7-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch18, adc0

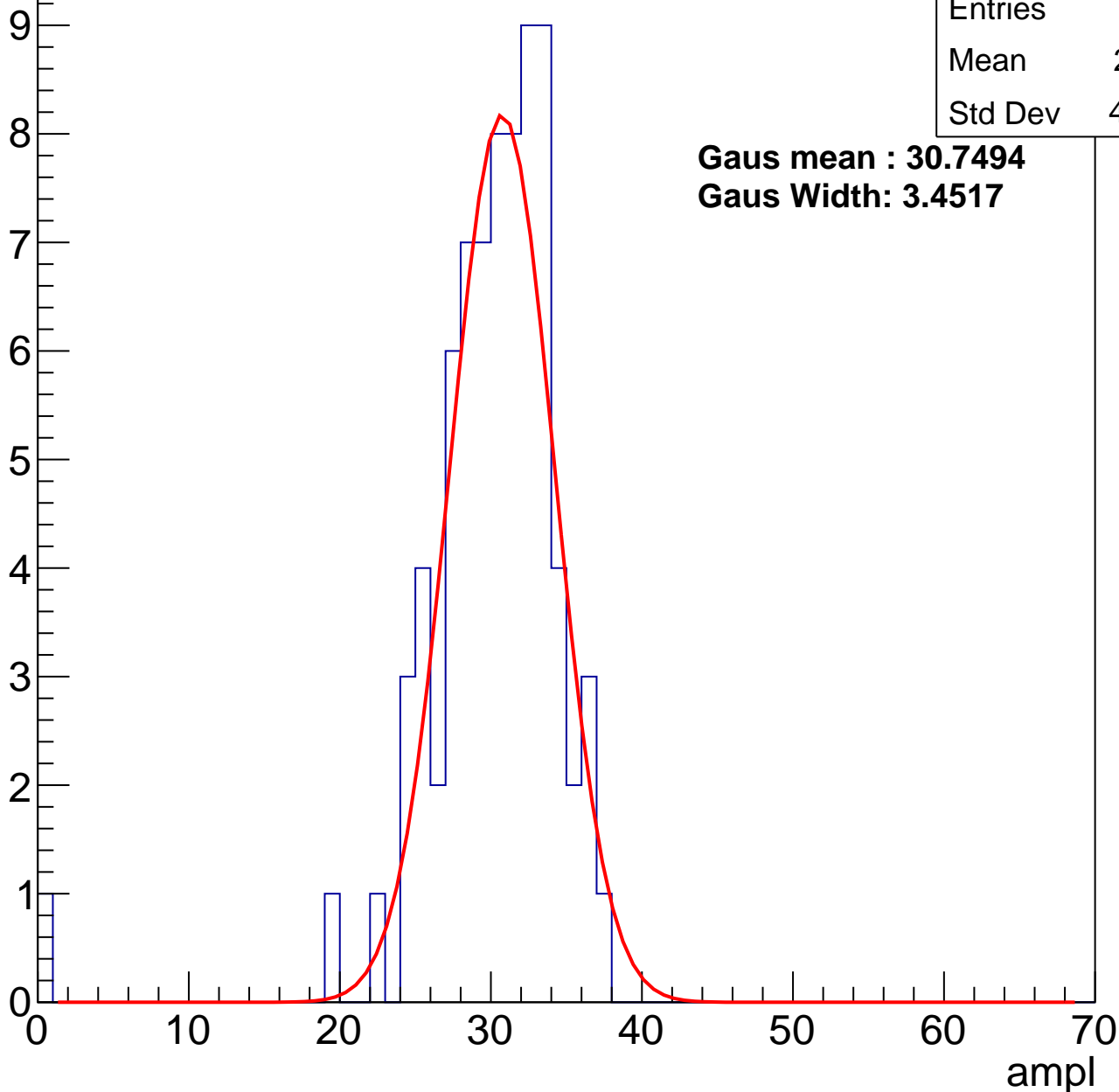
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.61 |
| Std Dev | 4.864 |

Gaus mean : 30.7494

Gaus Width: 3.4517



B0L001S, U7-ch18, adc1

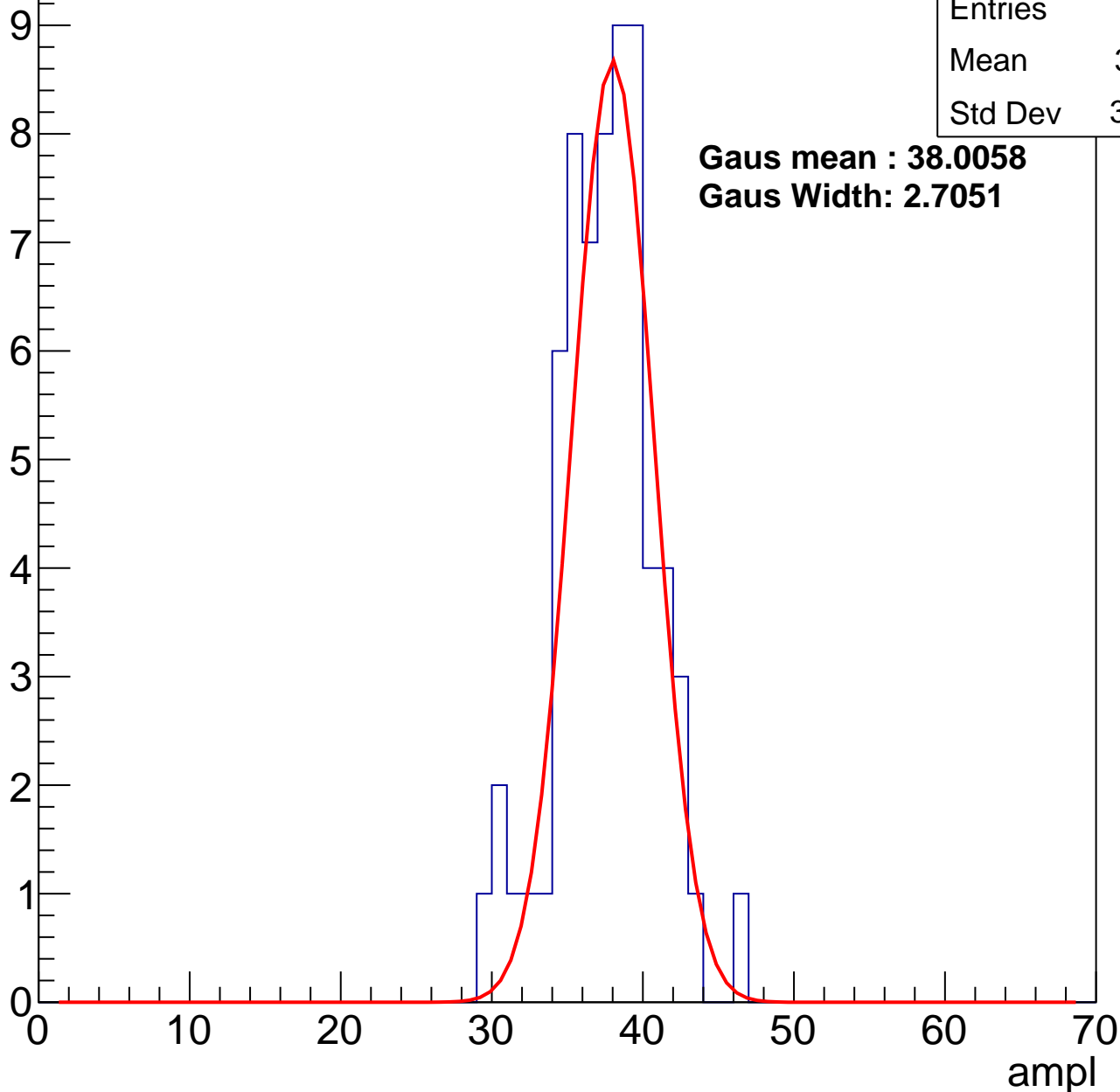
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 37.11 |
| Std Dev | 3.182 |

Gaus mean : 38.0058

Gaus Width: 2.7051



B0L001S, U7-ch18, adc2

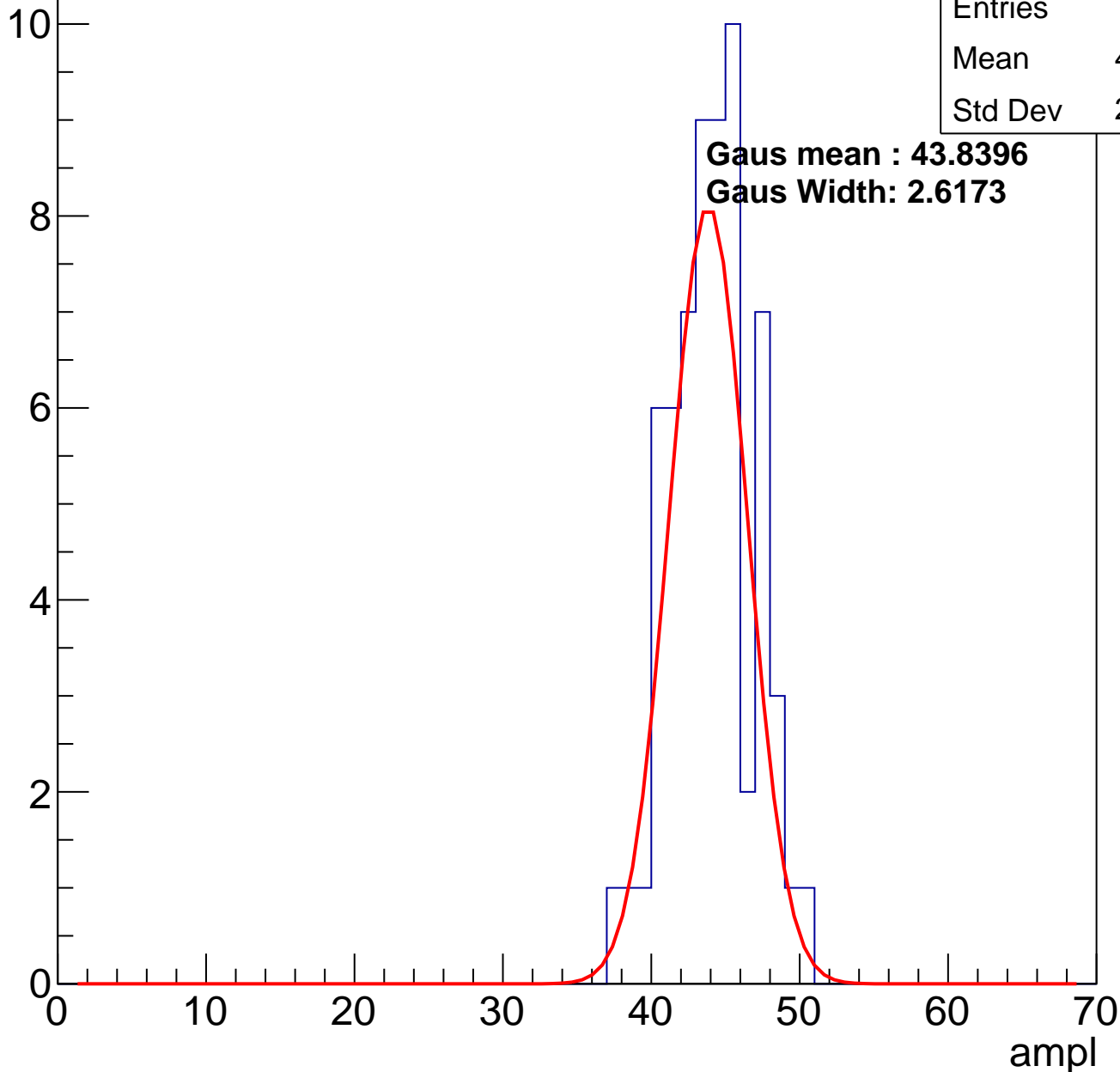
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.61 |
| Std Dev | 2.731 |

Gaus mean : 43.8396

Gaus Width: 2.6173

Entry

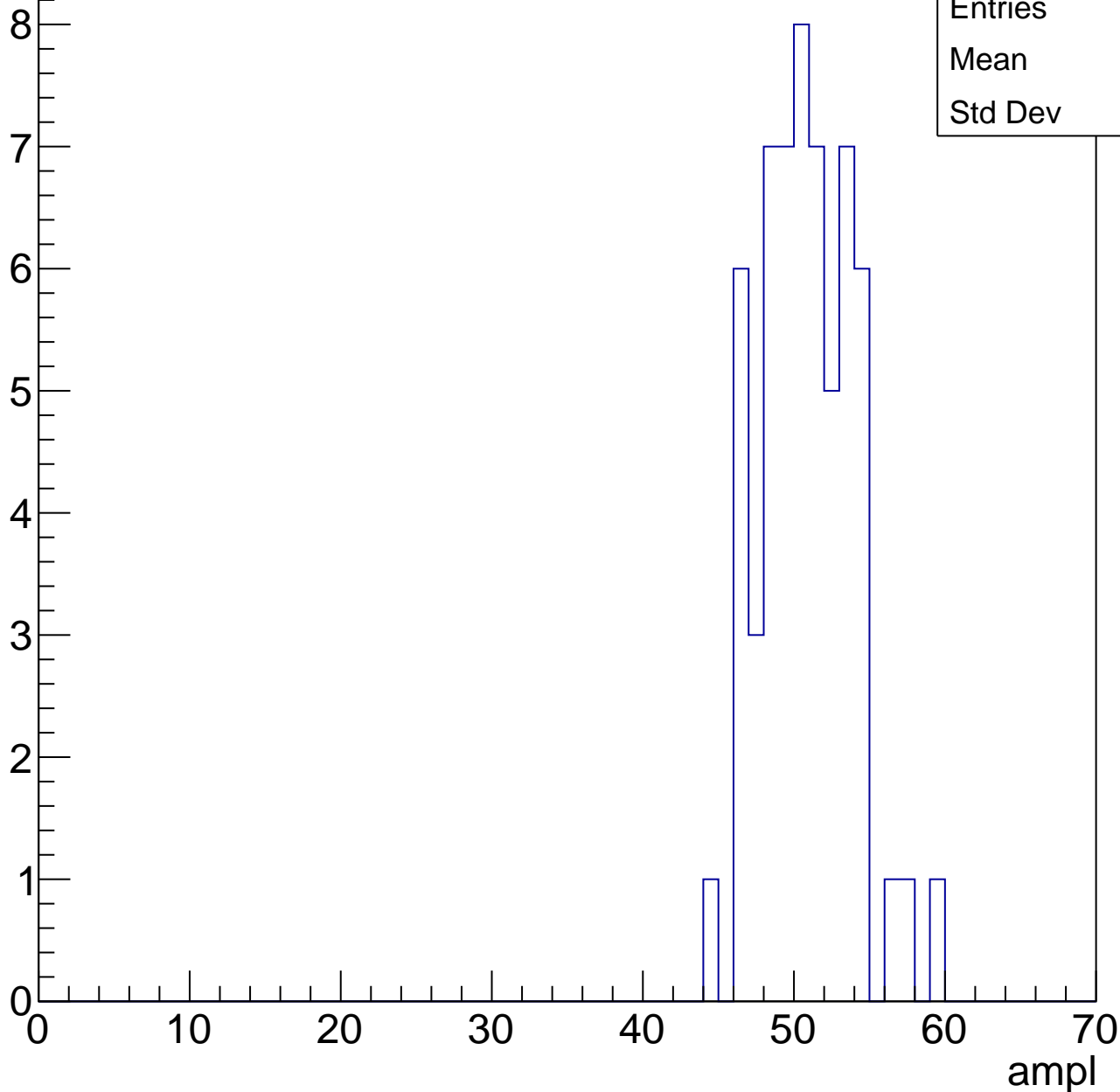


B0L001S, U7-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 60 |
| Mean | 50.4 |
| Std Dev | 2.99 |

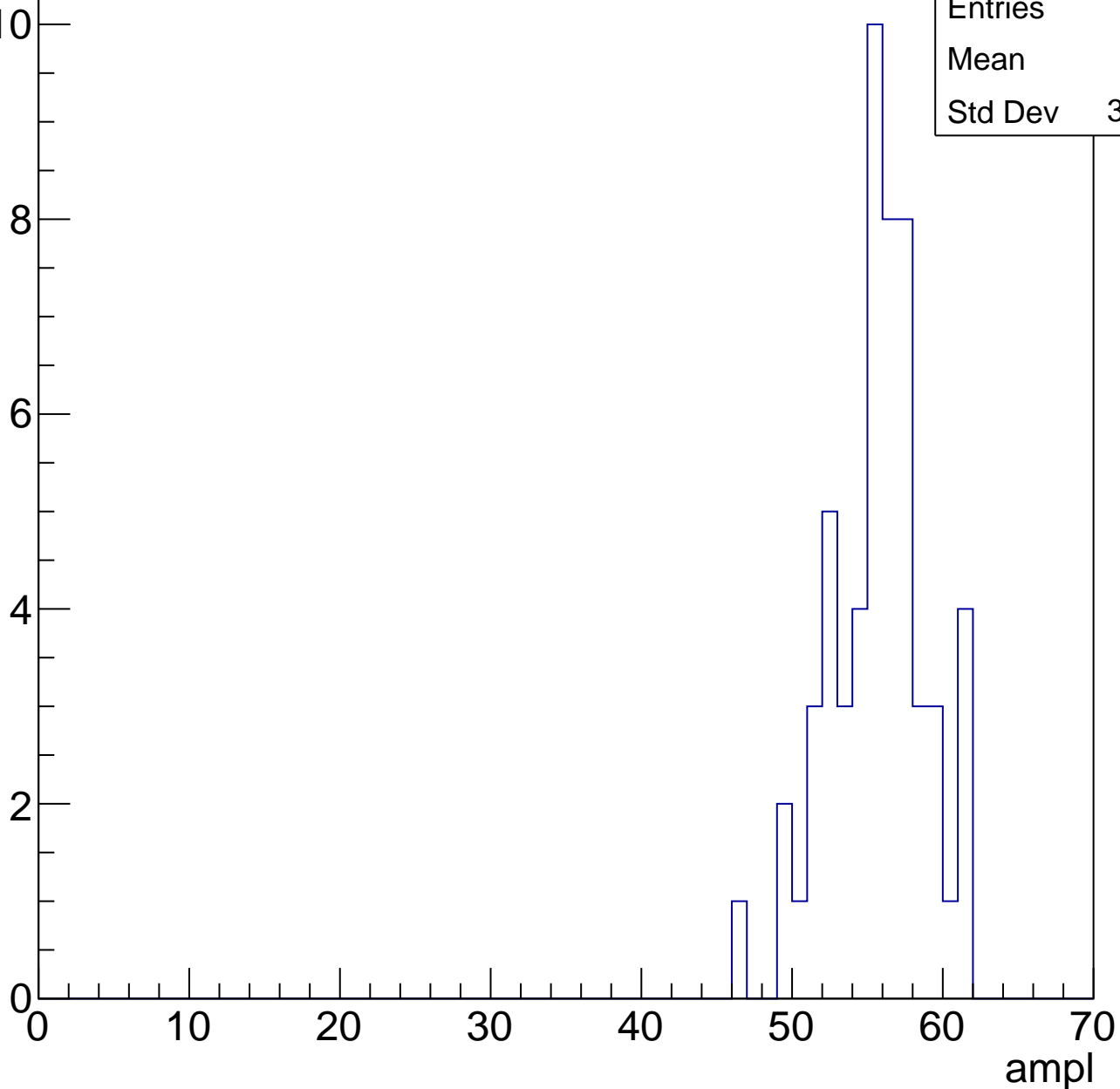


B0L001S, U7-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

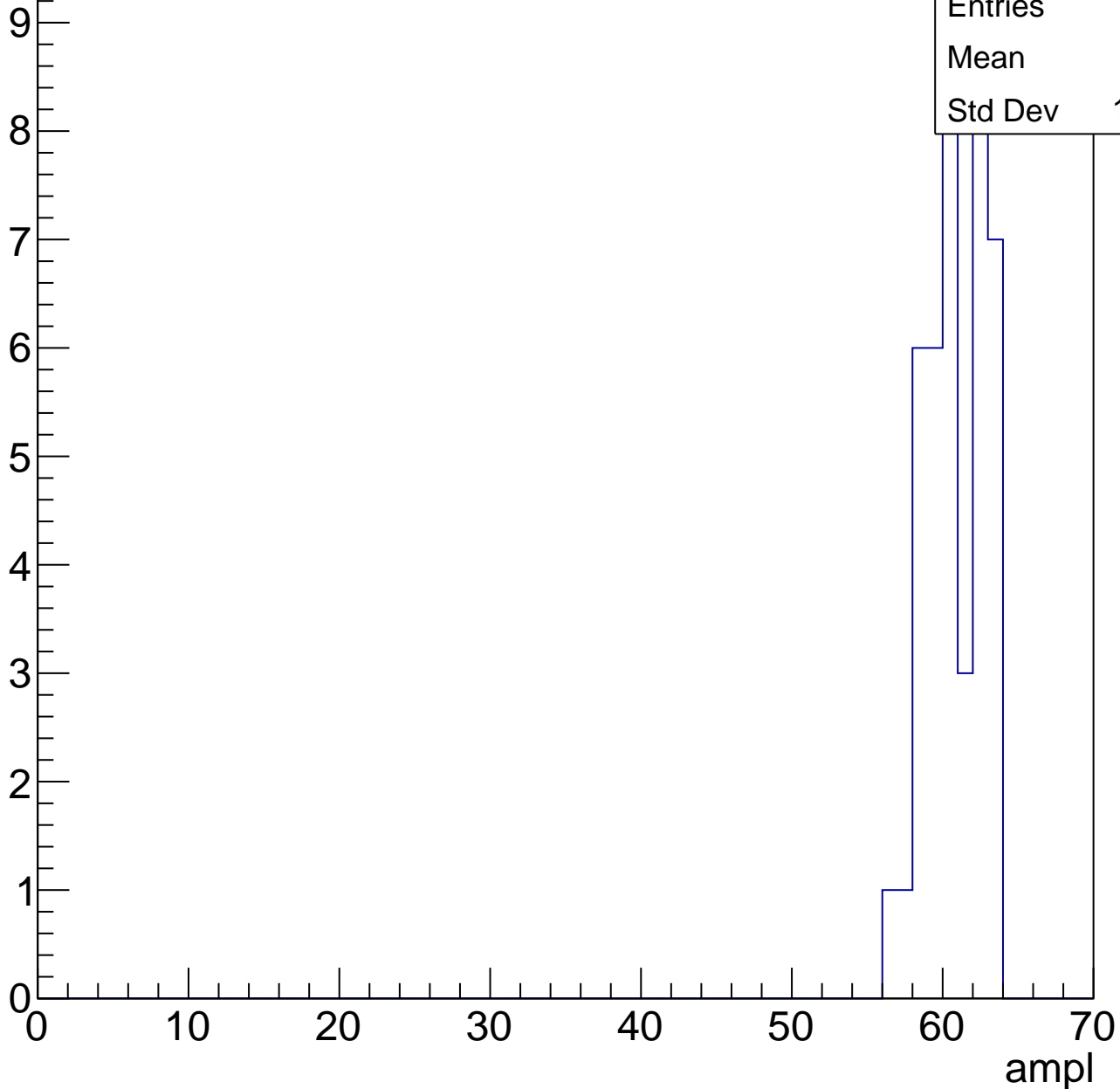
| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.2 |
| Std Dev | 3.187 |



B0L001S, U7-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

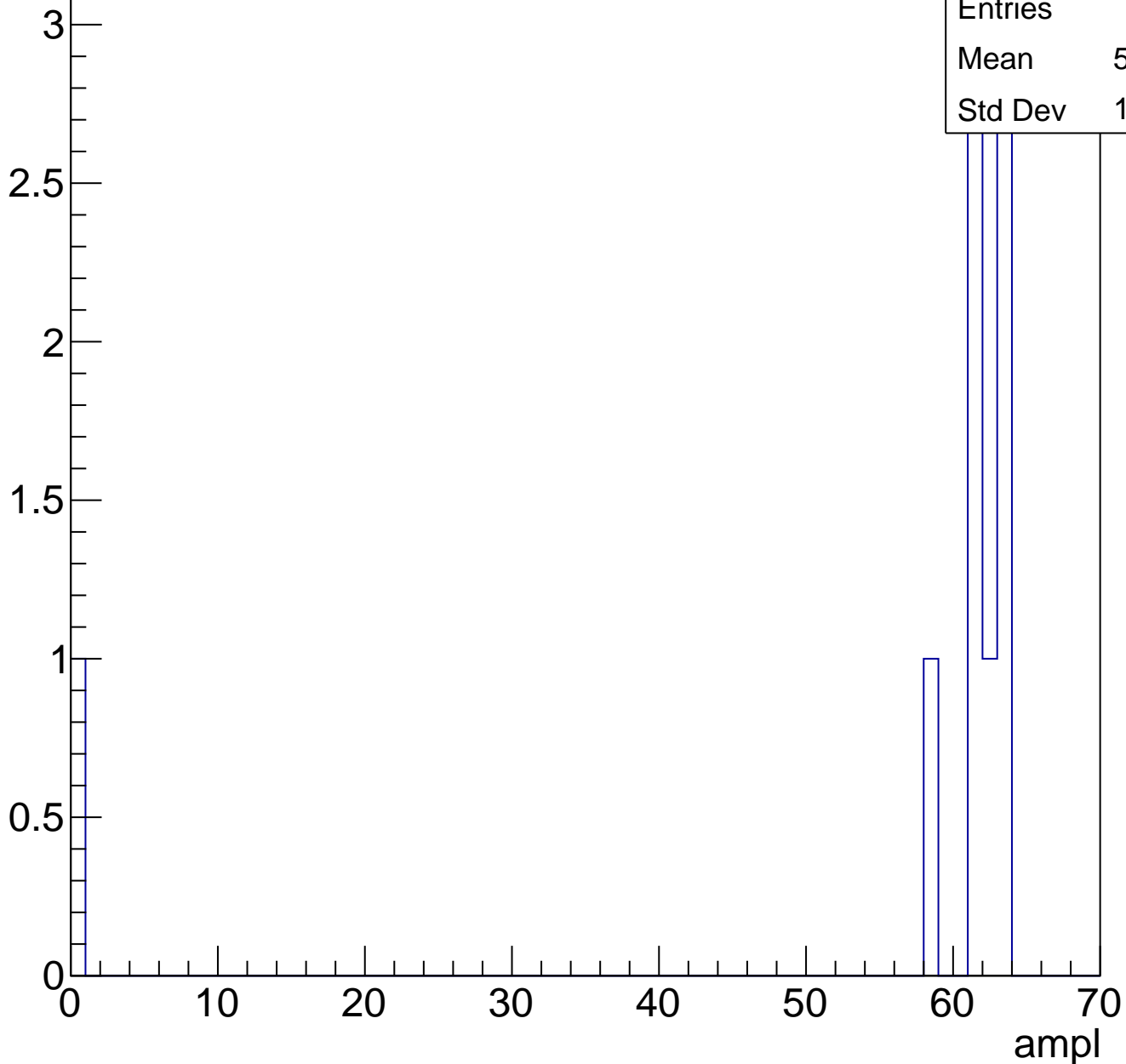
Entry



B0L001S, U7-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch19, adc0

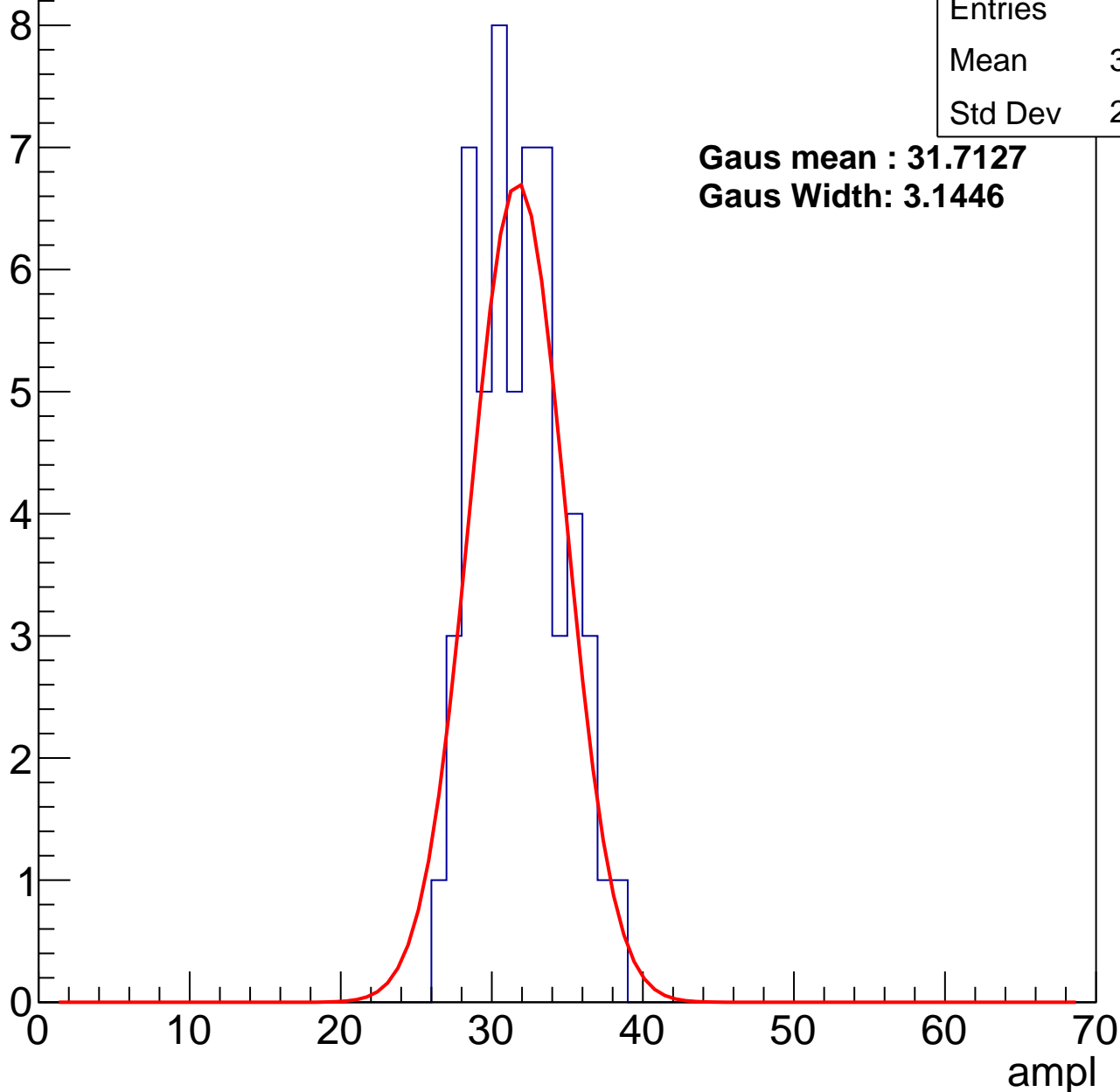
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 31.33 |
| Std Dev | 2.842 |

Gaus mean : 31.7127

Gaus Width: 3.1446



B0L001S, U7-ch19, adc1

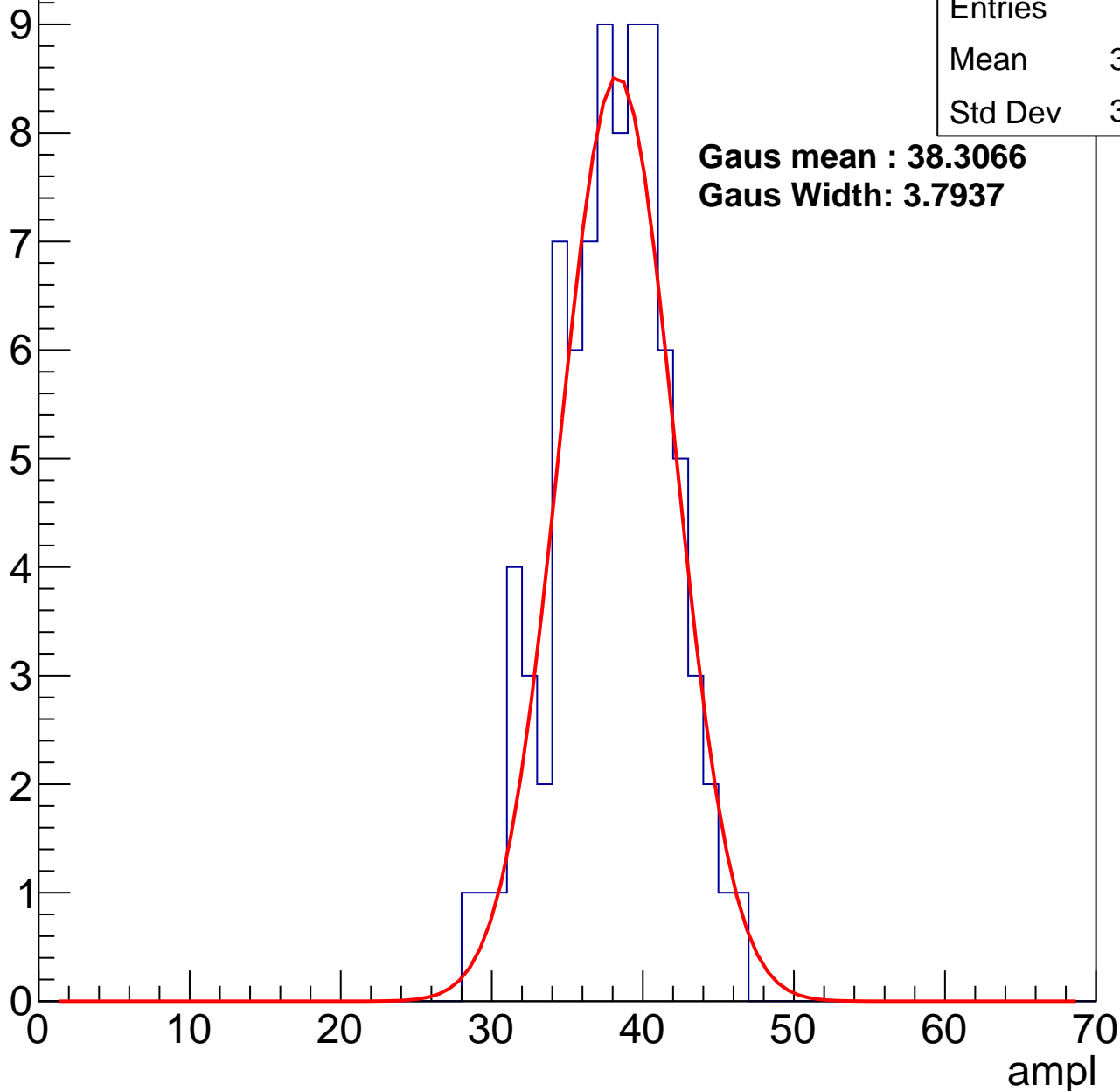
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 37.47 |
| Std Dev | 3.784 |

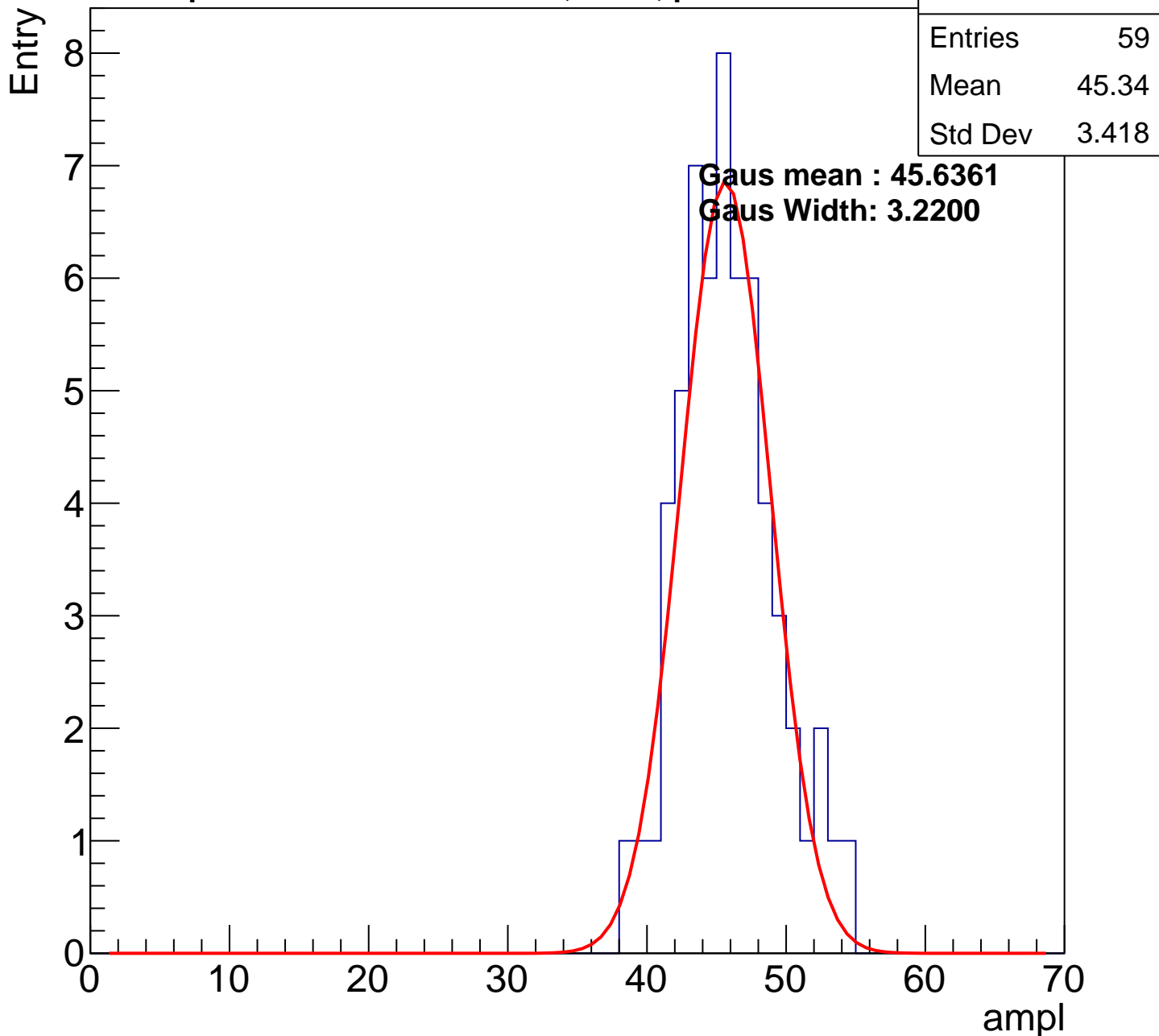
Gaus mean : 38.3066

Gaus Width: 3.7937



B0L001S, U7-ch19, adc2

calib_packv5_042523_0143.root, FC#9, port A1

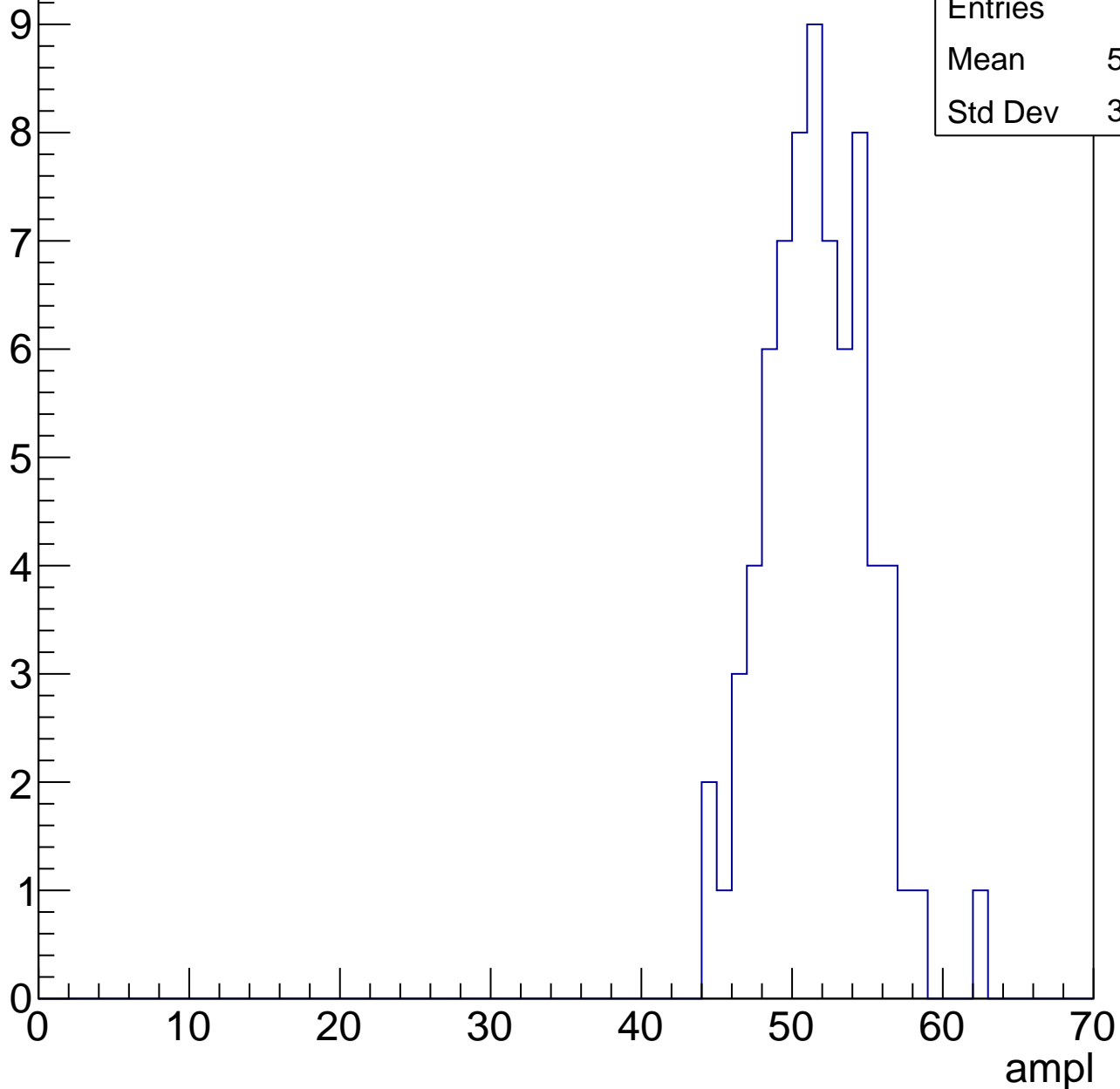


B0L001S, U7-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 51.17 |
| Std Dev | 3.403 |



B0L001S, U7-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

0

10

20

30

40

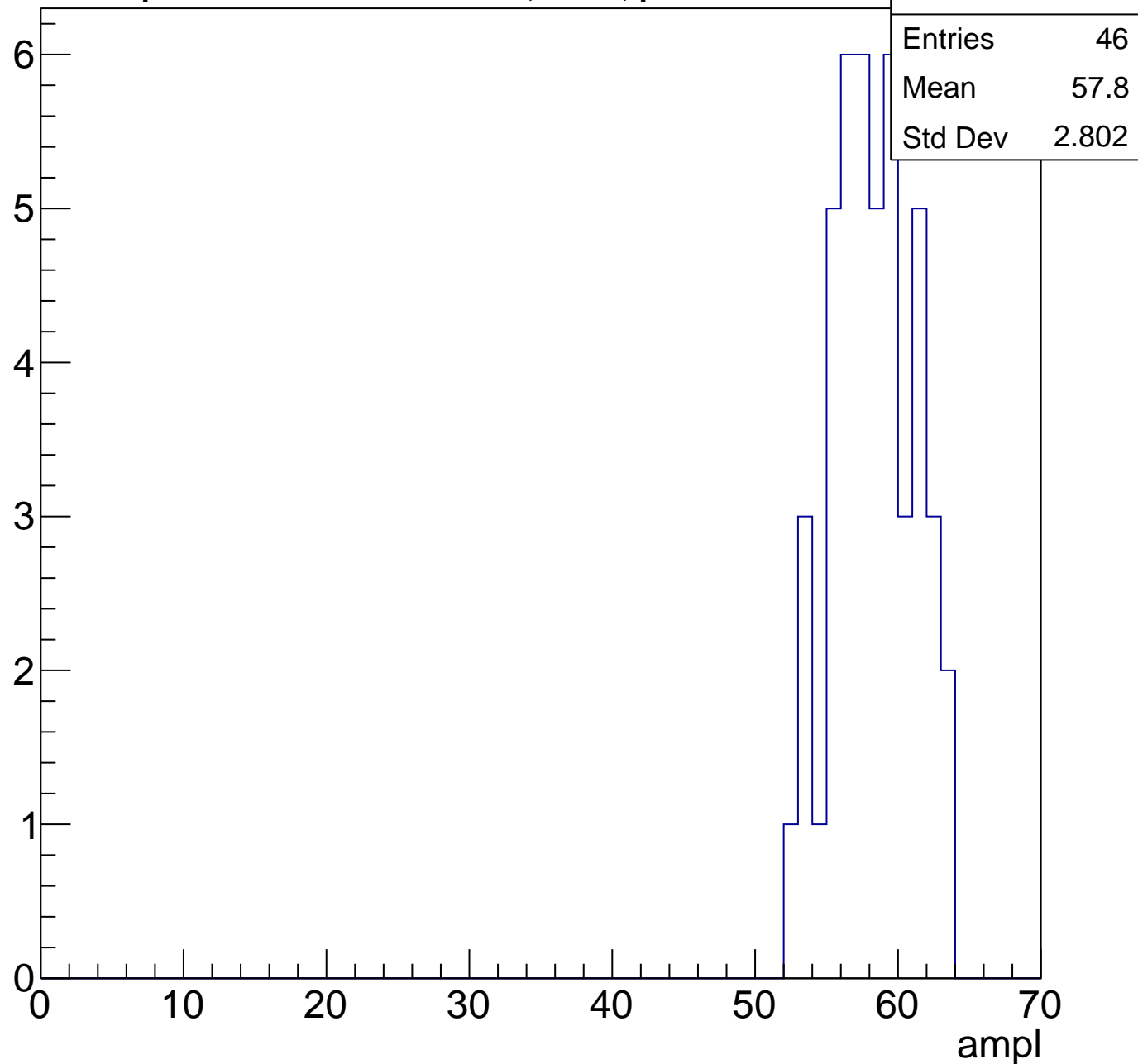
50

60

70

ampl

| | |
|---------|-------|
| Entries | 46 |
| Mean | 57.8 |
| Std Dev | 2.802 |

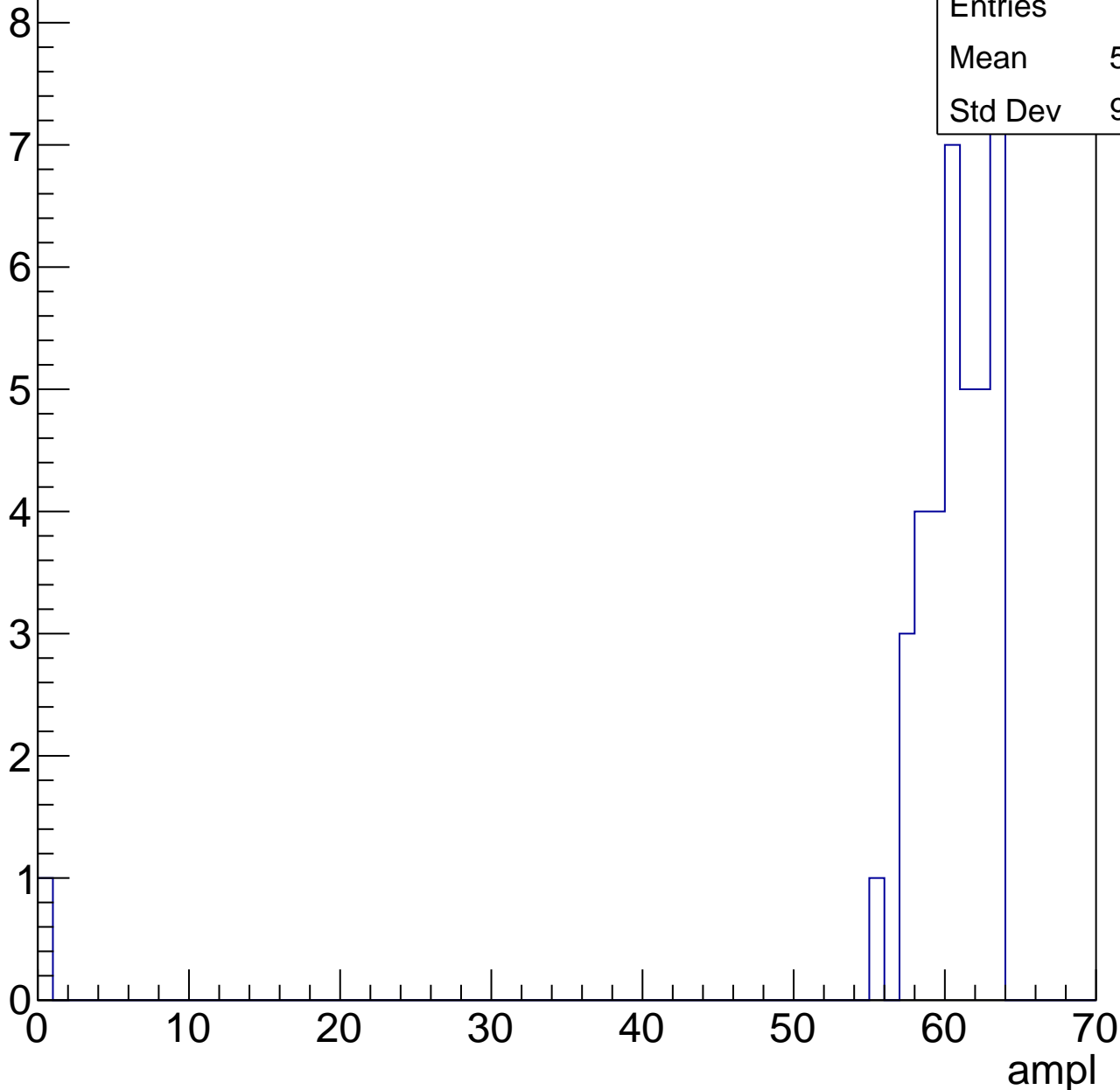


B0L001S, U7-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 58.76 |
| Std Dev | 9.882 |



B0L001S, U7-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch20, adc0

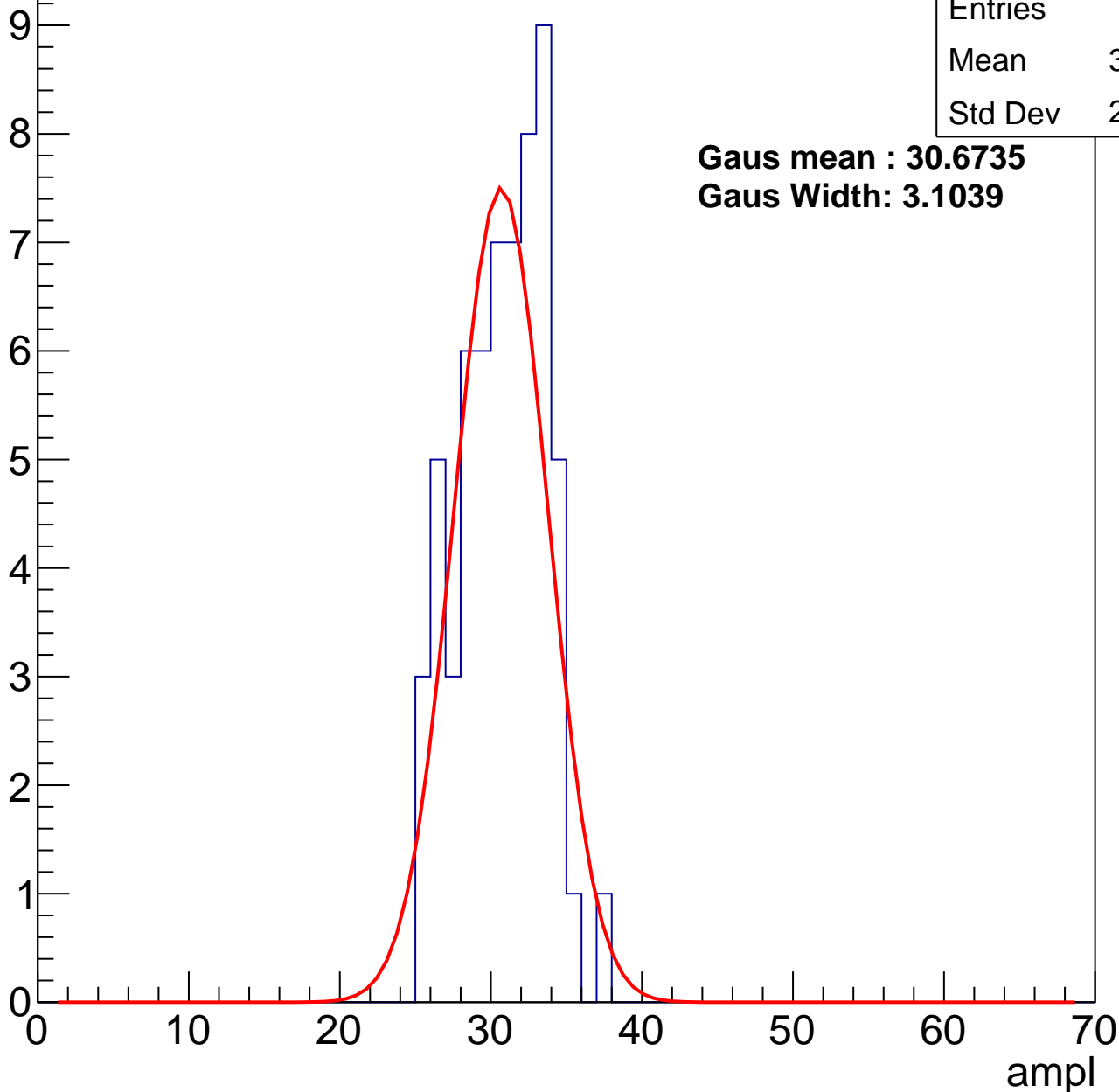
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 30.33 |
| Std Dev | 2.804 |

Gaus mean : 30.6735

Gaus Width: 3.1039



B0L001S, U7-ch20, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 37.42 |
| Std Dev | 3.282 |

Gaus mean : 38.1367

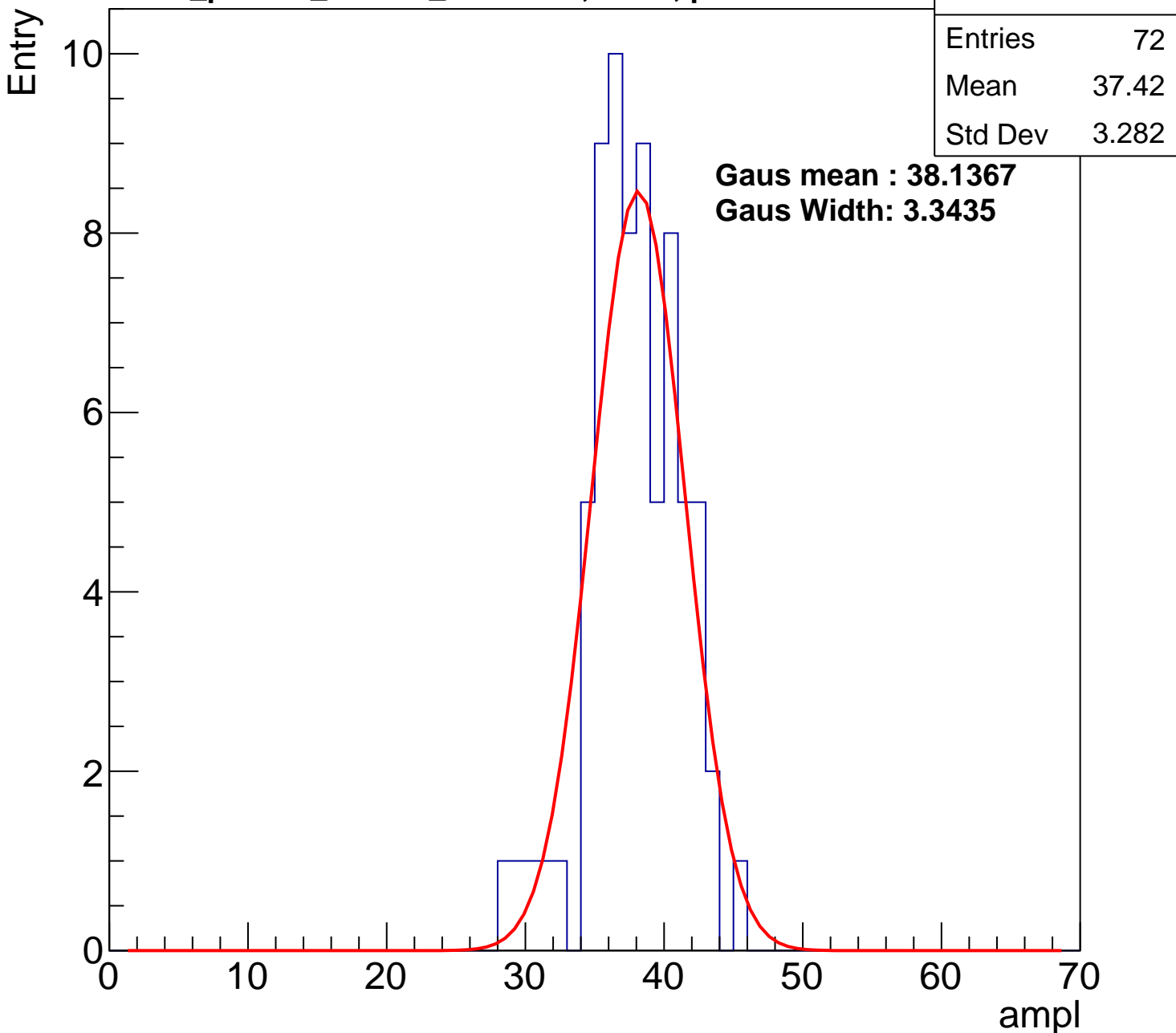
Gaus Width: 3.3435

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch20, adc2

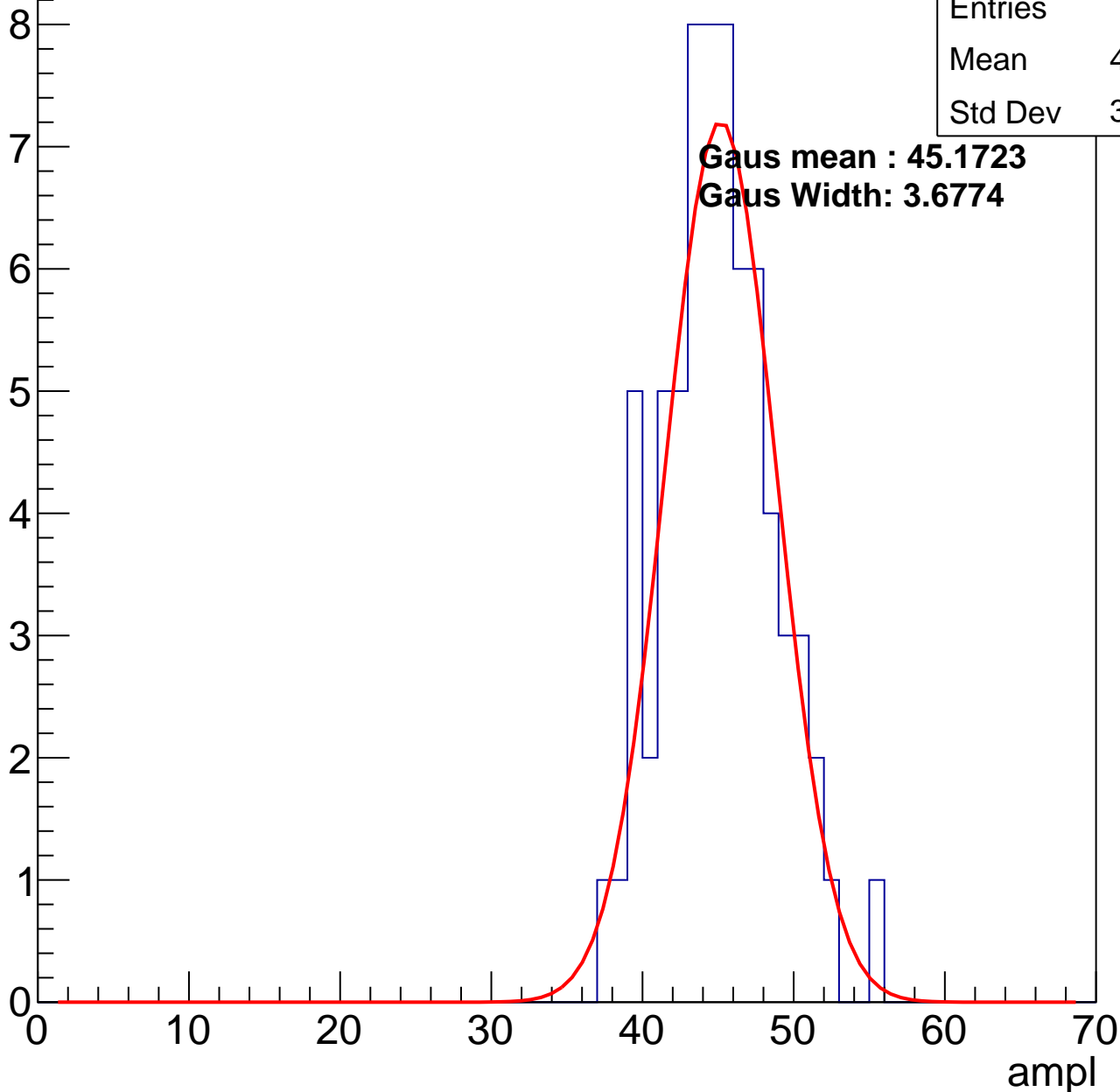
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 44.59 |
| Std Dev | 3.625 |

Gaus mean : 45.1723

Gaus Width: 3.6774

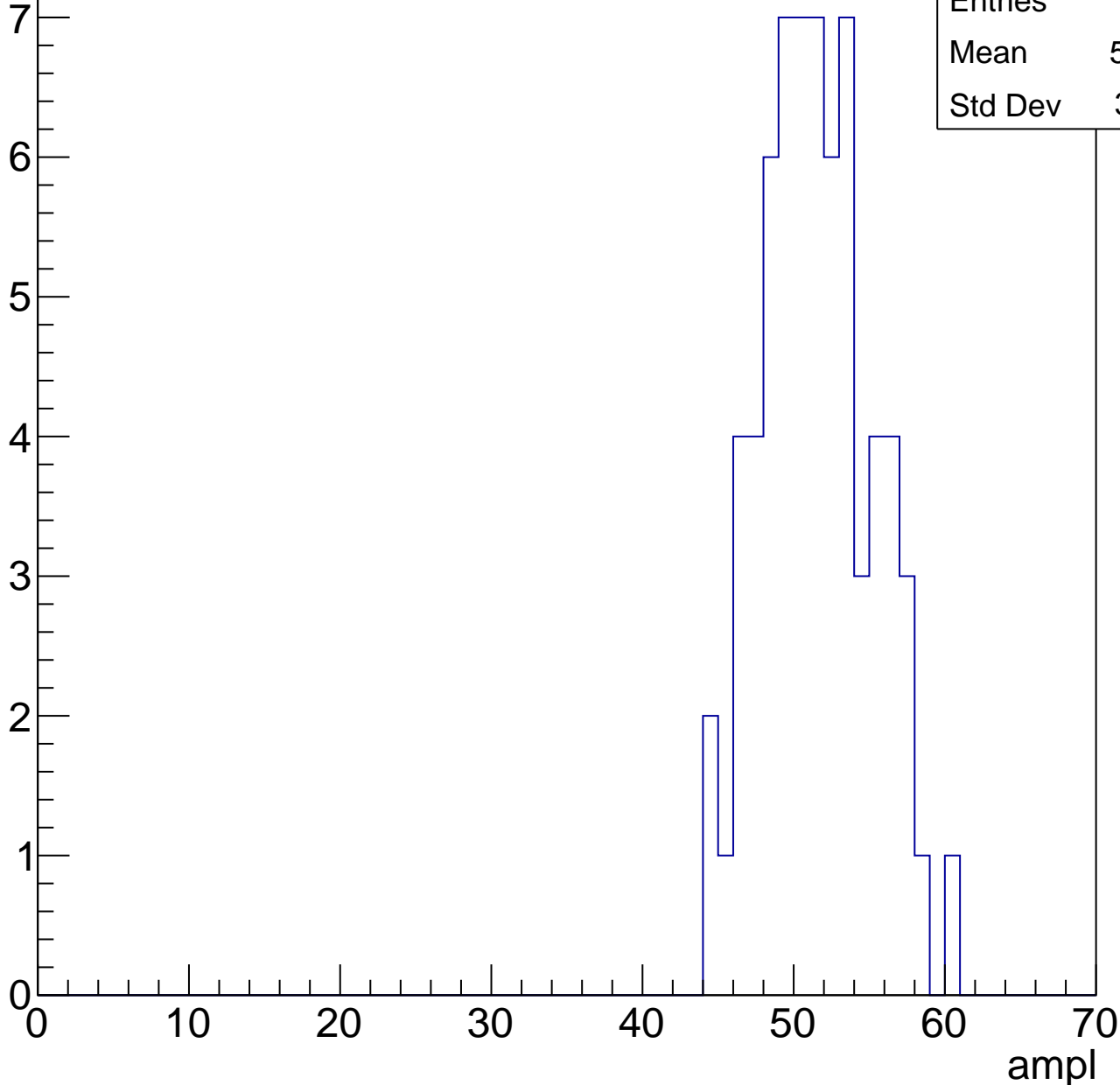


B0L001S, U7-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 51.06 |
| Std Dev | 3.561 |

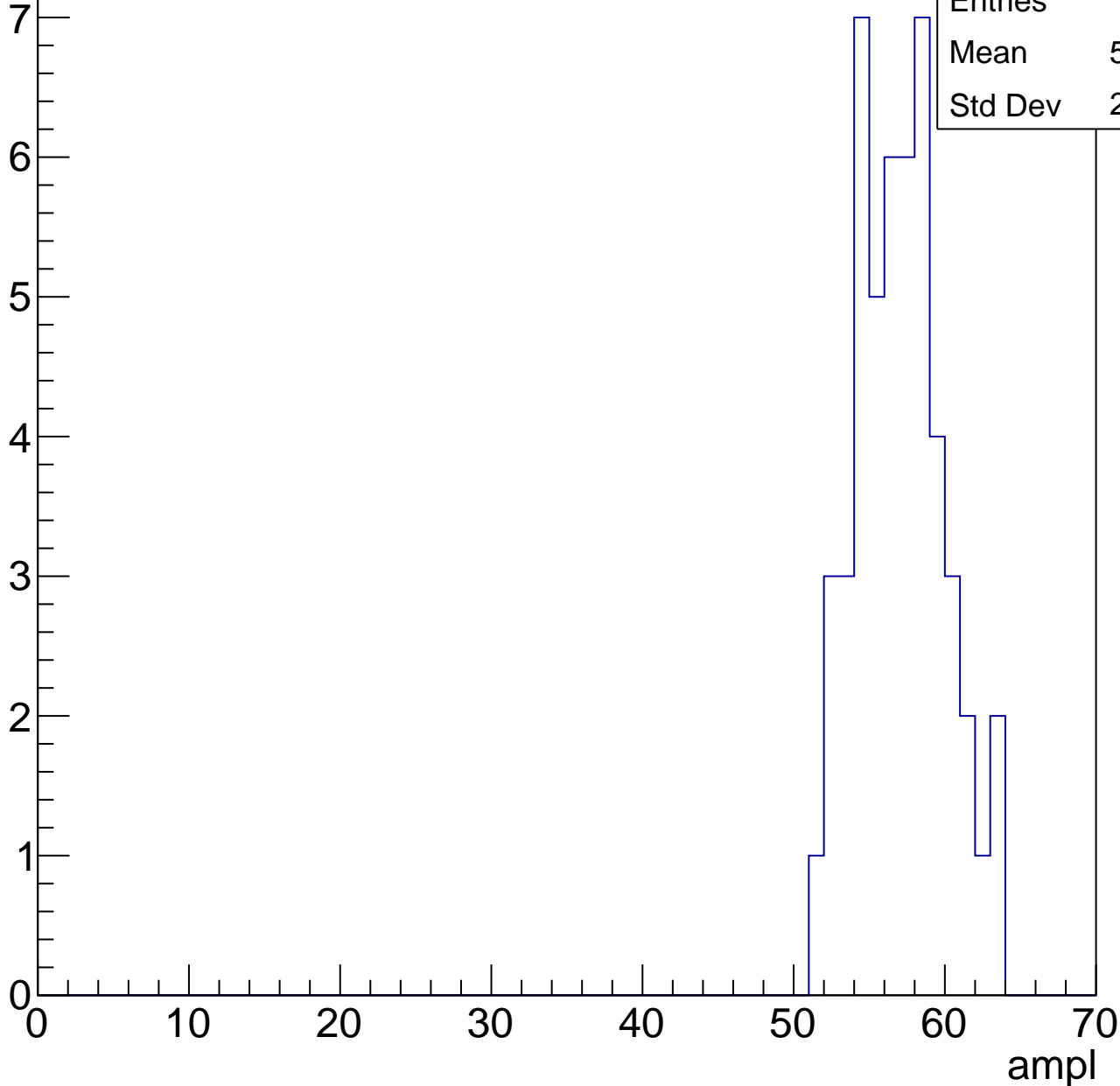


B0L001S, U7-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 56.58 |
| Std Dev | 2.892 |



B0L001S, U7-ch20, adc5

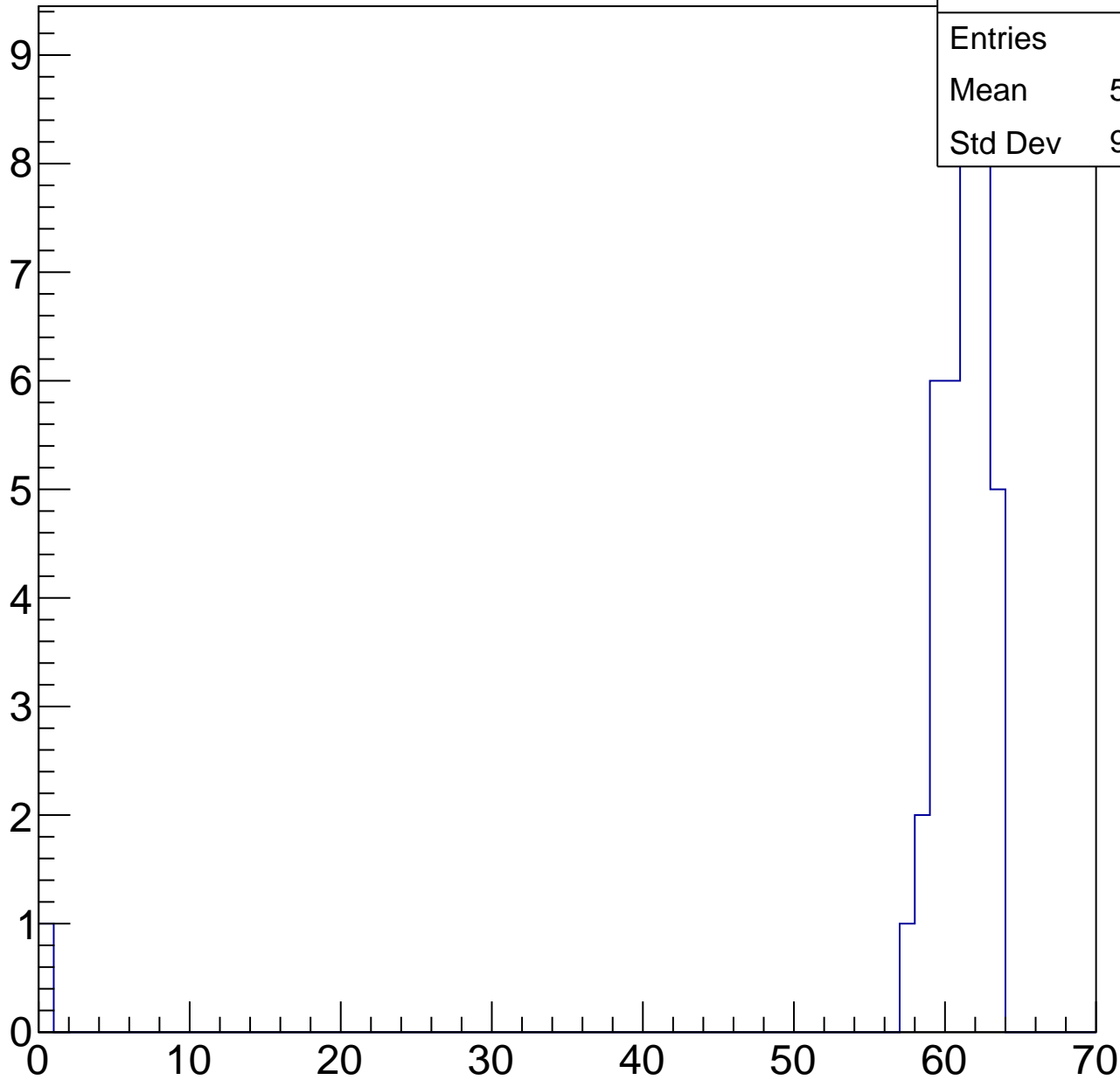
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 38 |
| Mean | 59.16 |
| Std Dev | 9.848 |

ampl



B0L001S, U7-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch21, adc0

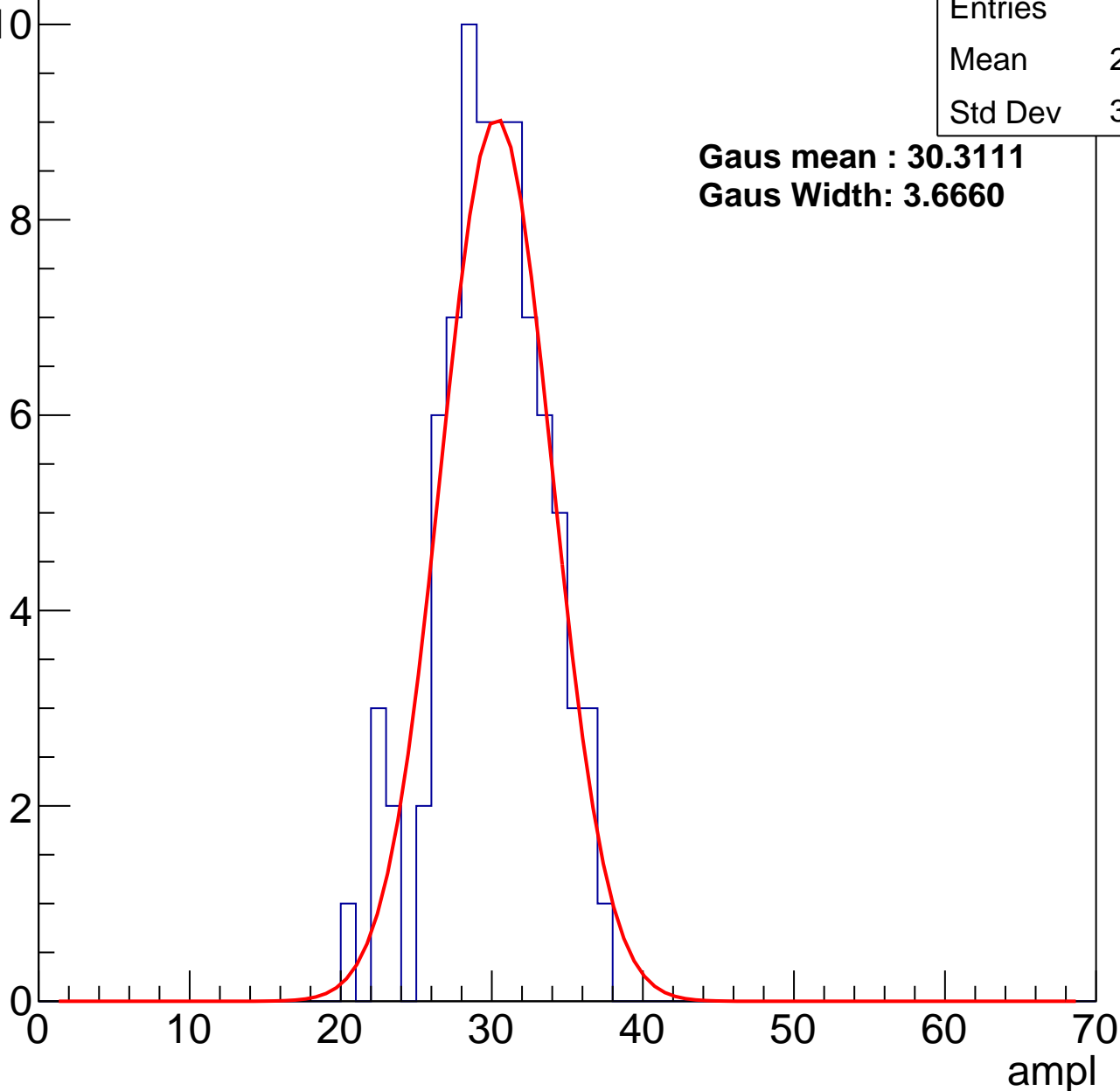
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 29.63 |
| Std Dev | 3.539 |

Gaus mean : 30.3111

Gaus Width: 3.6660



B0L001S, U7-ch21, adc1

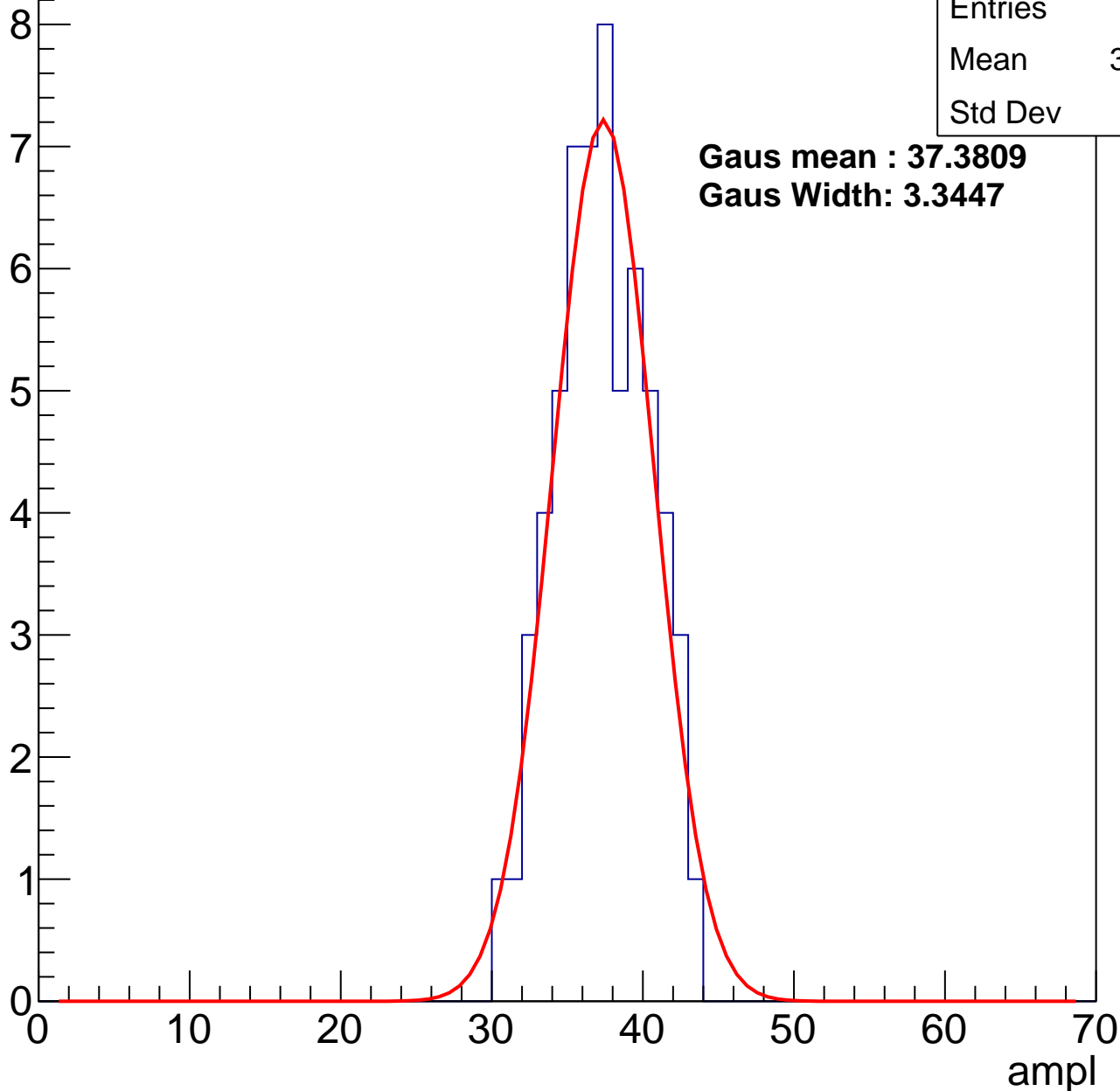
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 36.82 |
| Std Dev | 3.03 |

Gaus mean : 37.3809

Gaus Width: 3.3447



B0L001S, U7-ch21, adc2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.78 |
| Std Dev | 3.691 |

Gaus mean : 43.6660

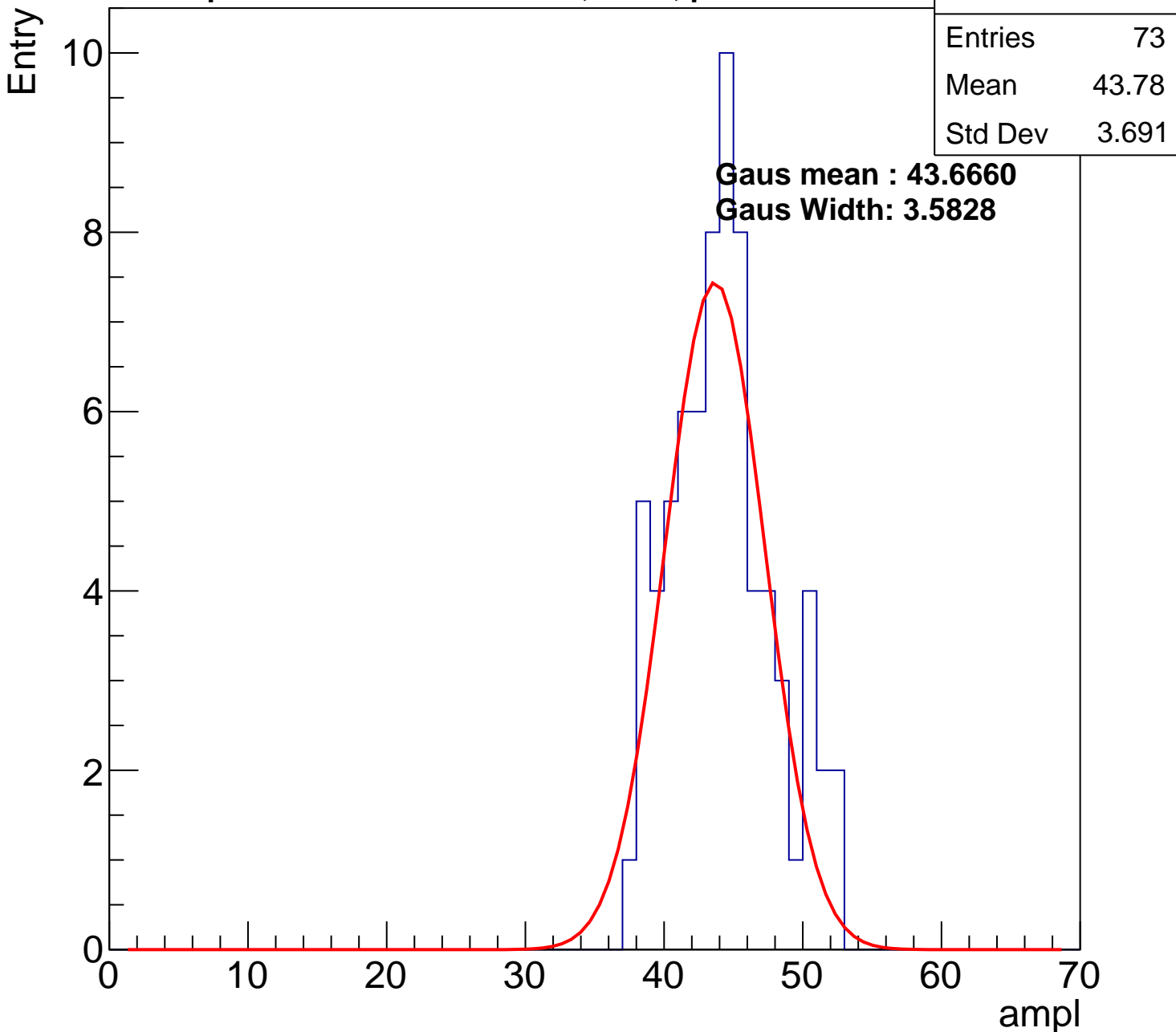
Gaus Width: 3.5828

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

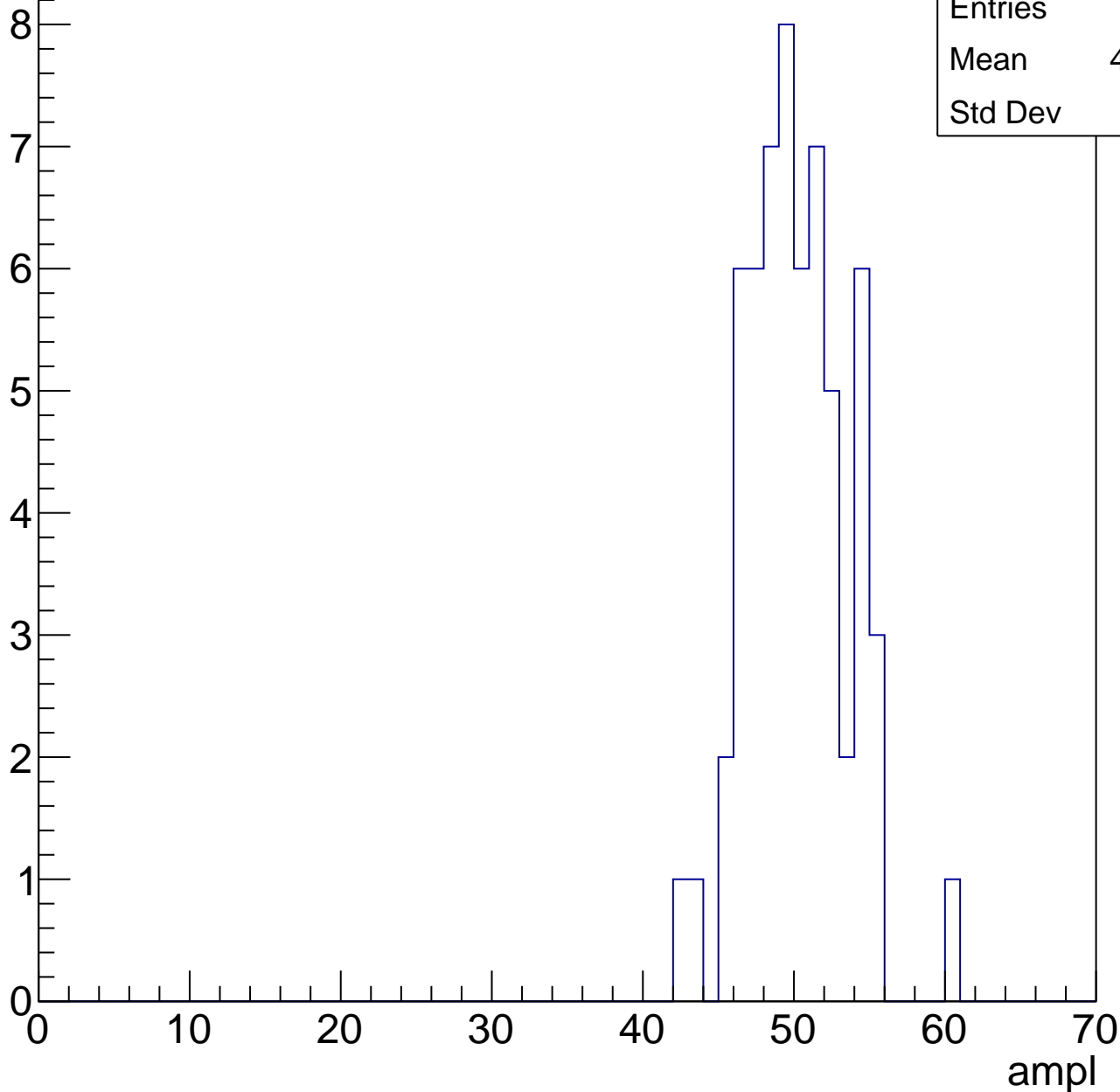


B0L001S, U7-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.72 |
| Std Dev | 3.29 |

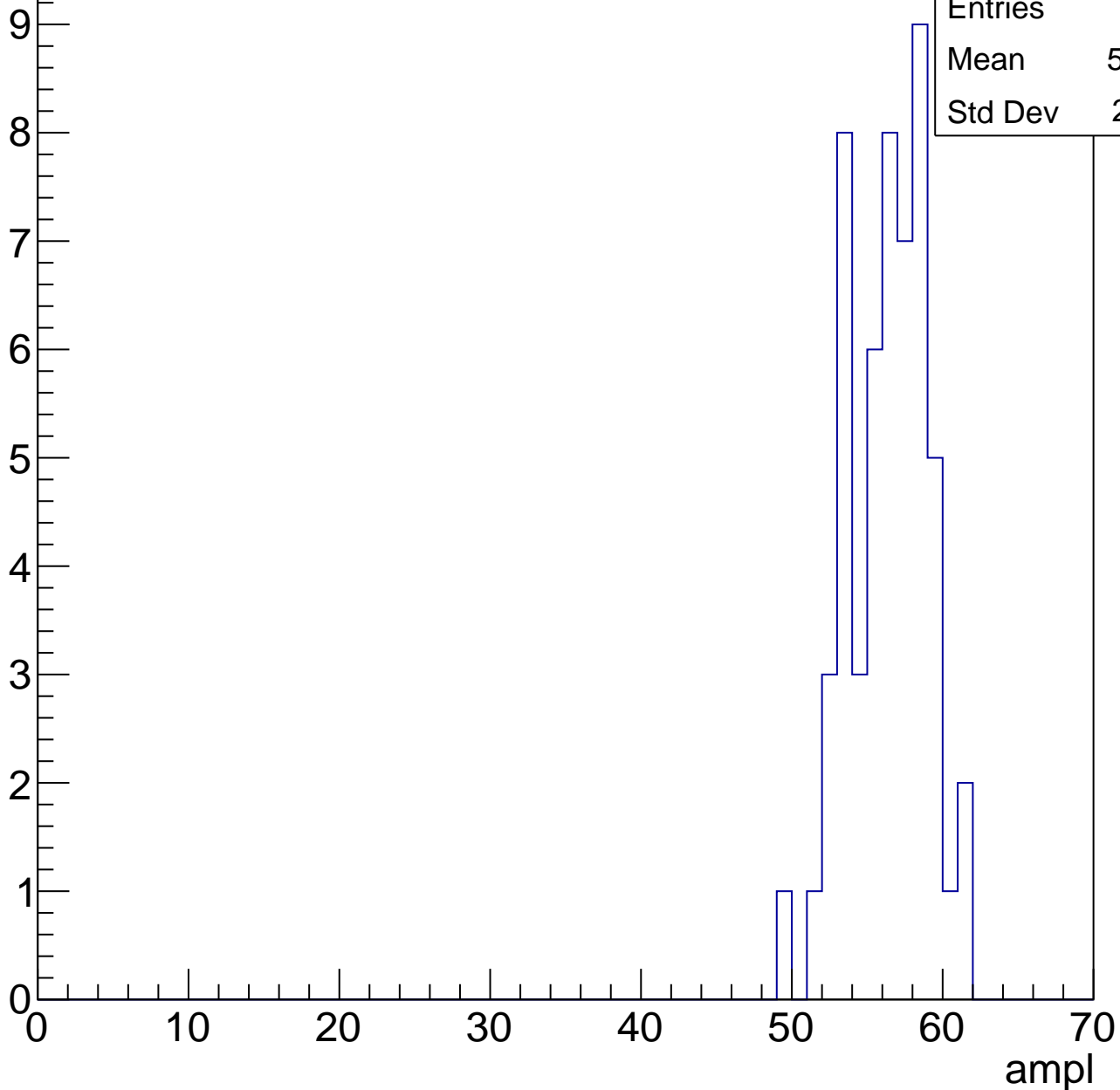


B0L001S, U7-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 55.89 |
| Std Dev | 2.601 |

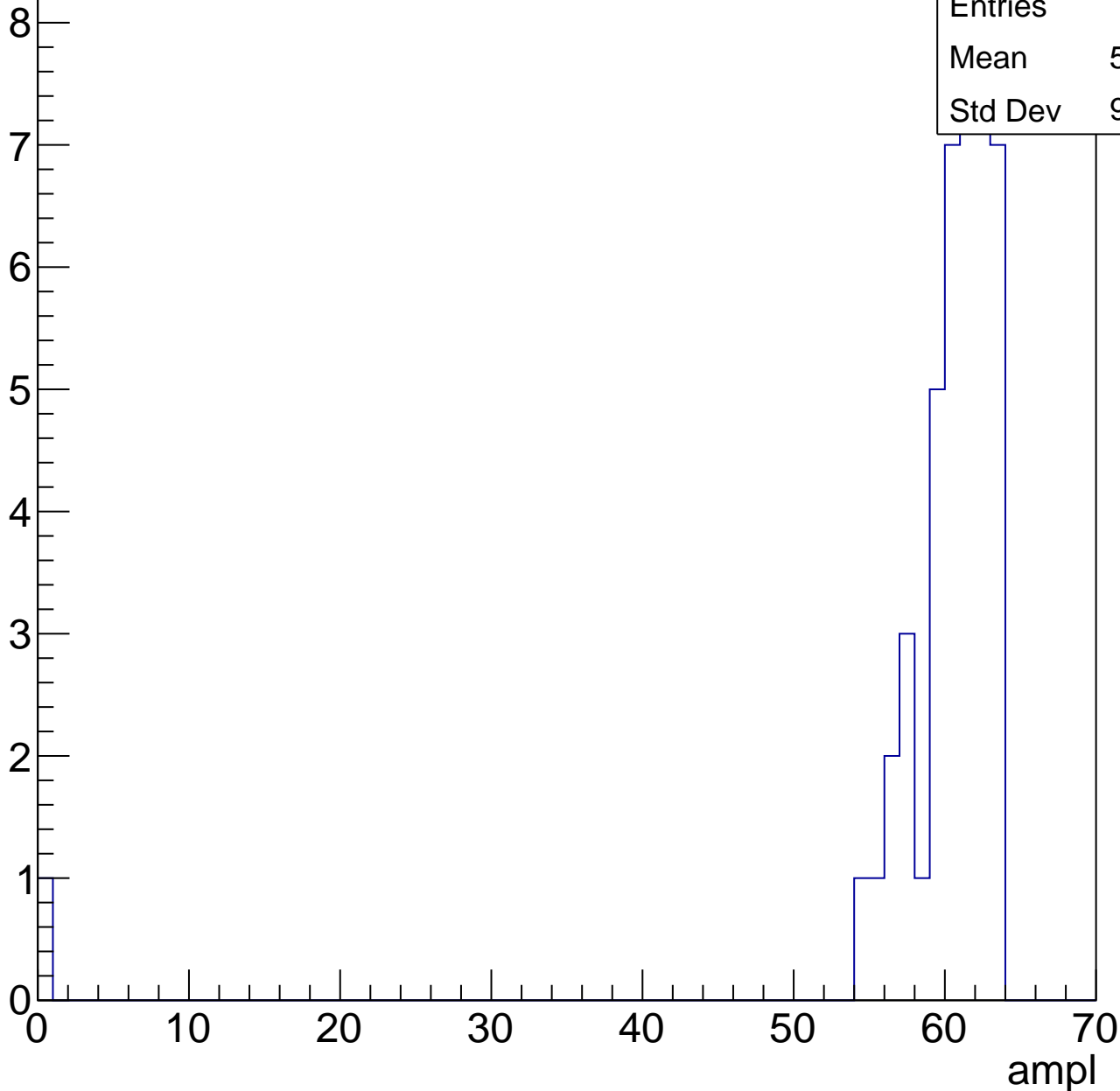


B0L001S, U7-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

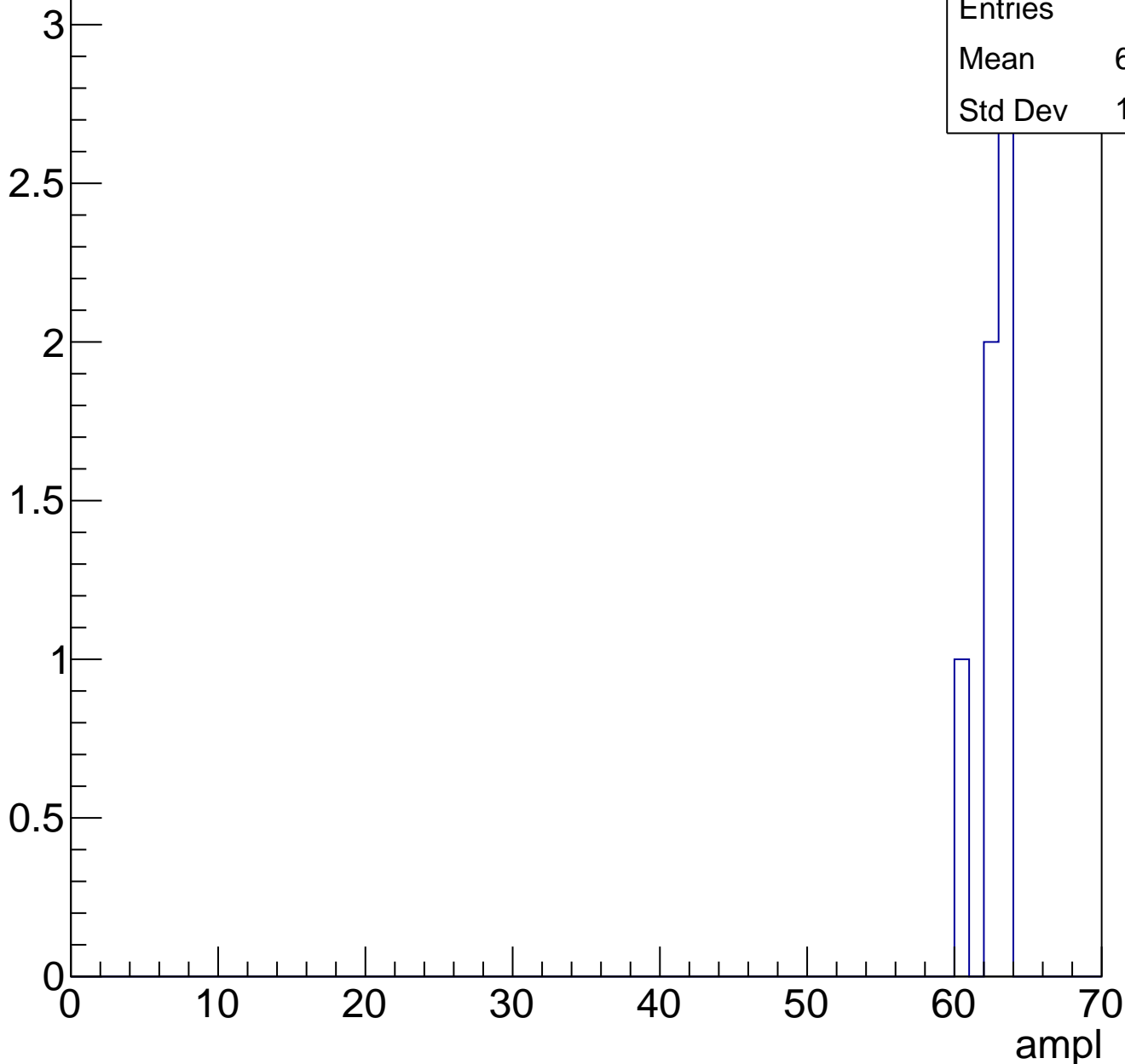
| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.86 |
| Std Dev | 9.263 |



B0L001S, U7-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch22, adc0

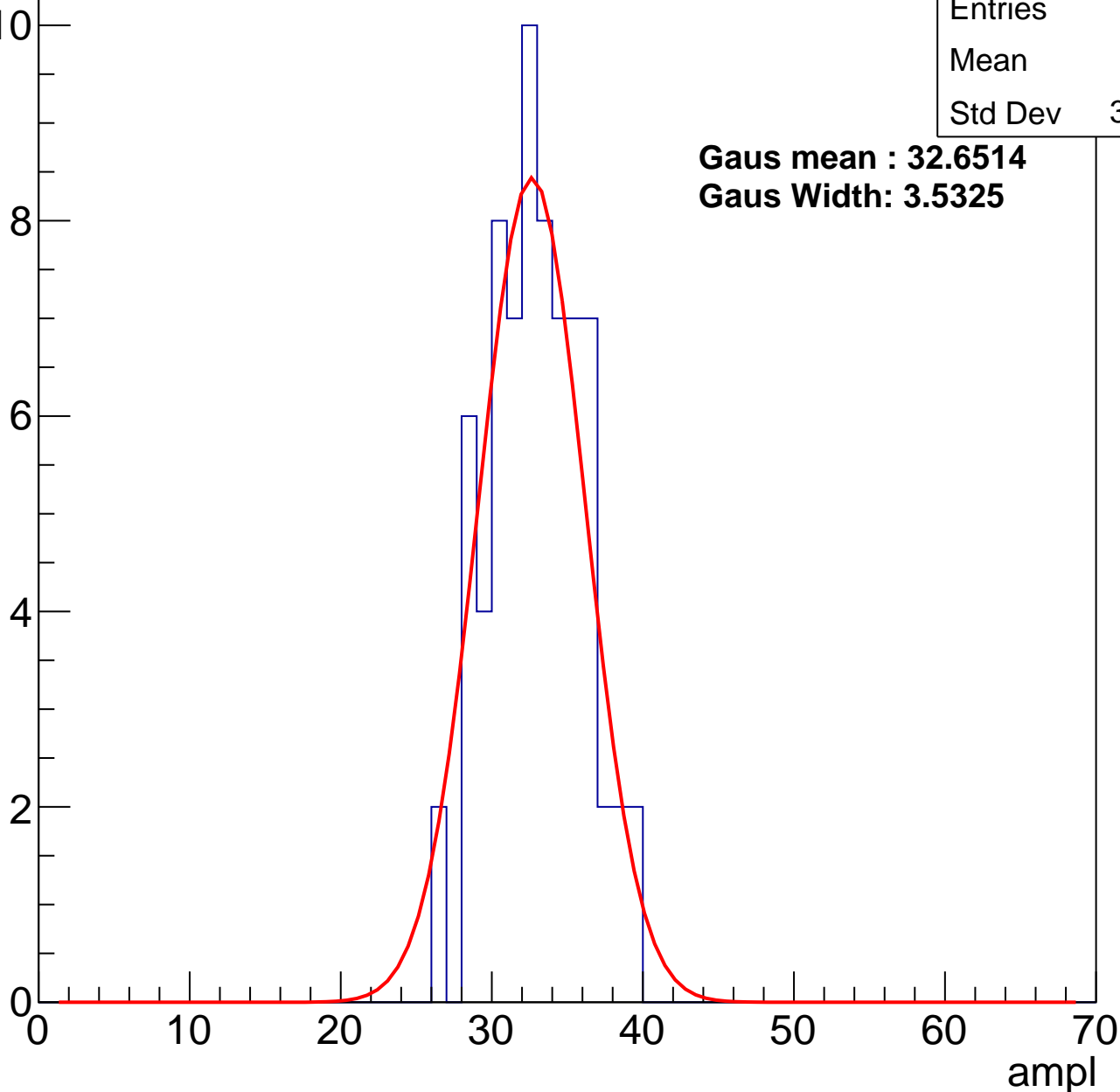
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 32.5 |
| Std Dev | 3.018 |

Gaus mean : 32.6514

Gaus Width: 3.5325



B0L001S, U7-ch22, adc1

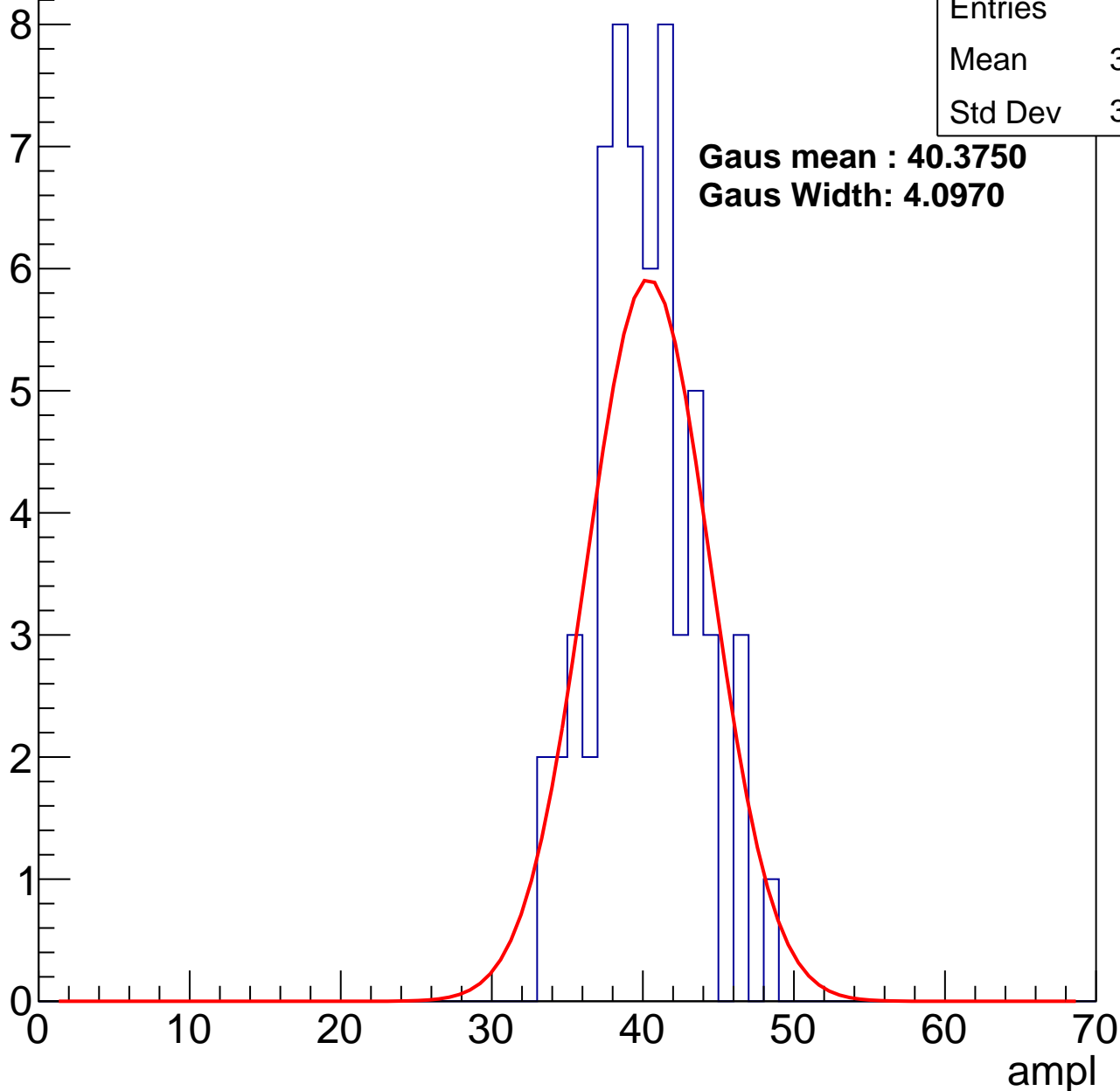
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 39.57 |
| Std Dev | 3.298 |

Gaus mean : 40.3750

Gaus Width: 4.0970



B0L001S, U7-ch22, adc2

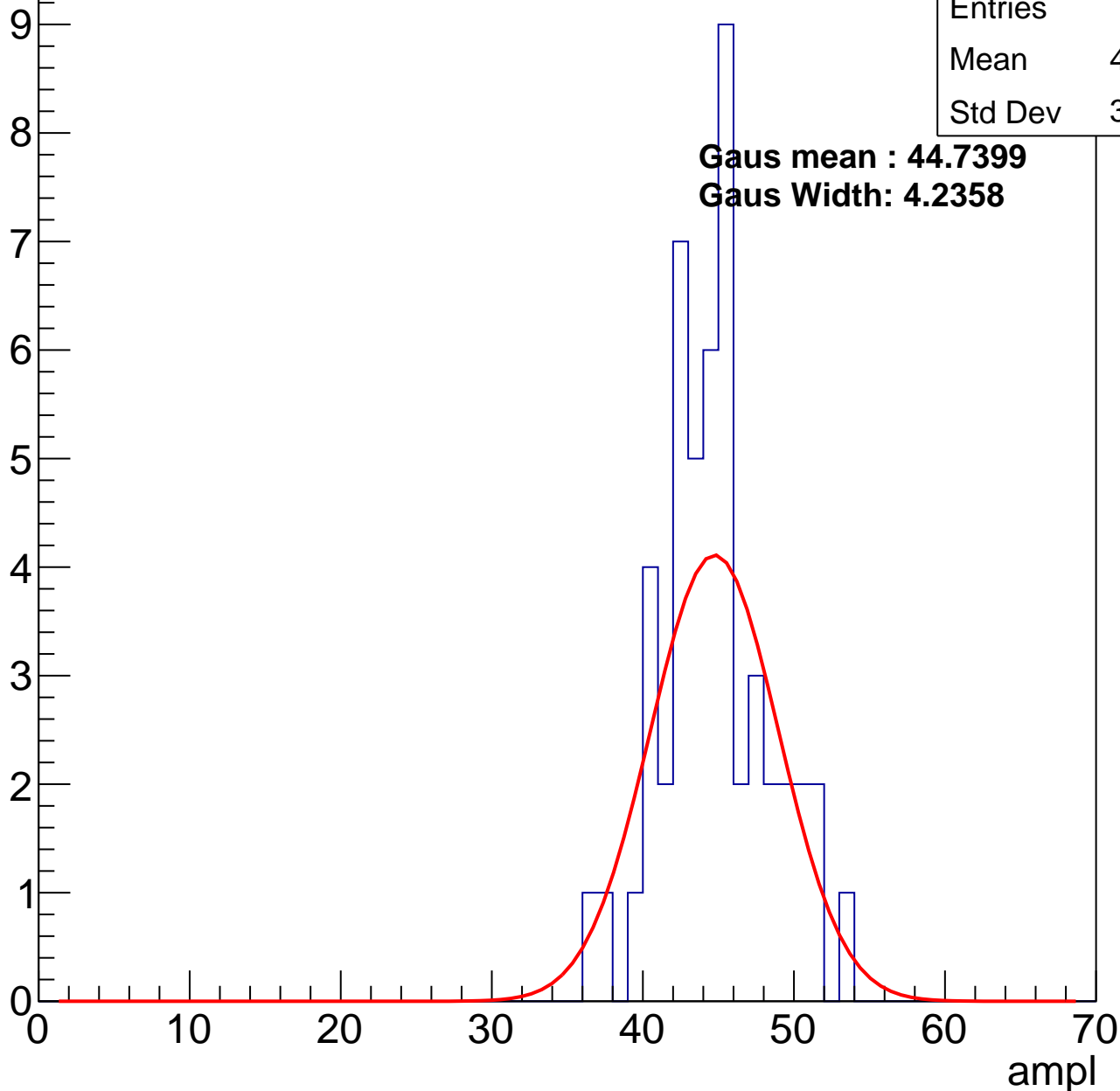
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 44.28 |
| Std Dev | 3.539 |

Gaus mean : 44.7399

Gaus Width: 4.2358

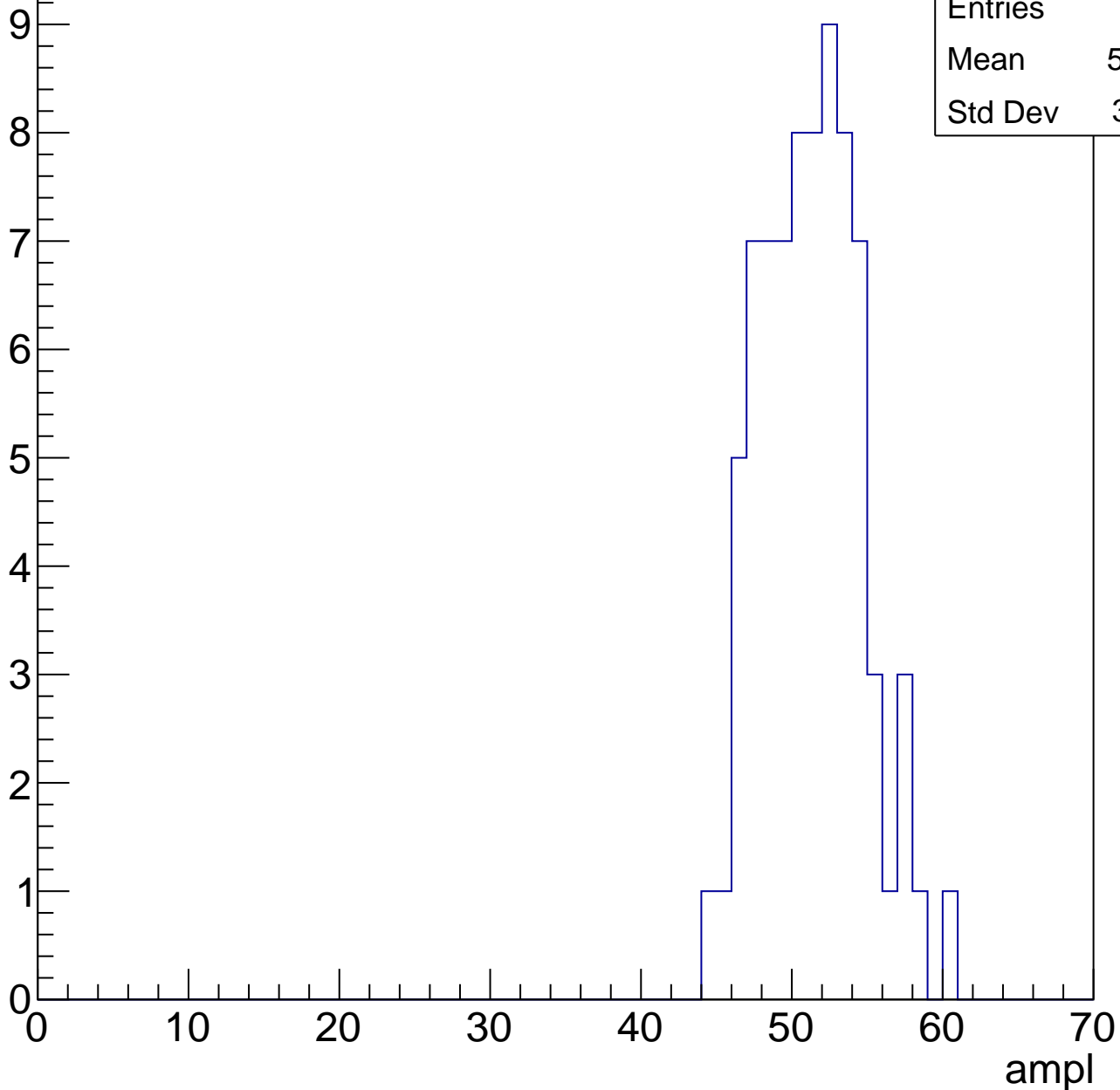


B0L001S, U7-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

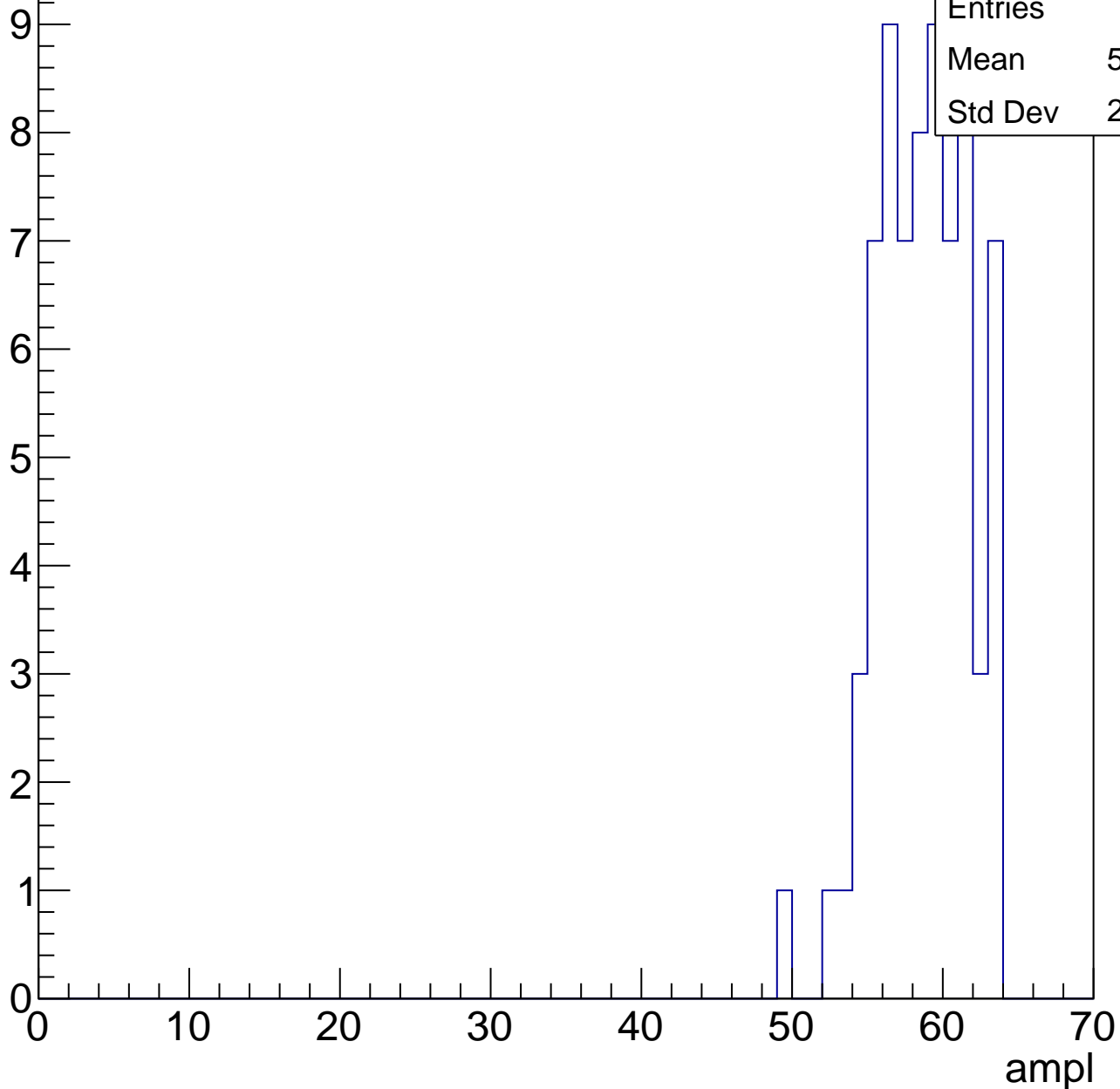
| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.84 |
| Std Dev | 3.291 |



B0L001S, U7-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

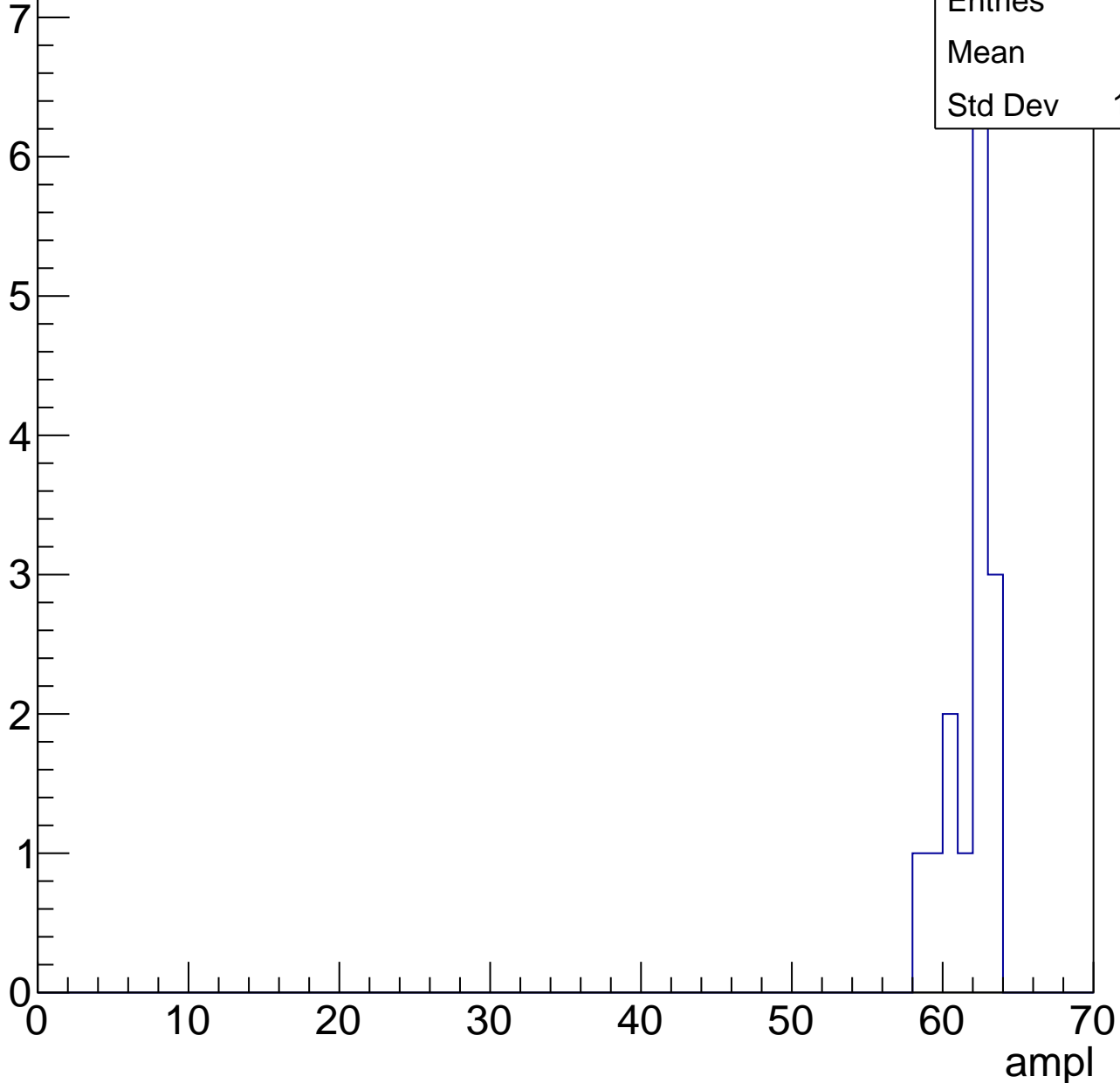


B0L001S, U7-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.4 |
| Std Dev | 1.451 |



B0L001S, U7-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch23, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 76 |
| Mean | 31.53 |
| Std Dev | 5.009 |

Gaus mean : 31.9327

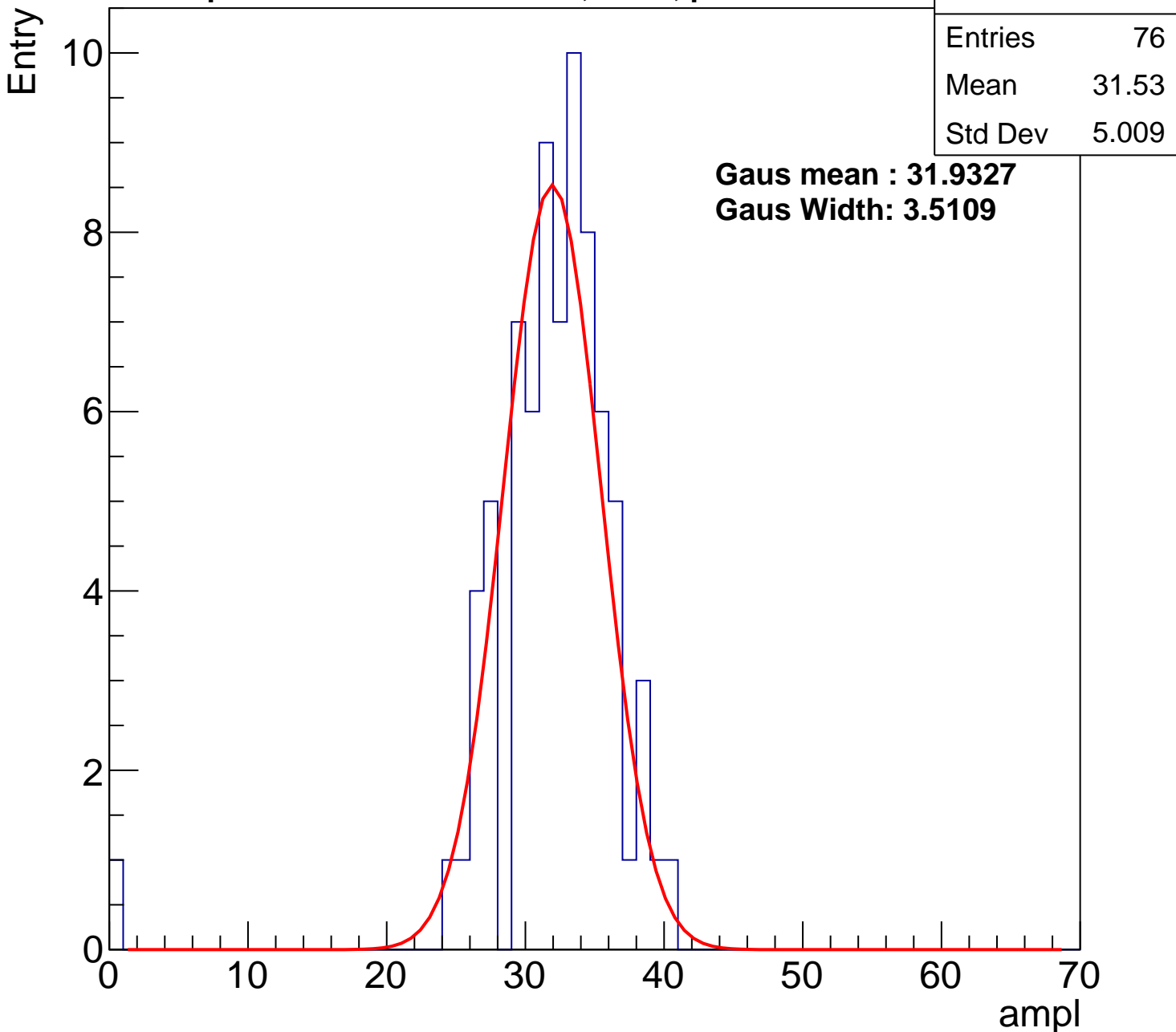
Gaus Width: 3.5109

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch23, adc1

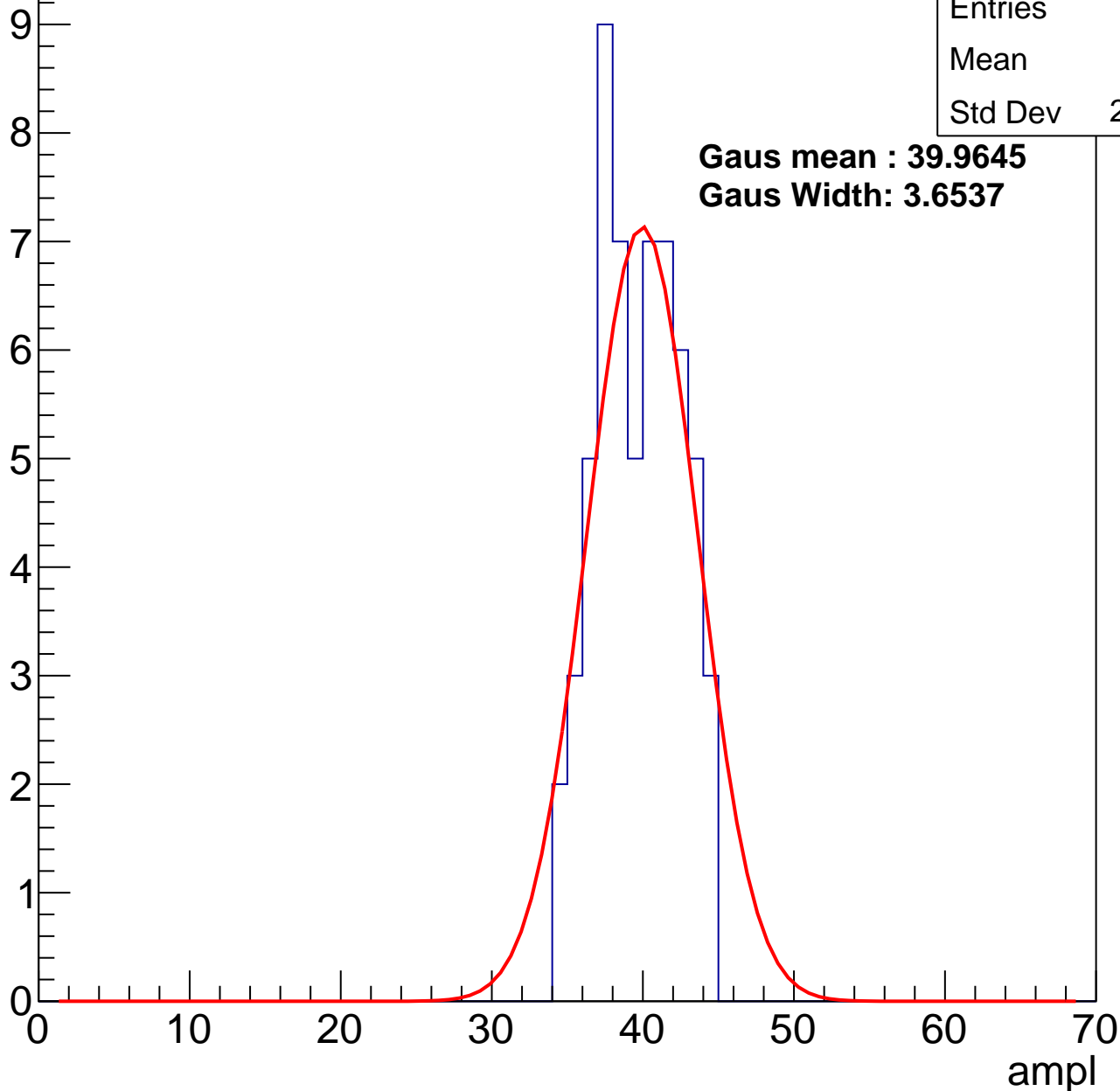
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 39.2 |
| Std Dev | 2.692 |

Gaus mean : 39.9645

Gaus Width: 3.6537



B0L001S, U7-ch23, adc2

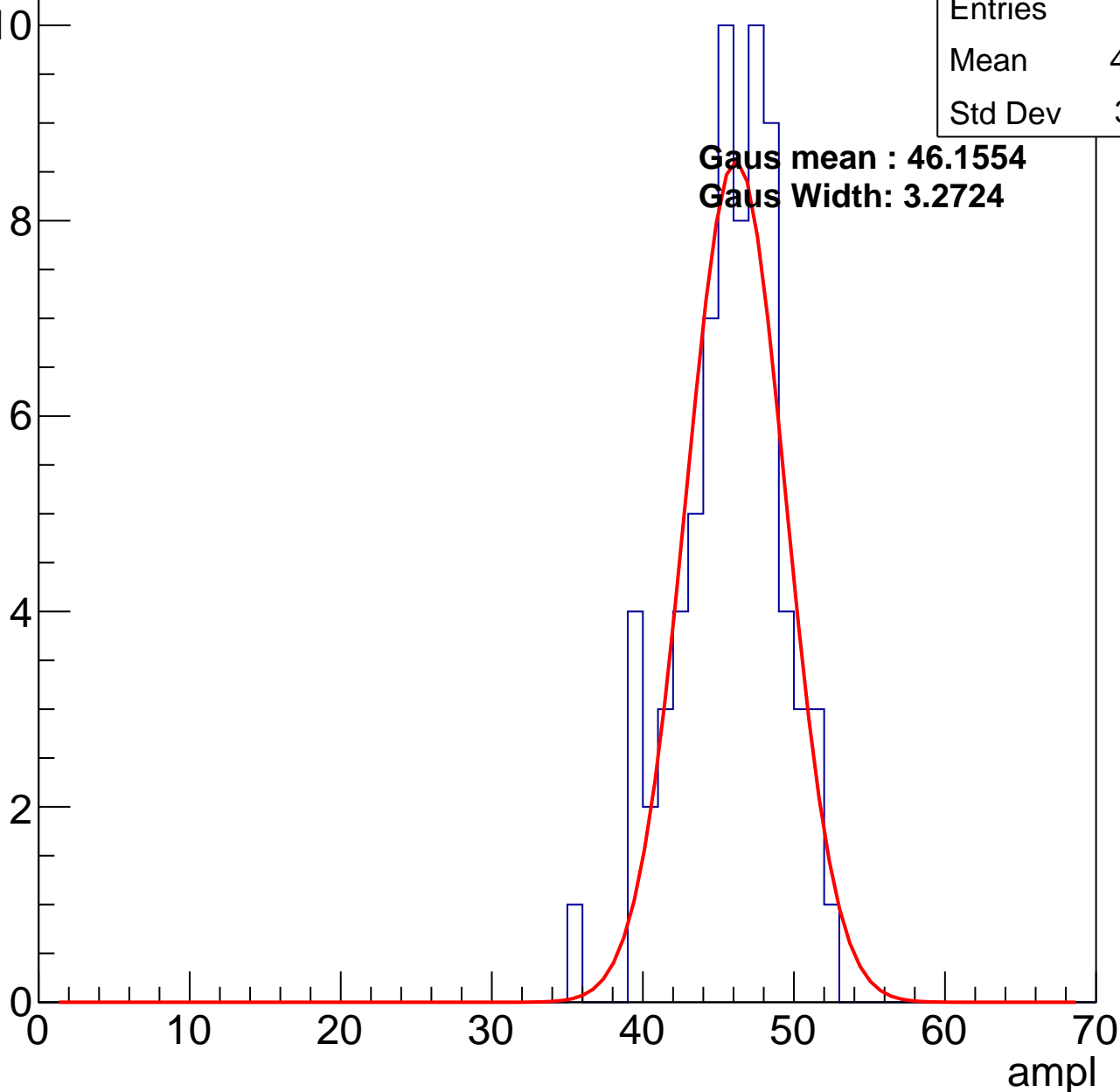
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 45.35 |
| Std Dev | 3.331 |

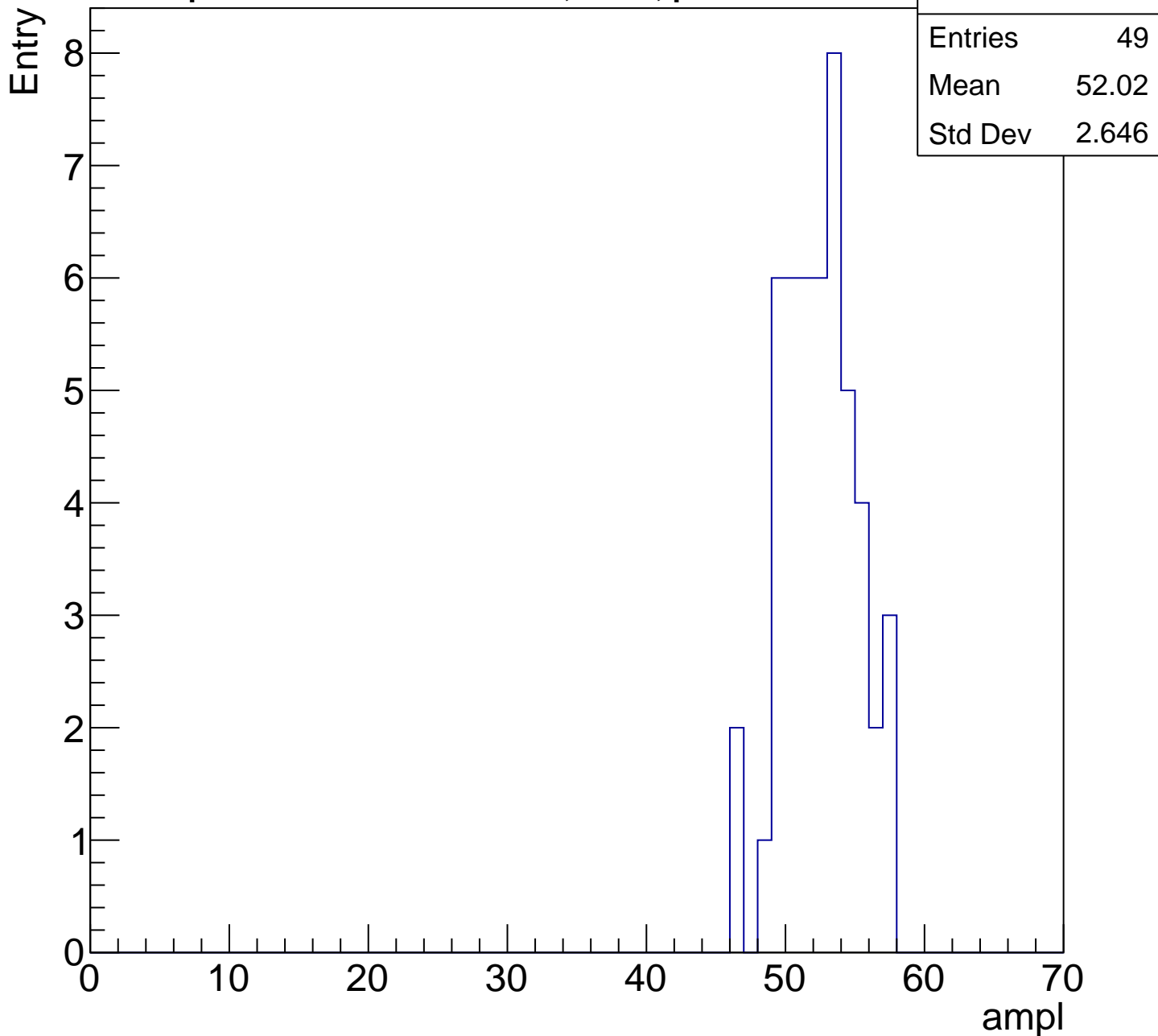
Gaus mean : 46.1554

Gaus Width: 3.2724



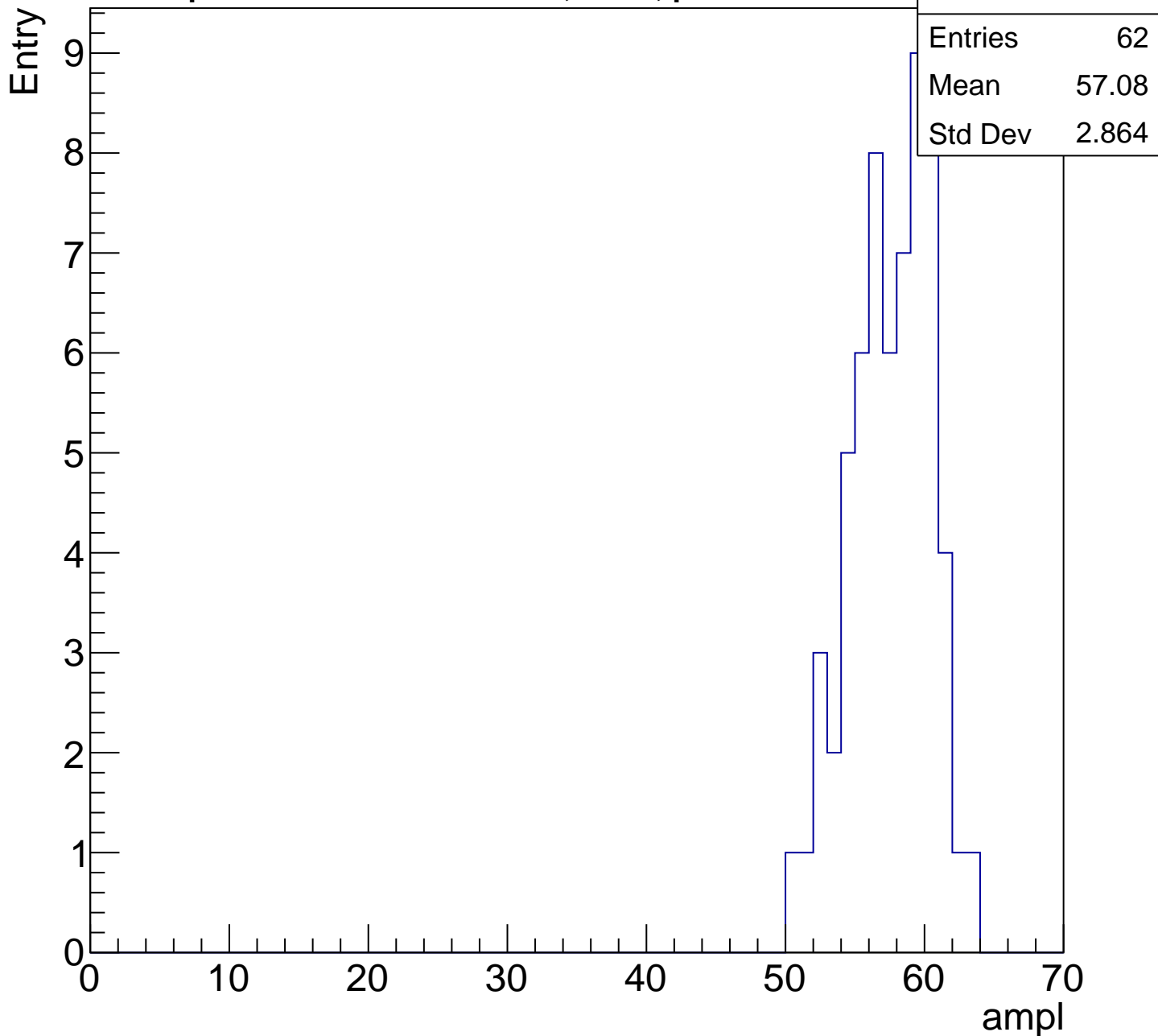
B0L001S, U7-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

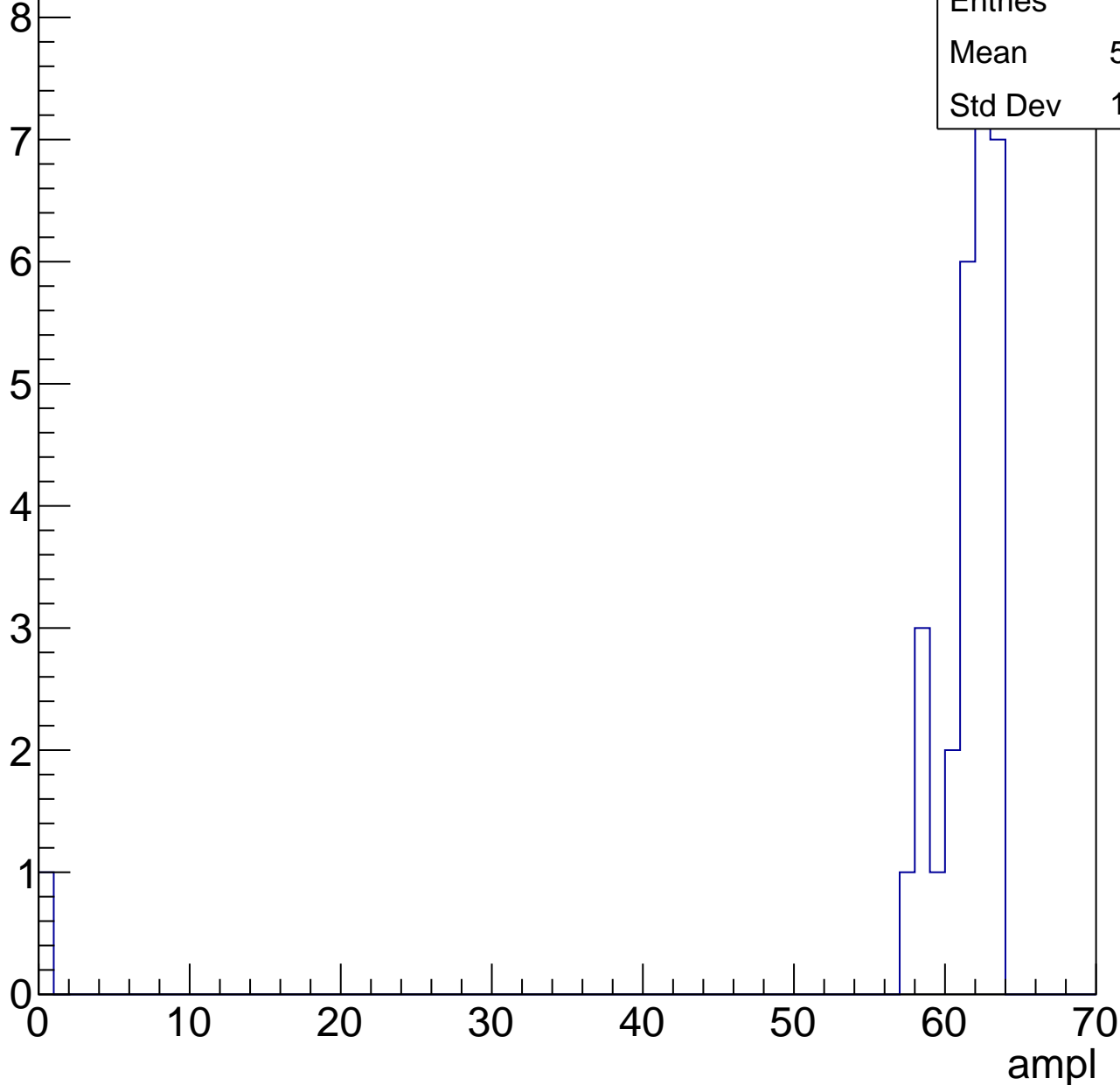


B0L001S, U7-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 59.07 |
| Std Dev | 11.29 |



B0L001S, U7-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch24, adc0

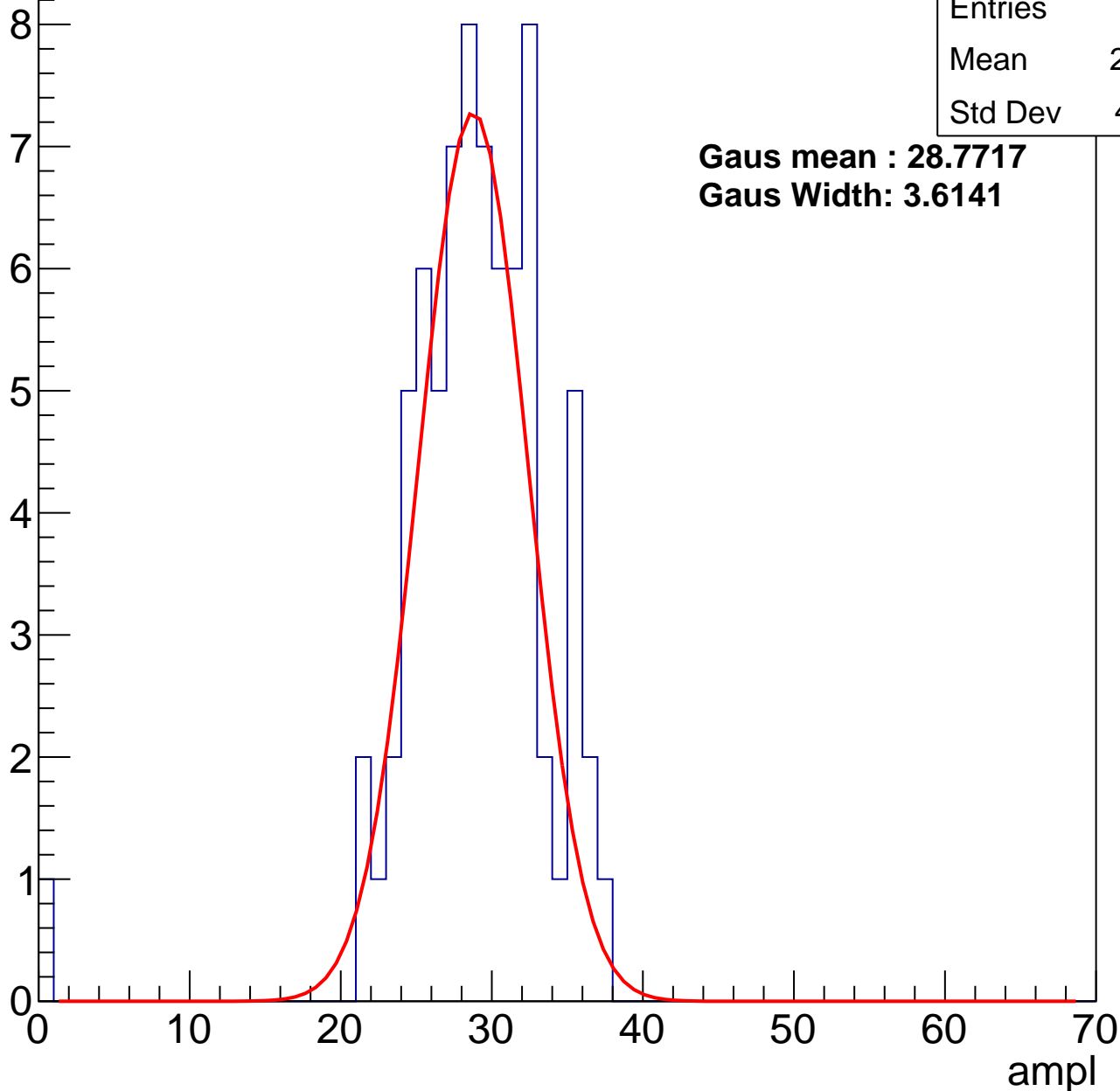
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 28.43 |
| Std Dev | 4.991 |

Gaus mean : 28.7717

Gaus Width: 3.6141



B0L001S, U7-ch24, adc1

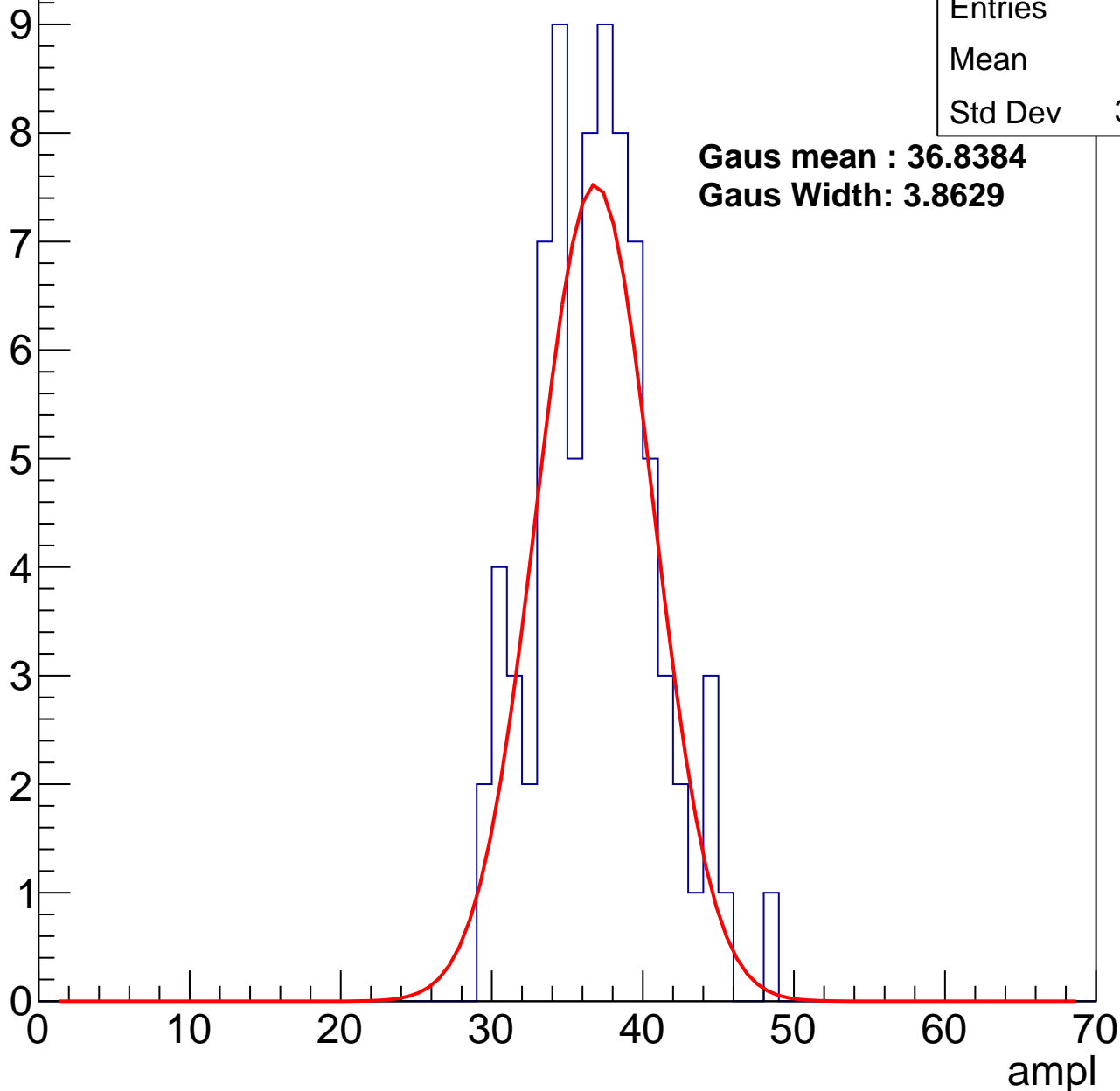
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 36.5 |
| Std Dev | 3.921 |

Gaus mean : 36.8384

Gaus Width: 3.8629



B0L001S, U7-ch24, adc2

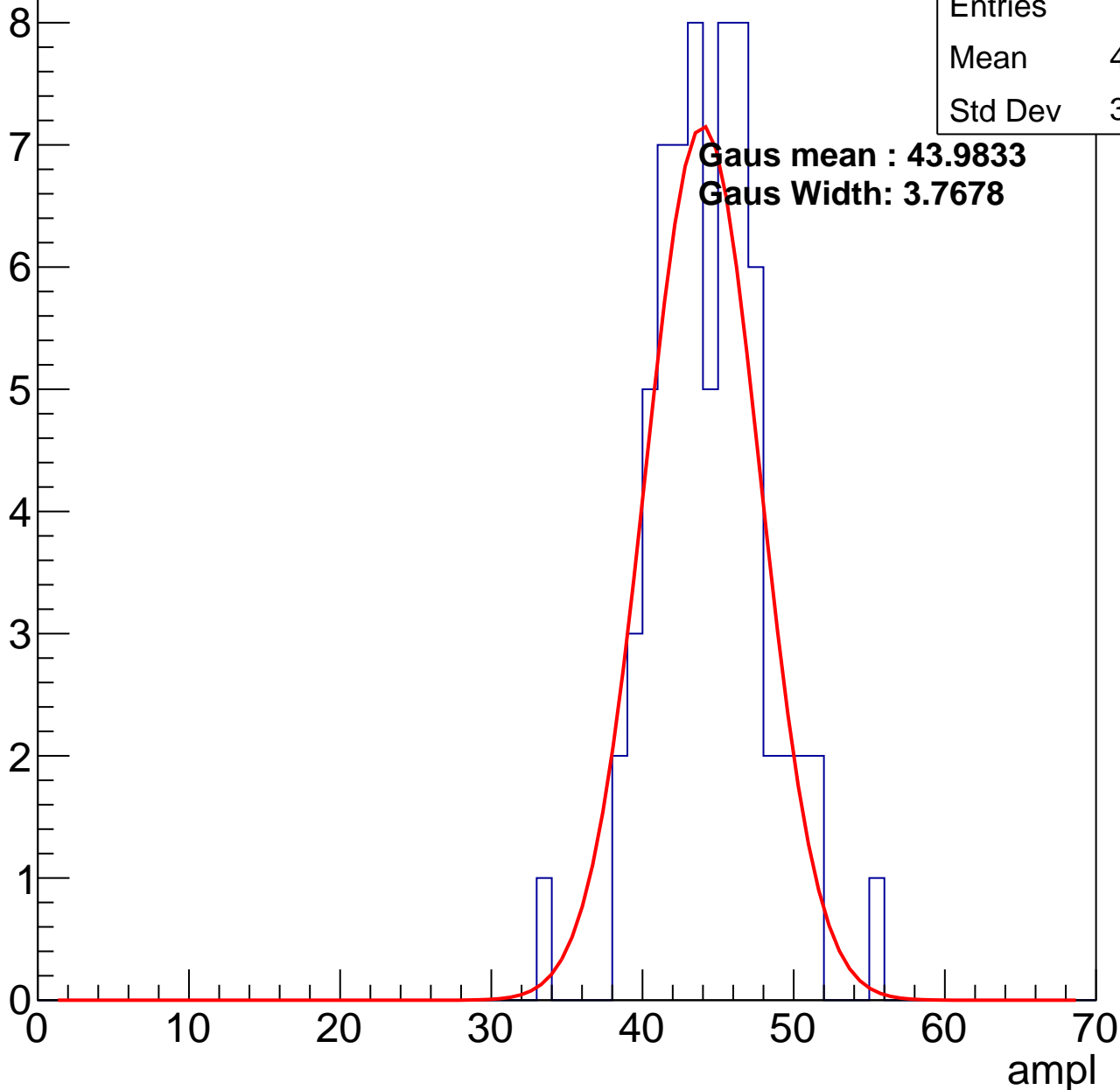
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.94 |
| Std Dev | 3.639 |

Gaus mean : 43.9833

Gaus Width: 3.7678

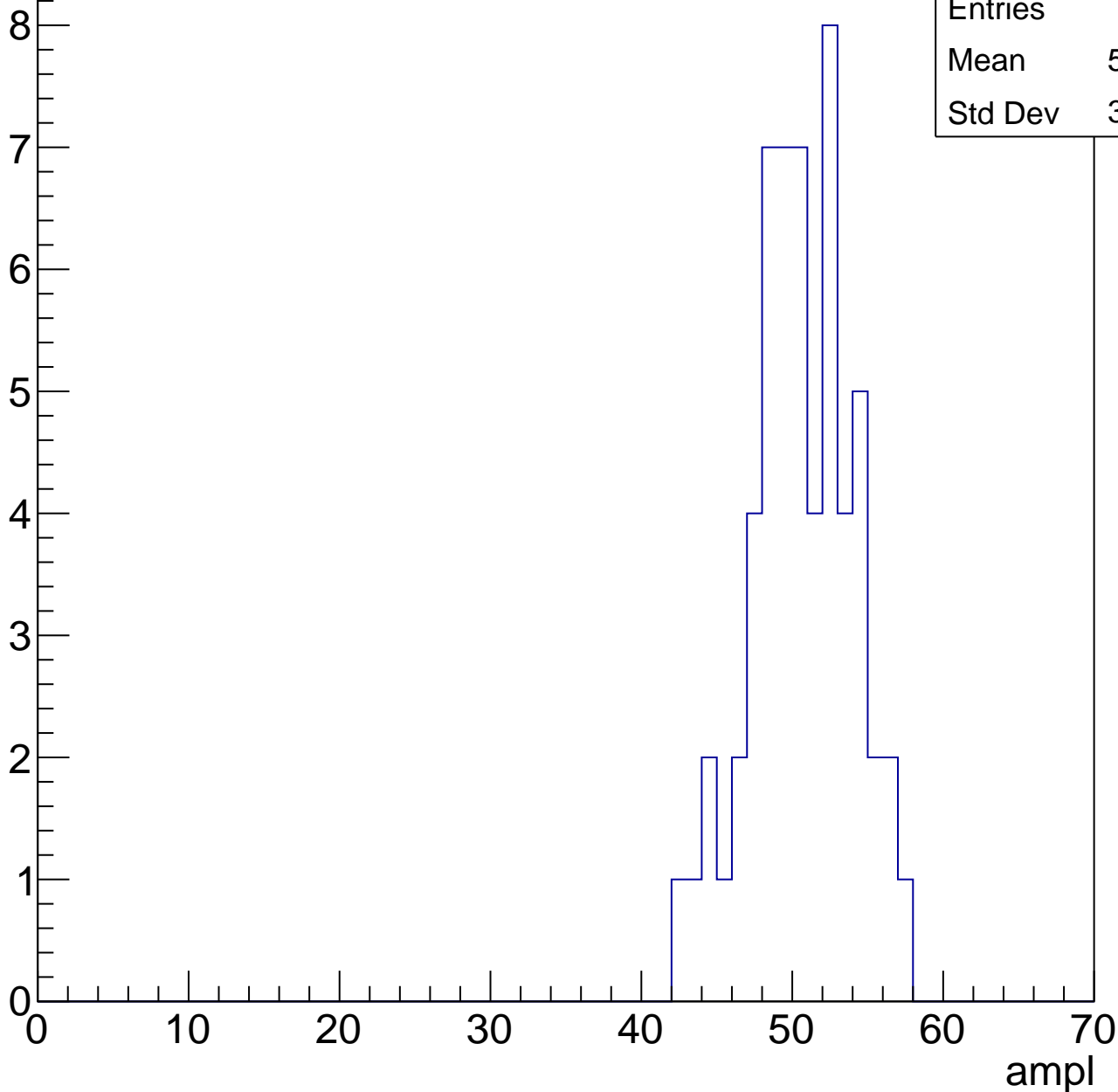


B0L001S, U7-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 50.14 |
| Std Dev | 3.309 |

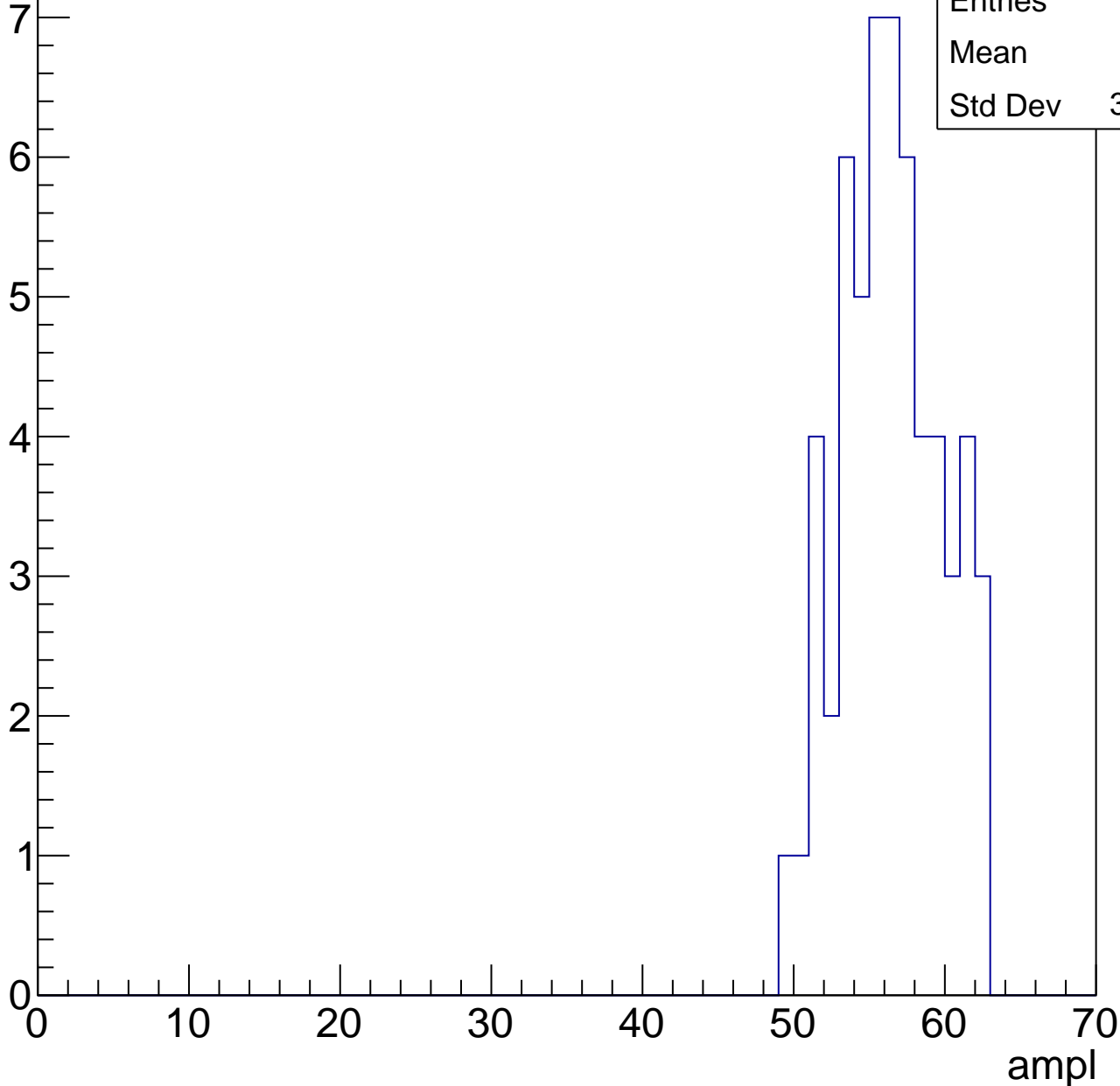


B0L001S, U7-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 56 |
| Std Dev | 3.277 |



B0L001S, U7-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.86 |
| Std Dev | 9.303 |

ampl

0

10

20

30

40

50

60

70

B0L001S, U7-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

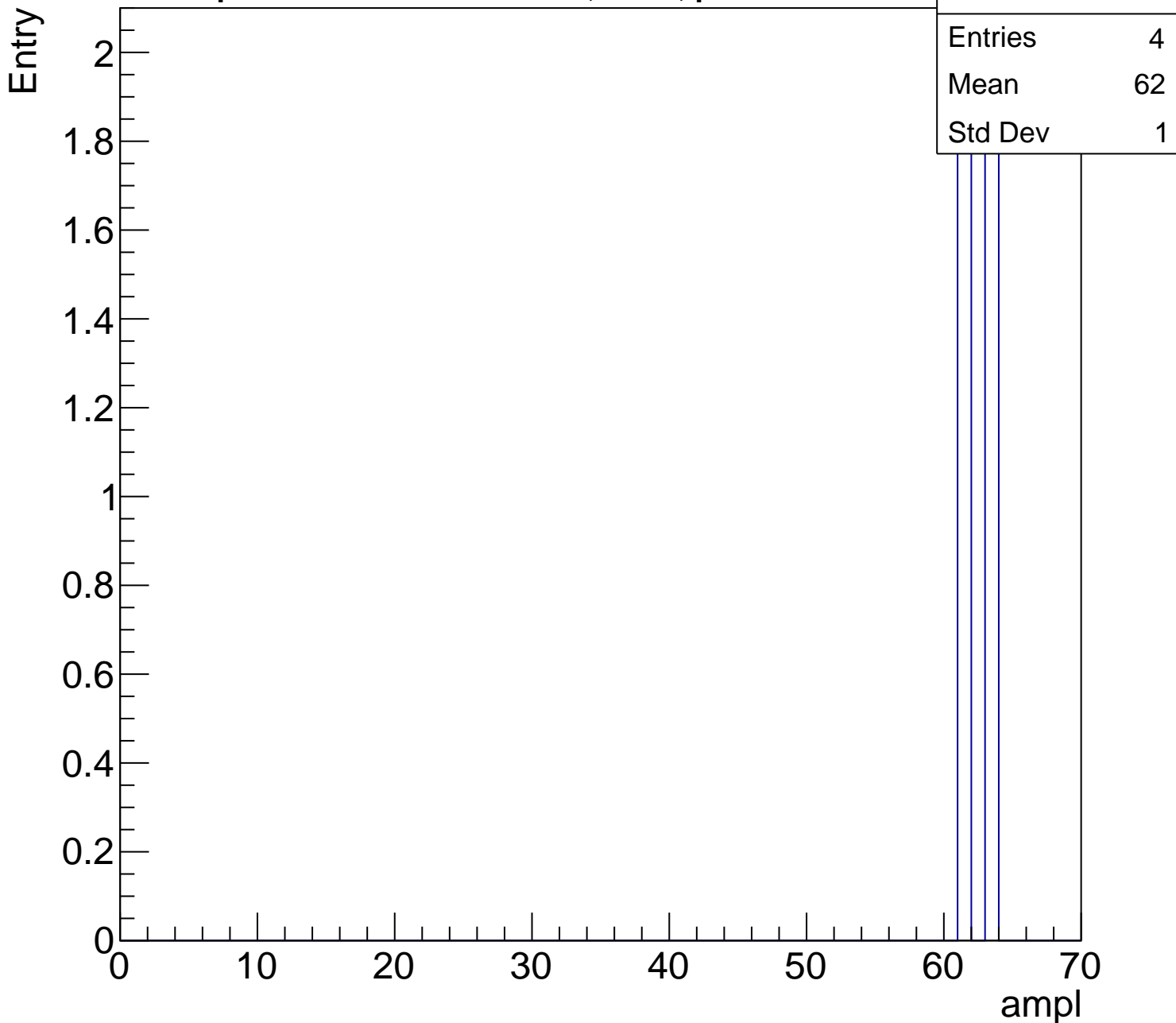
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 4 |
| Mean | 62 |
| Std Dev | 1 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch25, adc0

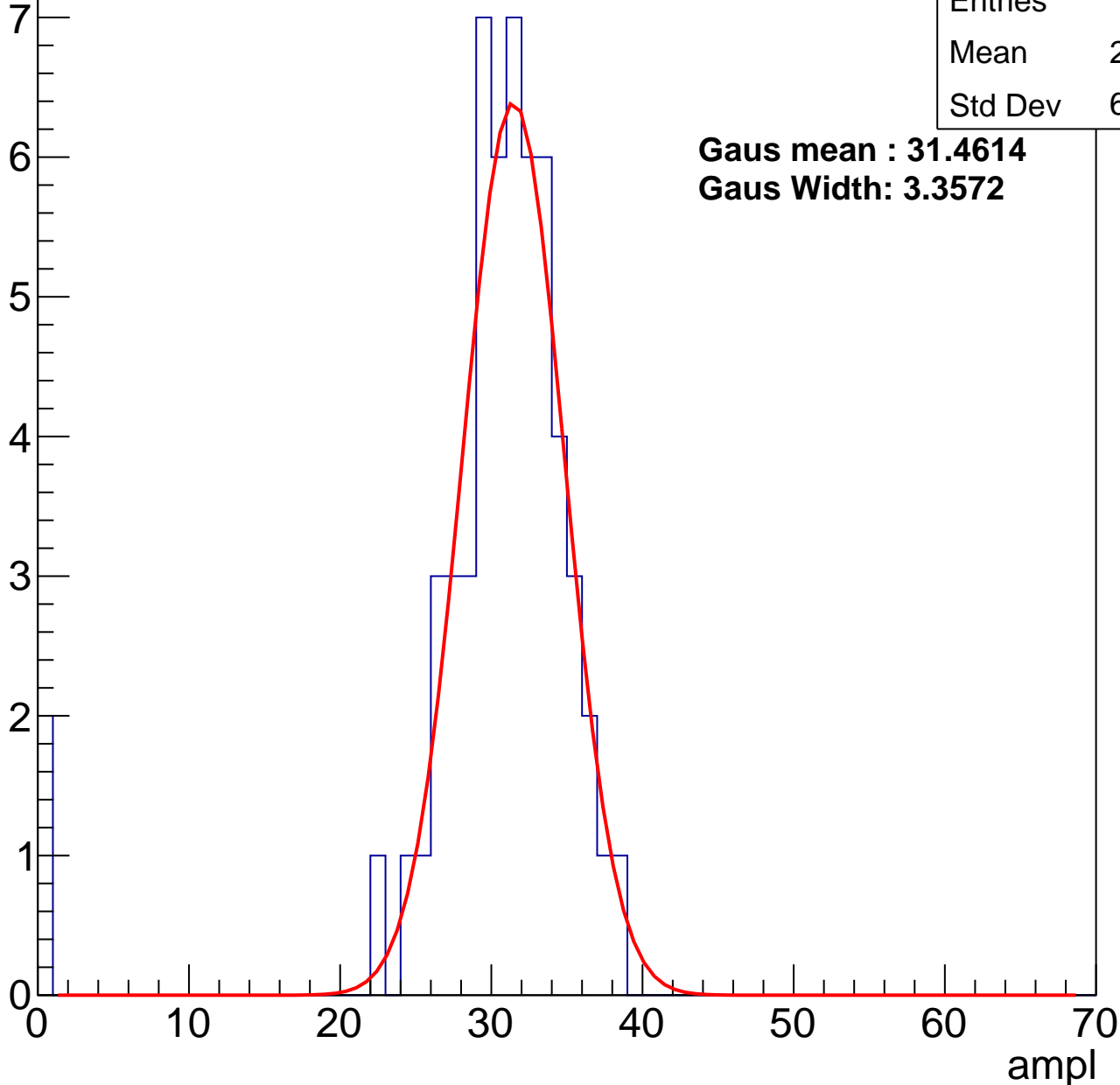
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 29.68 |
| Std Dev | 6.519 |

Gaus mean : 31.4614

Gaus Width: 3.3572



B0L001S, U7-ch25, adc1

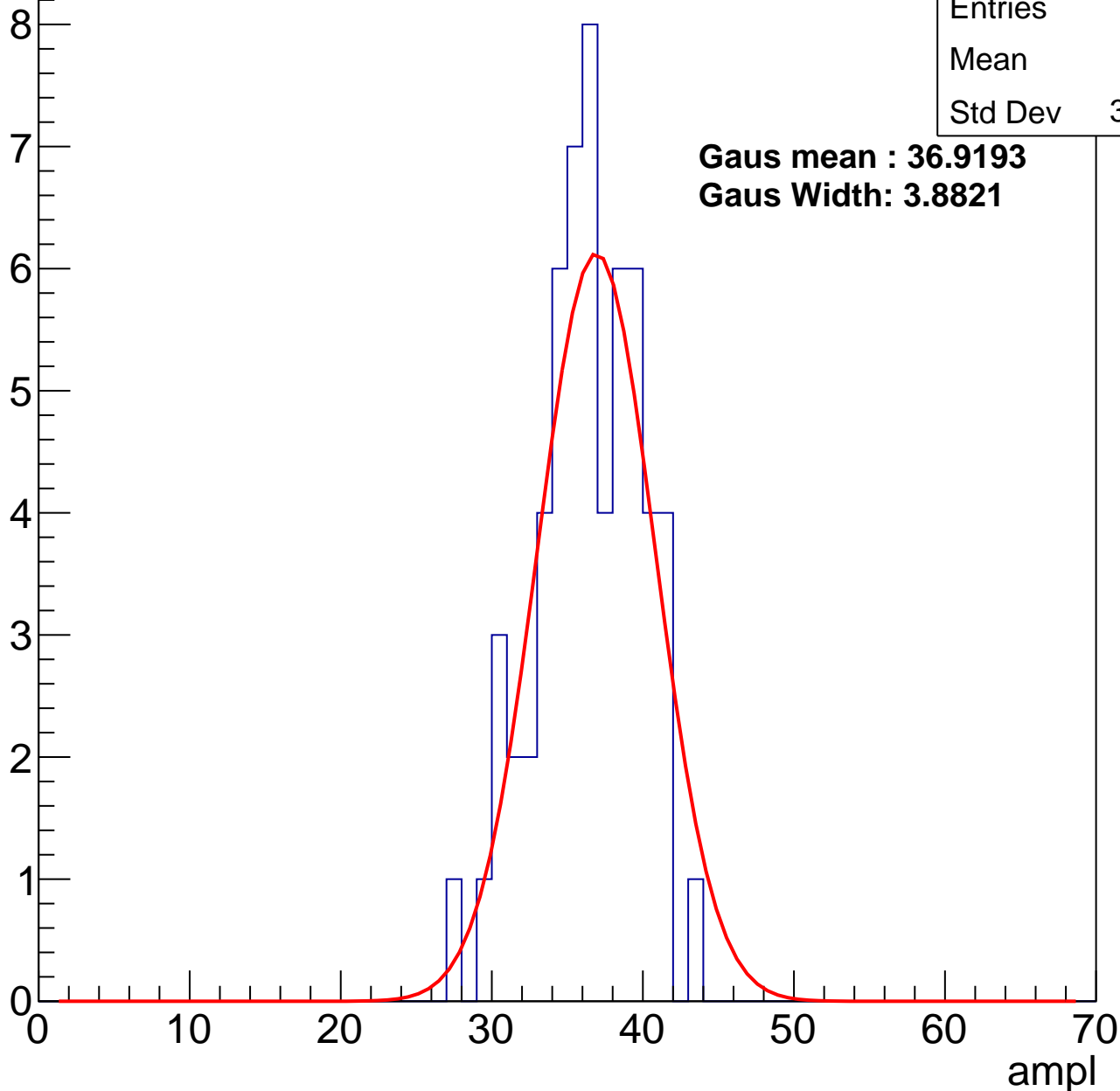
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.9 |
| Std Dev | 3.398 |

Gaus mean : 36.9193

Gaus Width: 3.8821



B0L001S, U7-ch25, adc2

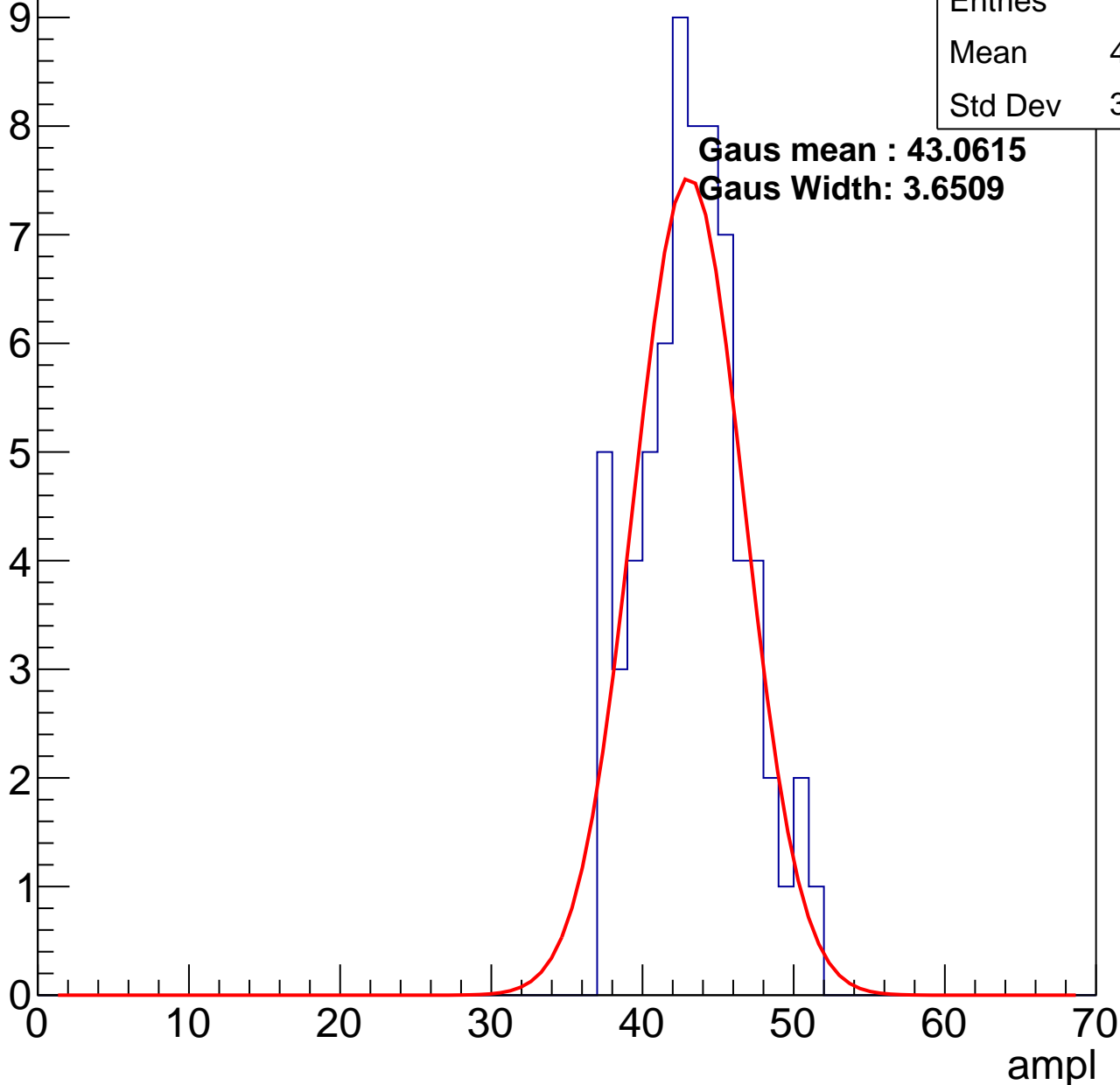
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 42.87 |
| Std Dev | 3.362 |

Gaus mean : 43.0615

Gaus Width: 3.6509

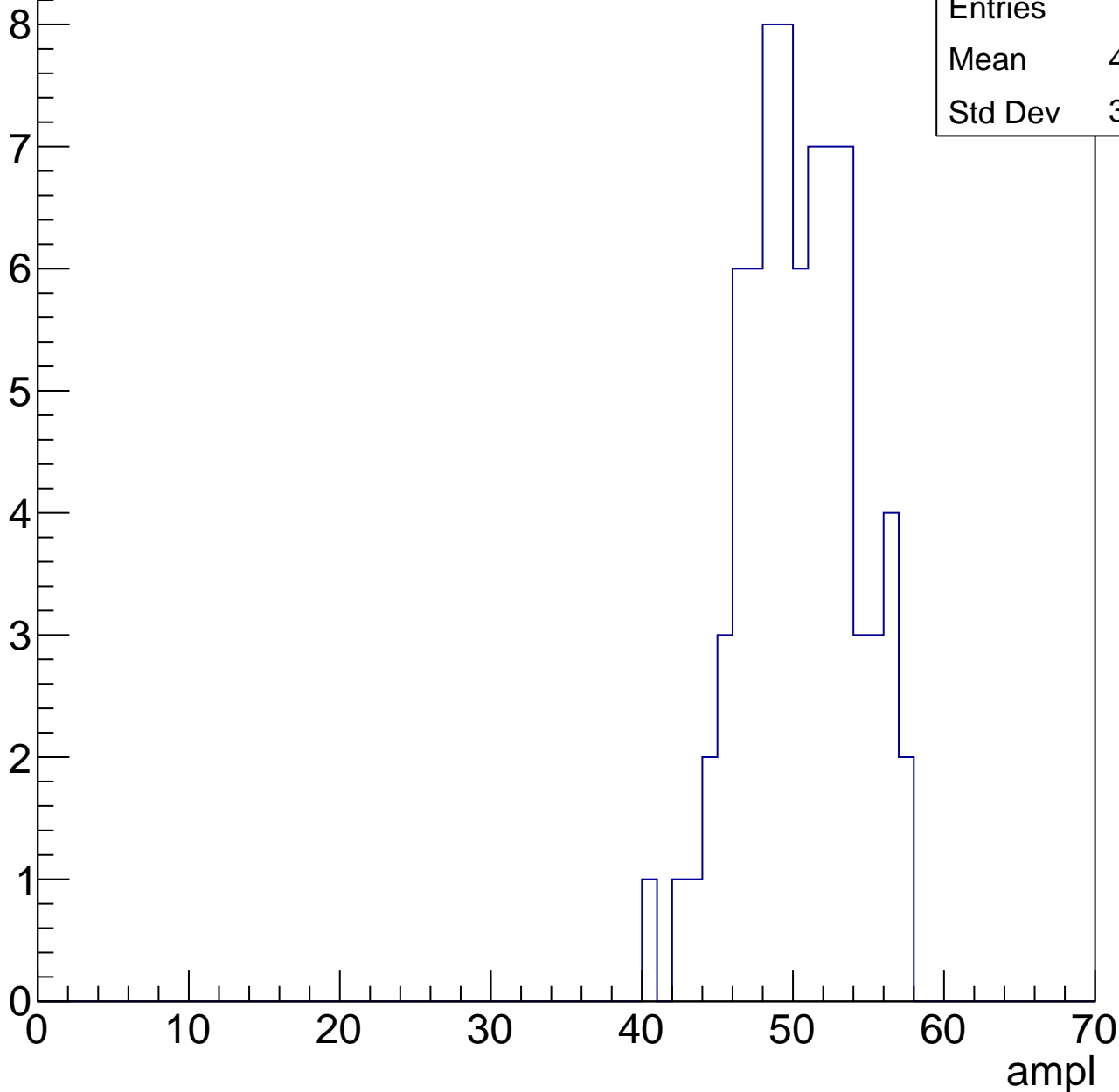


B0L001S, U7-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 49.85 |
| Std Dev | 3.672 |

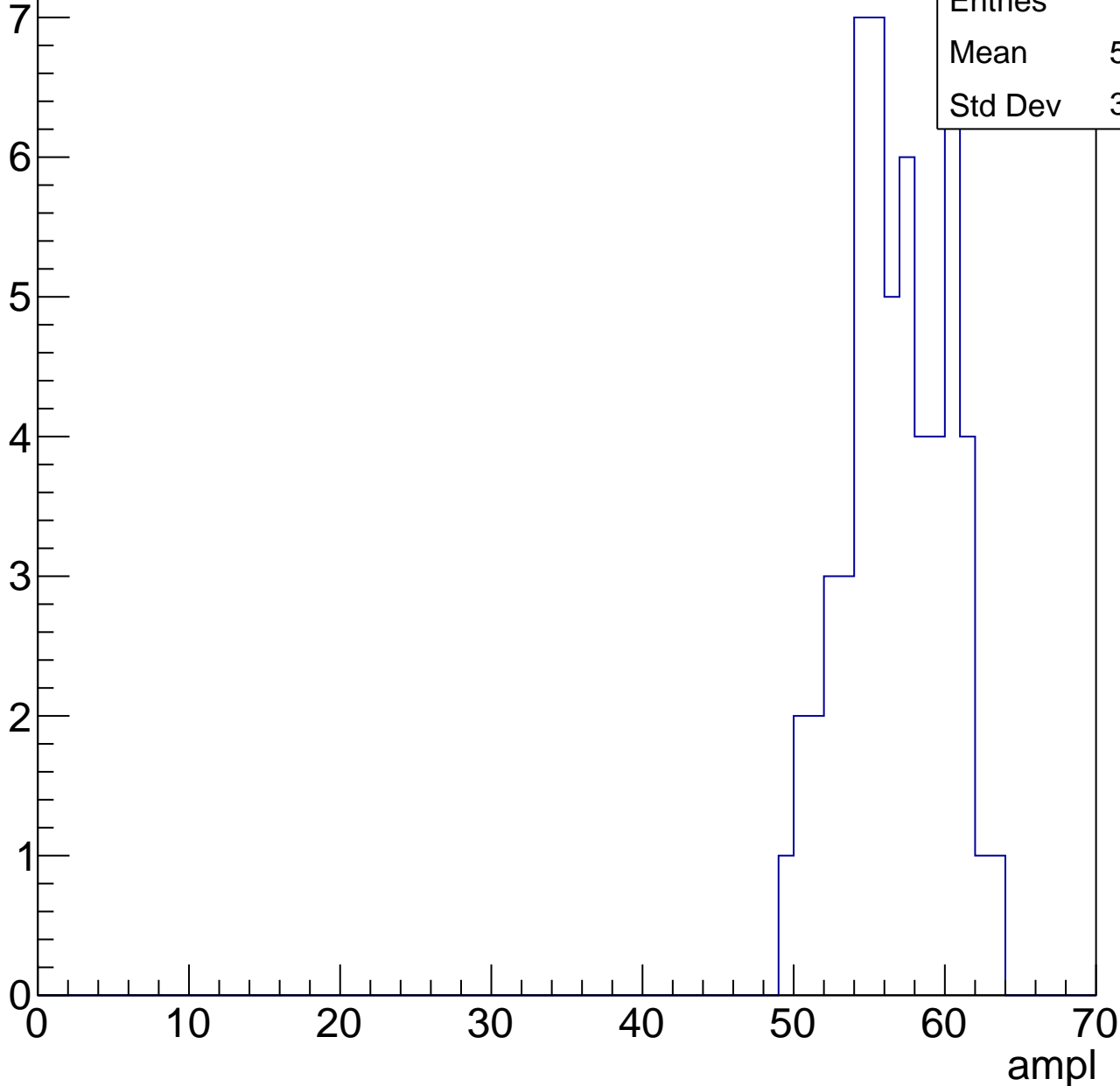


B0L001S, U7-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 56.28 |
| Std Dev | 3.329 |

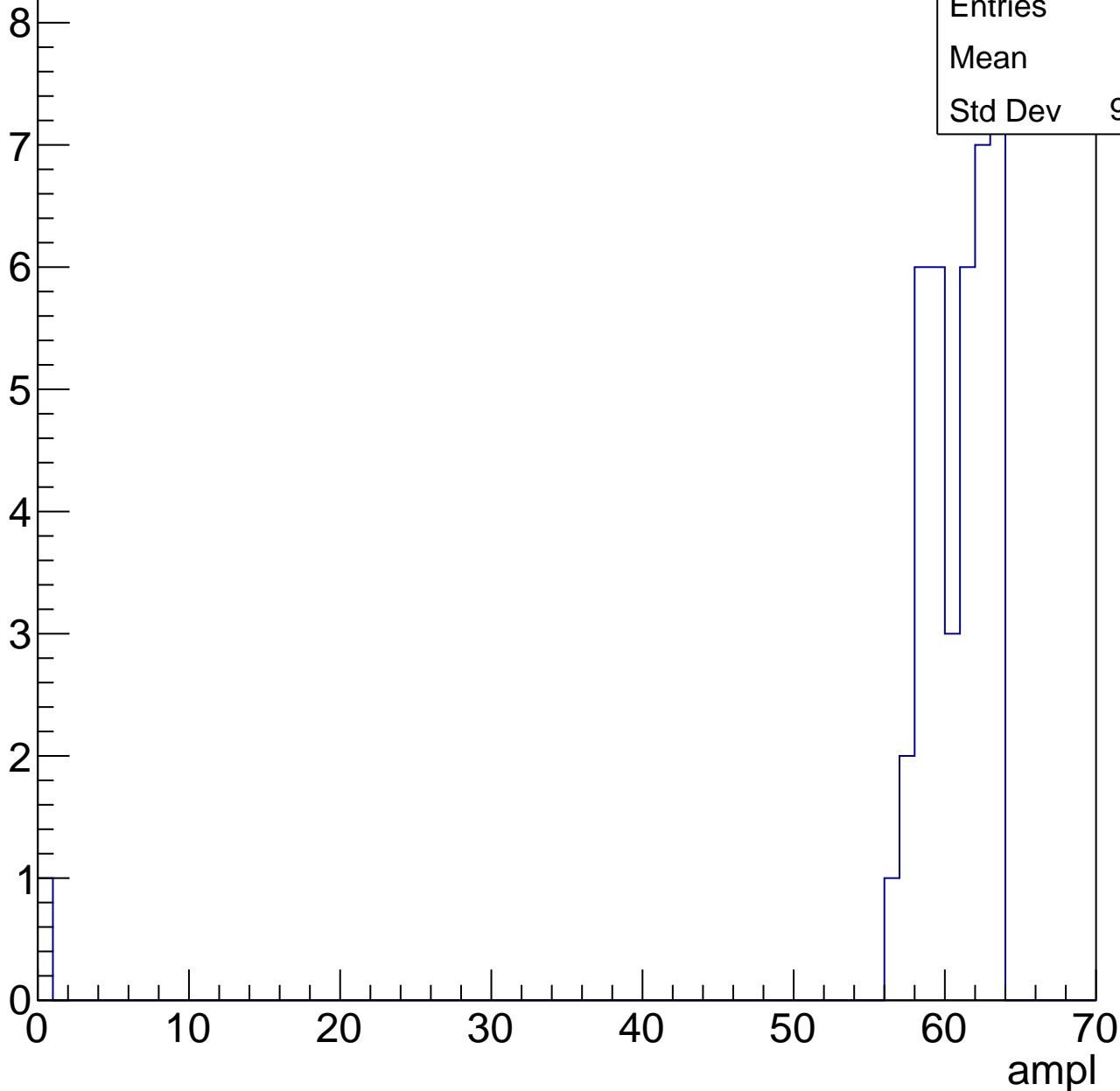


B0L001S, U7-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.9 |
| Std Dev | 9.646 |



B0L001S, U7-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch26, adc0

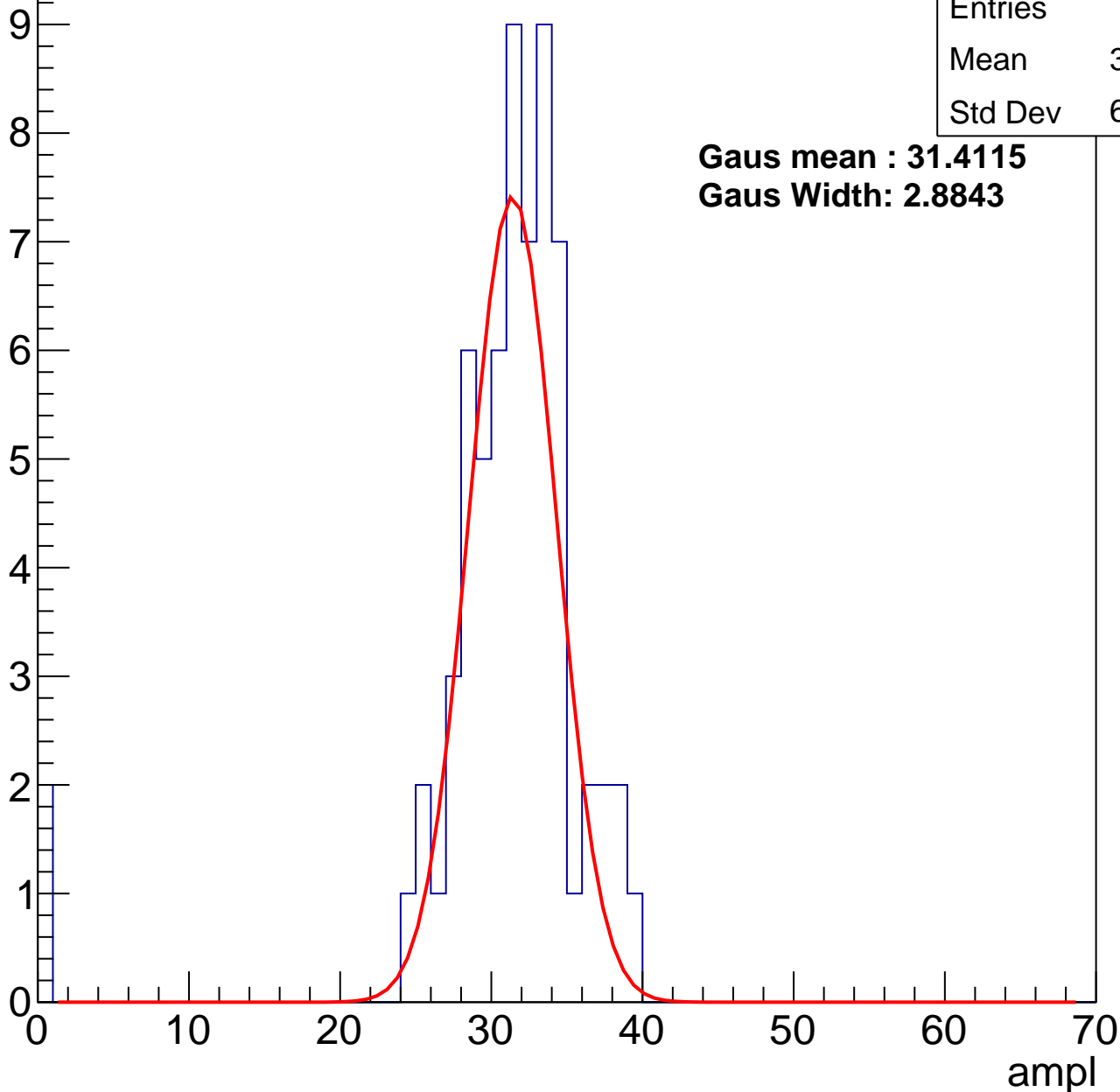
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 30.42 |
| Std Dev | 6.252 |

Gaus mean : 31.4115

Gaus Width: 2.8843



B0L001S, U7-ch26, adc1

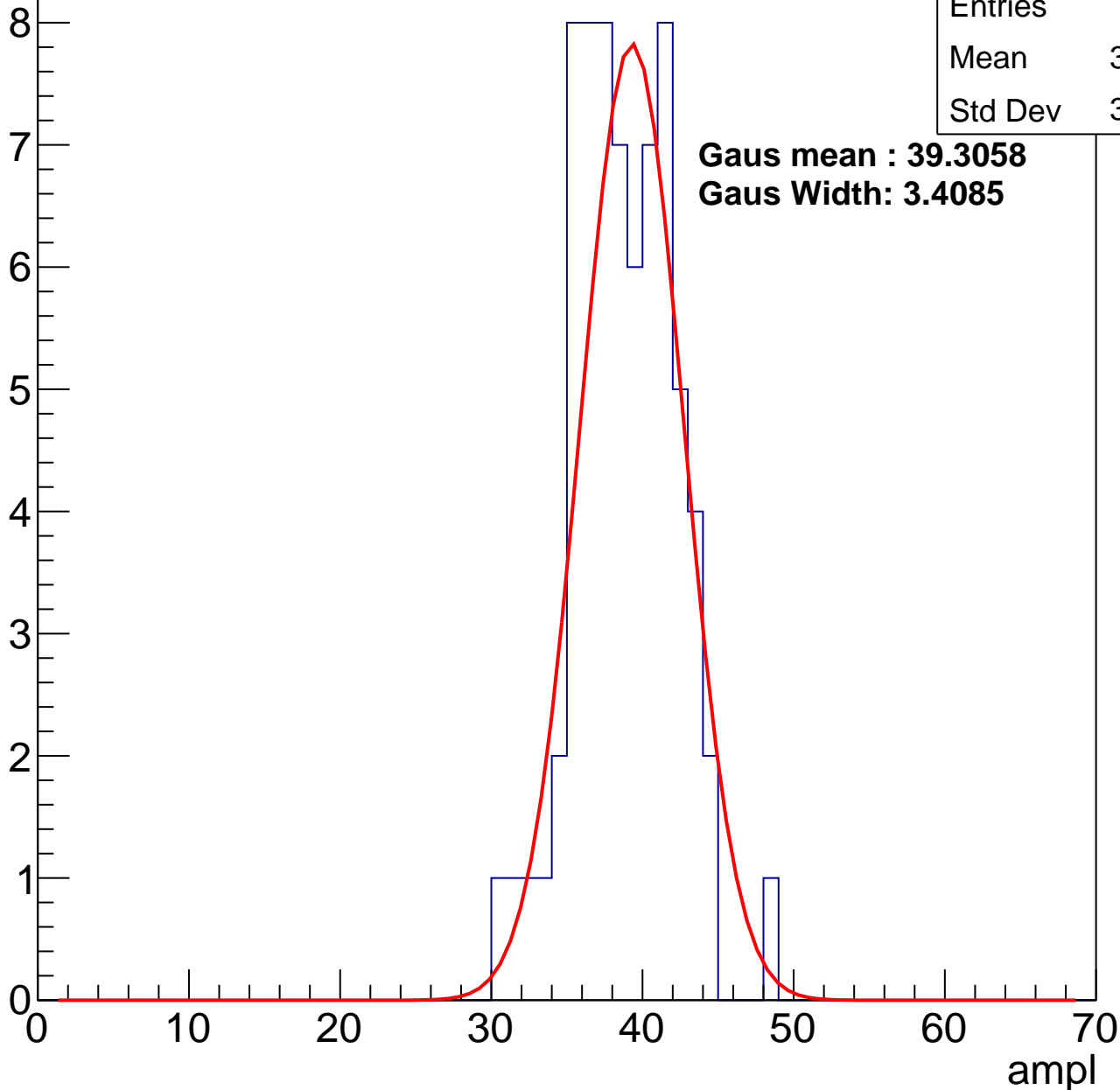
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 38.34 |
| Std Dev | 3.312 |

Gaus mean : 39.3058

Gaus Width: 3.4085



B0L001S, U7-ch26, adc2

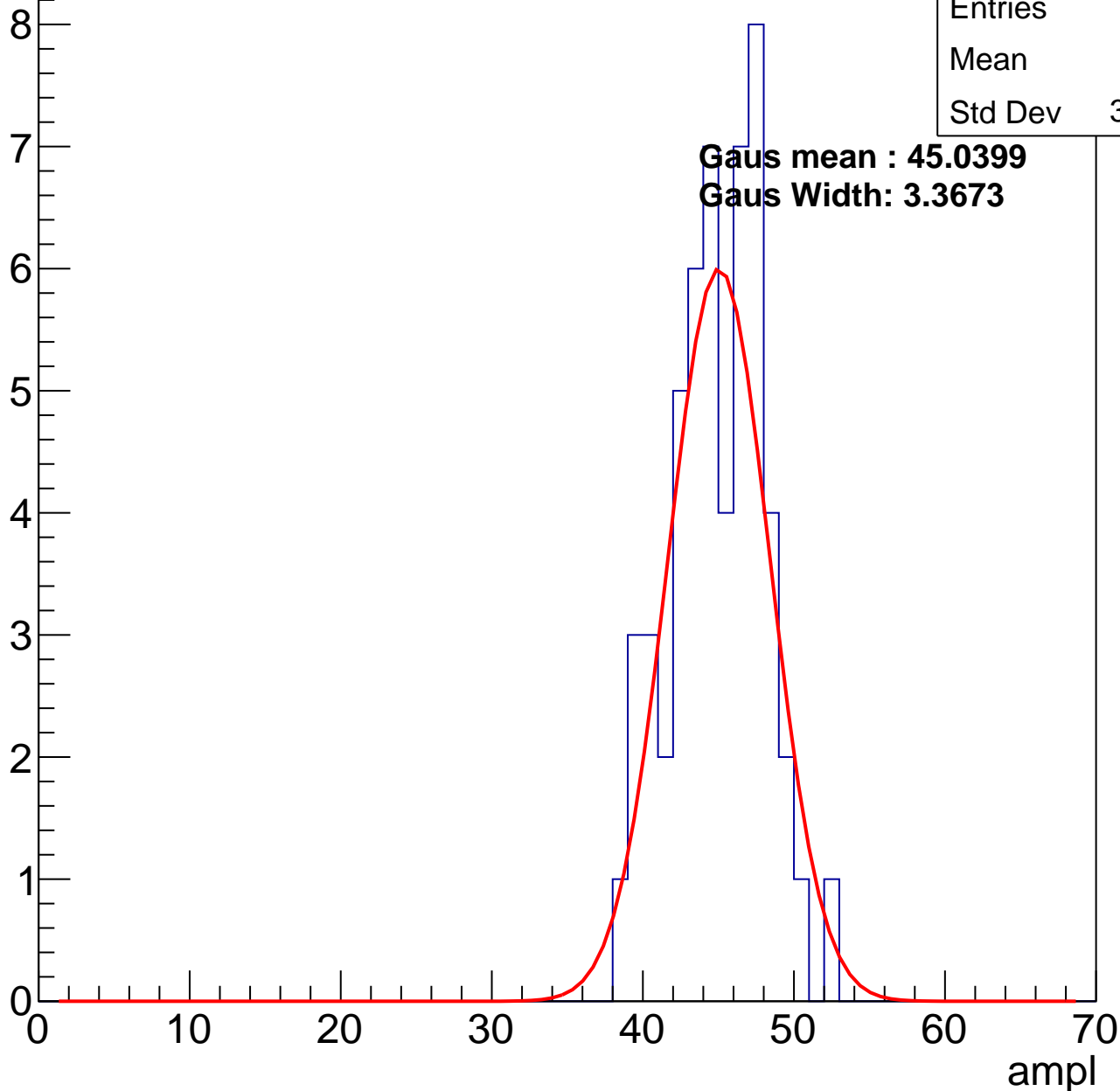
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 44.5 |
| Std Dev | 3.066 |

Gaus mean : 45.0399

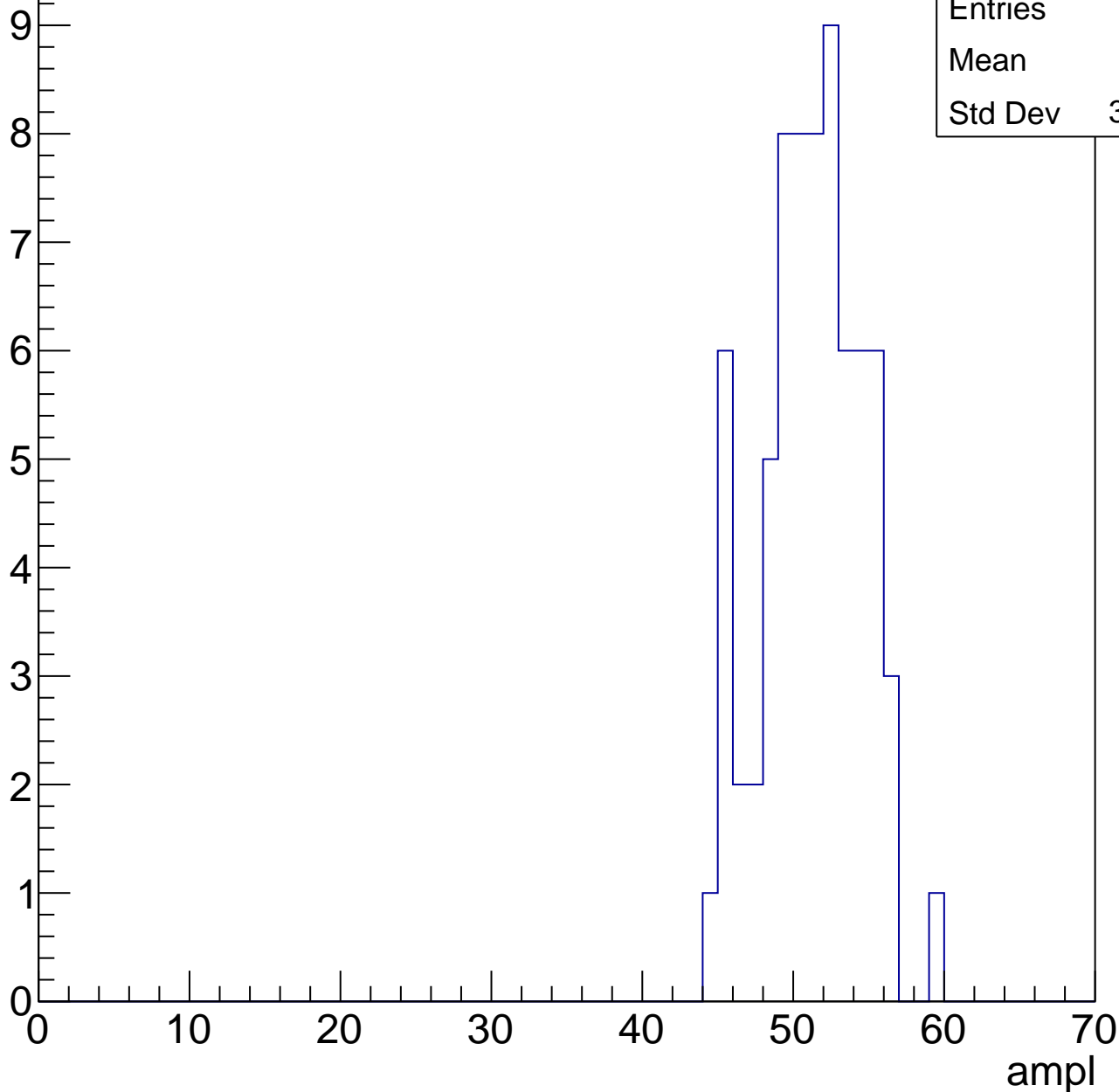
Gaus Width: 3.3673



B0L001S, U7-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

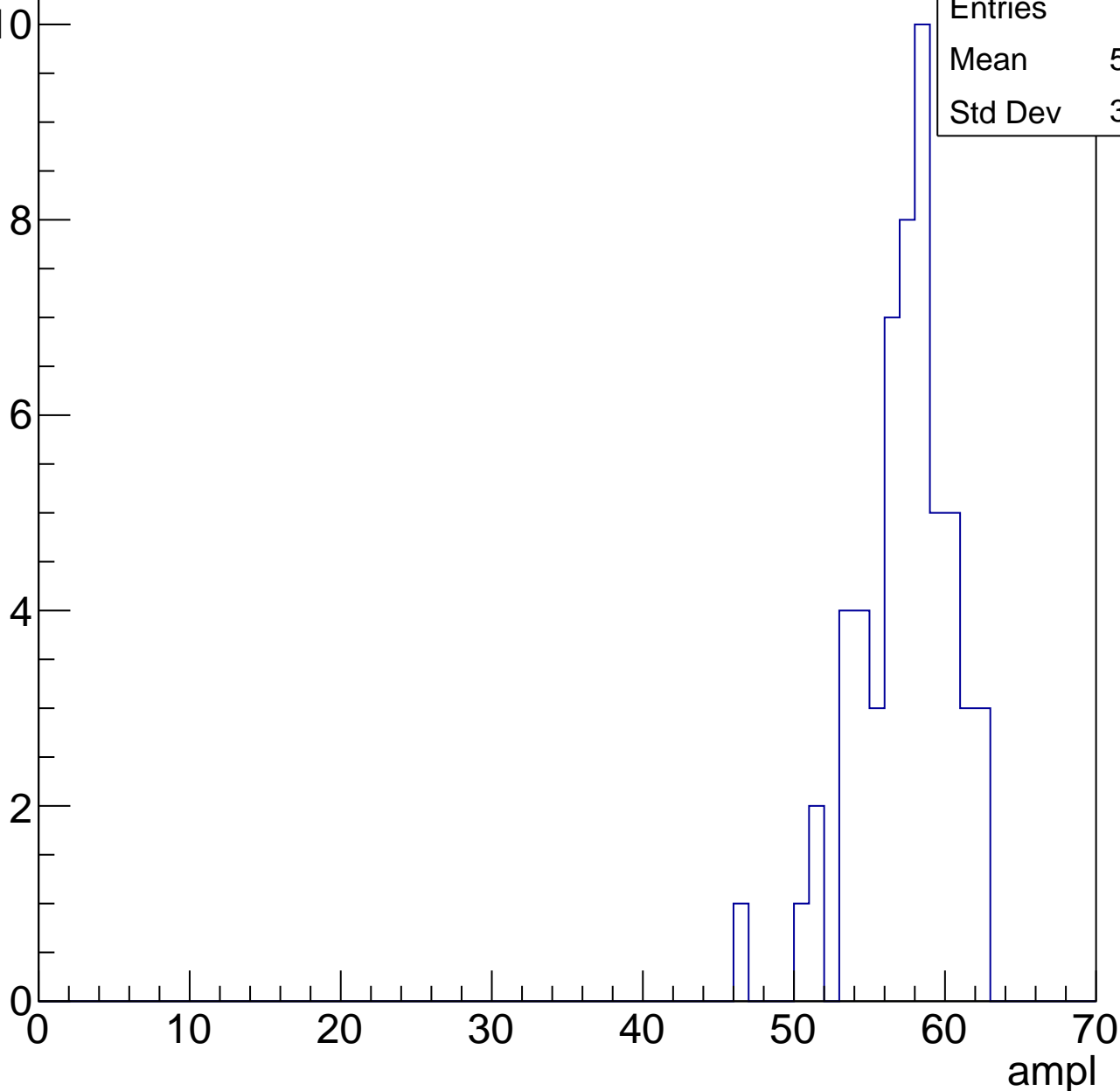


B0L001S, U7-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 56.84 |
| Std Dev | 3.155 |

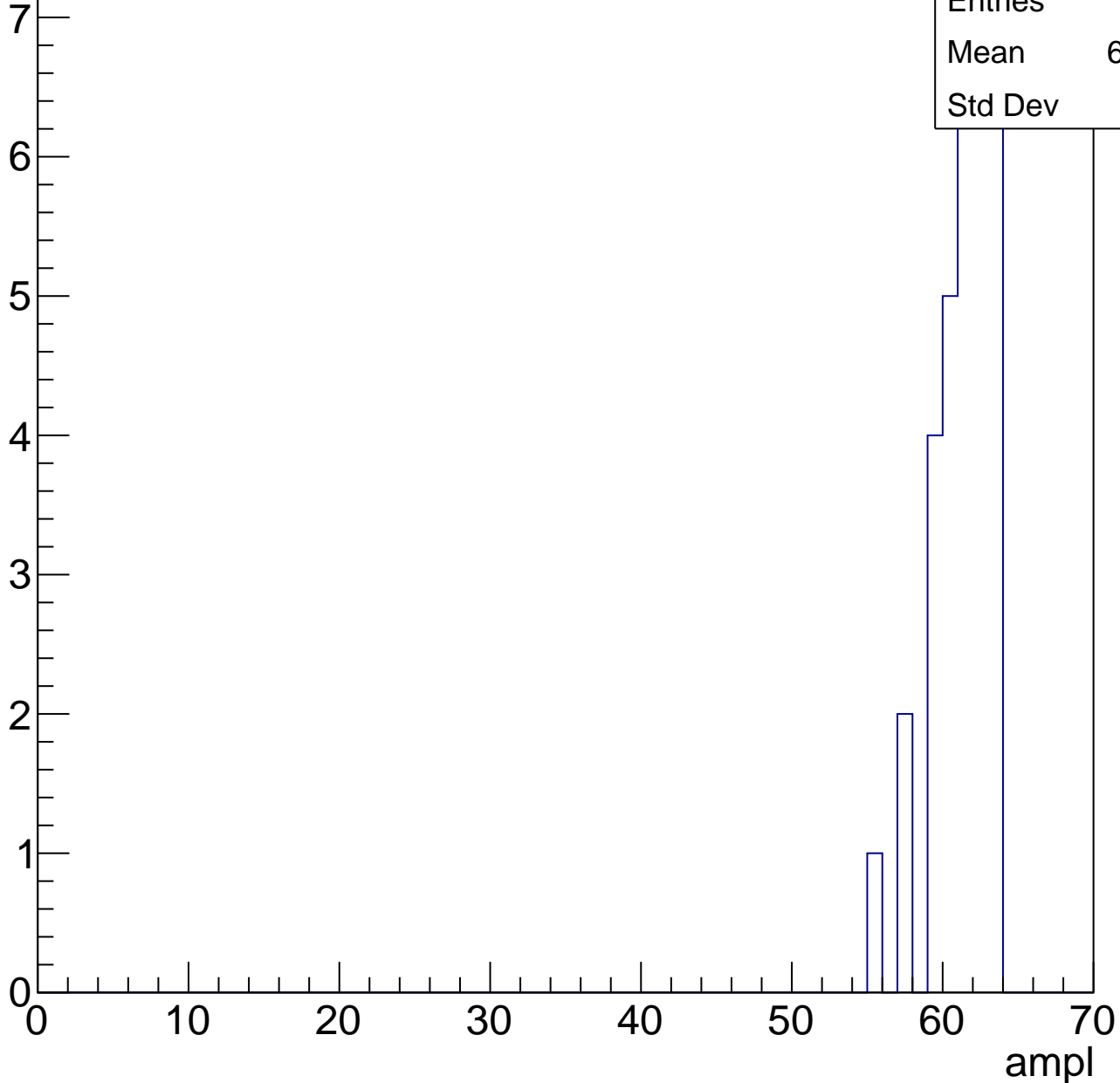


B0L001S, U7-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

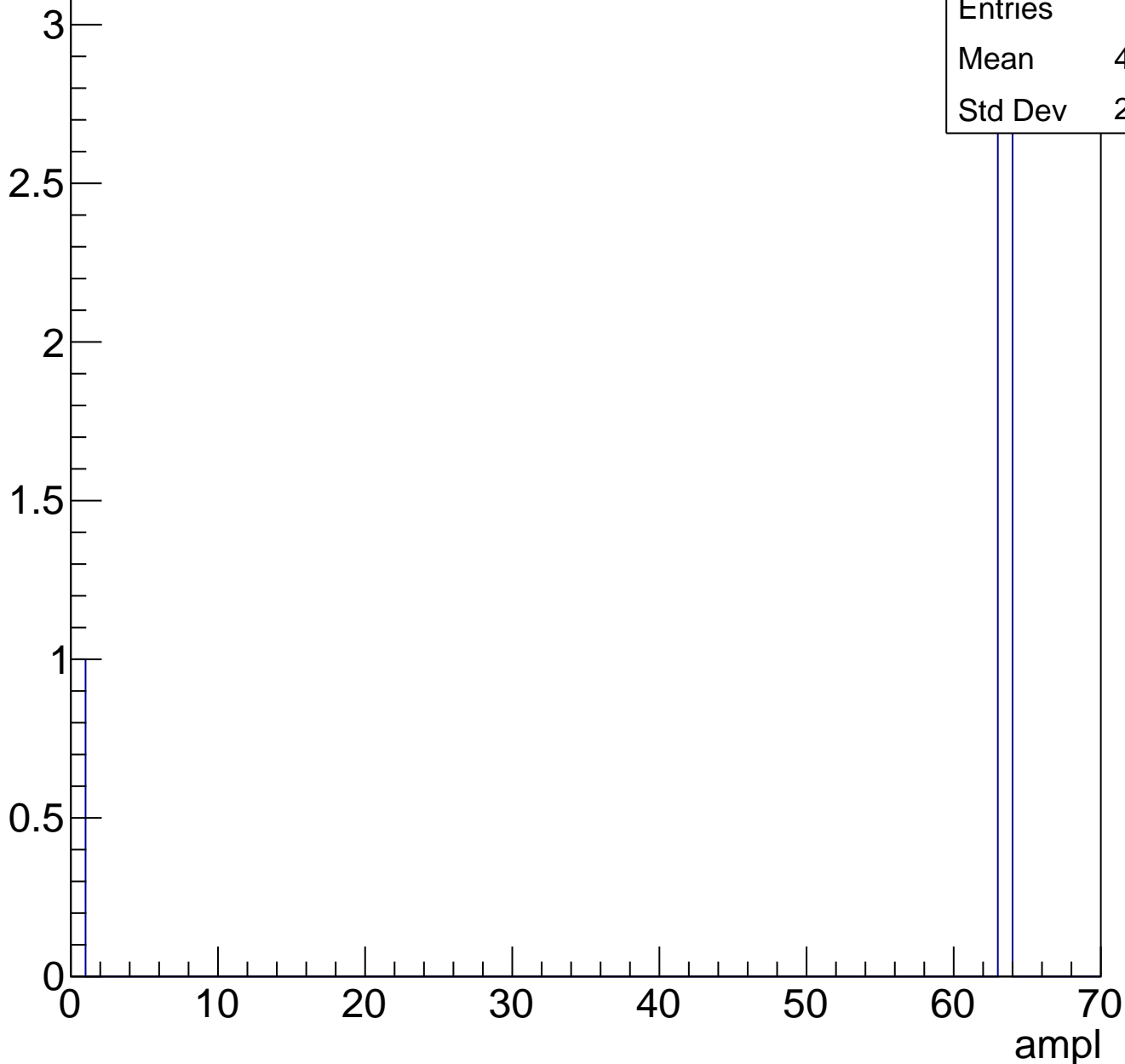
| | |
|---------|-------|
| Entries | 33 |
| Mean | 60.82 |
| Std Dev | 1.93 |



B0L001S, U7-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

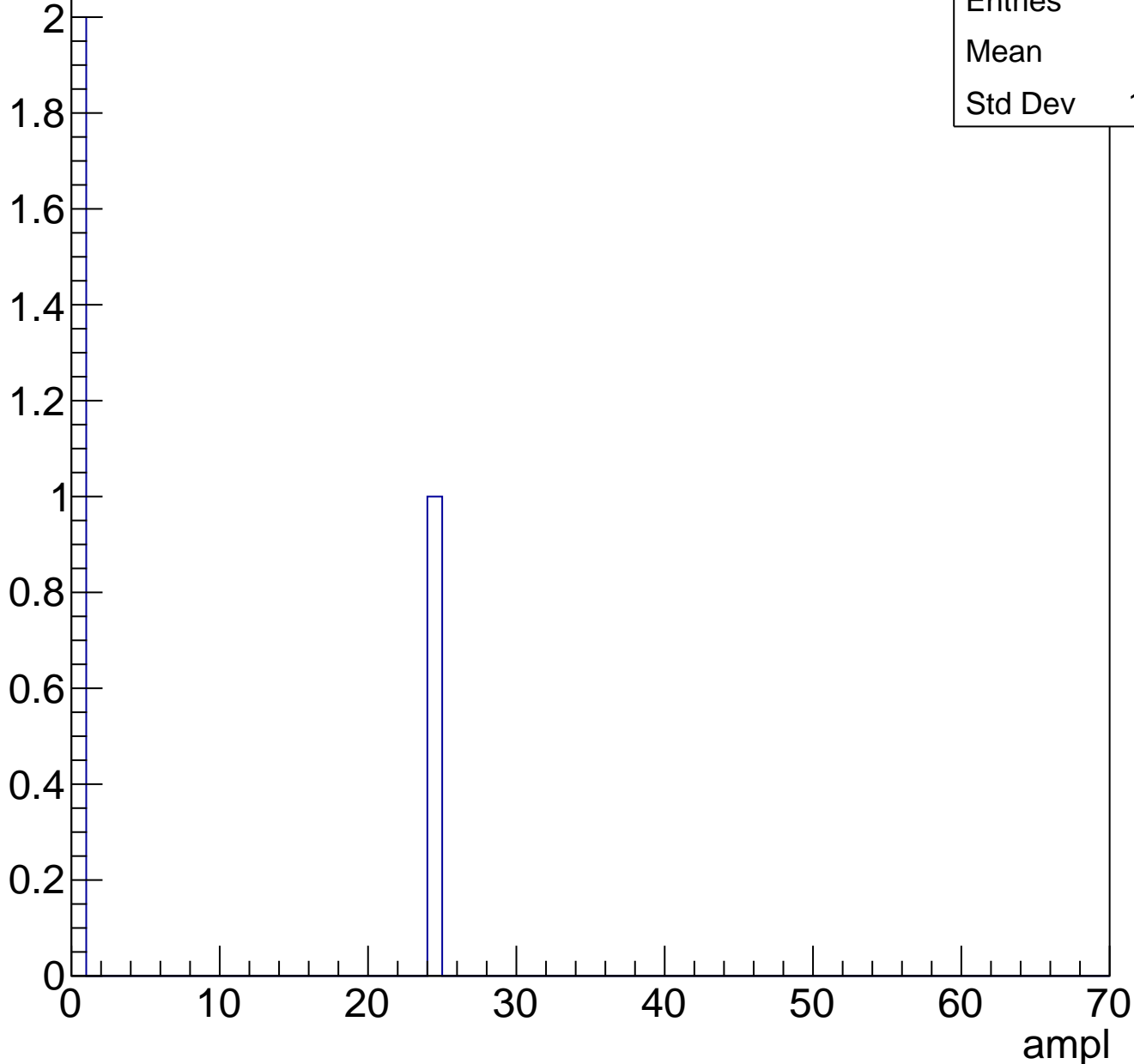
Entry



B0L001S, U7-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch27, adc0

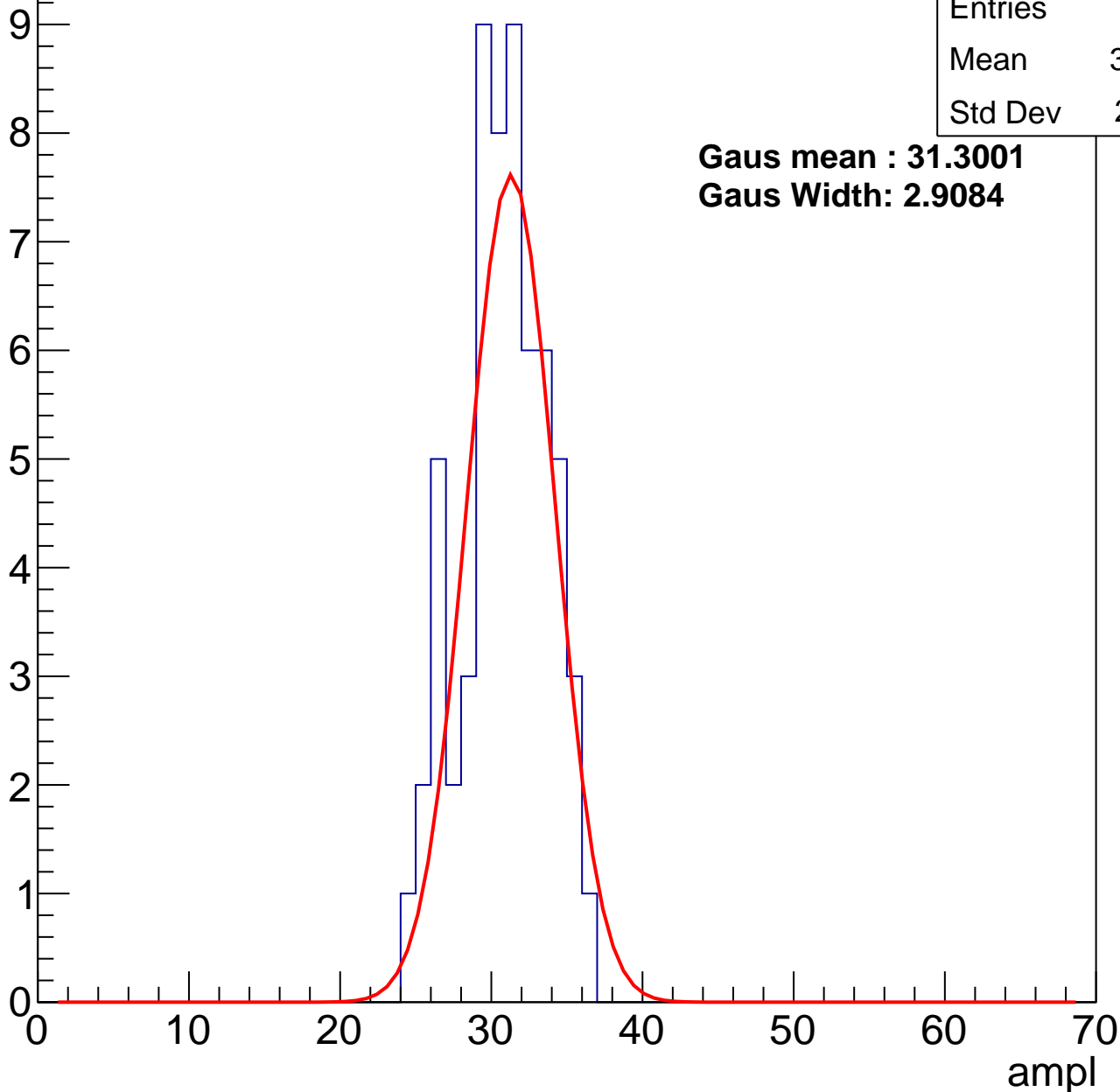
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 30.38 |
| Std Dev | 2.811 |

Gaus mean : 31.3001

Gaus Width: 2.9084



B0L001S, U7-ch27, adc1

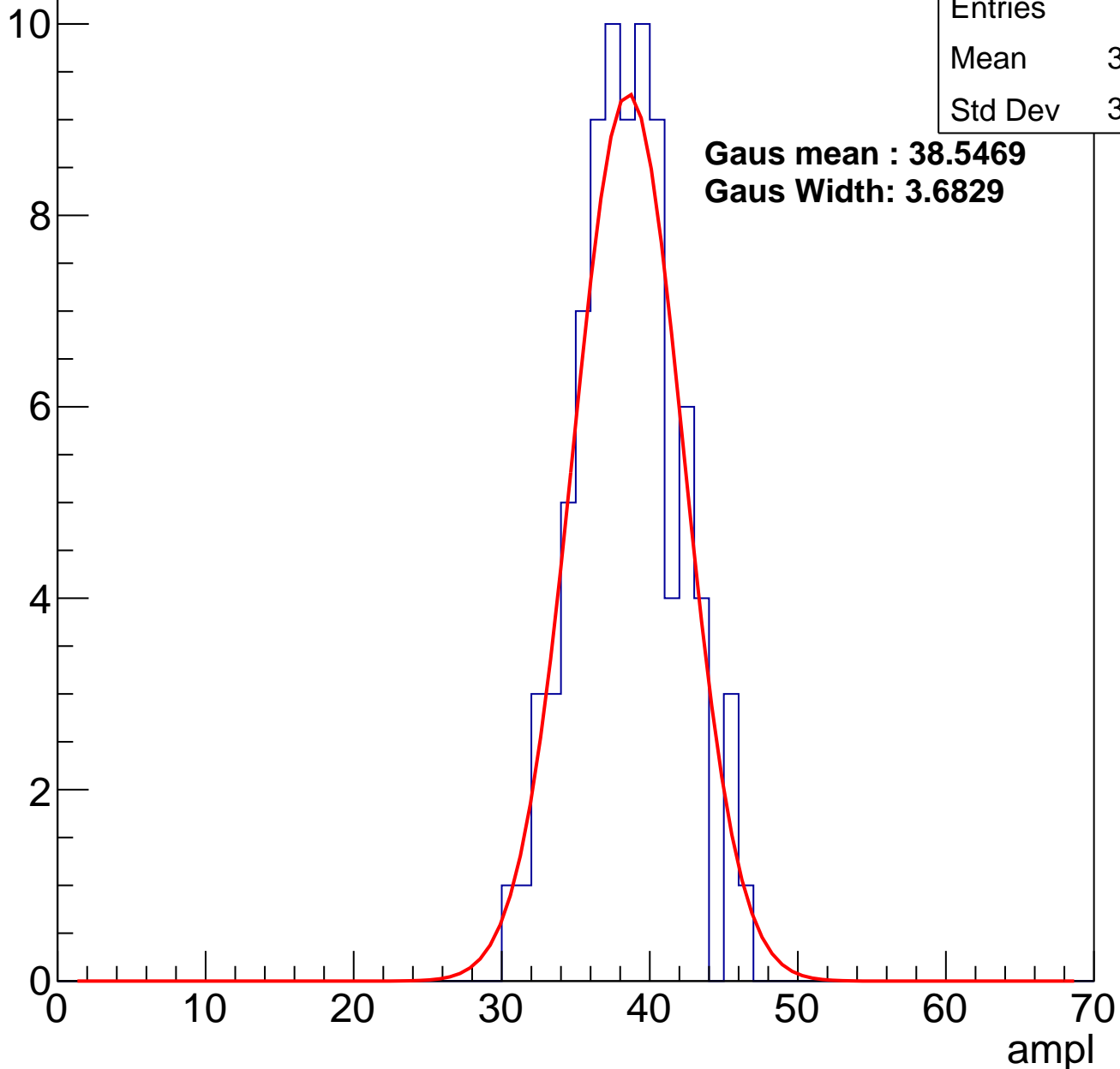
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 85 |
| Mean | 37.95 |
| Std Dev | 3.385 |

Gaus mean : 38.5469

Gaus Width: 3.6829

Entry



B0L001S, U7-ch27, adc2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 62 |
| Mean | 45.16 |
| Std Dev | 2.985 |

Gaus mean : 45.7643

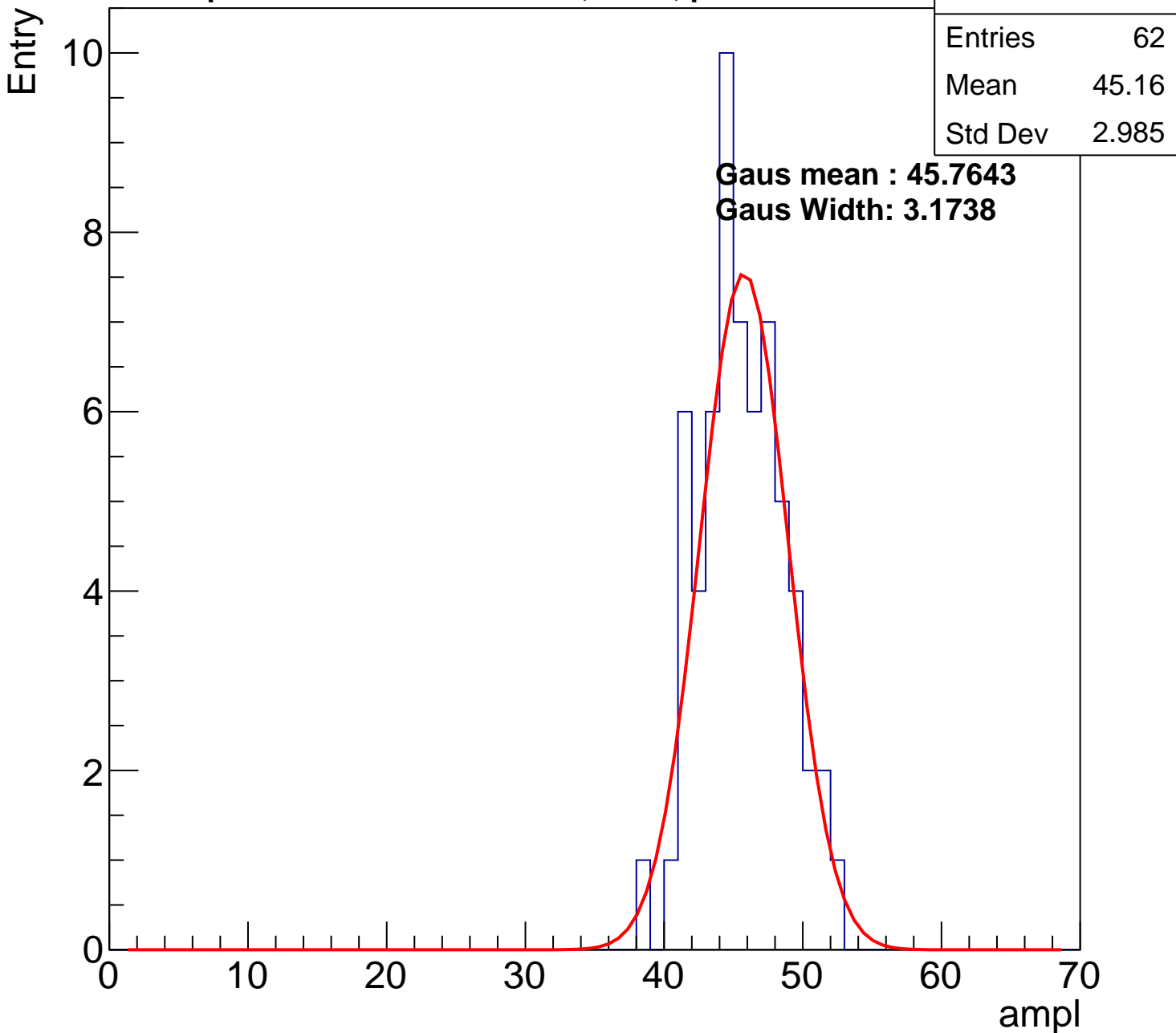
Gaus Width: 3.1738

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

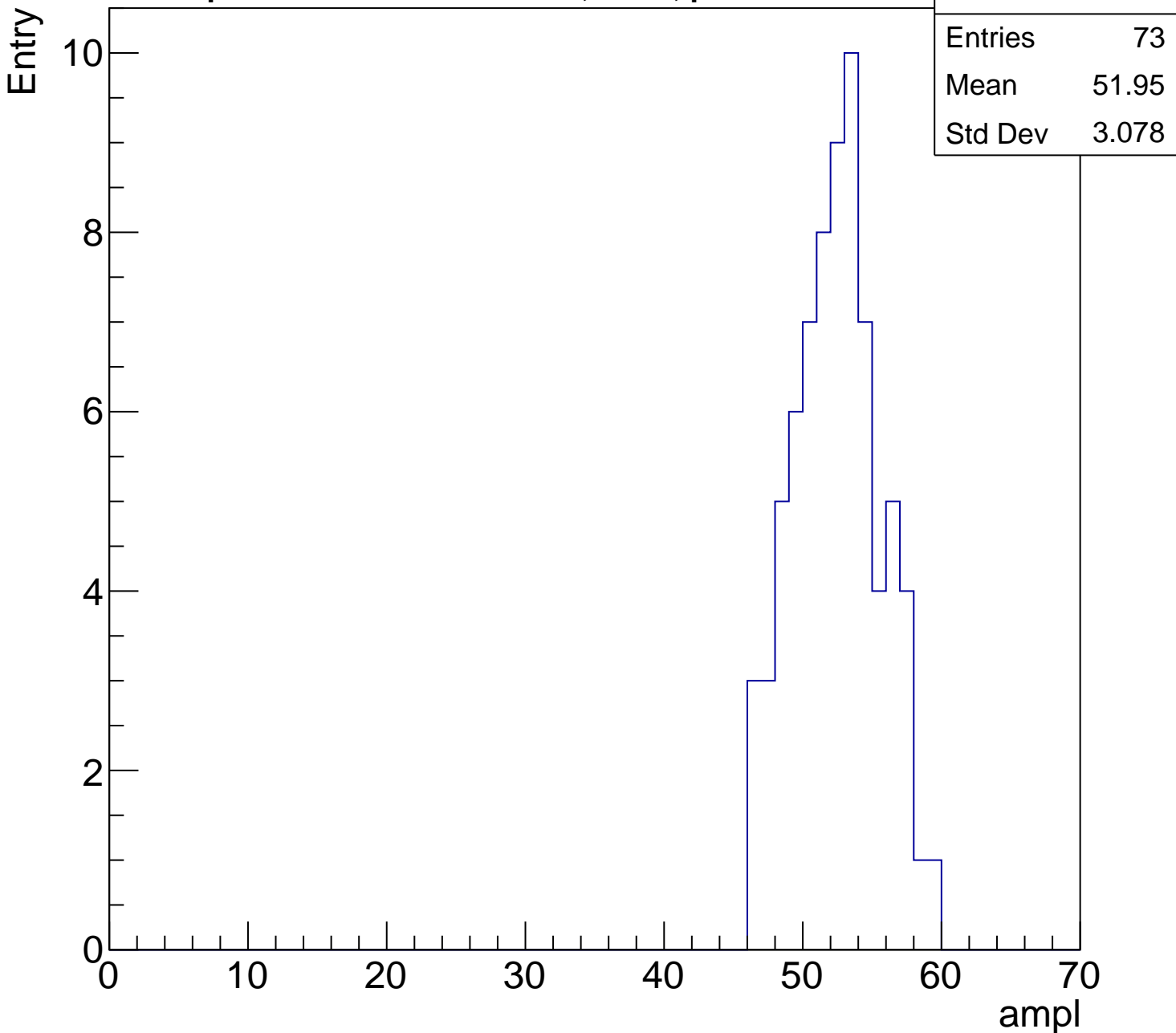
| | |
|---------|-------|
| Entries | 73 |
| Mean | 51.95 |
| Std Dev | 3.078 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

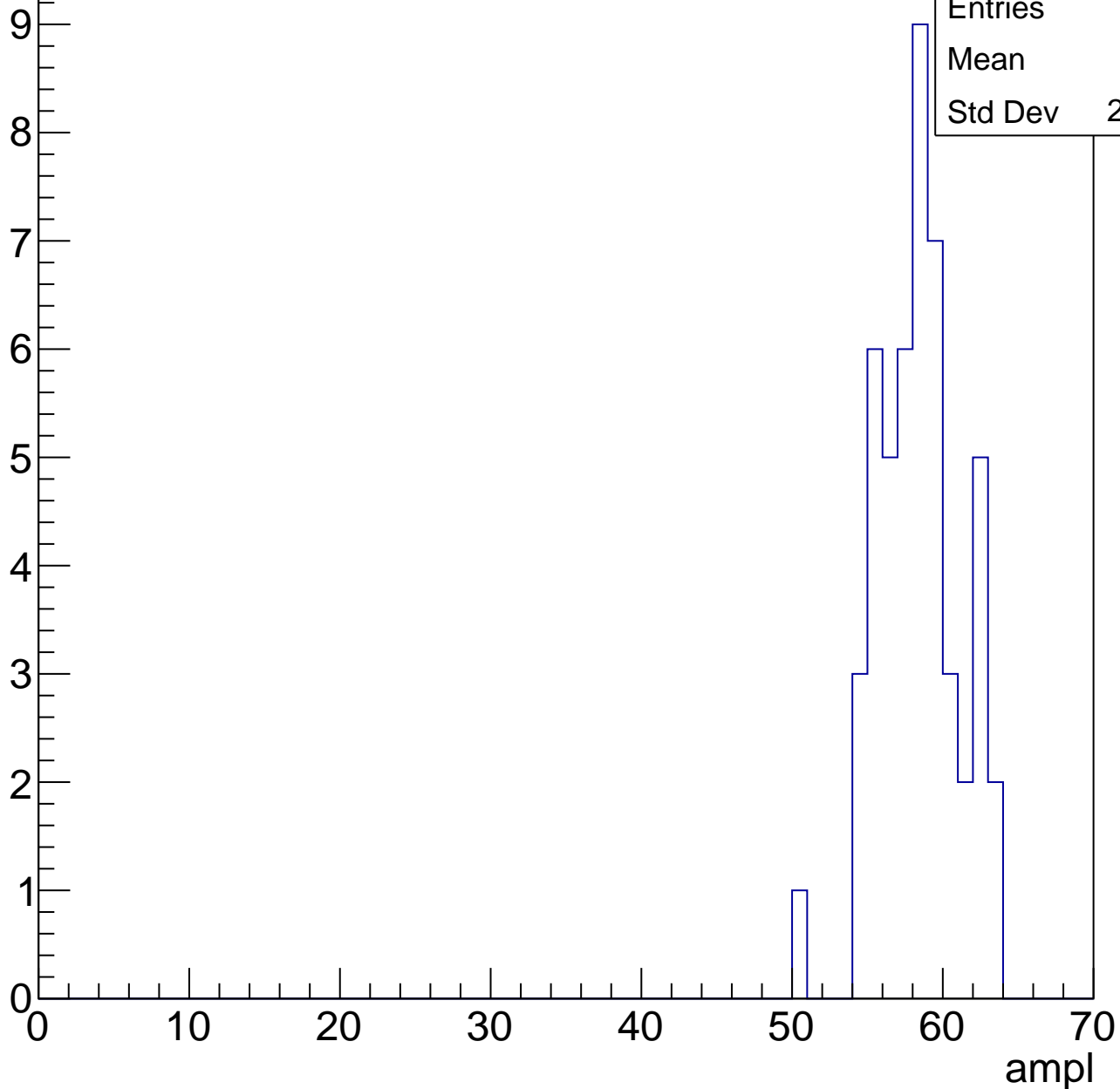
ampl



B0L001S, U7-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

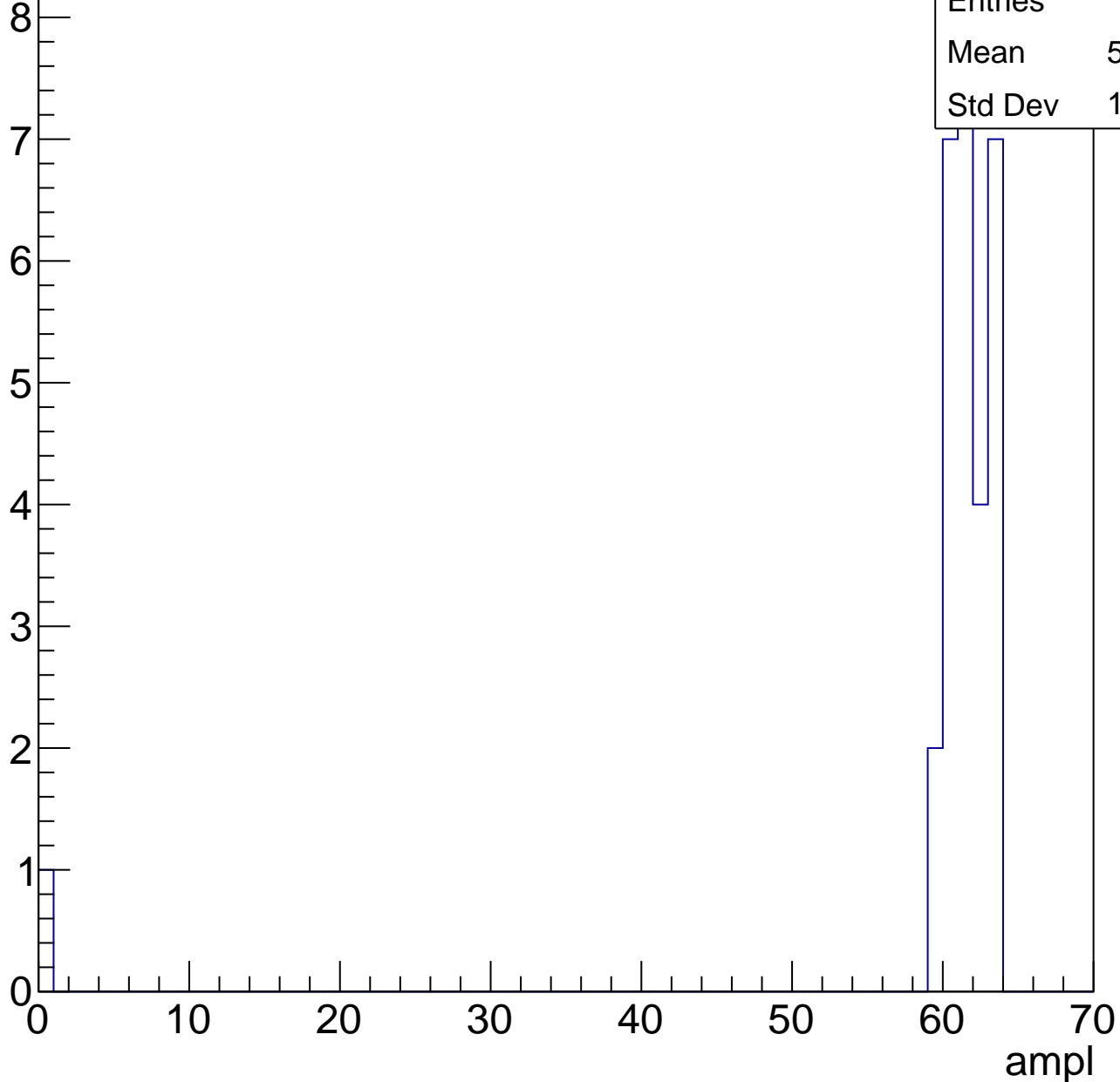
Entry



B0L001S, U7-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch28, adc0

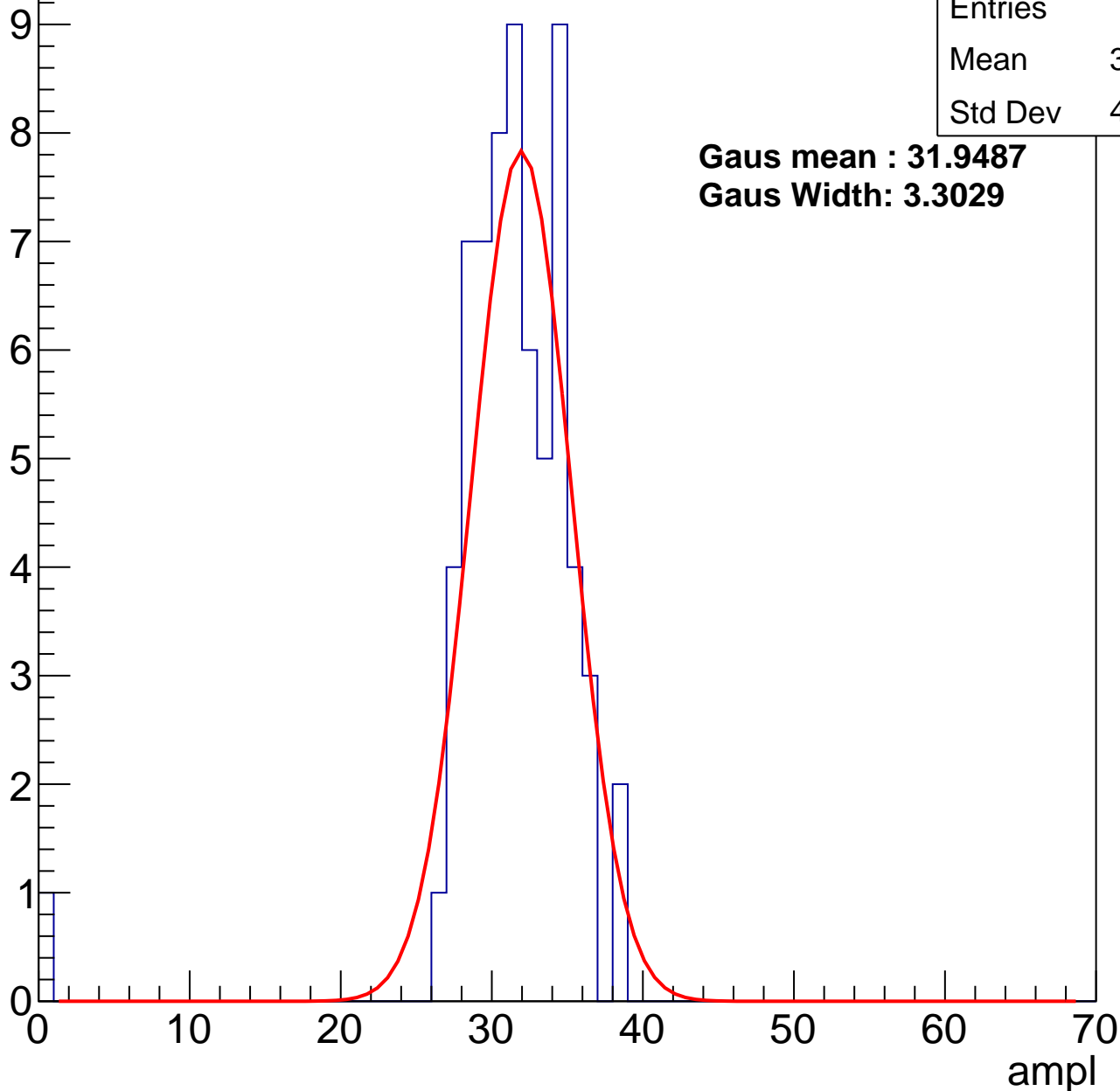
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 30.89 |
| Std Dev | 4.755 |

Gaus mean : 31.9487

Gaus Width: 3.3029



B0L001S, U7-ch28, adc1

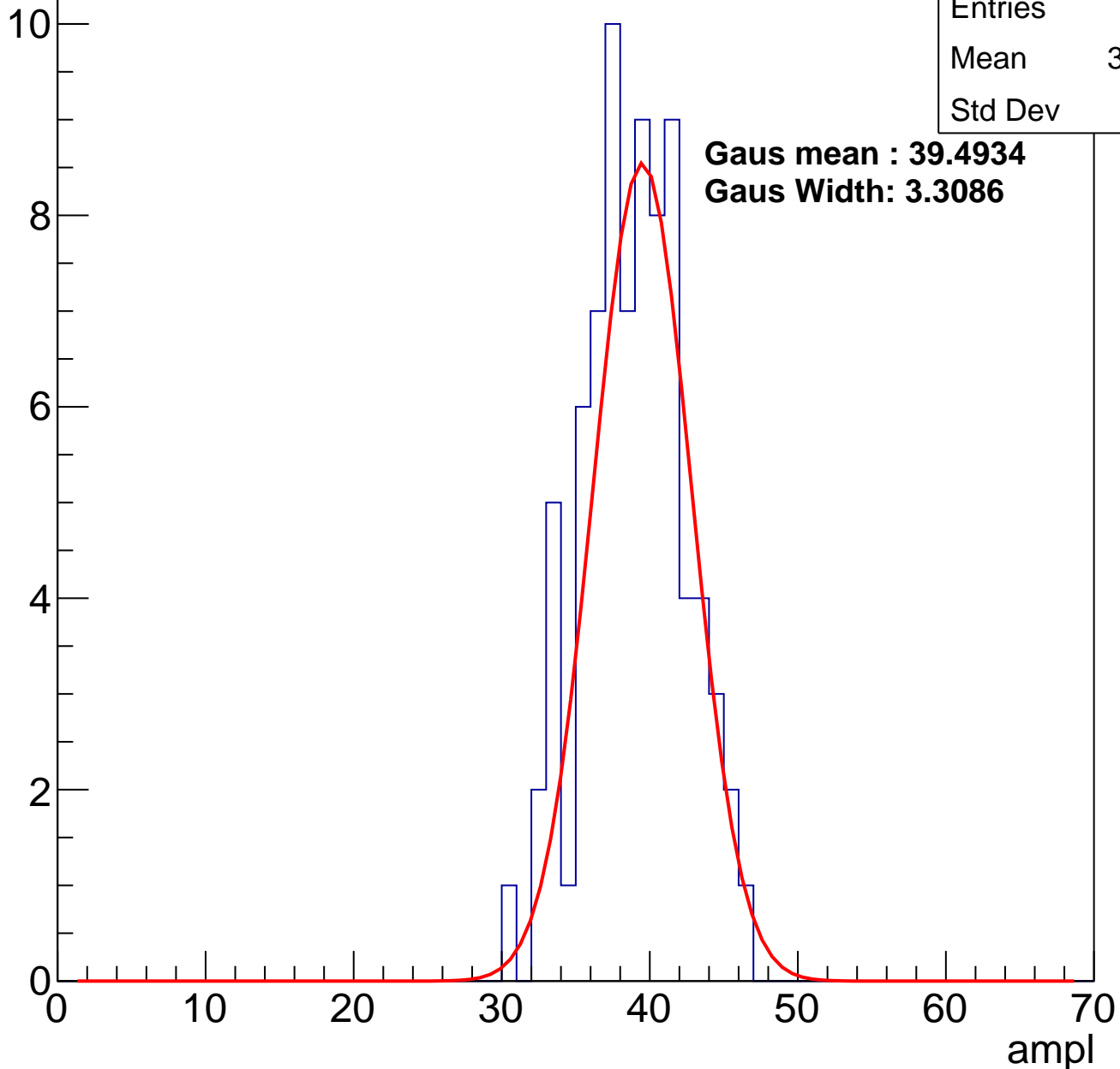
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 79 |
| Mean | 38.47 |
| Std Dev | 3.39 |

Gaus mean : 39.4934

Gaus Width: 3.3086

Entry



B0L001S, U7-ch28, adc2

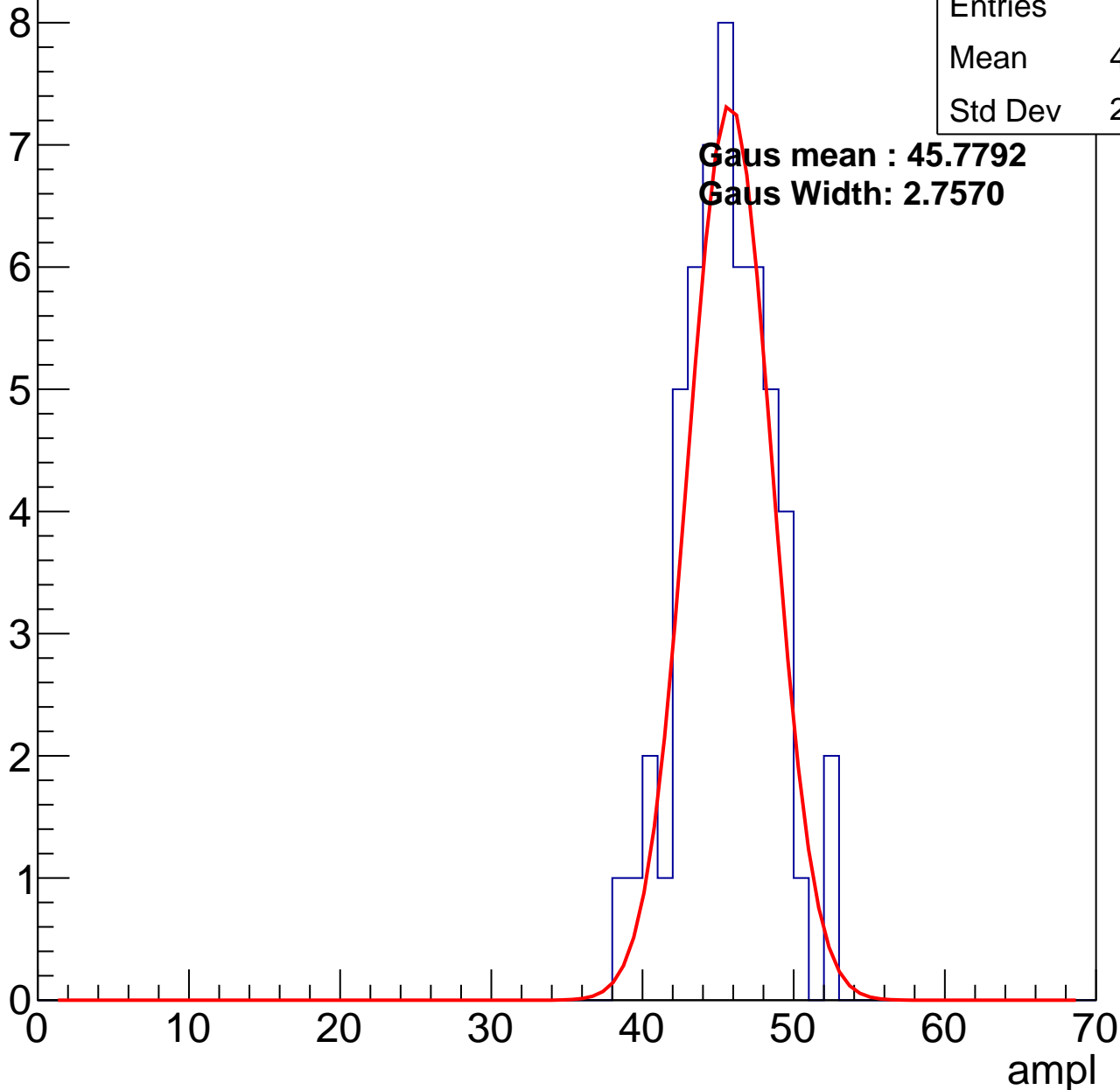
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 45.13 |
| Std Dev | 2.979 |

Gaus mean : 45.7792

Gaus Width: 2.7570

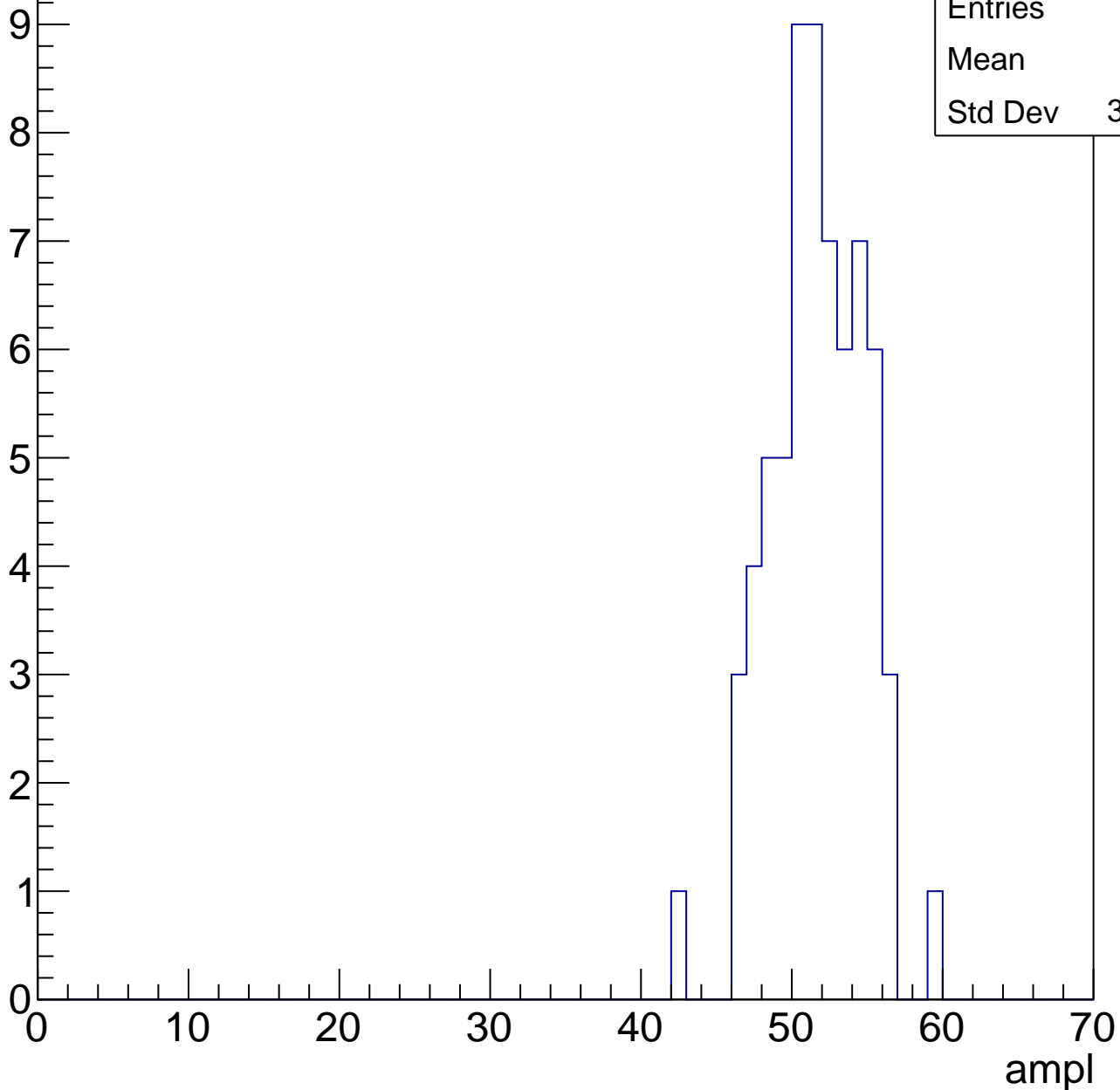


B0L001S, U7-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 51.2 |
| Std Dev | 3.066 |



B0L001S, U7-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

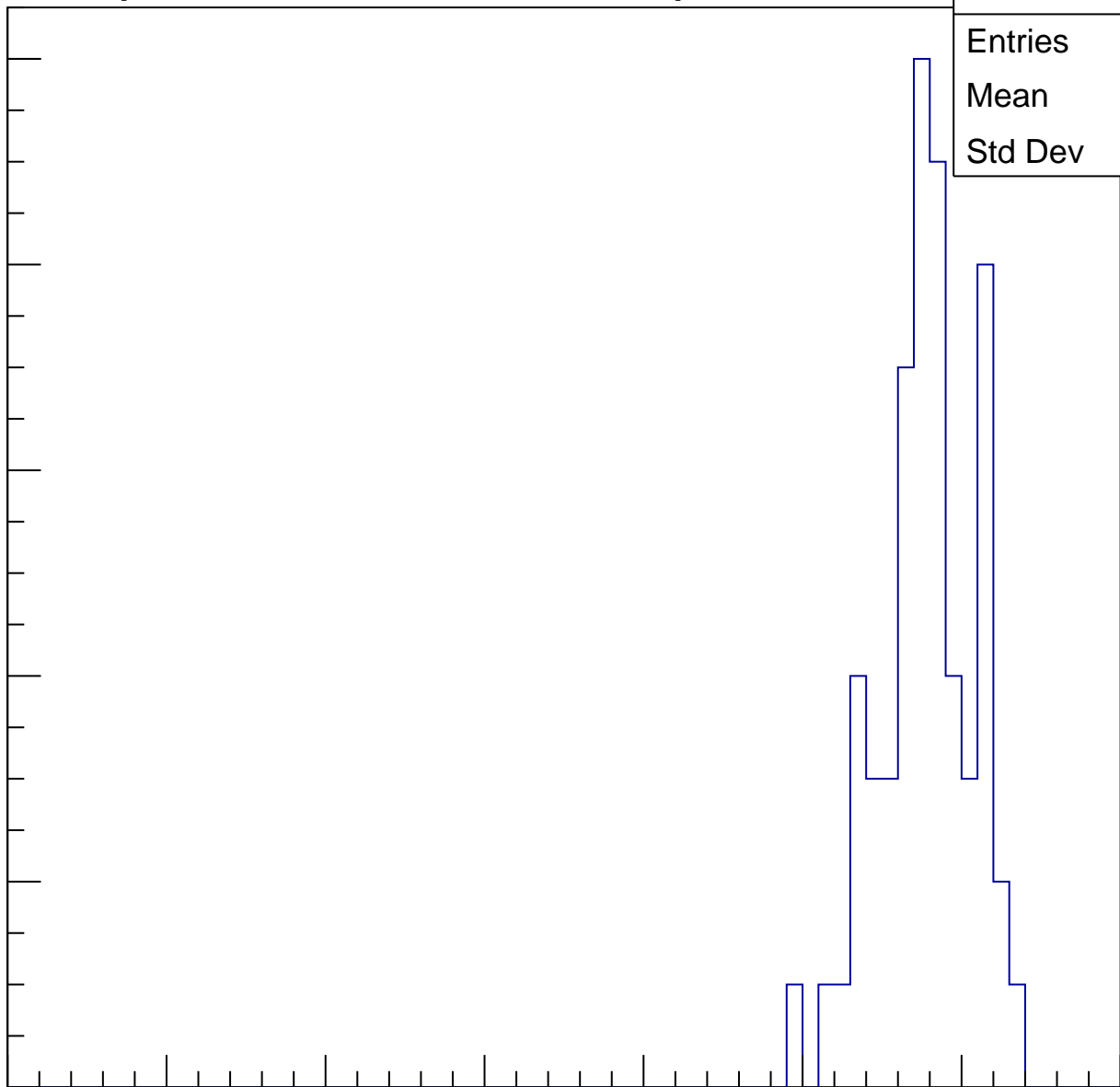
| | |
|---------|------|
| Entries | 57 |
| Mean | 57.3 |
| Std Dev | 2.95 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

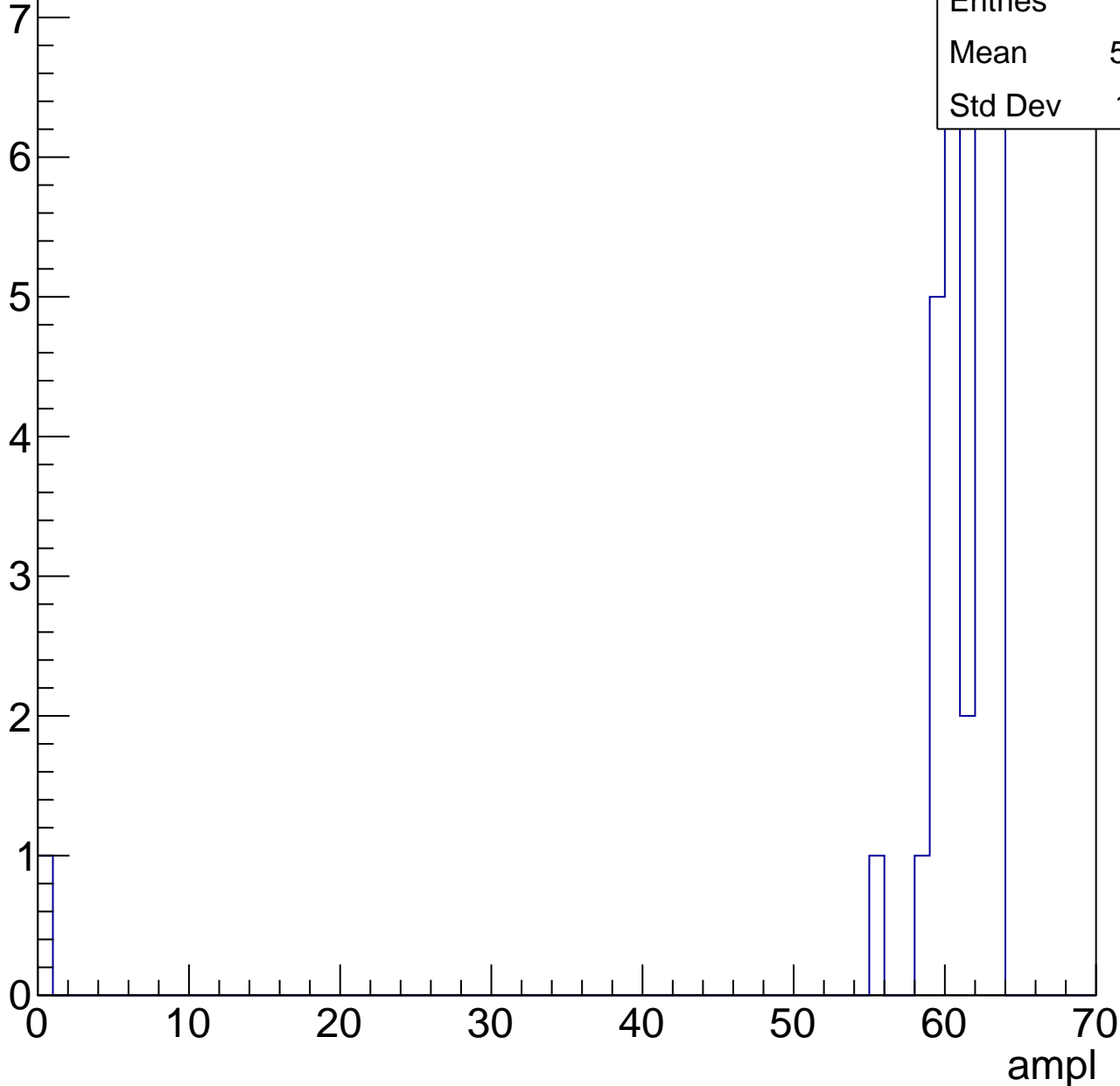


B0L001S, U7-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

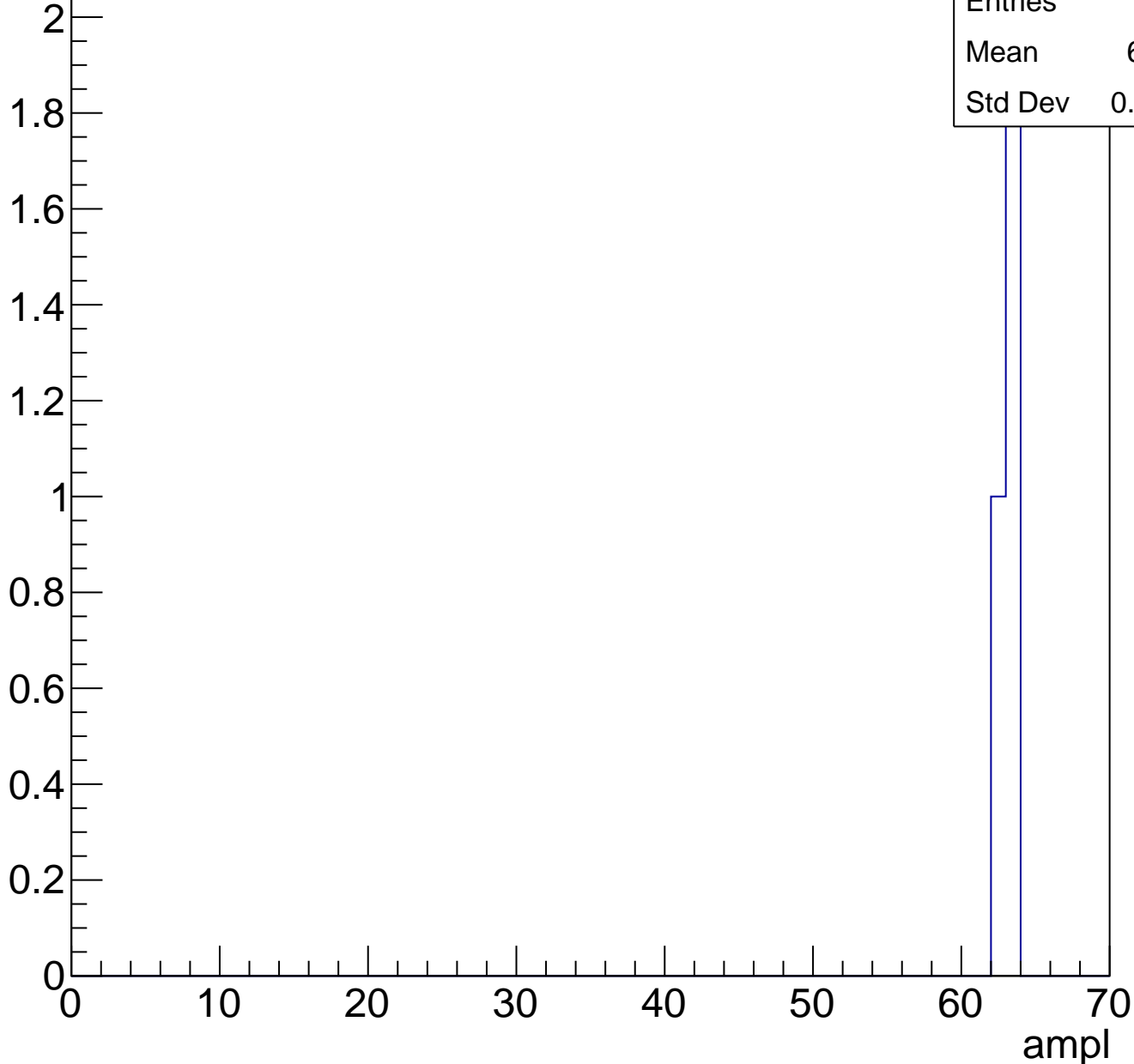
| | |
|---------|-------|
| Entries | 31 |
| Mean | 58.87 |
| Std Dev | 10.91 |



B0L001S, U7-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 25 |
| Std Dev | 0 |

B0L001S, U7-ch29, adc0

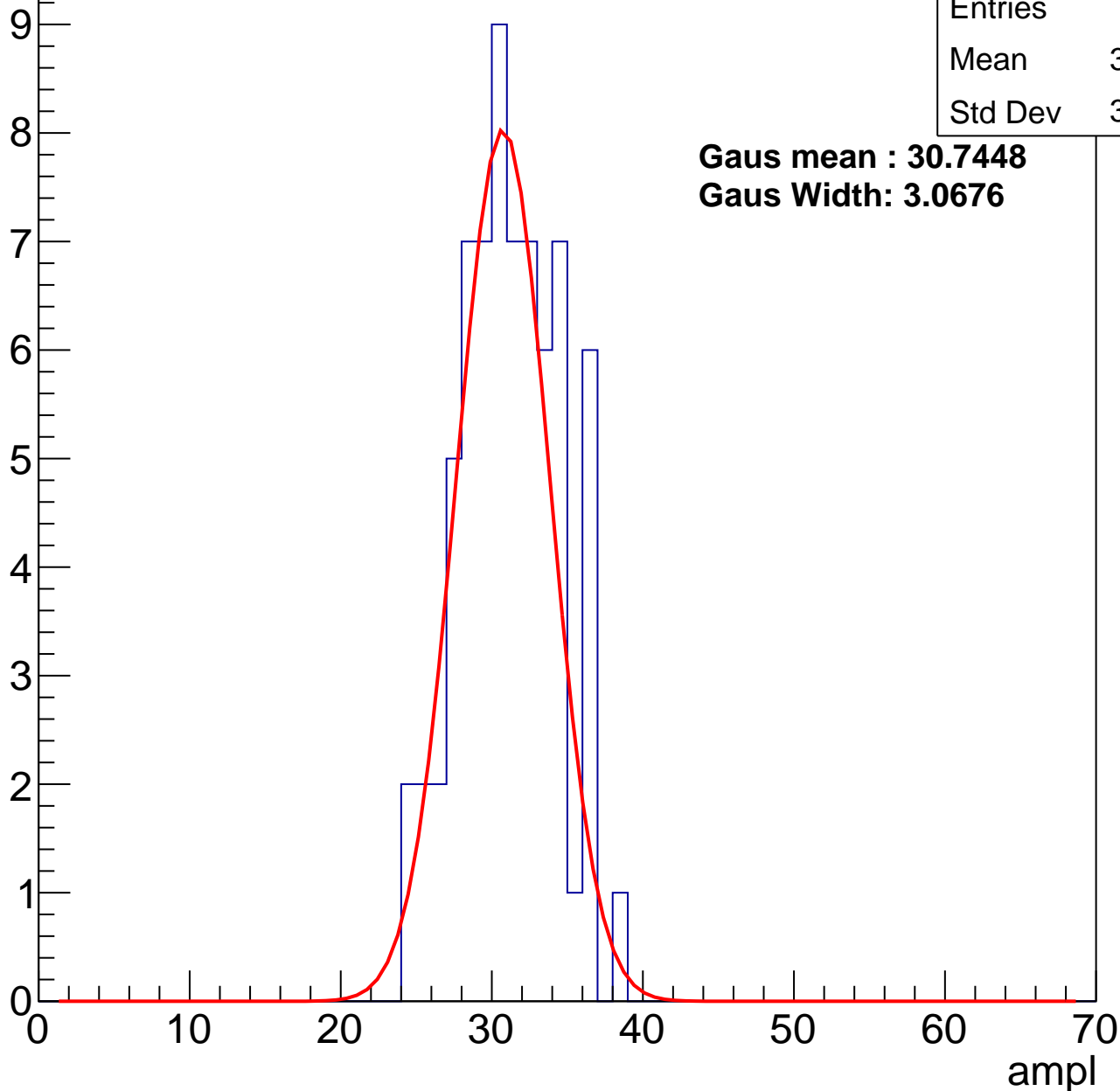
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 30.72 |
| Std Dev | 3.194 |

Gaus mean : 30.7448

Gaus Width: 3.0676



B0L001S, U7-ch29, adc1

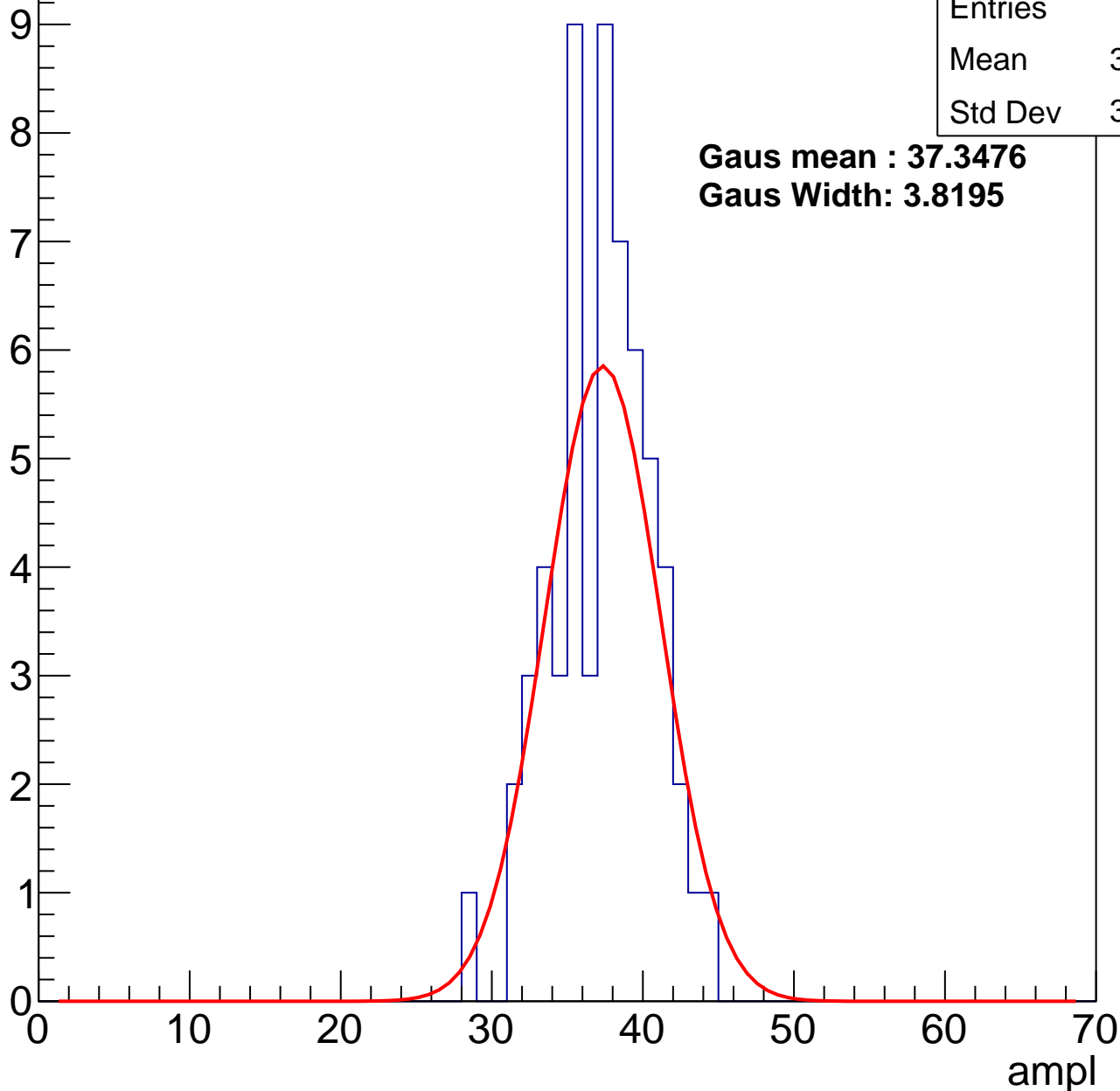
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 36.85 |
| Std Dev | 3.245 |

Gaus mean : 37.3476

Gaus Width: 3.8195



B0L001S, U7-ch29, adc2

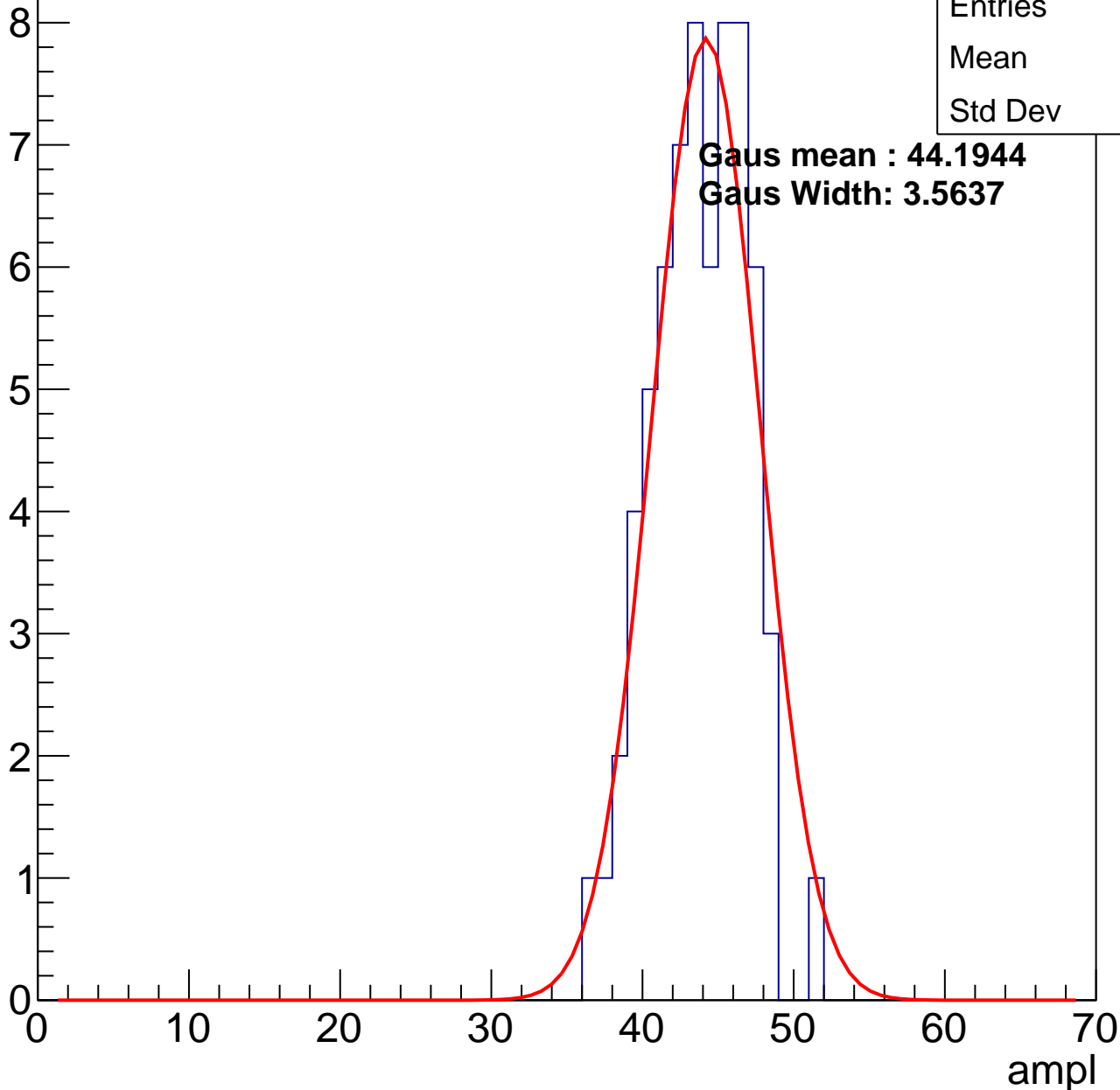
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 66 |
| Mean | 43.3 |
| Std Dev | 3.05 |

Gaus mean : 44.1944

Gaus Width: 3.5637

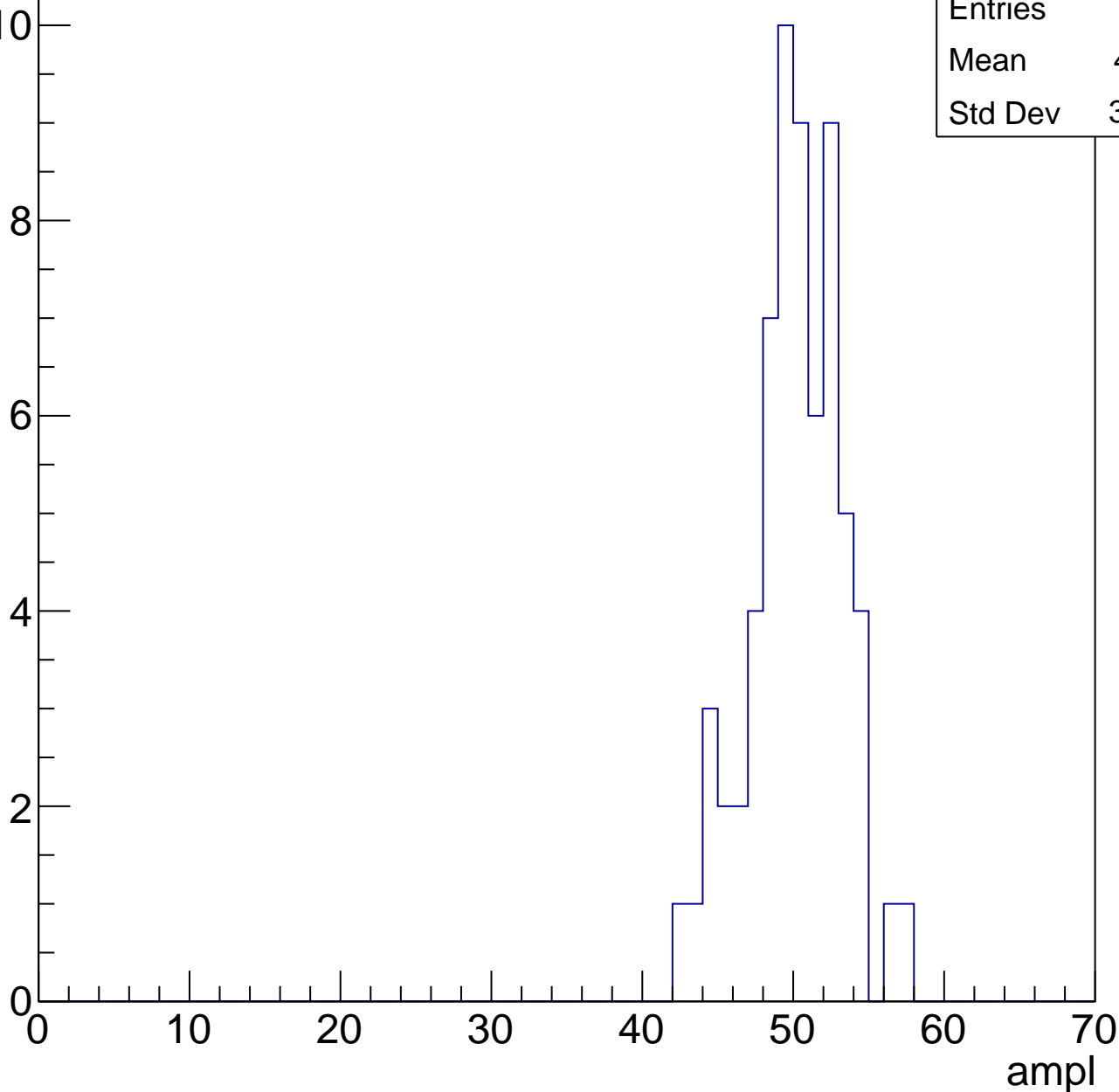


B0L001S, U7-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 49.71 |
| Std Dev | 3.057 |

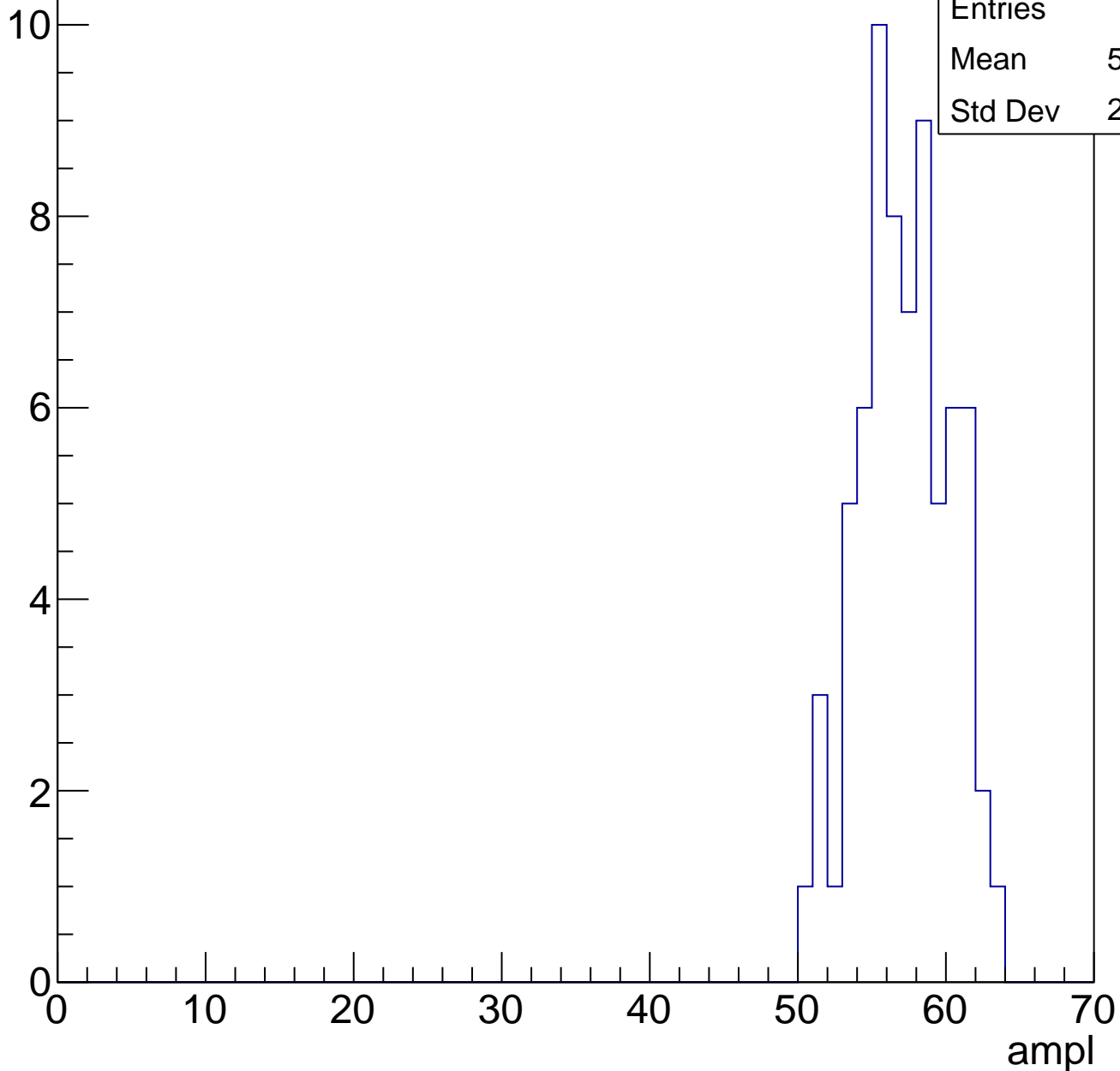


B0L001S, U7-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 56.73 |
| Std Dev | 2.985 |

Entry

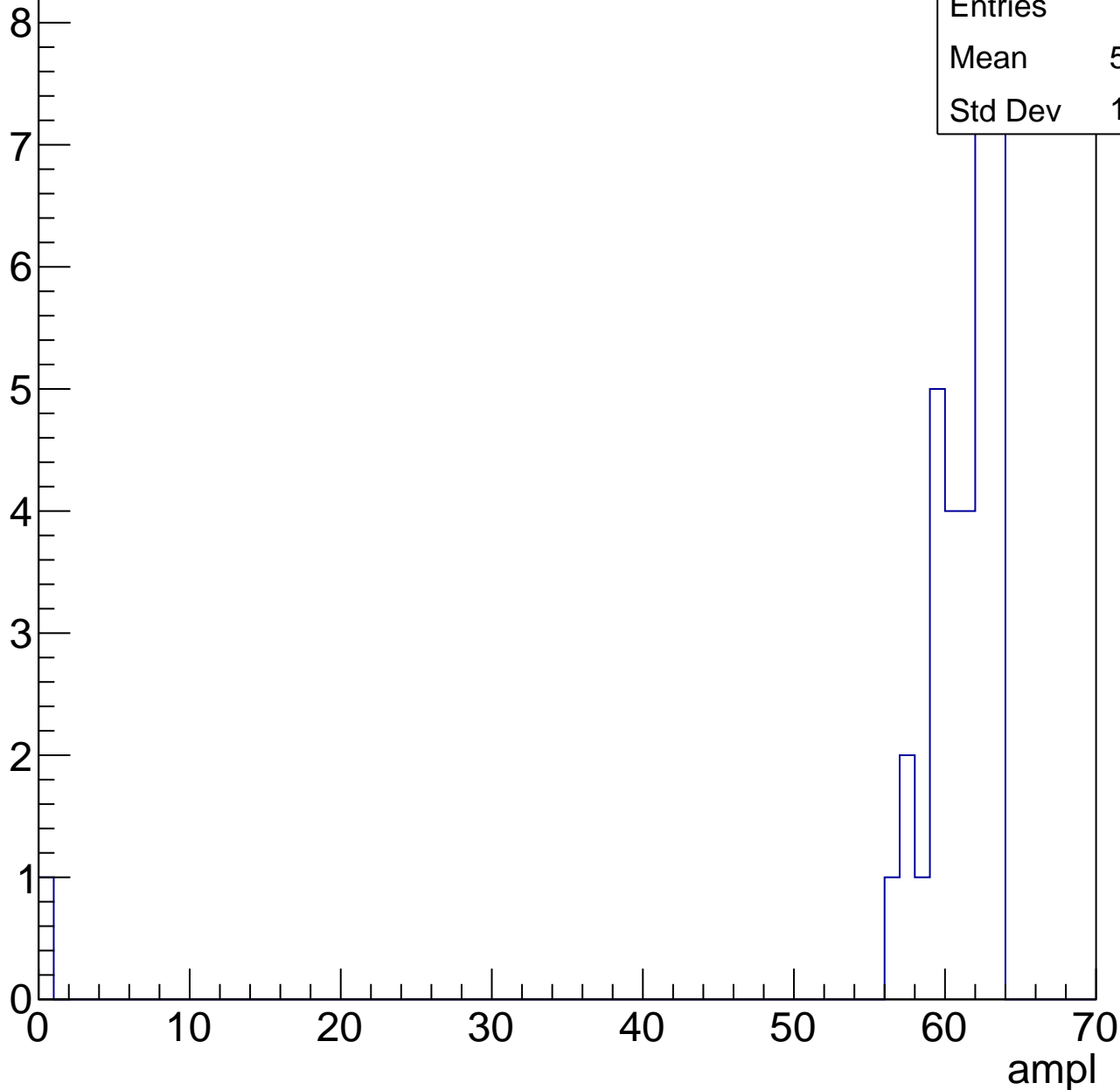


B0L001S, U7-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 59.03 |
| Std Dev | 10.46 |



B0L001S, U7-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch30, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 31.72 |
| Std Dev | 5.162 |

Gaus mean : 32.2561

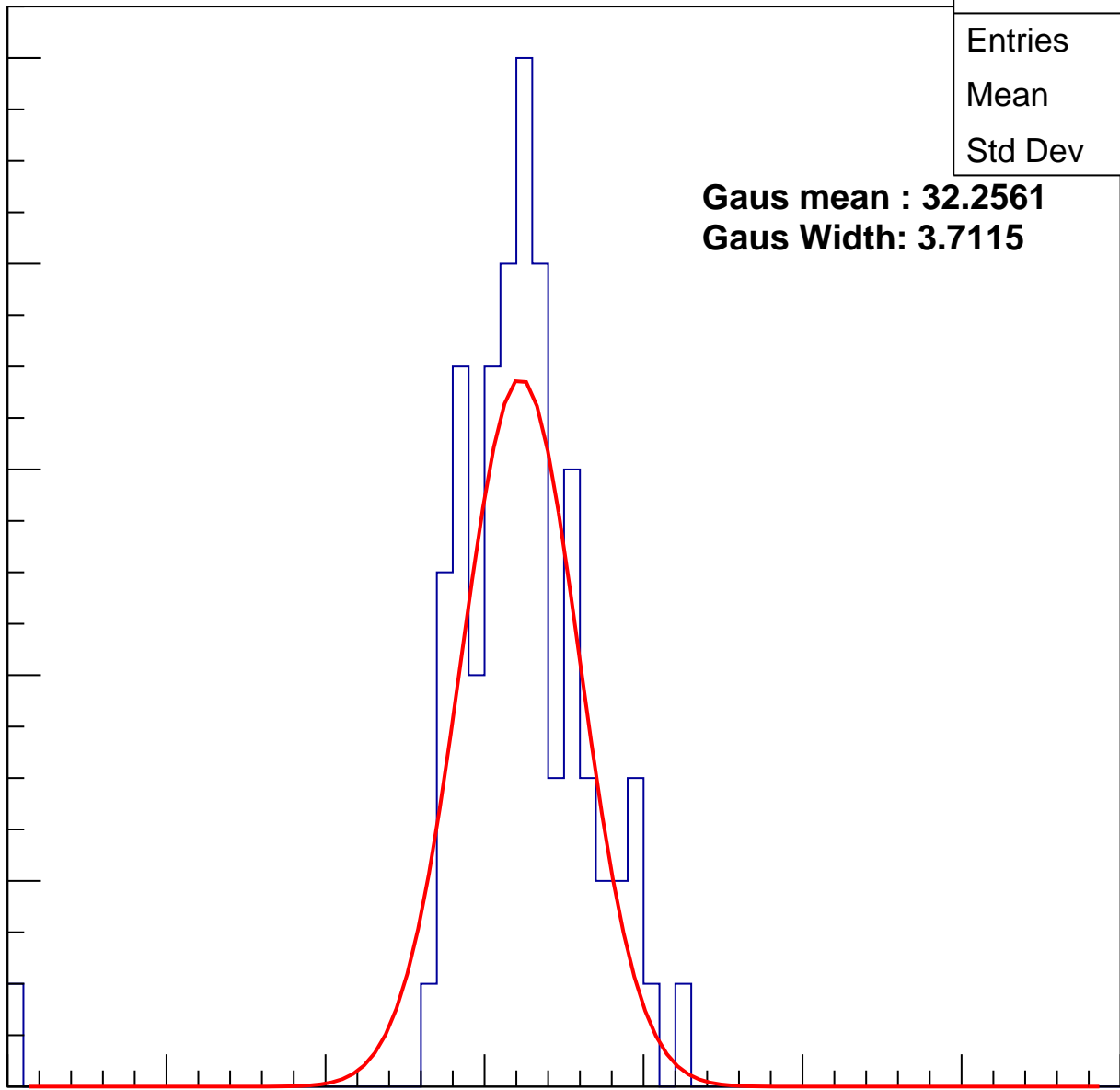
Gaus Width: 3.7115

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch30, adc1

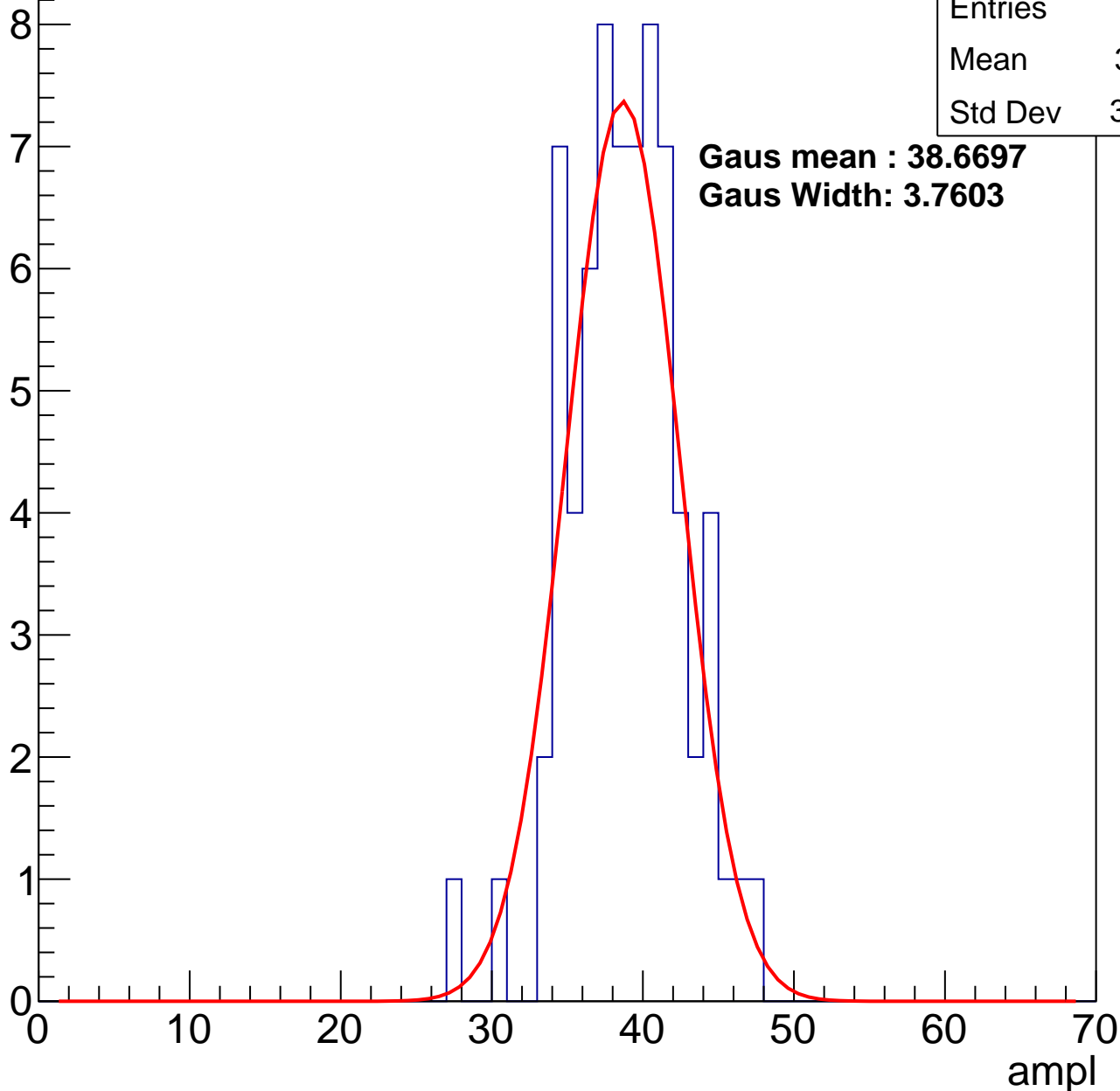
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 38.41 |
| Std Dev | 3.672 |

Gaus mean : 38.6697

Gaus Width: 3.7603



B0L001S, U7-ch30, adc2

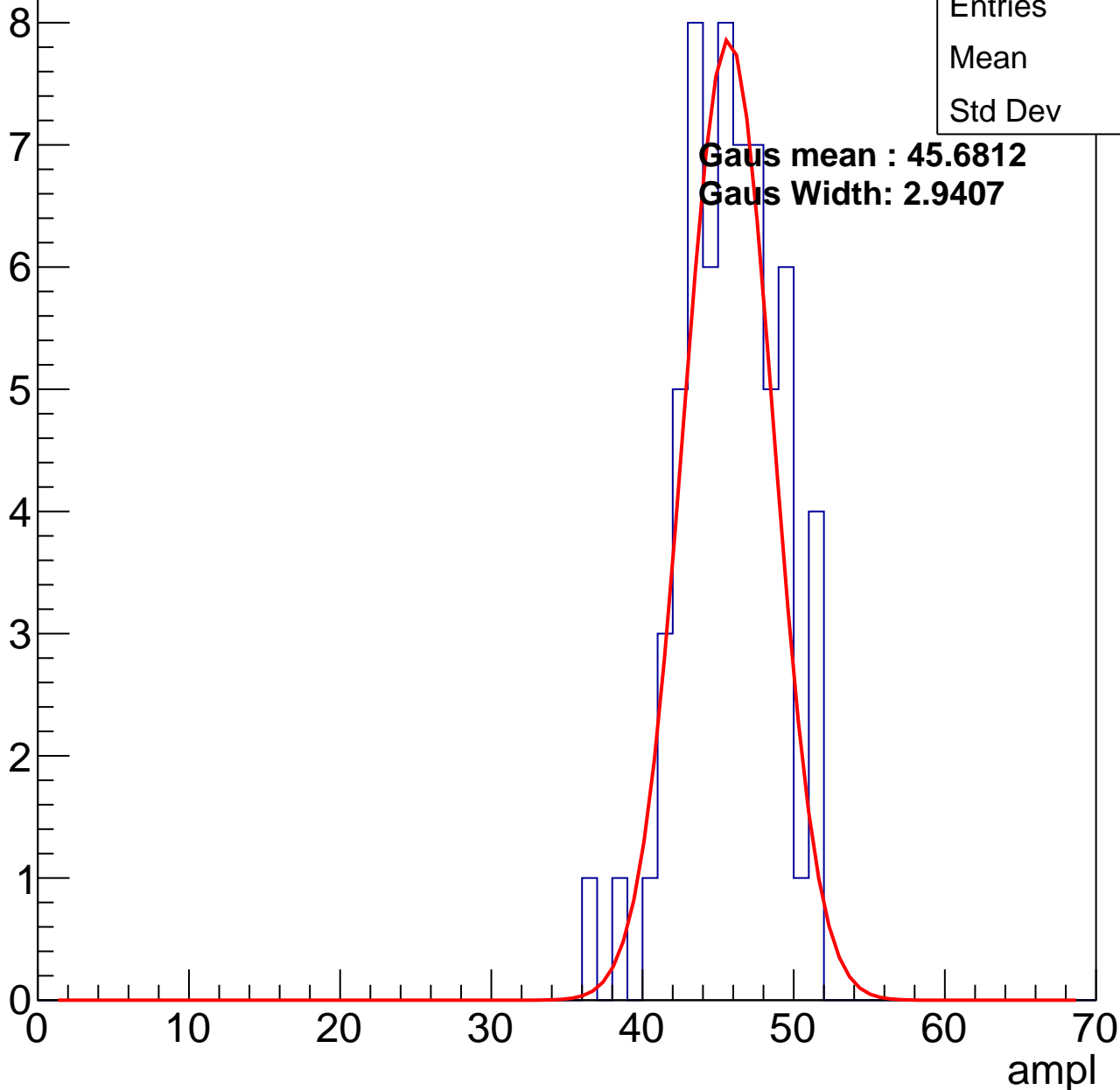
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 63 |
| Mean | 45.3 |
| Std Dev | 3.15 |

Gaus mean : 45.6812

Gaus Width: 2.9407

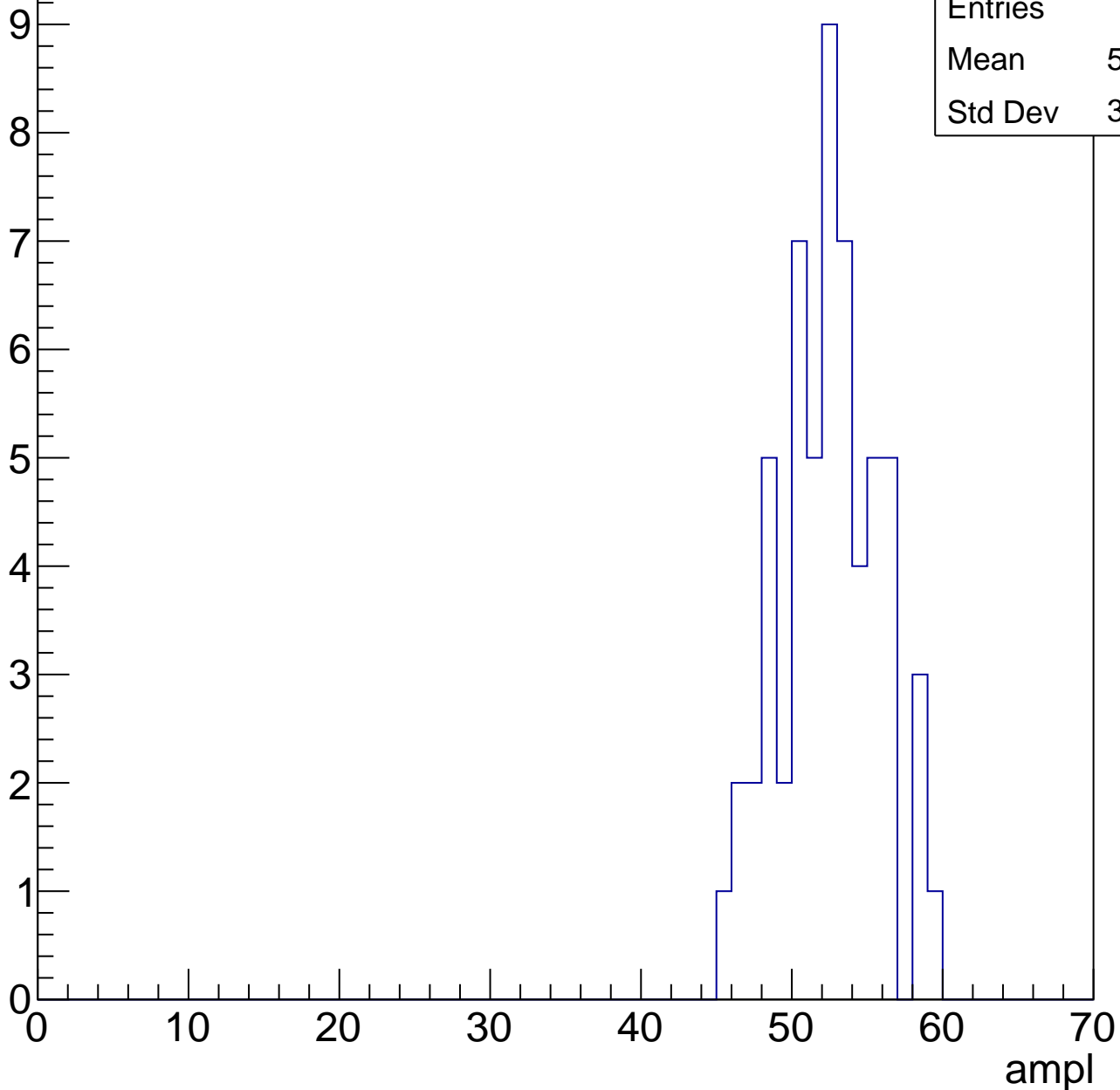


B0L001S, U7-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

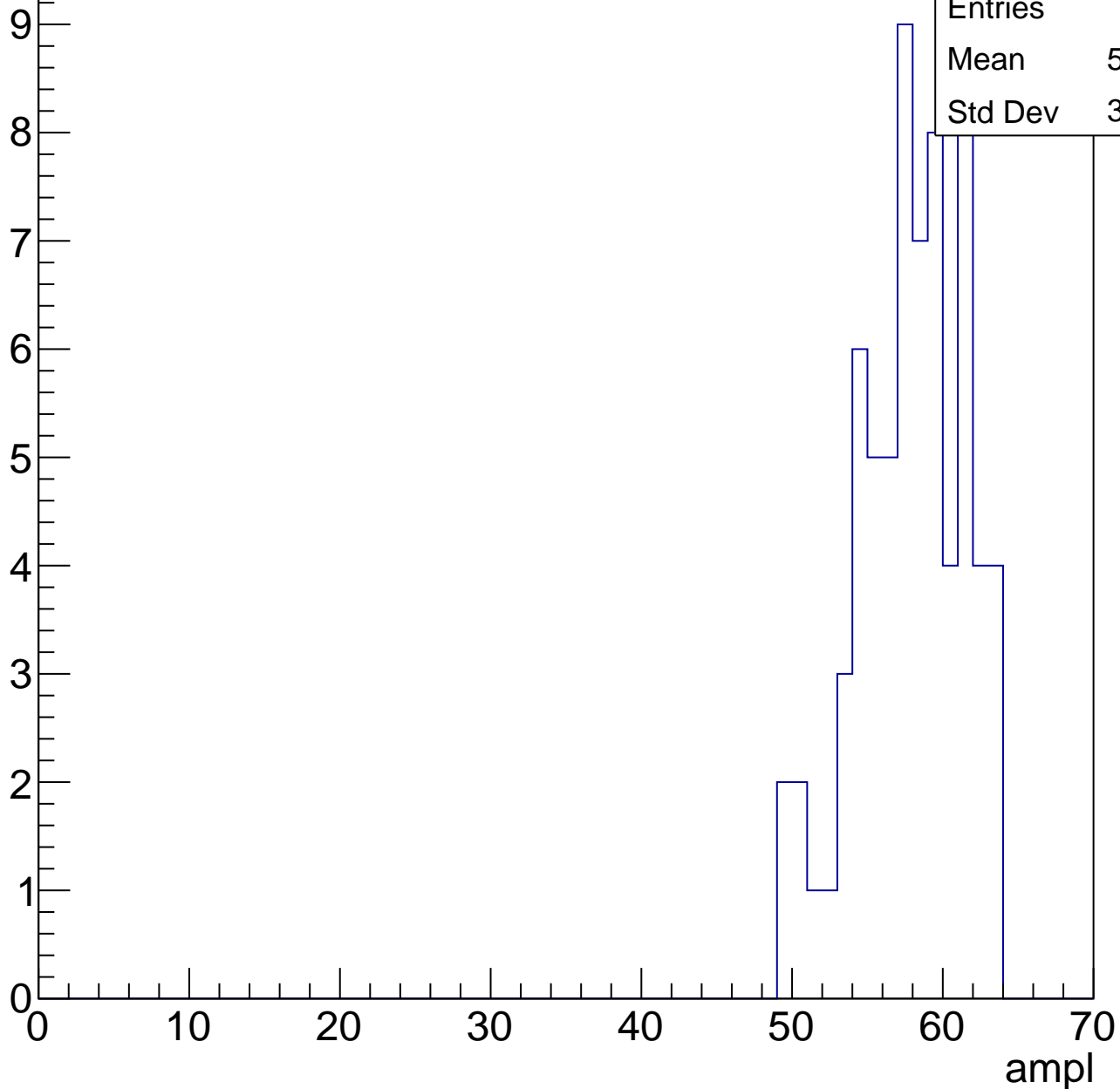
| | |
|---------|-------|
| Entries | 58 |
| Mean | 52.02 |
| Std Dev | 3.235 |



B0L001S, U7-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



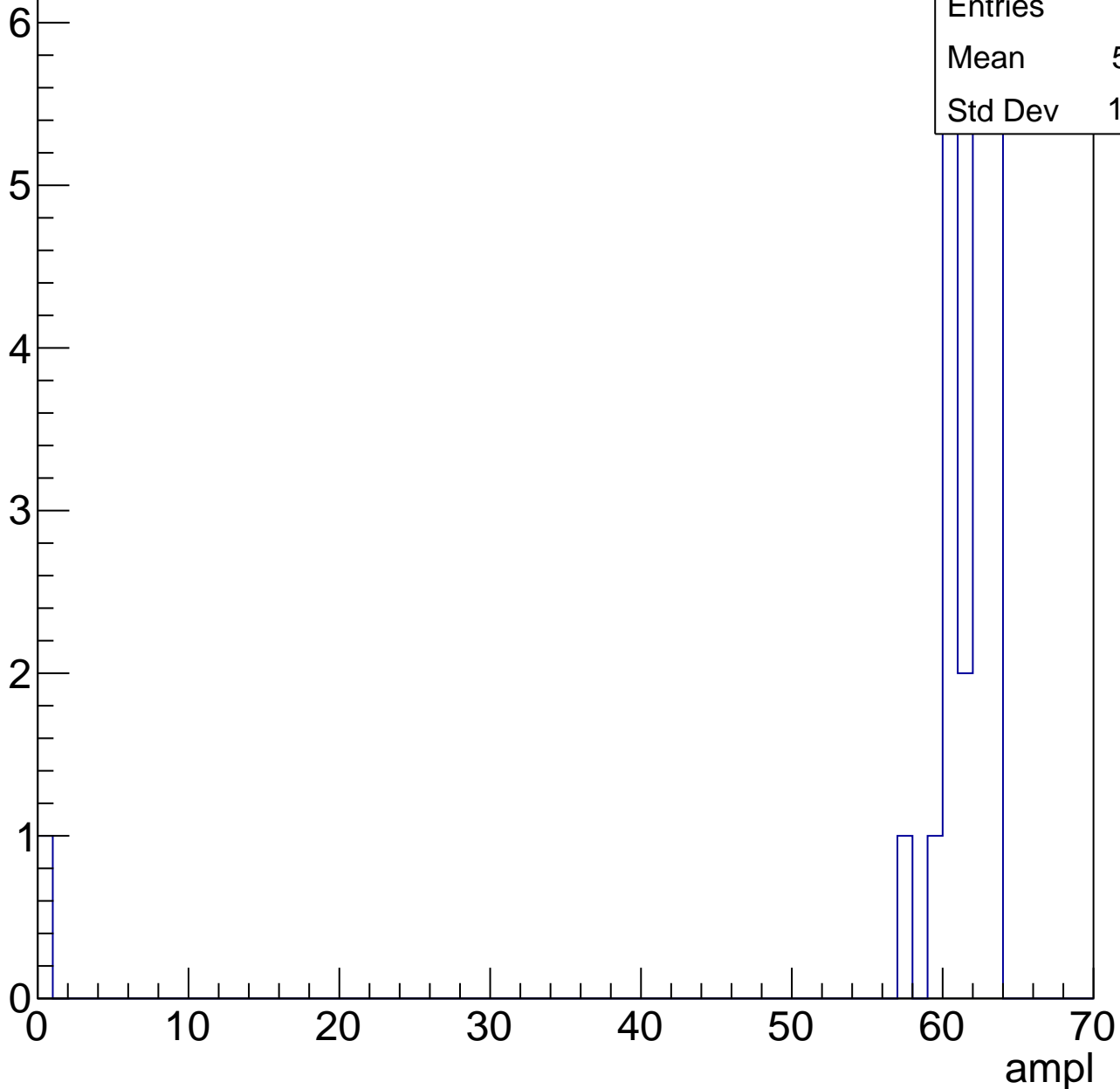
| | |
|---------|-------|
| Entries | 69 |
| Mean | 57.36 |
| Std Dev | 3.514 |

B0L001S, U7-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 23 |
| Mean | 58.61 |
| Std Dev | 12.59 |



B0L001S, U7-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch31, adc0

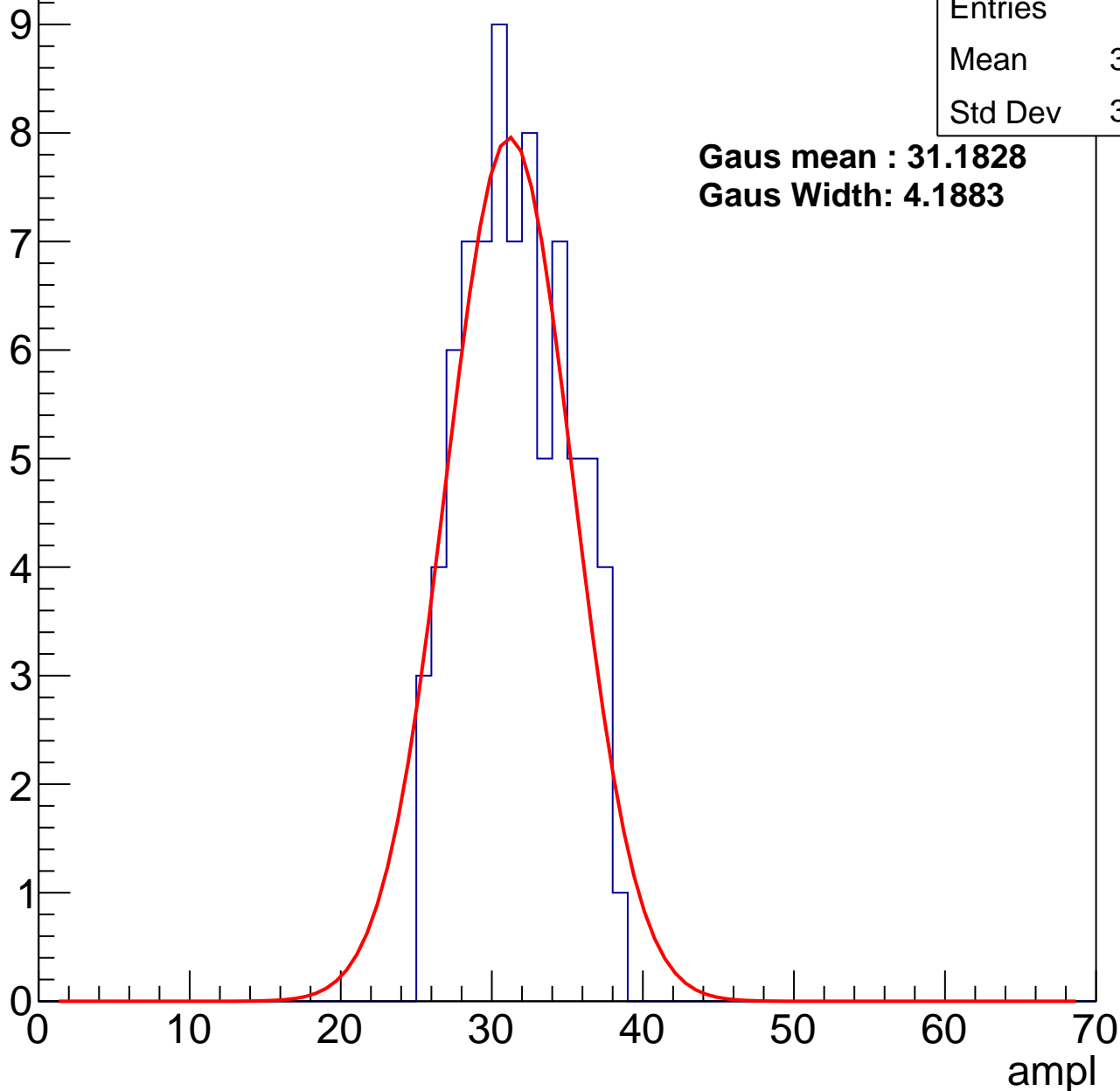
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 31.12 |
| Std Dev | 3.382 |

Gaus mean : 31.1828

Gaus Width: 4.1883



B0L001S, U7-ch31, adc1

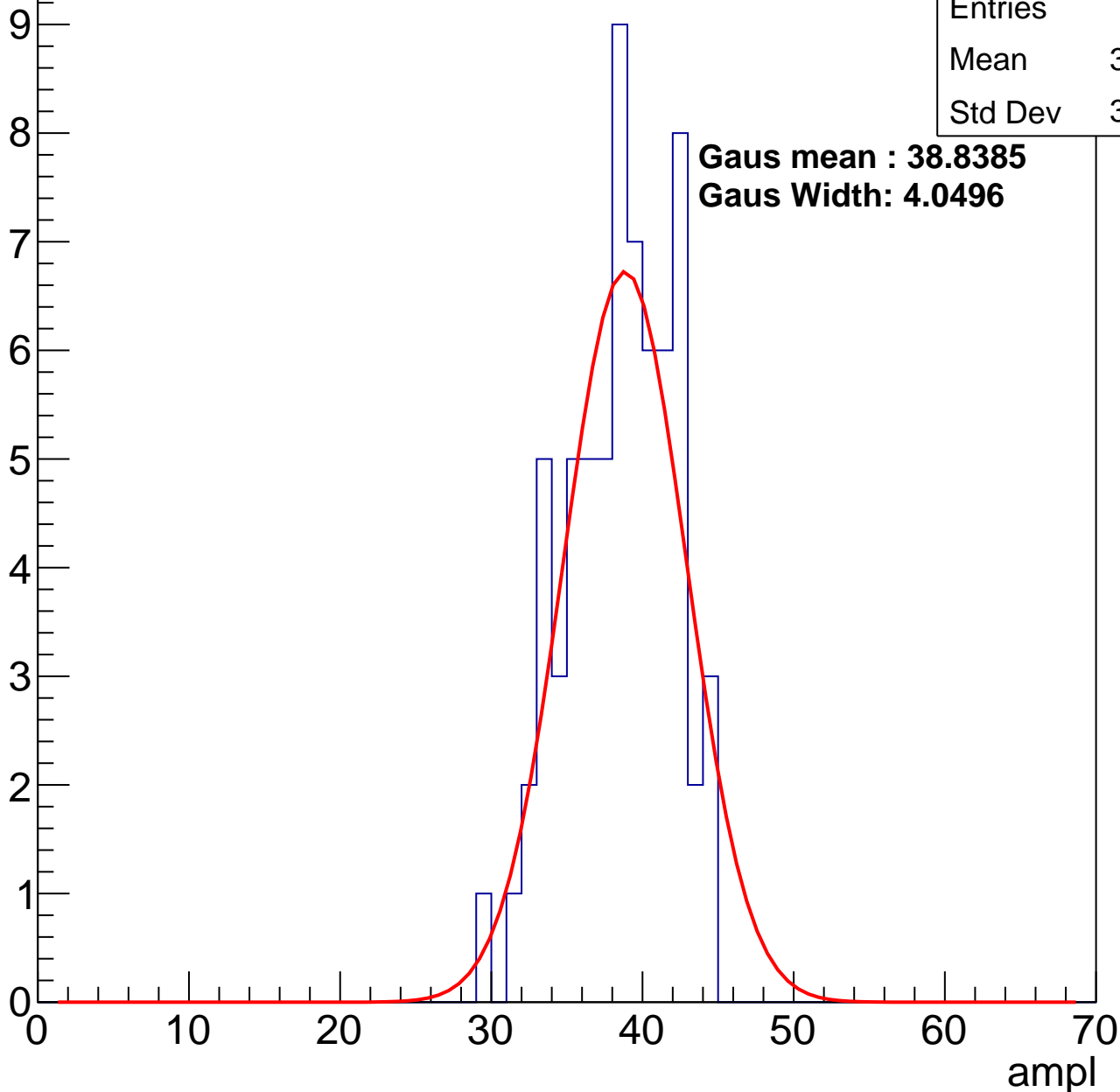
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 38.03 |
| Std Dev | 3.464 |

Gaus mean : 38.8385

Gaus Width: 4.0496



B0L001S, U7-ch31, adc2

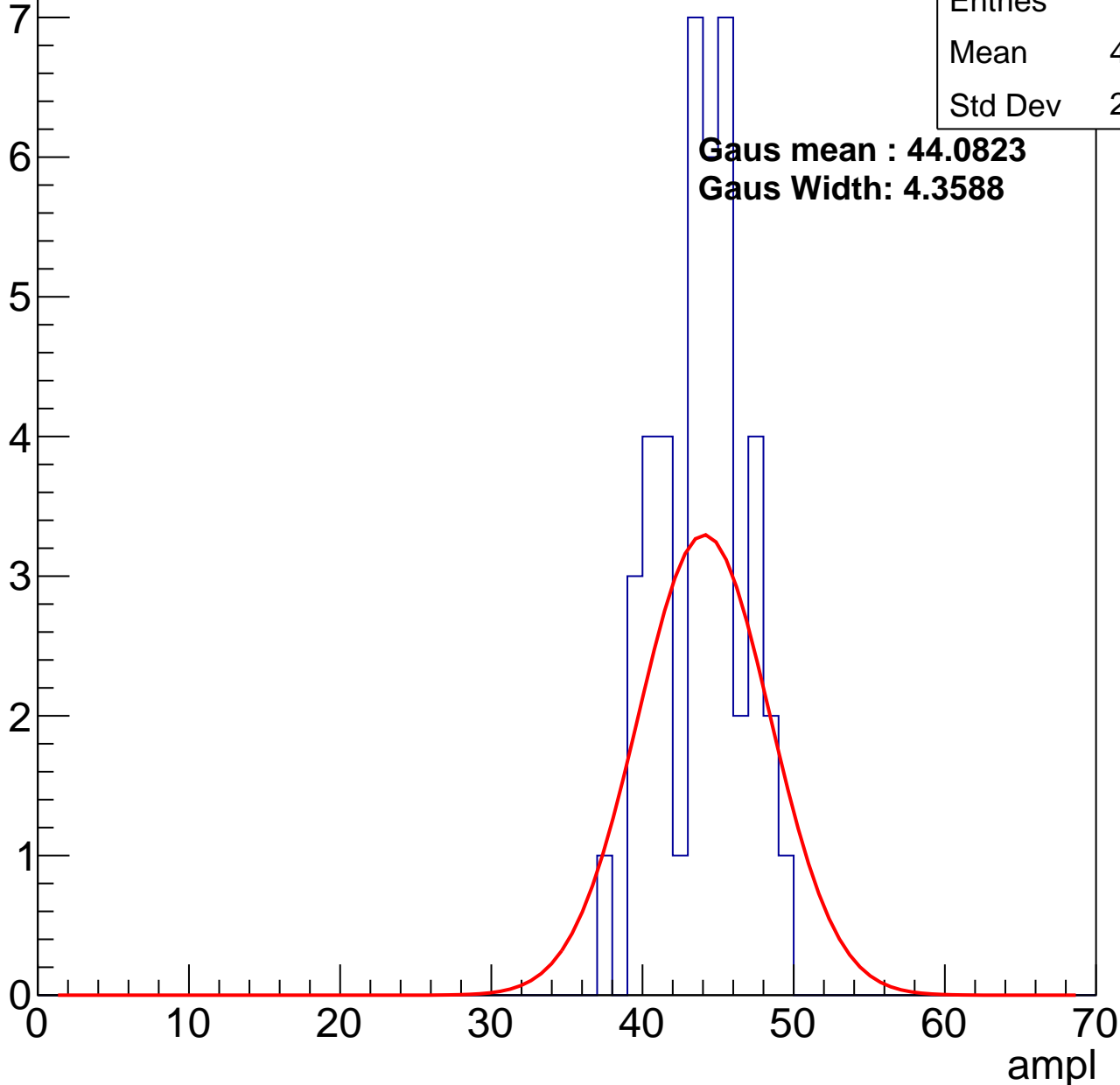
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 43.45 |
| Std Dev | 2.805 |

Gaus mean : 44.0823

Gaus Width: 4.3588

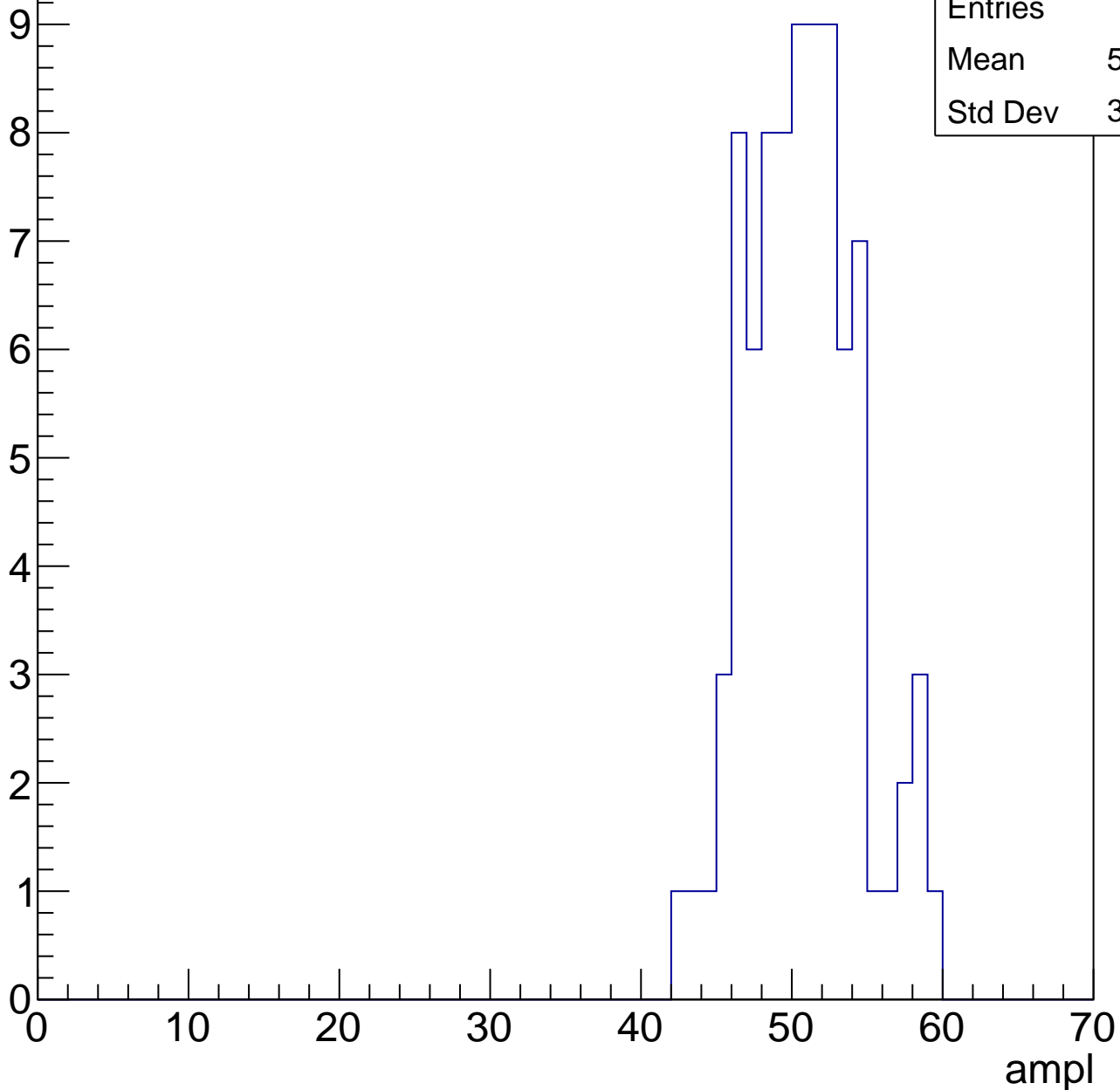


B0L001S, U7-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 50.25 |
| Std Dev | 3.592 |

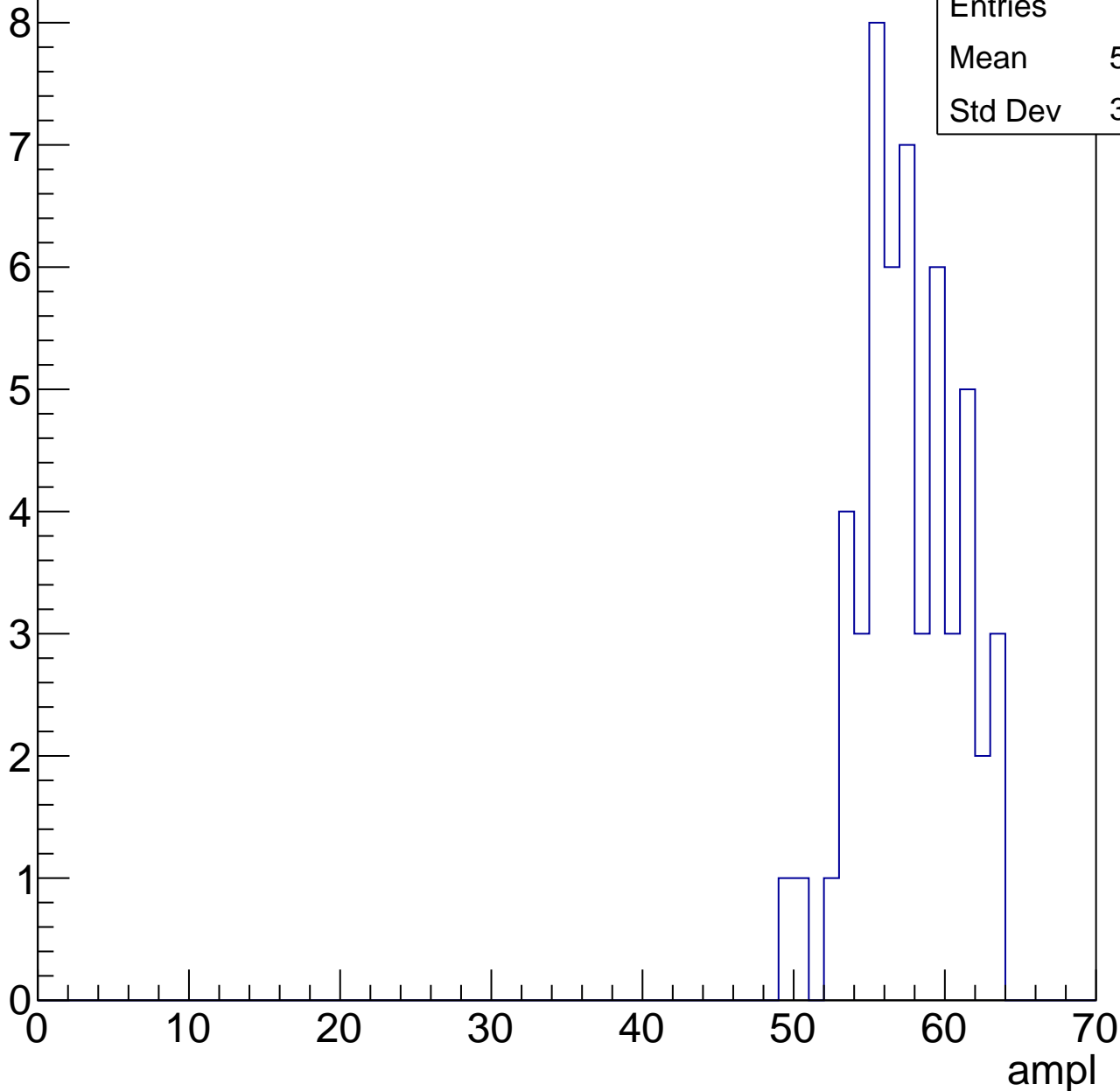


B0L001S, U7-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.09 |
| Std Dev | 3.246 |

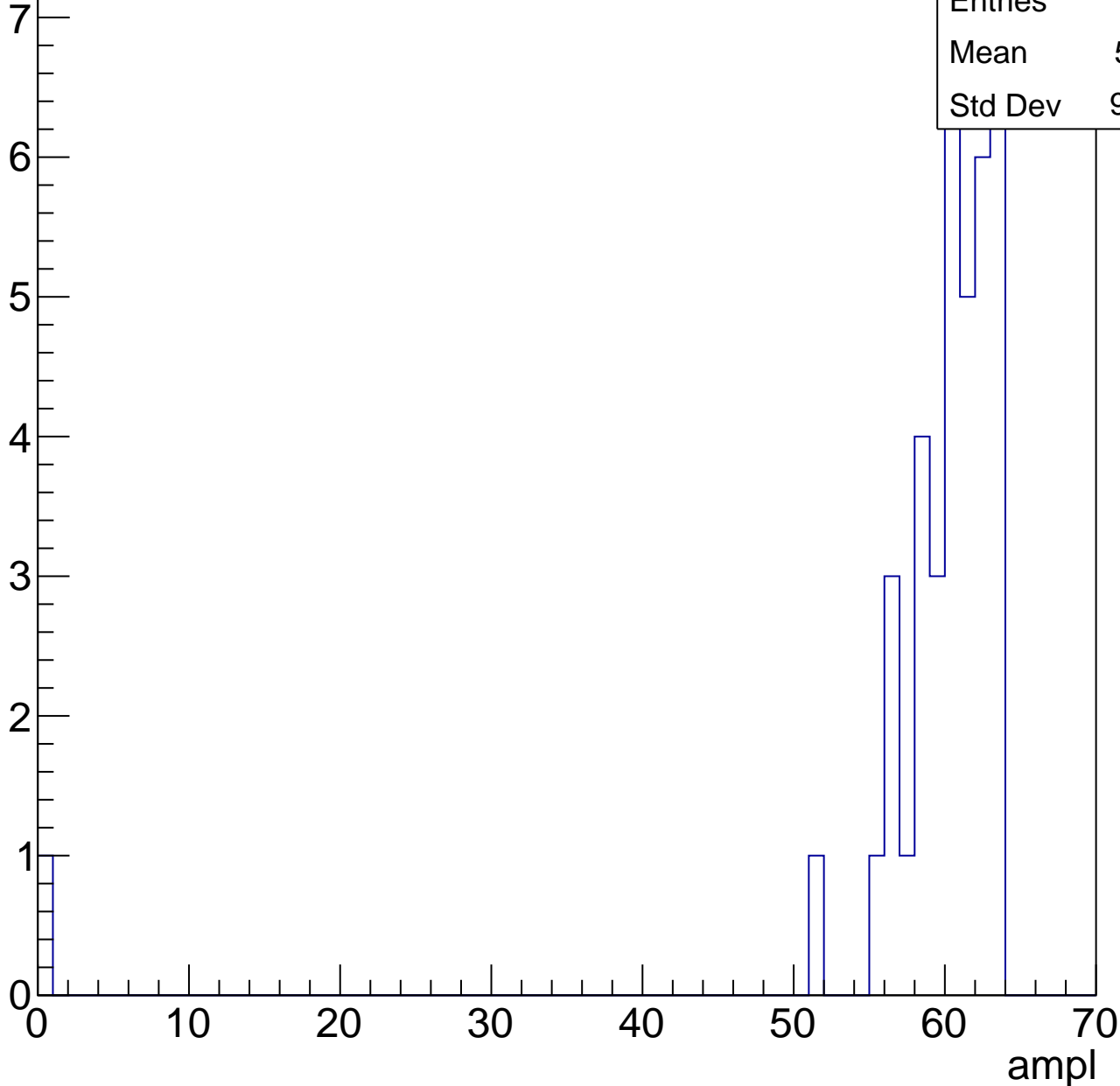


B0L001S, U7-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.41 |
| Std Dev | 9.839 |



B0L001S, U7-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch32, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.8 |
| Std Dev | 4.855 |

Gaus mean : 30.8869

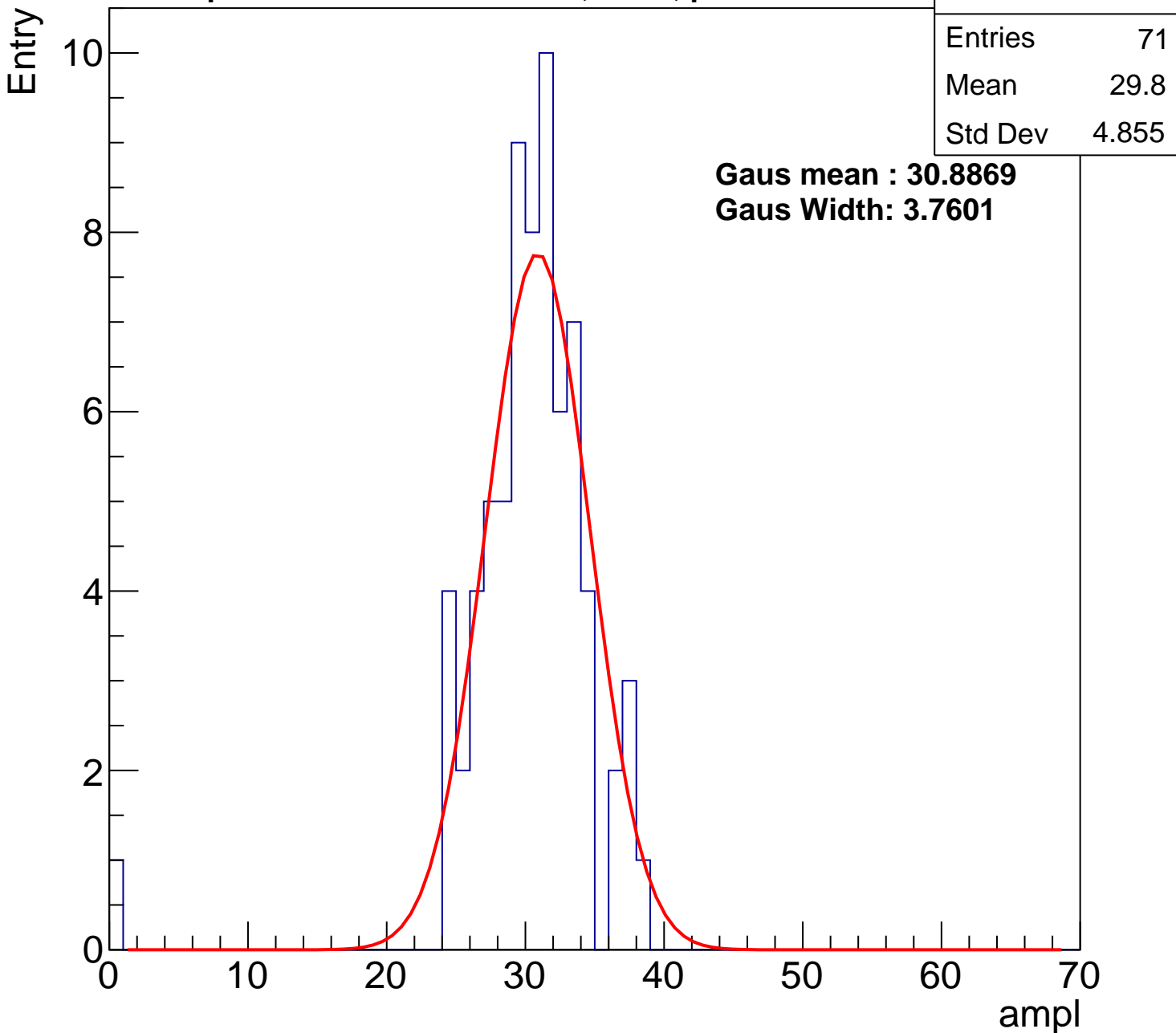
Gaus Width: 3.7601

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch32, adc1

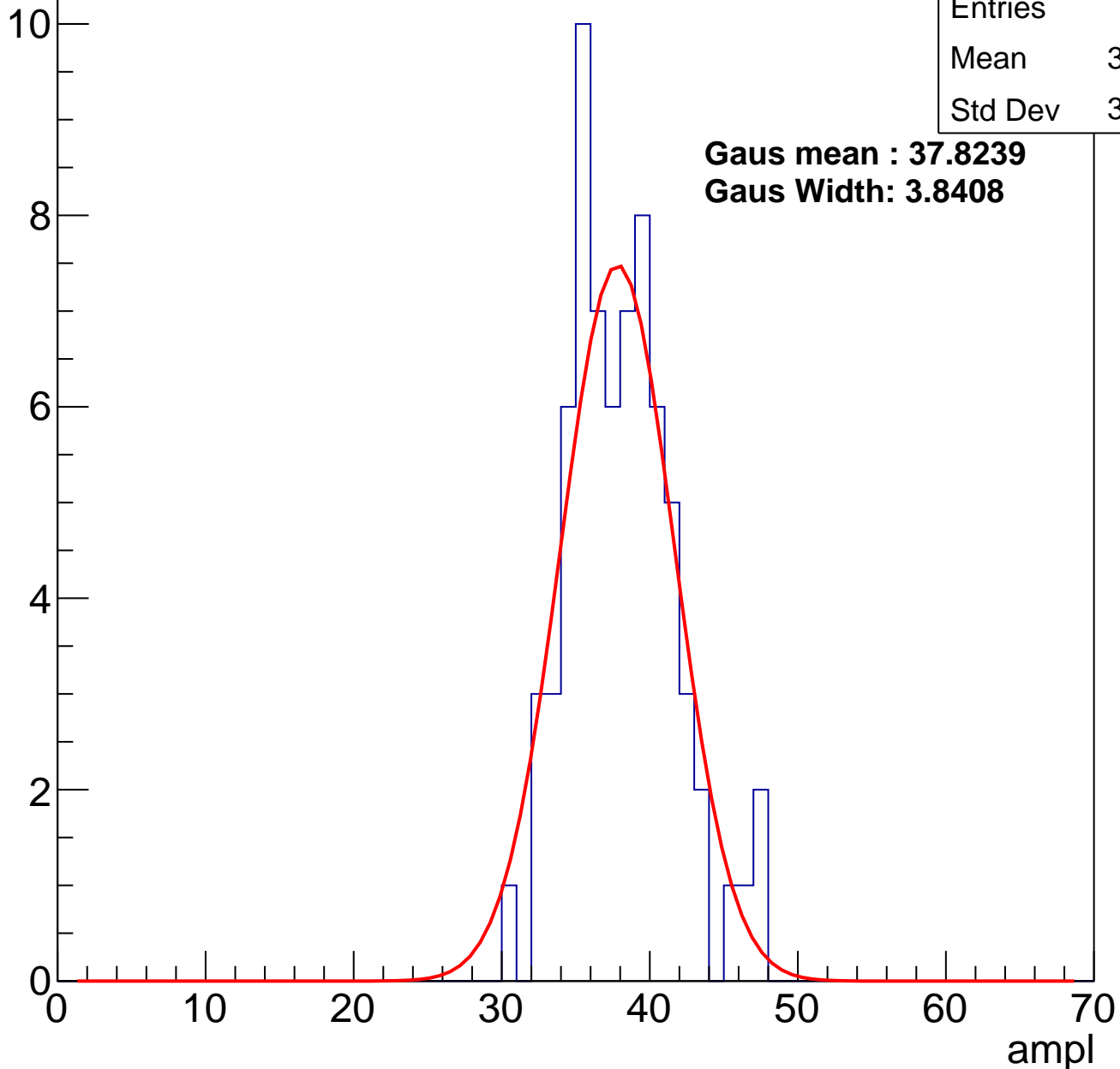
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.65 |
| Std Dev | 3.577 |

Gaus mean : 37.8239

Gaus Width: 3.8408

Entry



B0L001S, U7-ch32, adc2

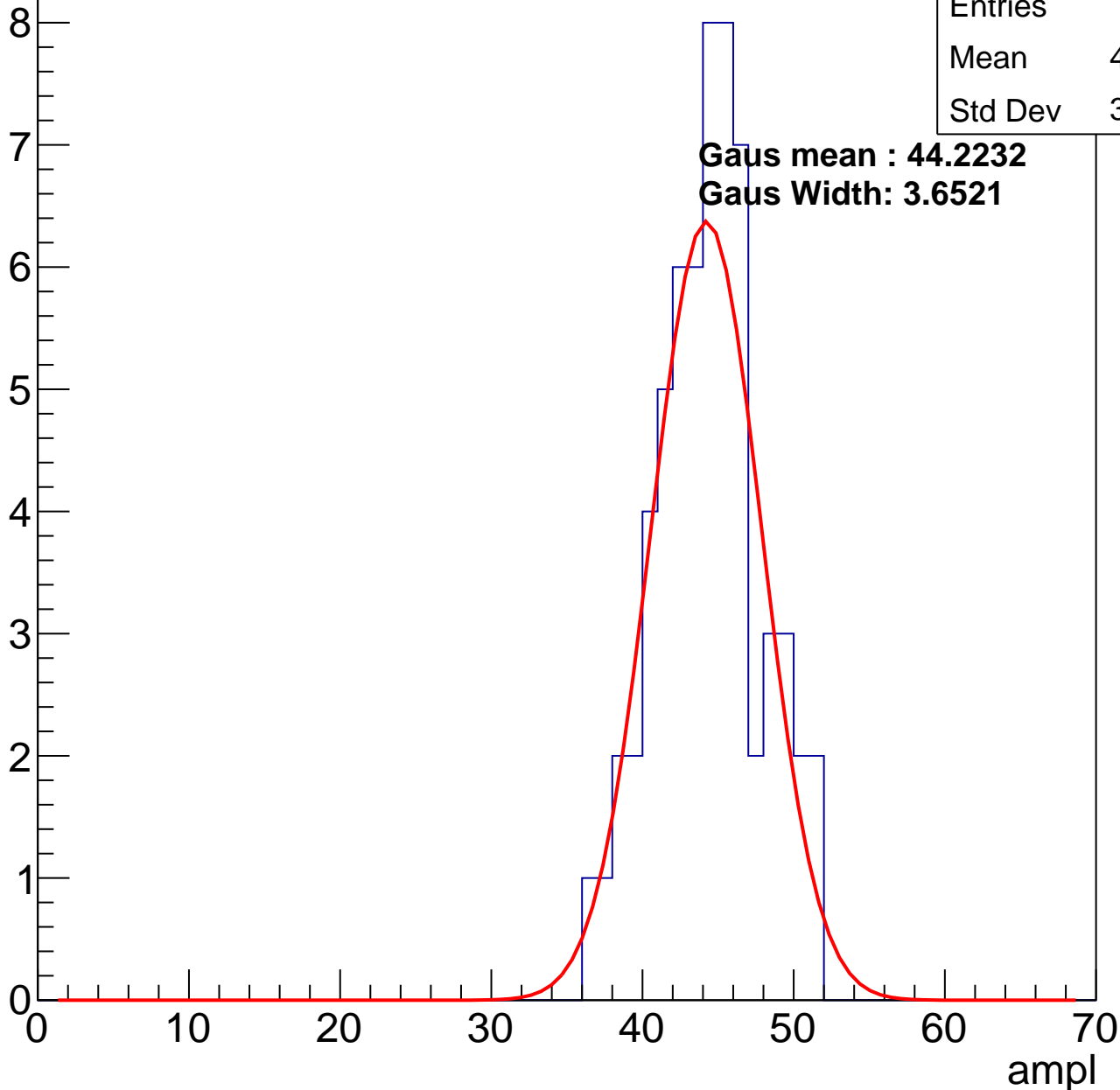
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.92 |
| Std Dev | 3.409 |

Gaus mean : 44.2232

Gaus Width: 3.6521

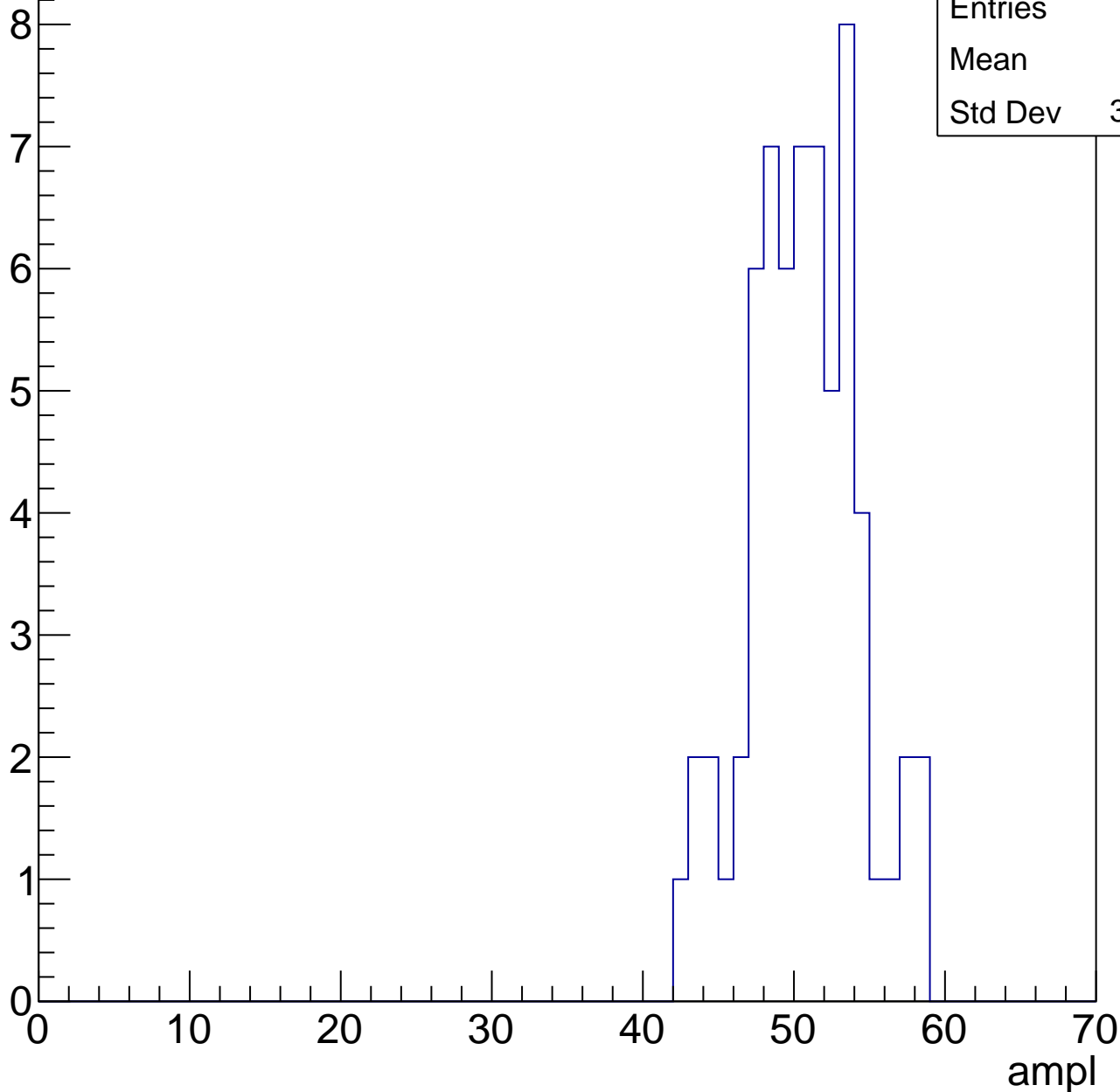


B0L001S, U7-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.2 |
| Std Dev | 3.593 |

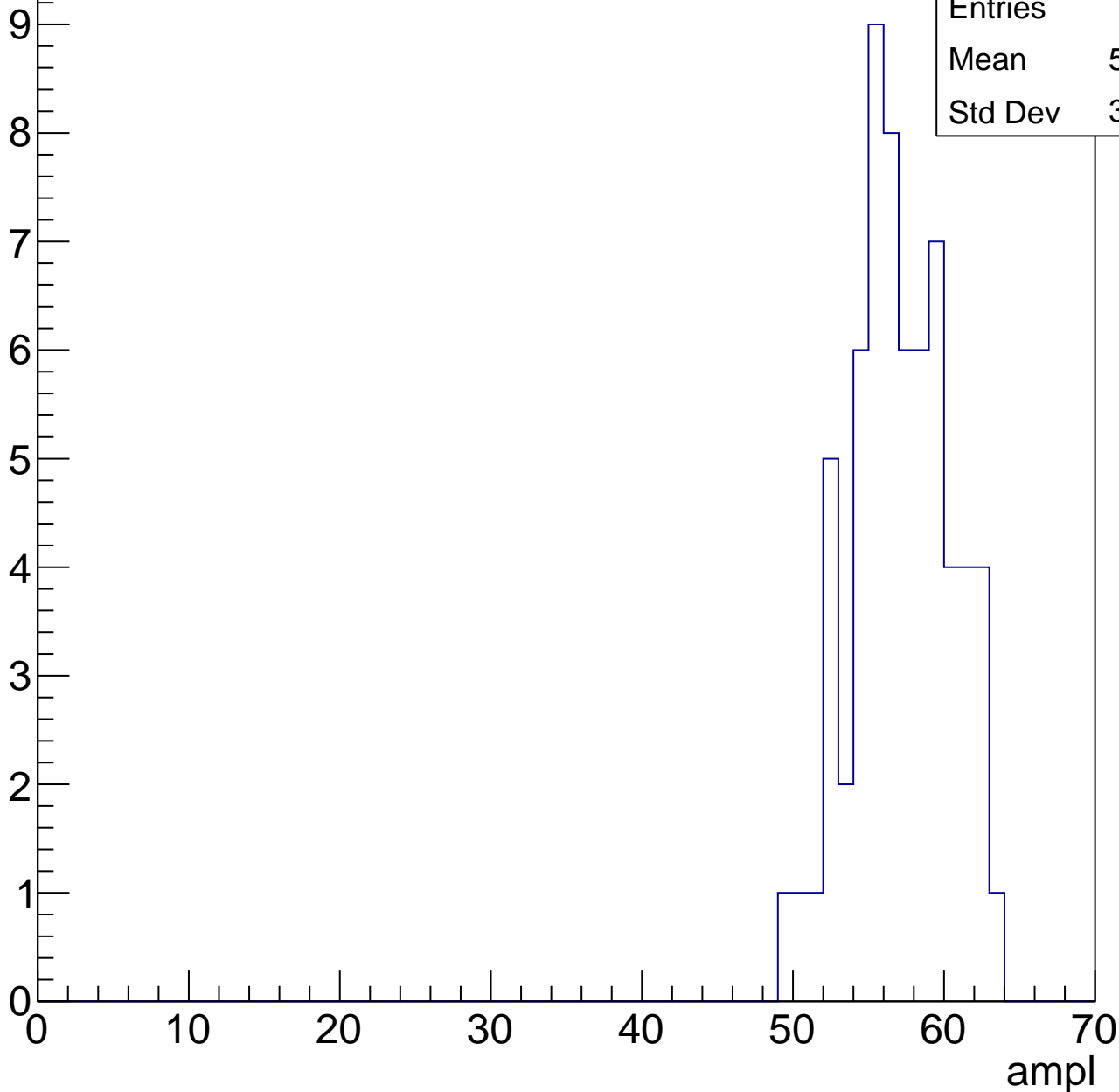


B0L001S, U7-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 56.63 |
| Std Dev | 3.199 |

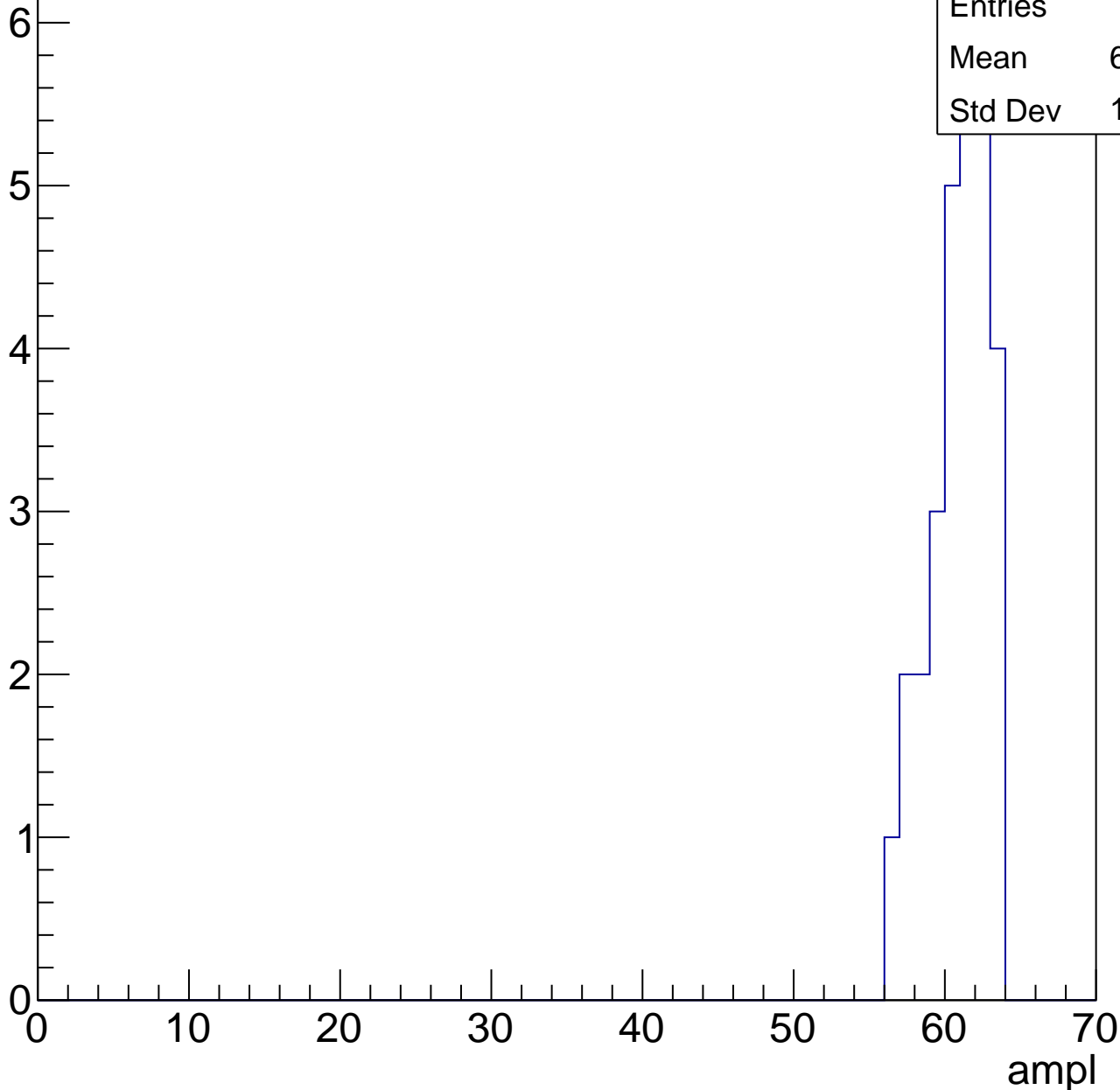


B0L001S, U7-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 60.45 |
| Std Dev | 1.904 |

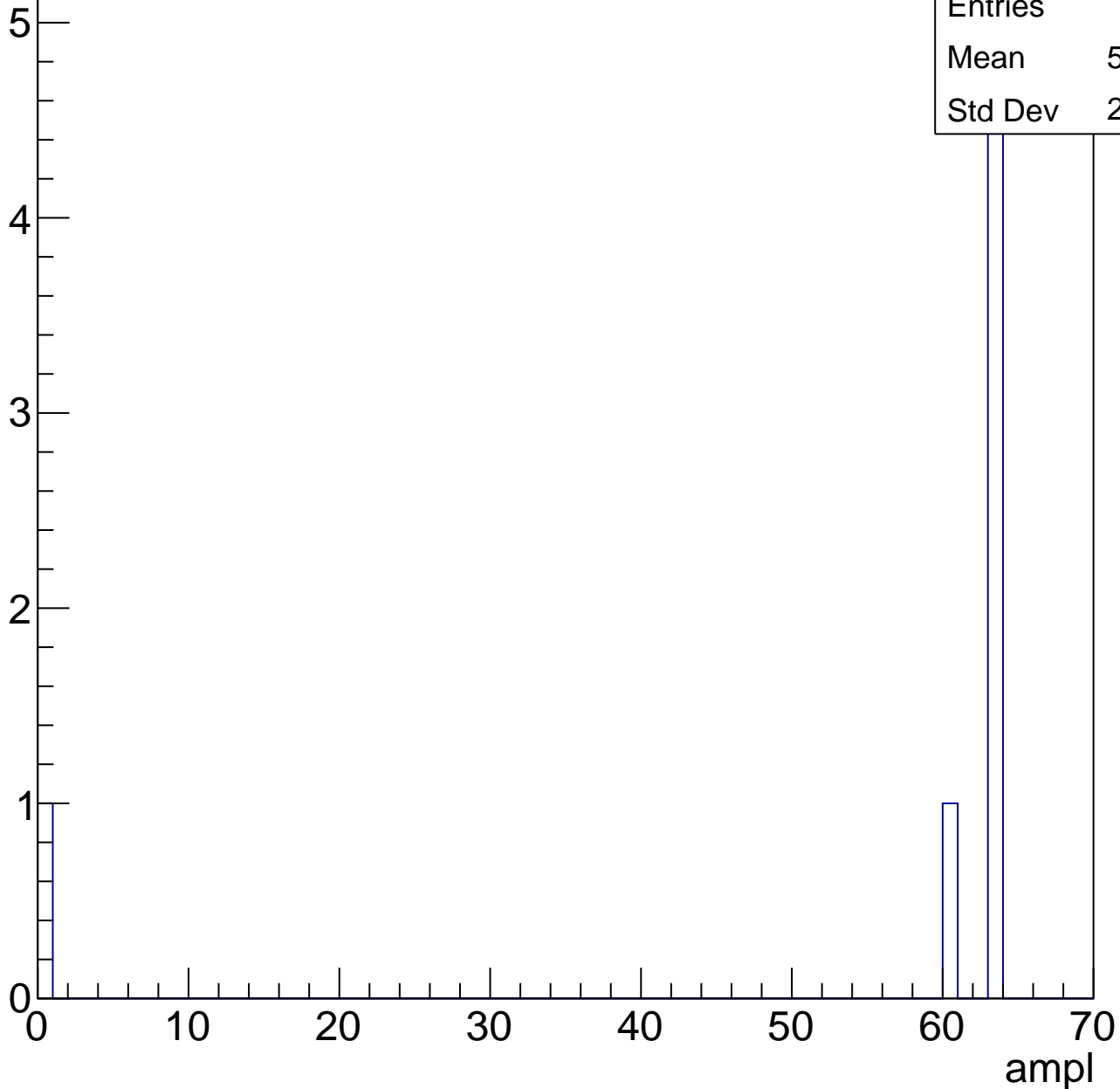


B0L001S, U7-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 7 |
| Mean | 53.57 |
| Std Dev | 21.89 |



B0L001S, U7-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 11 |
| Std Dev | 11 |

B0L001S, U7-ch33, adc0

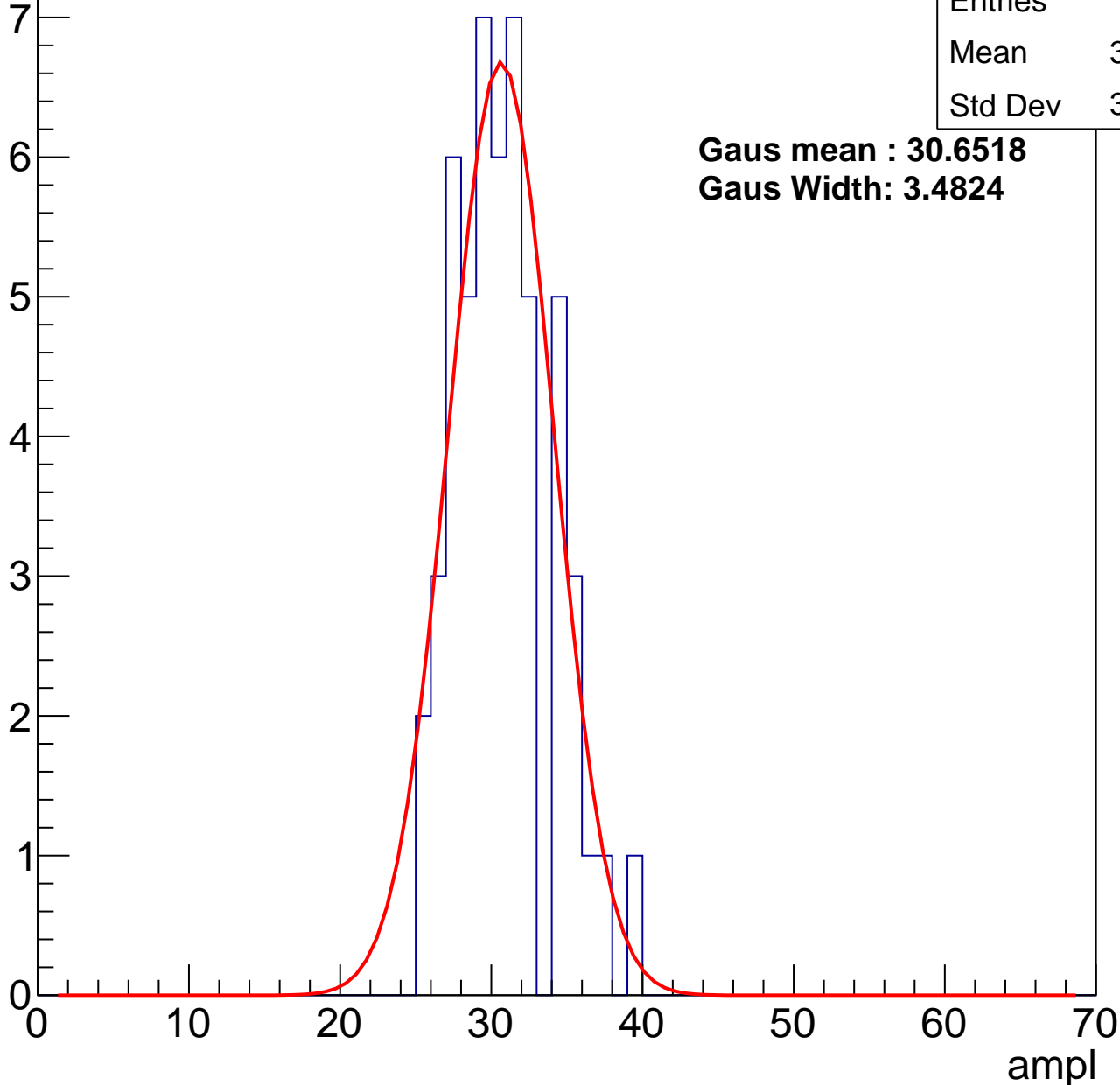
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 30.33 |
| Std Dev | 3.167 |

Gaus mean : 30.6518

Gaus Width: 3.4824



B0L001S, U7-ch33, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 91 |
| Mean | 37.1 |
| Std Dev | 3.662 |

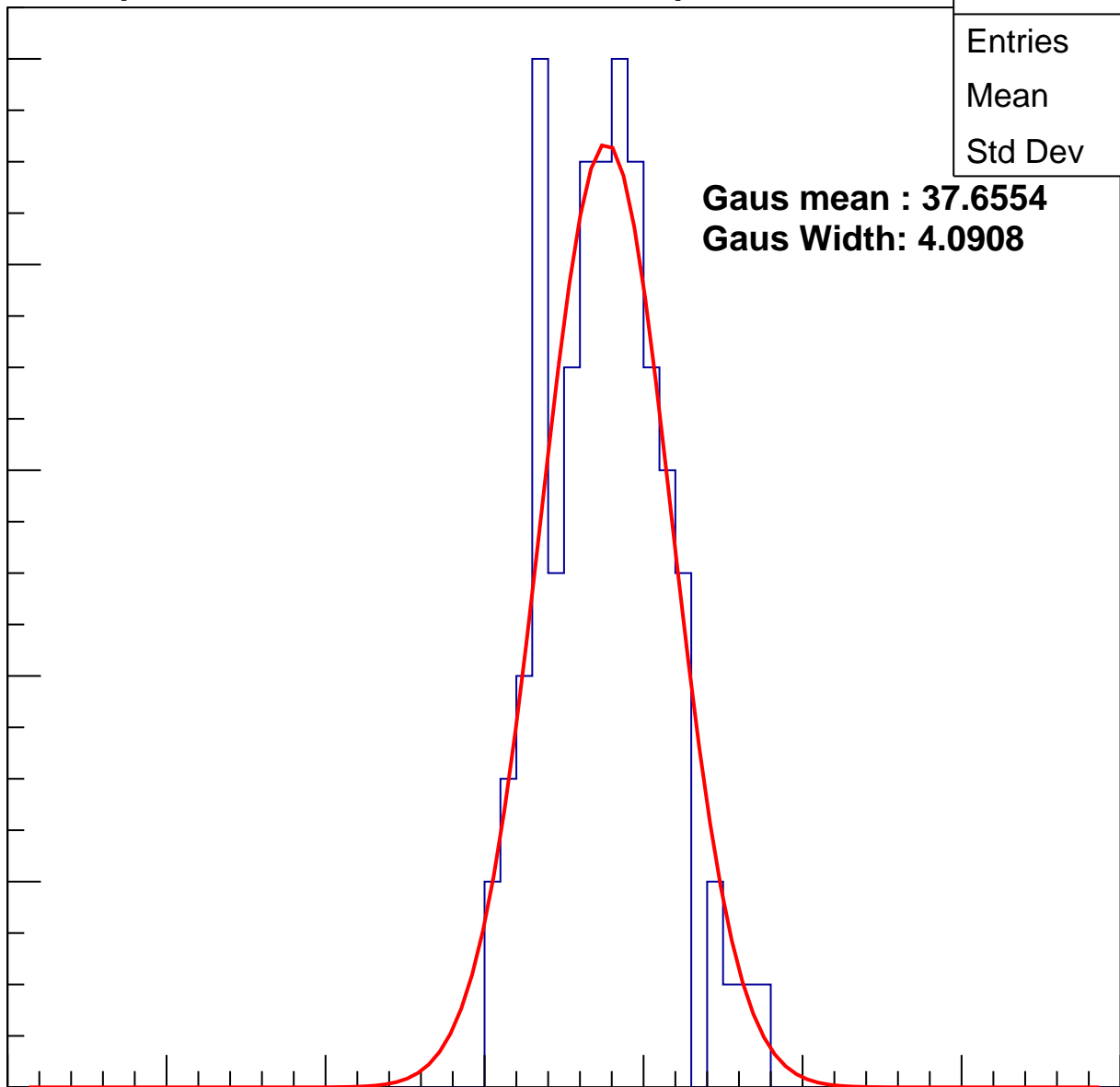
Gaus mean : 37.6554
Gaus Width: 4.0908

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



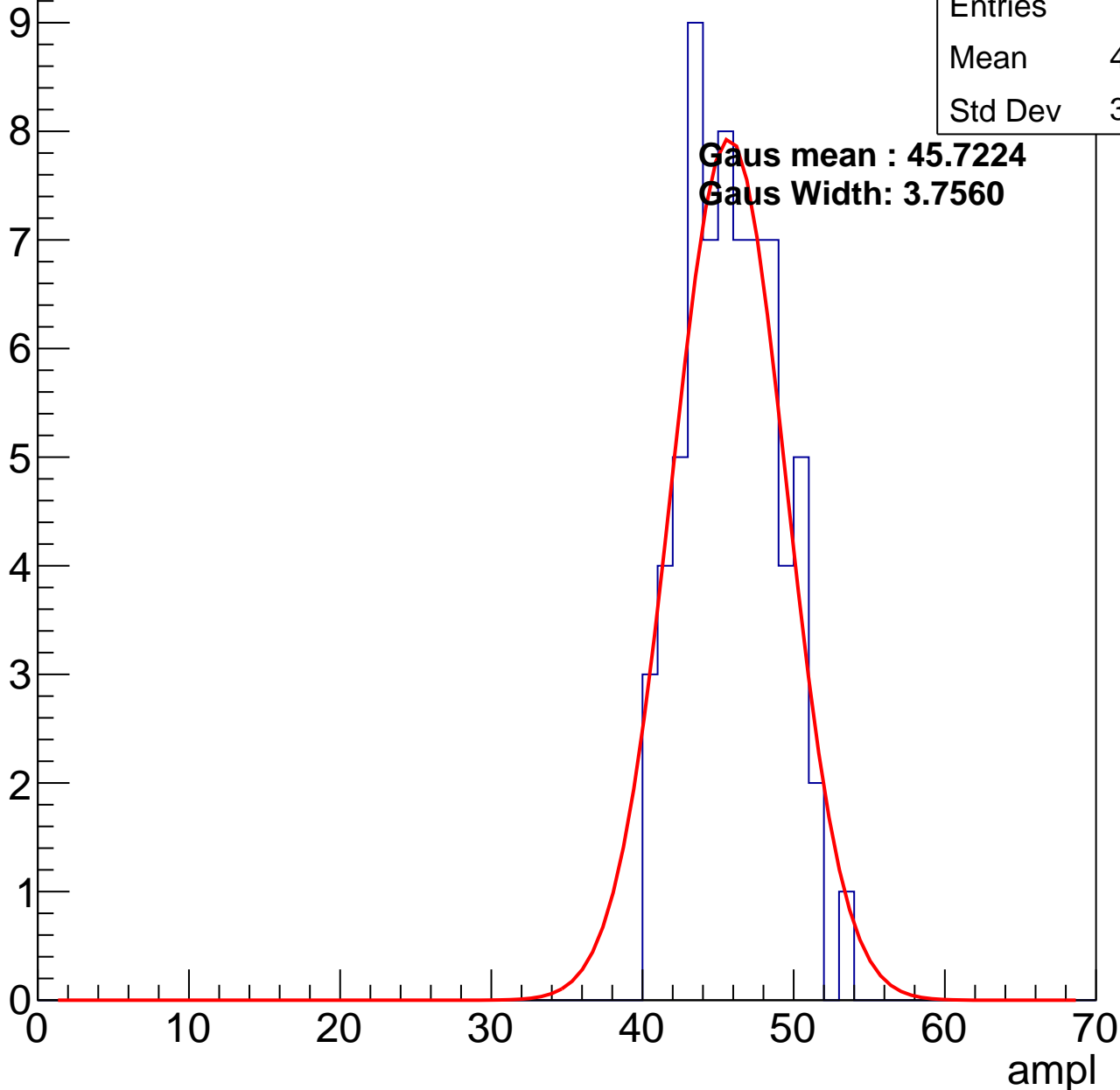
B0L001S, U7-ch33, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 45.46 |
| Std Dev | 3.034 |

Gaus mean : 45.7224
Gaus Width: 3.7560

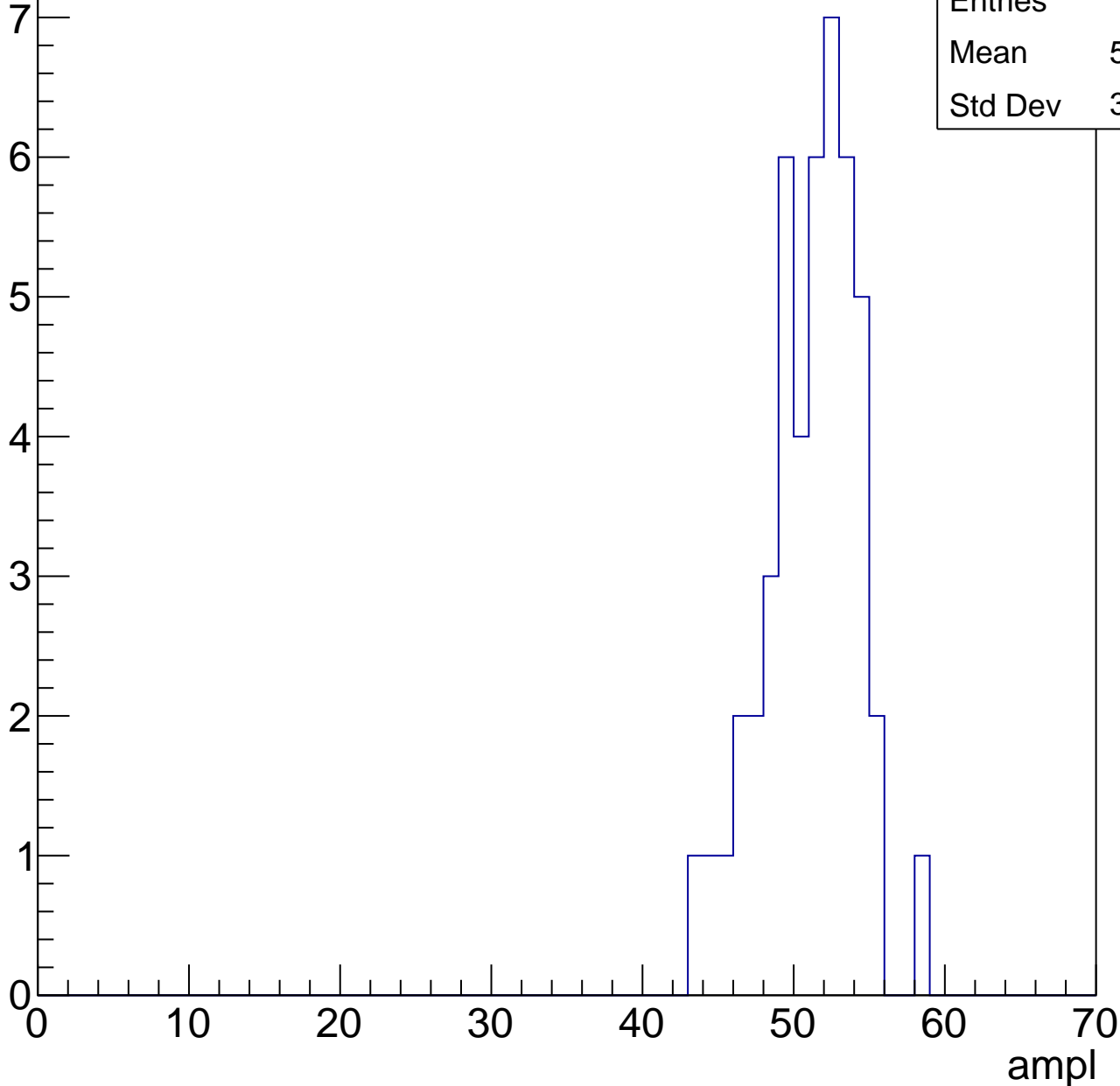


B0L001S, U7-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 50.68 |
| Std Dev | 3.053 |

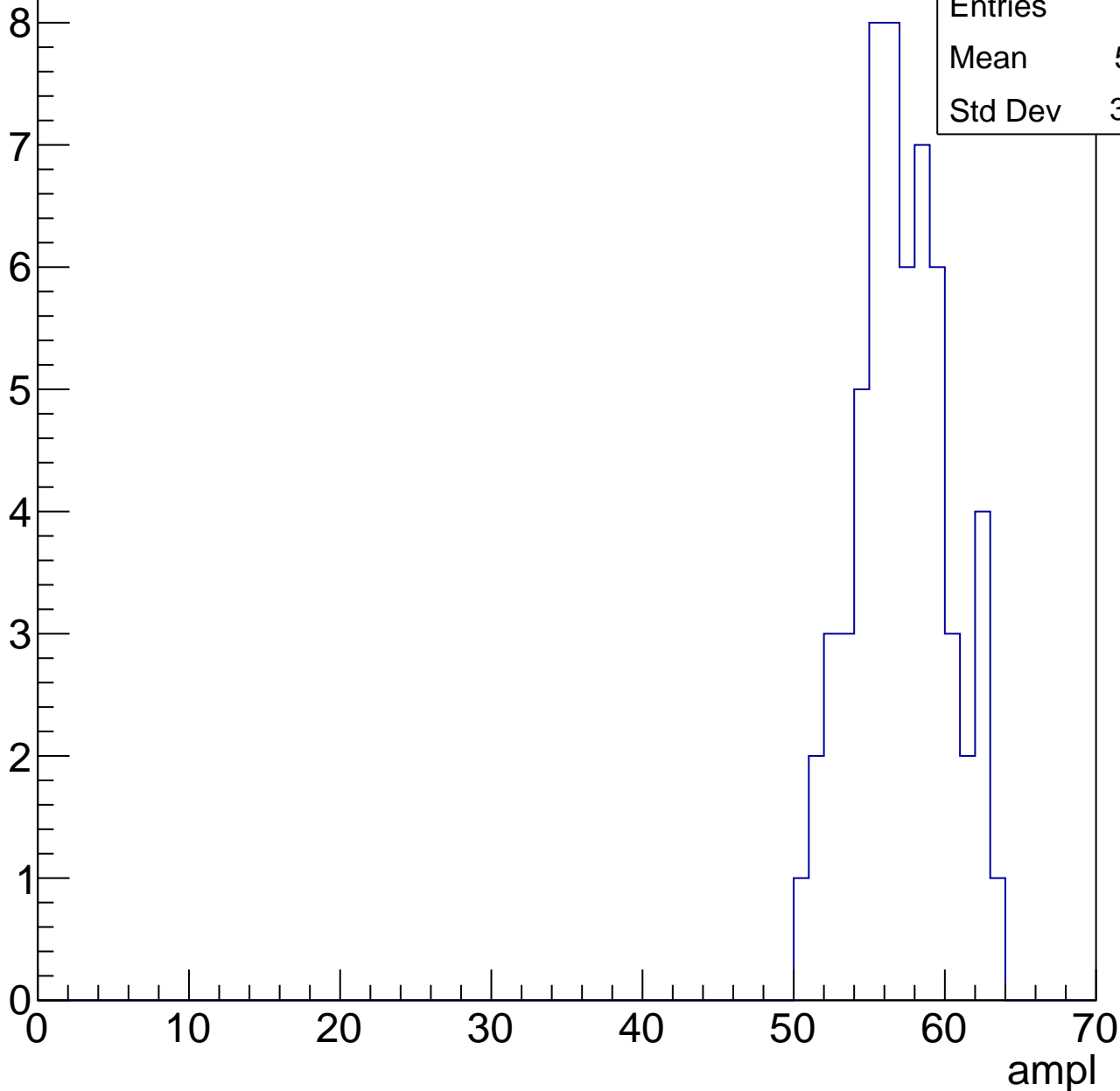


B0L001S, U7-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.61 |
| Std Dev | 3.042 |

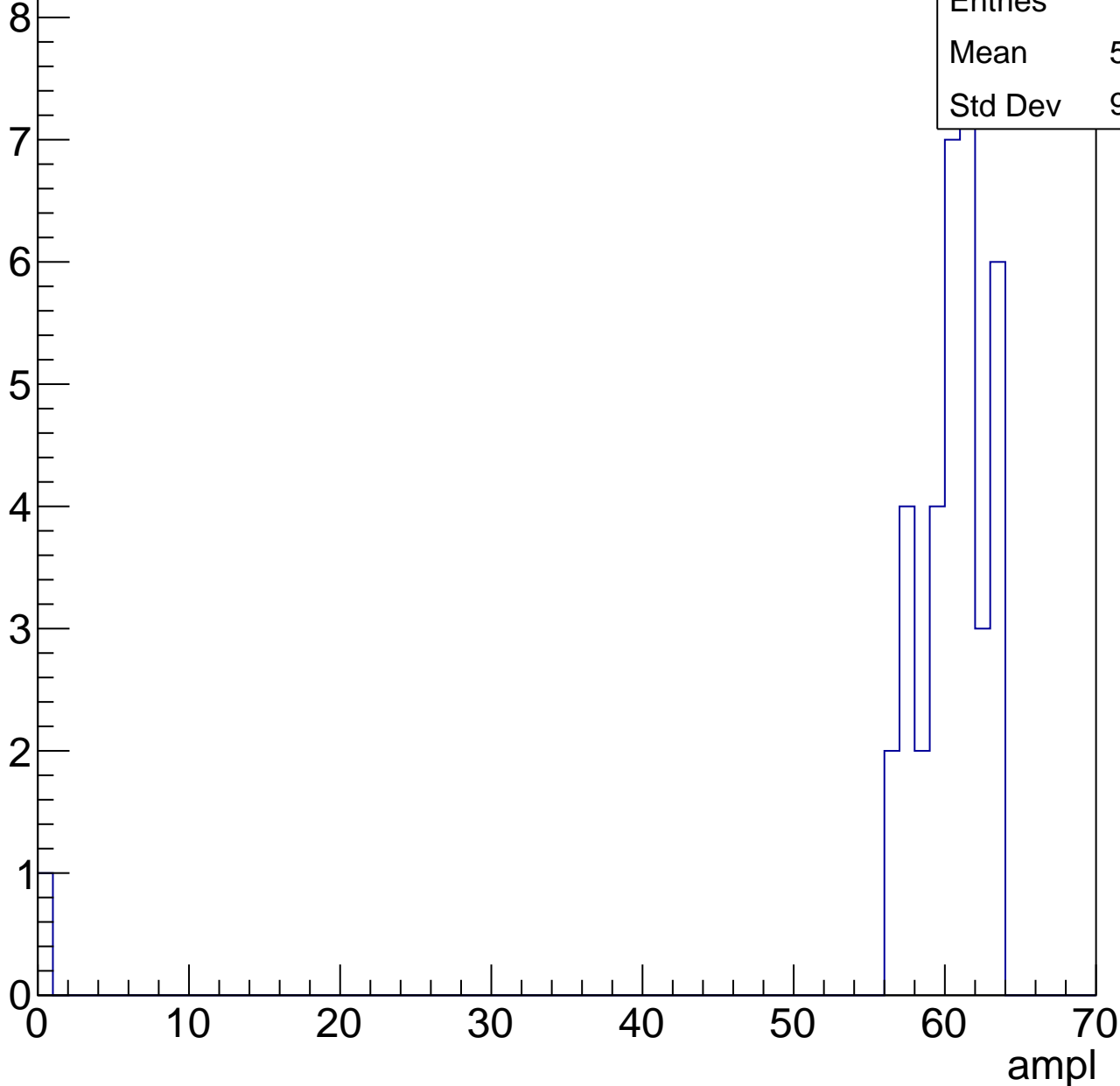


B0L001S, U7-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

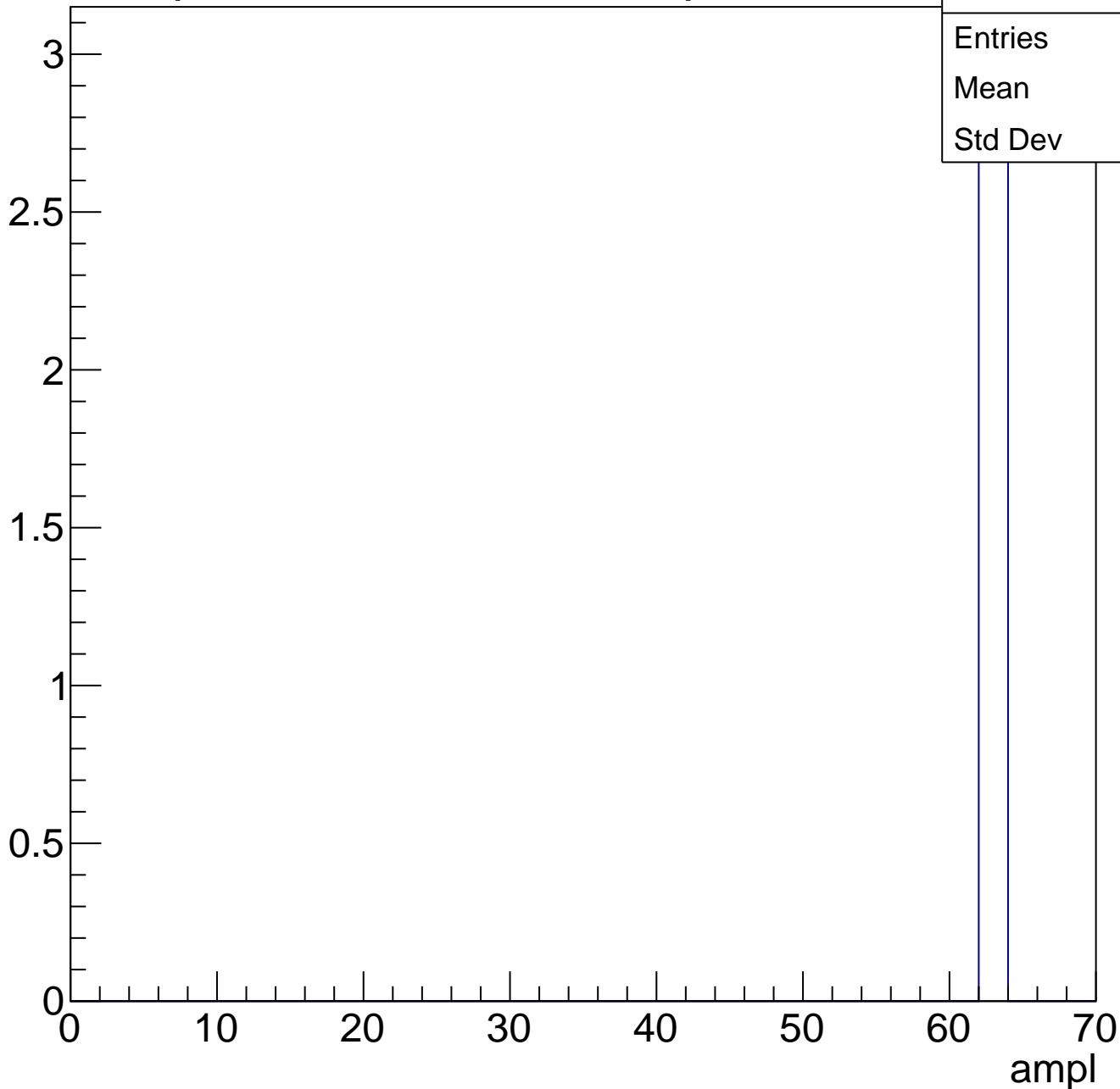
| | |
|---------|-------|
| Entries | 37 |
| Mean | 58.49 |
| Std Dev | 9.958 |



B0L001S, U7-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 7.667 |
| Std Dev | 10.84 |

B0L001S, U7-ch34, adc0

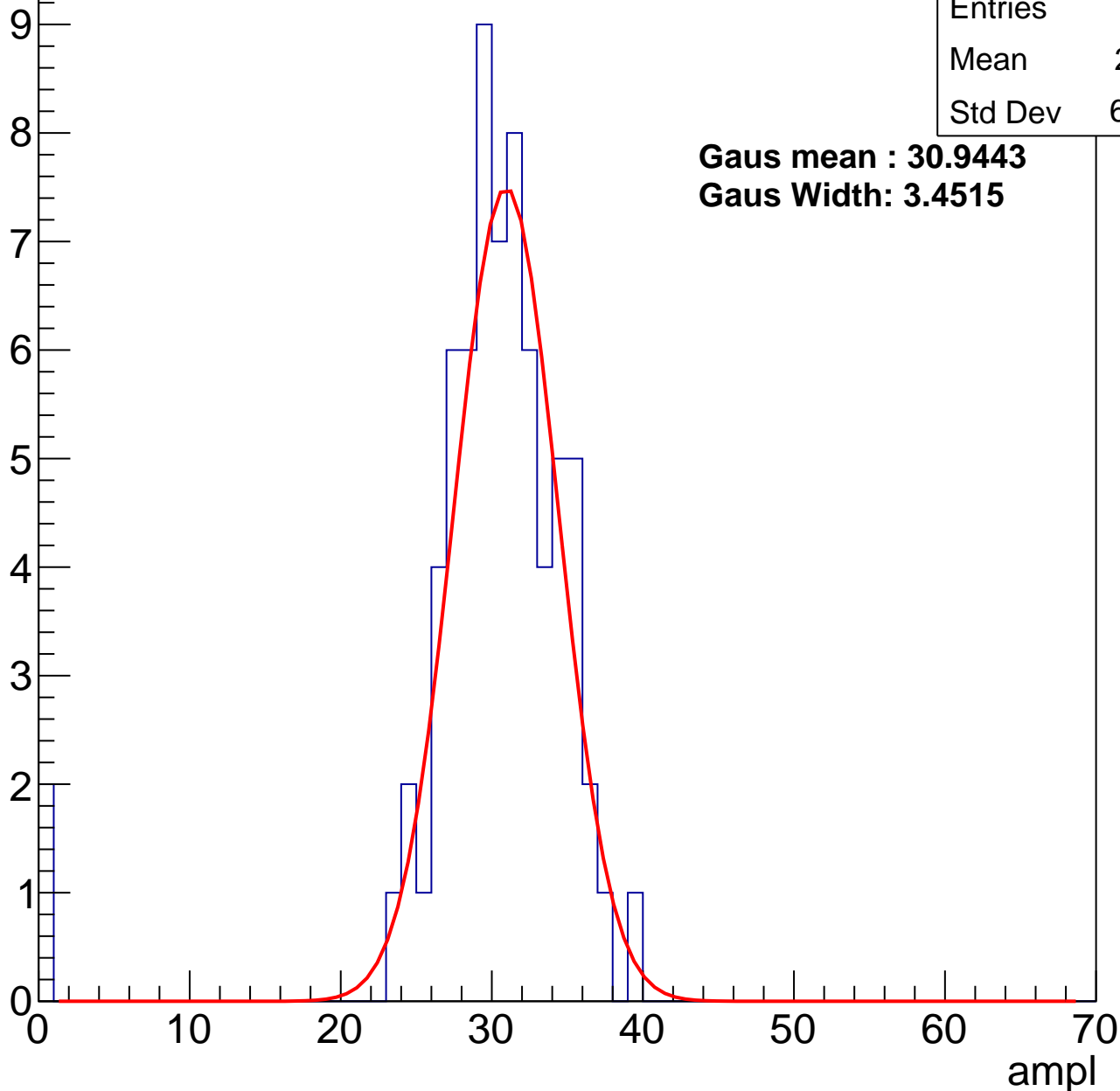
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 29.51 |
| Std Dev | 6.042 |

Gaus mean : 30.9443

Gaus Width: 3.4515



B0L001S, U7-ch34, adc1

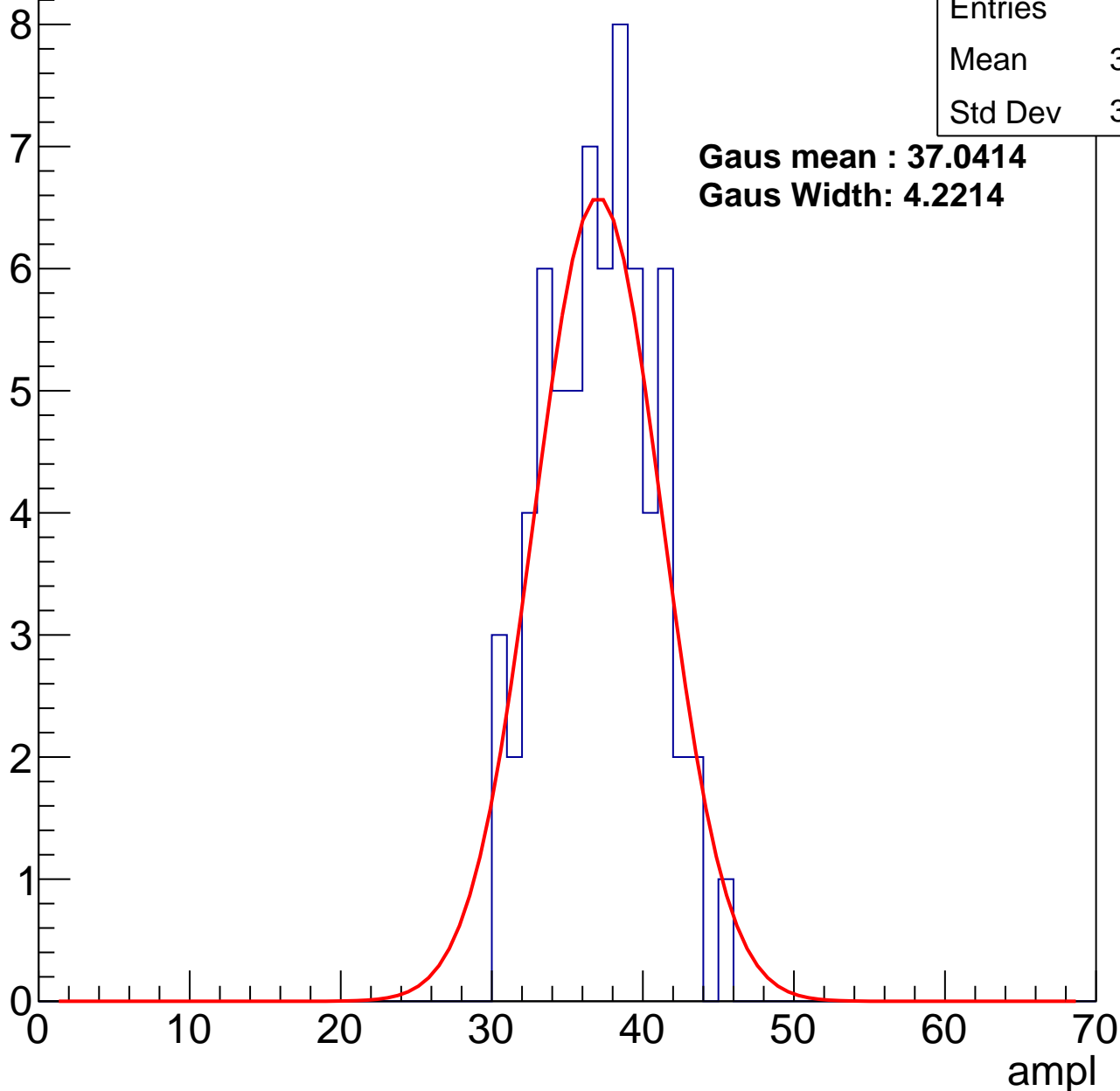
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.66 |
| Std Dev | 3.518 |

Gaus mean : 37.0414

Gaus Width: 4.2214



B0L001S, U7-ch34, adc2

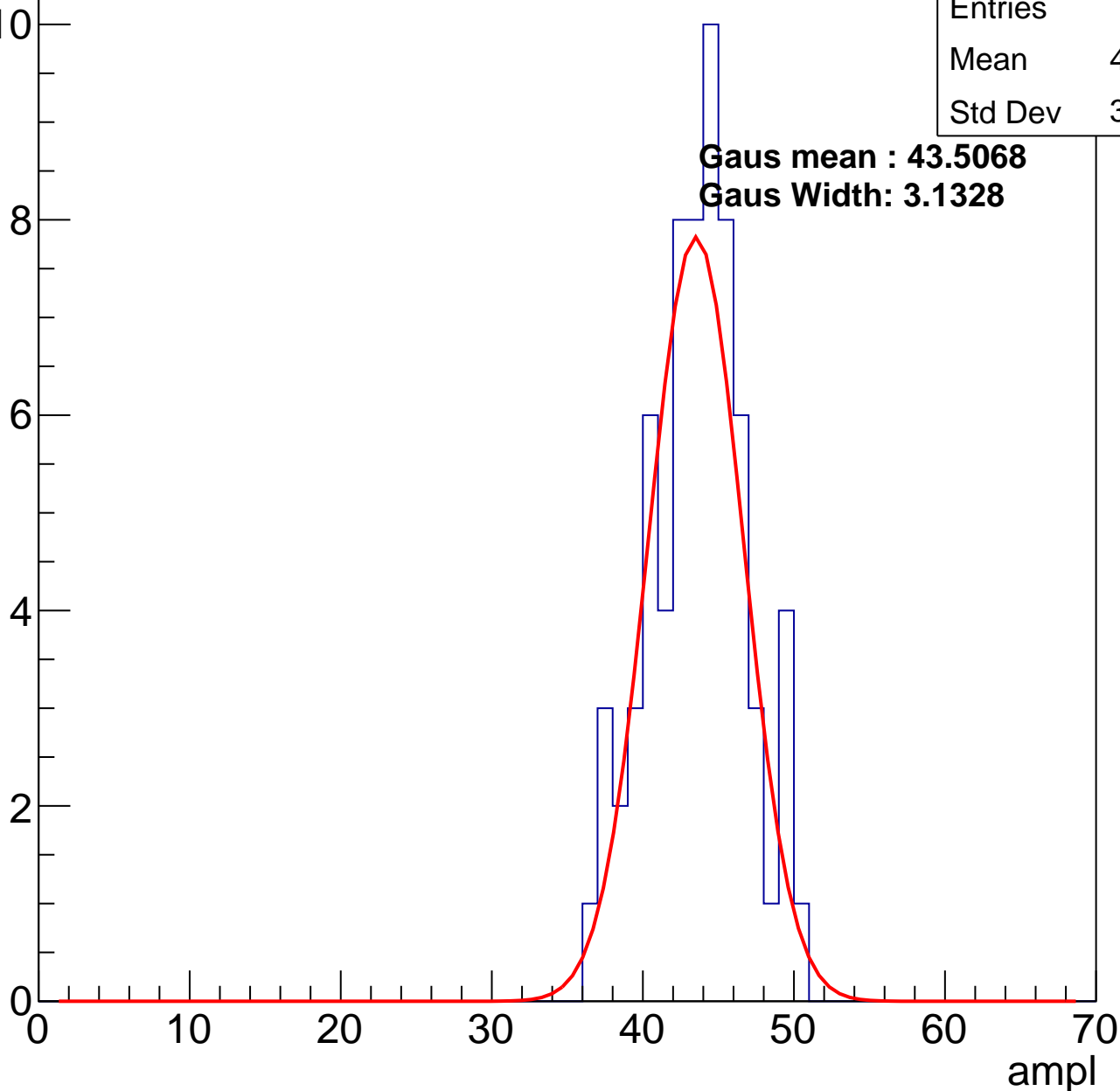
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 43.16 |
| Std Dev | 3.193 |

Gaus mean : 43.5068

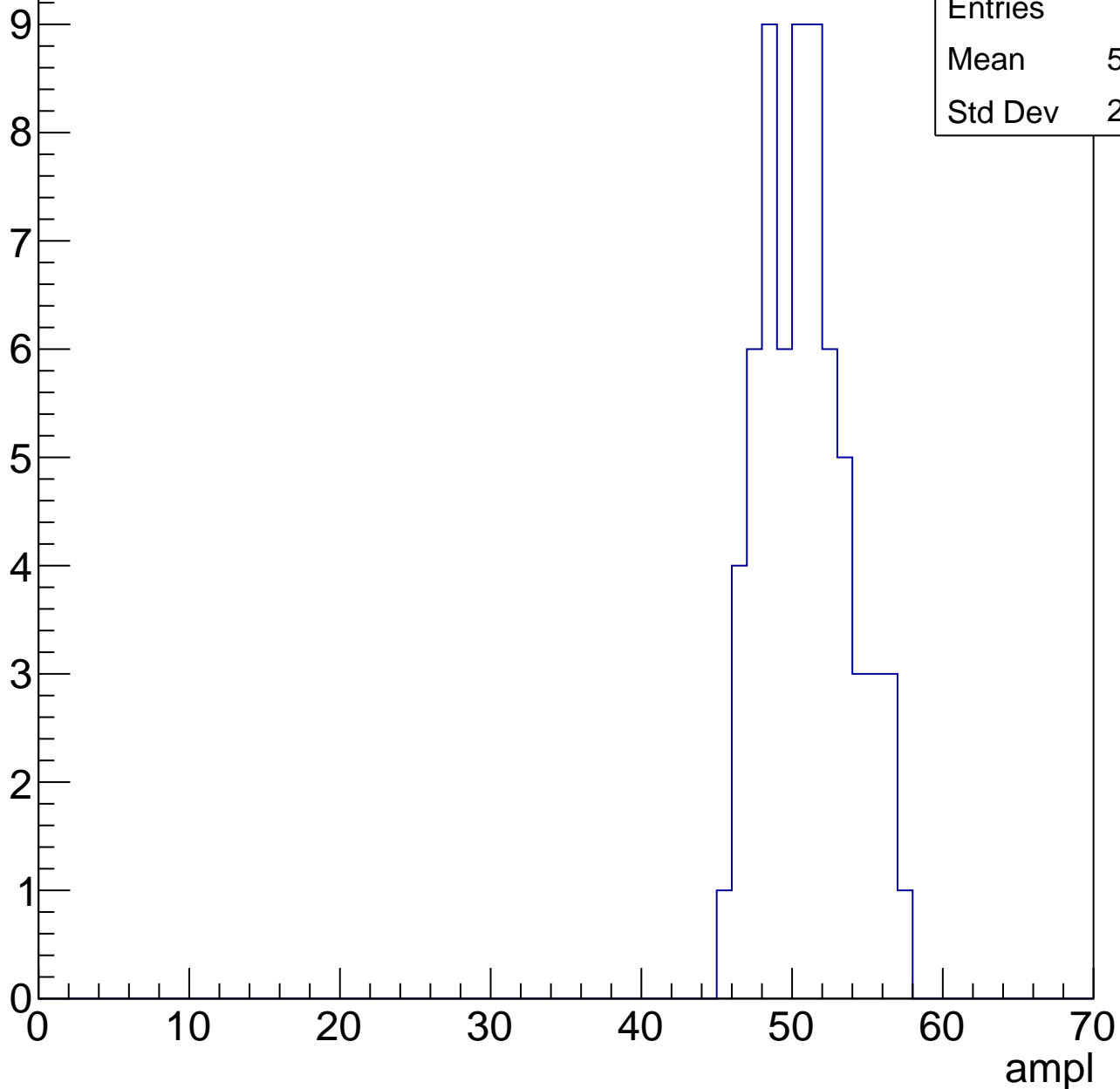
Gaus Width: 3.1328



B0L001S, U7-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

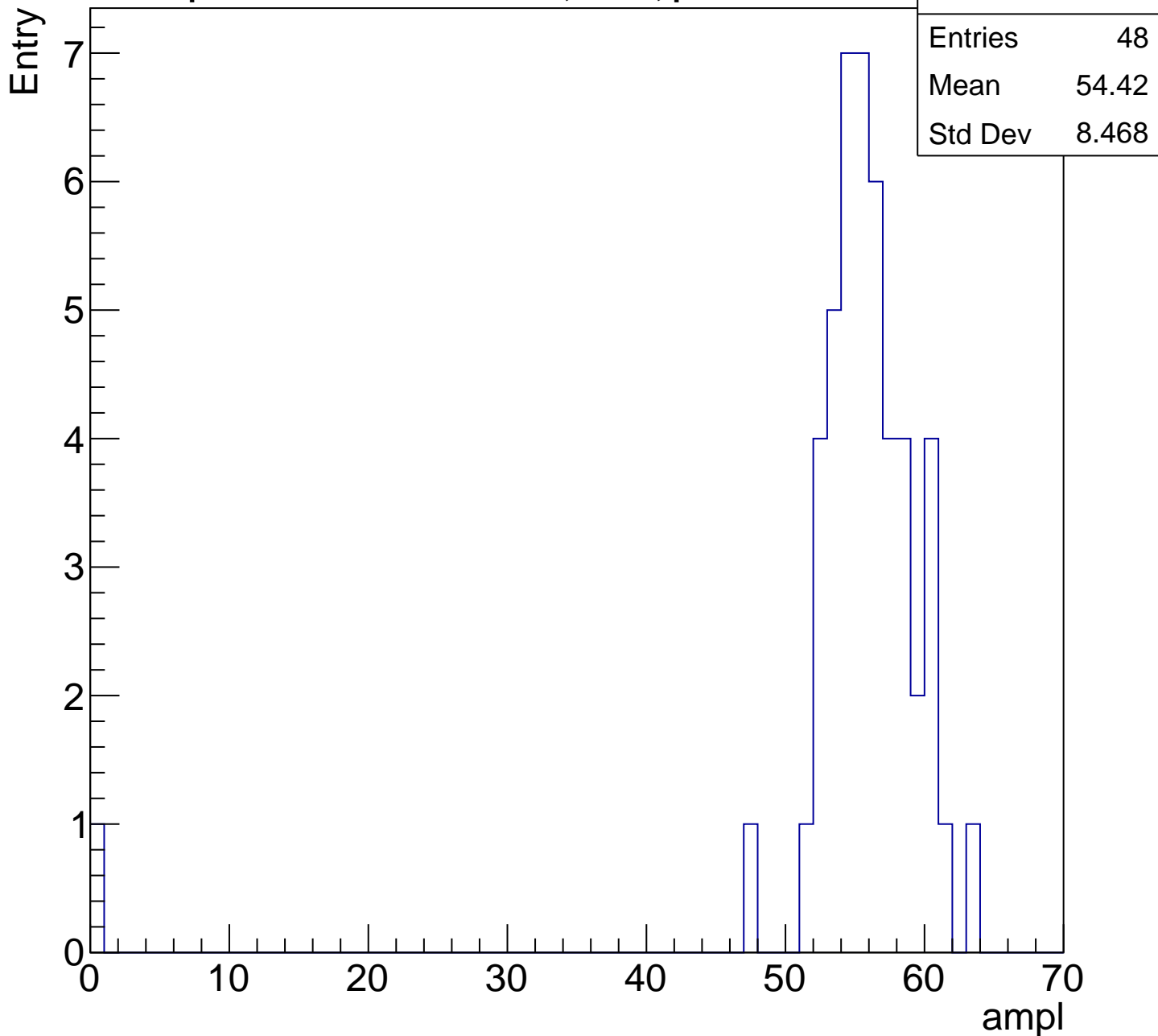
Entry



| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.38 |
| Std Dev | 2.865 |

B0L001S, U7-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 53 |
| Mean | 60.3 |
| Std Dev | 2.024 |

B0L001S, U7-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

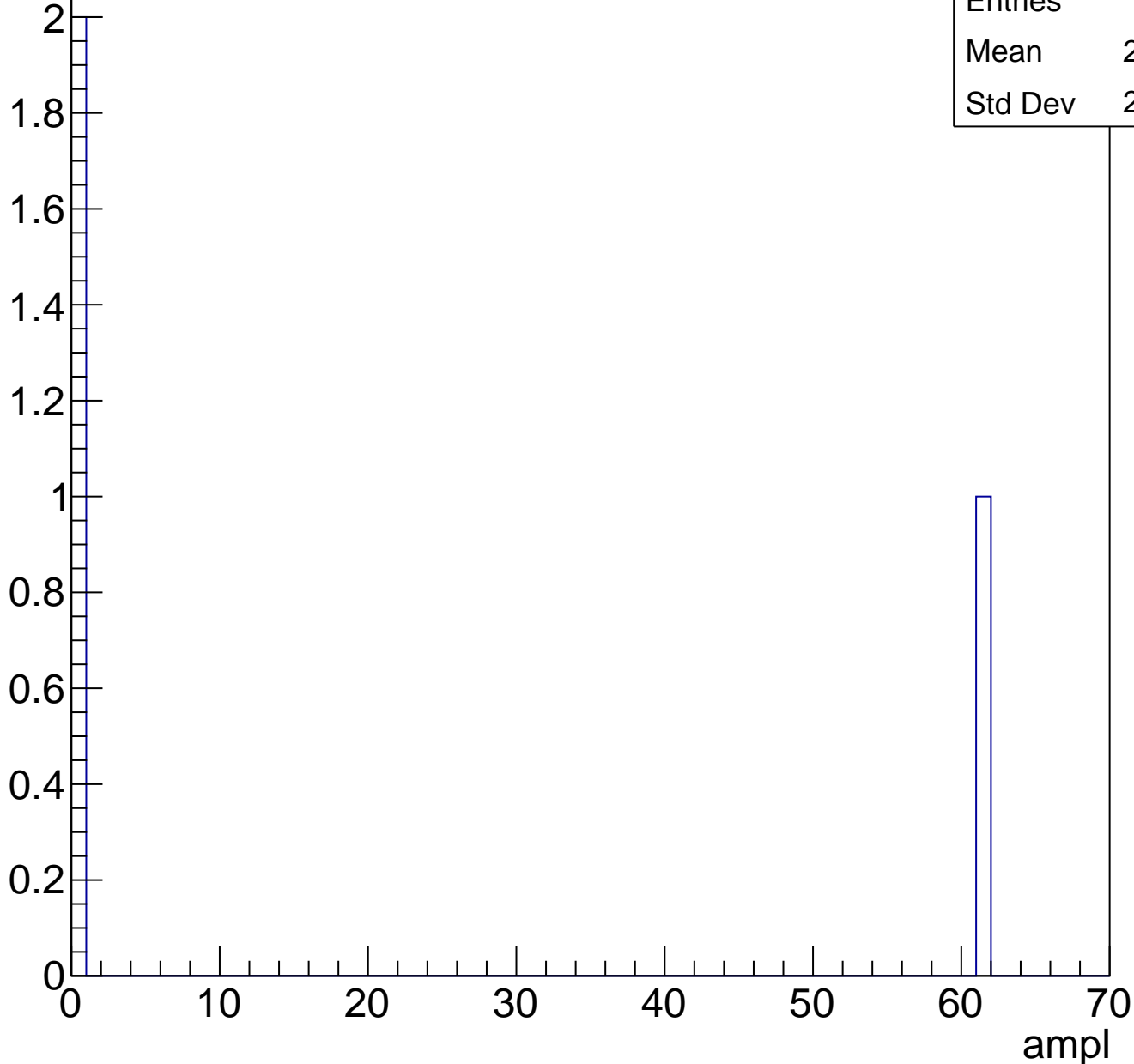


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 20.33 |
| Std Dev | 28.76 |

B0L001S, U7-ch35, adc0

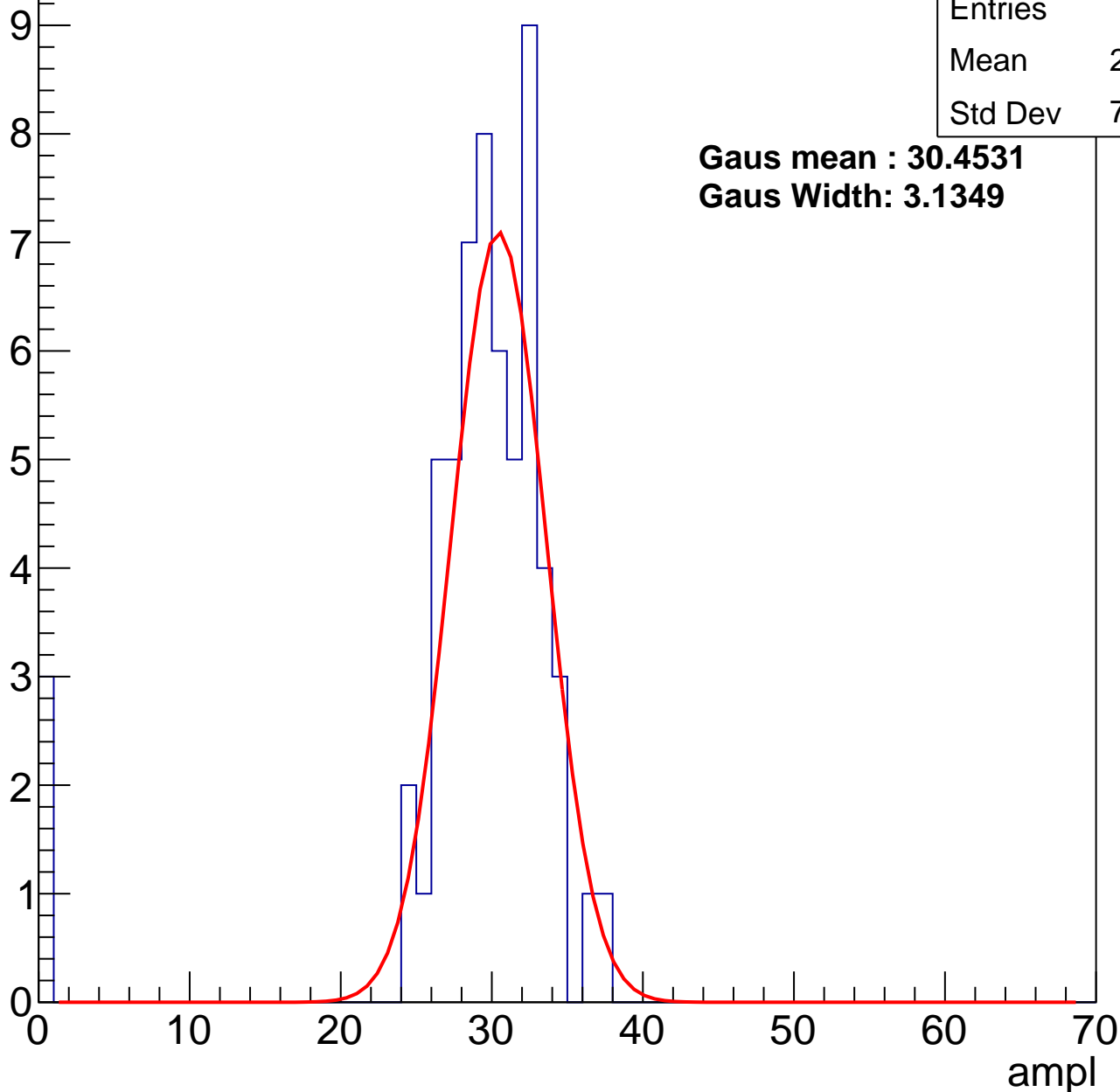
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 28.27 |
| Std Dev | 7.057 |

Gaus mean : 30.4531

Gaus Width: 3.1349



B0L001S, U7-ch35, adc1

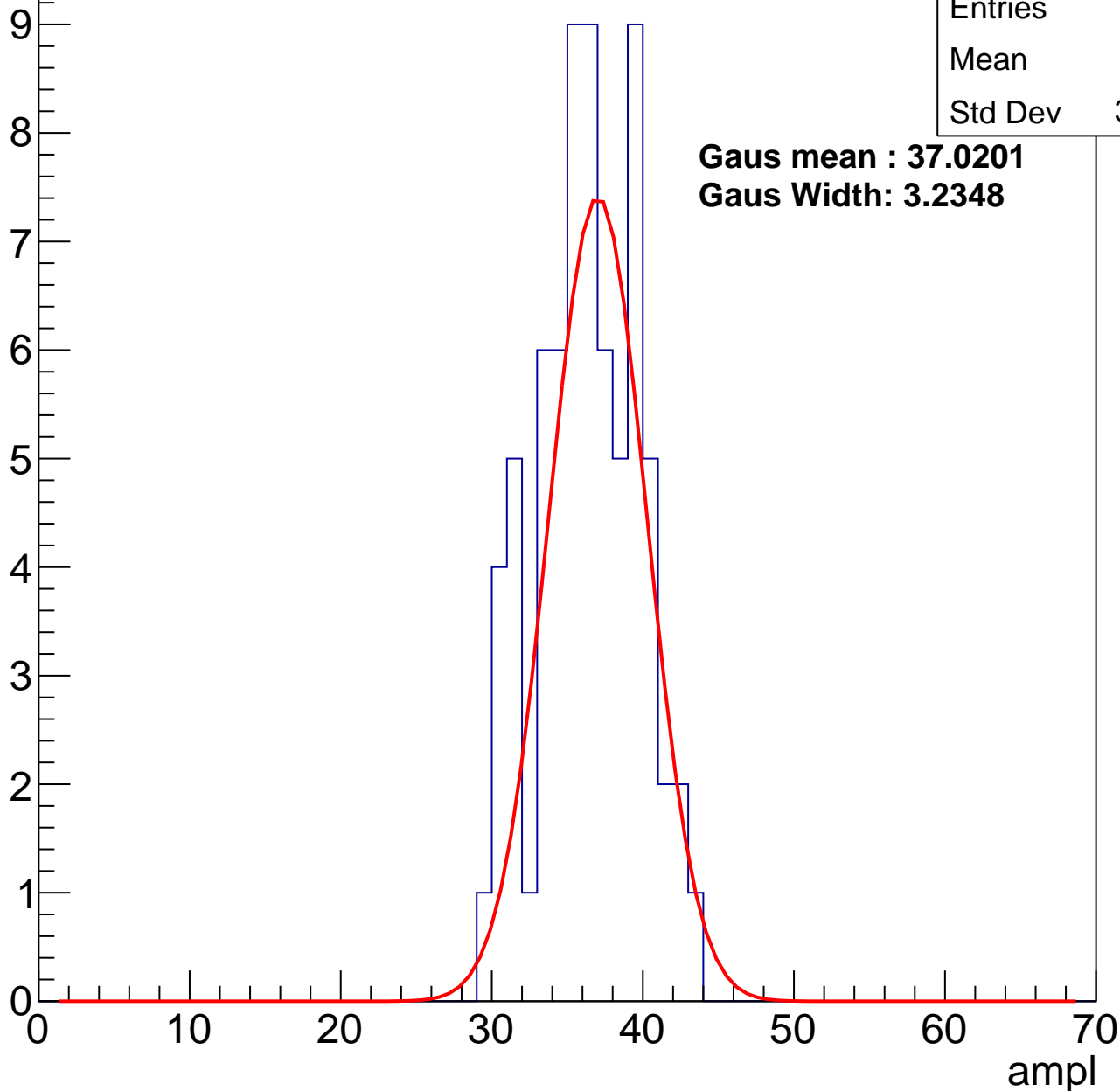
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.9 |
| Std Dev | 3.311 |

Gaus mean : 37.0201

Gaus Width: 3.2348



B0L001S, U7-ch35, adc2

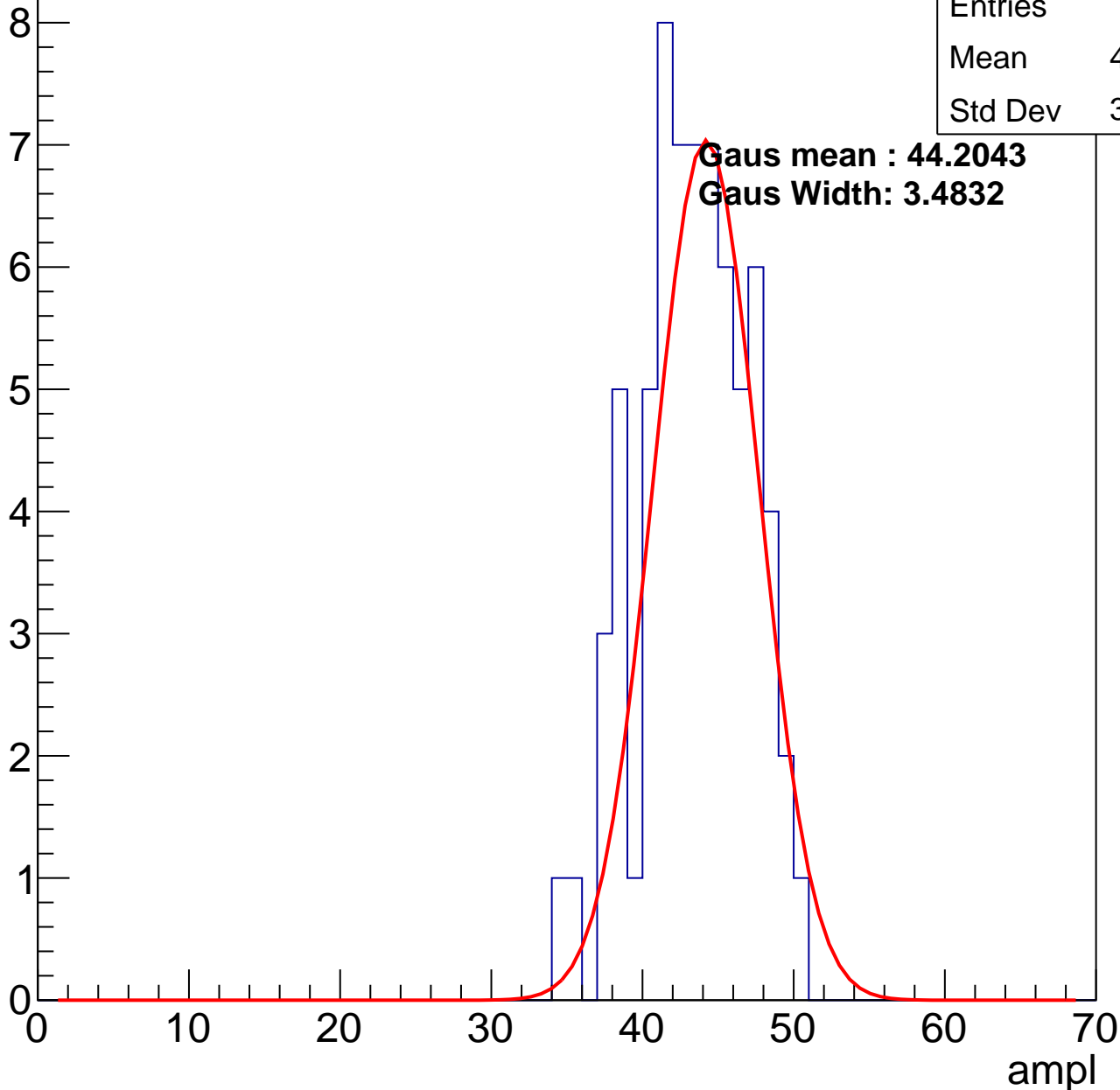
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 42.93 |
| Std Dev | 3.552 |

Gaus mean : 44.2043

Gaus Width: 3.4832

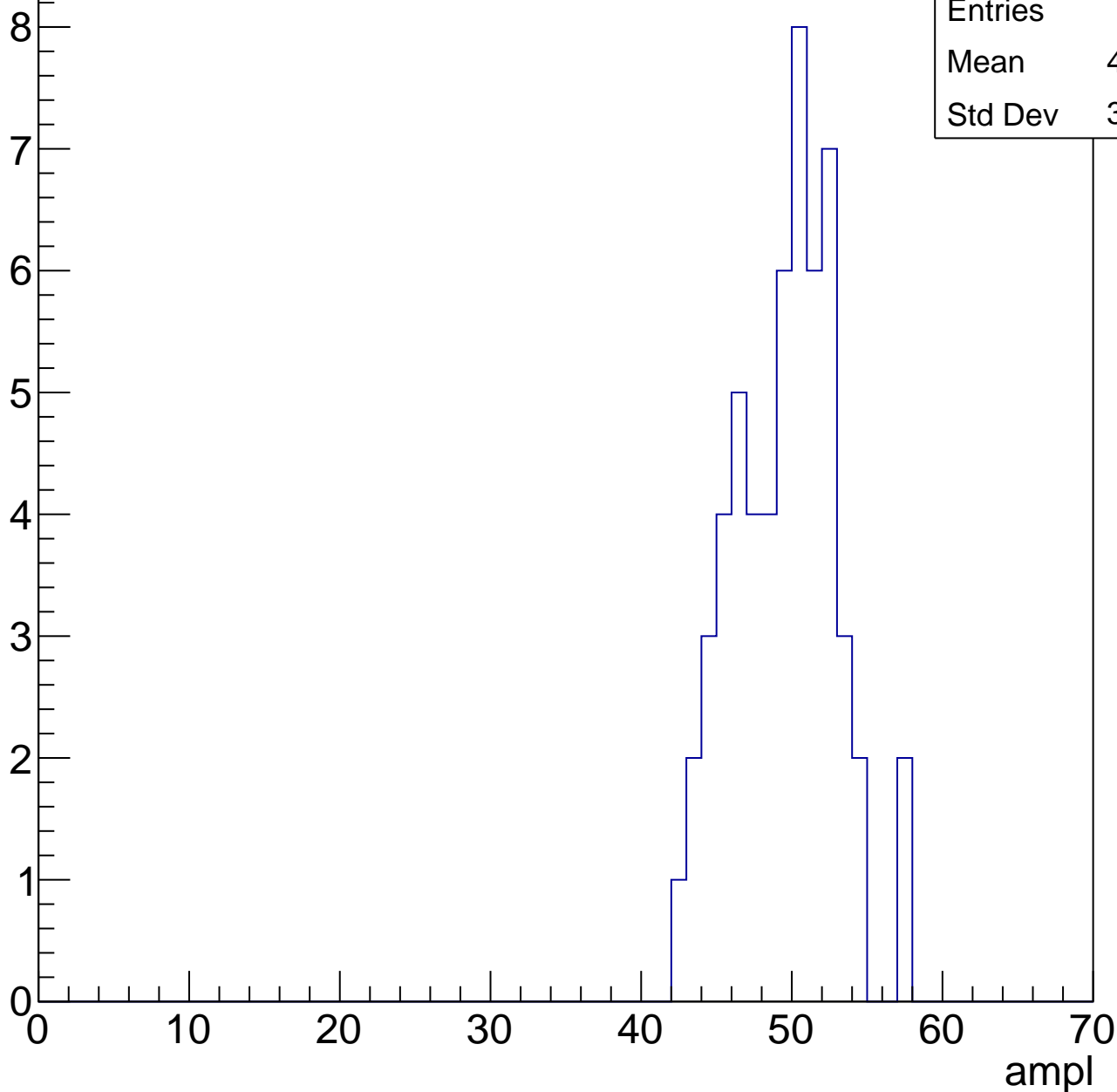


B0L001S, U7-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

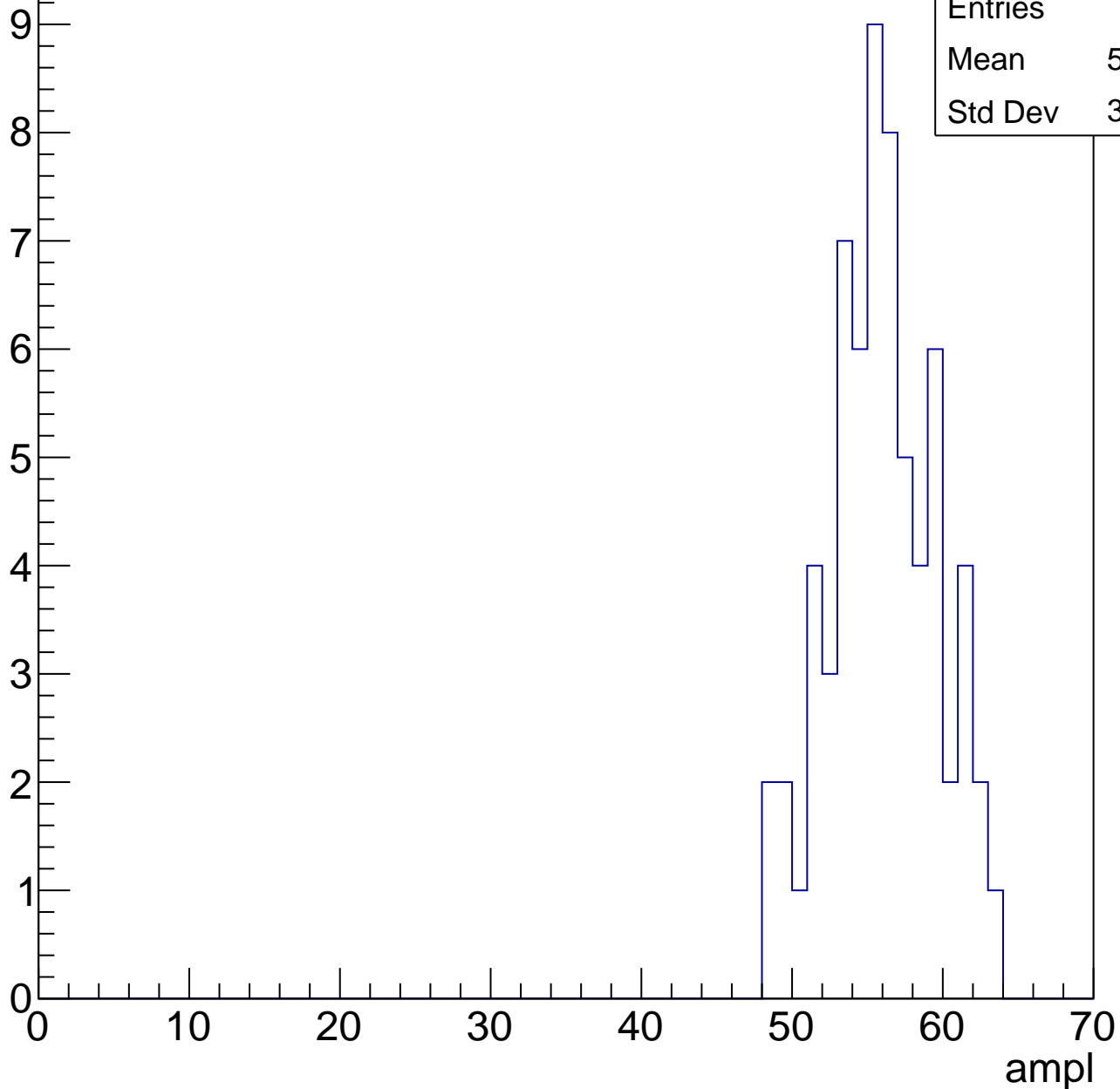
| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.04 |
| Std Dev | 3.366 |



B0L001S, U7-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

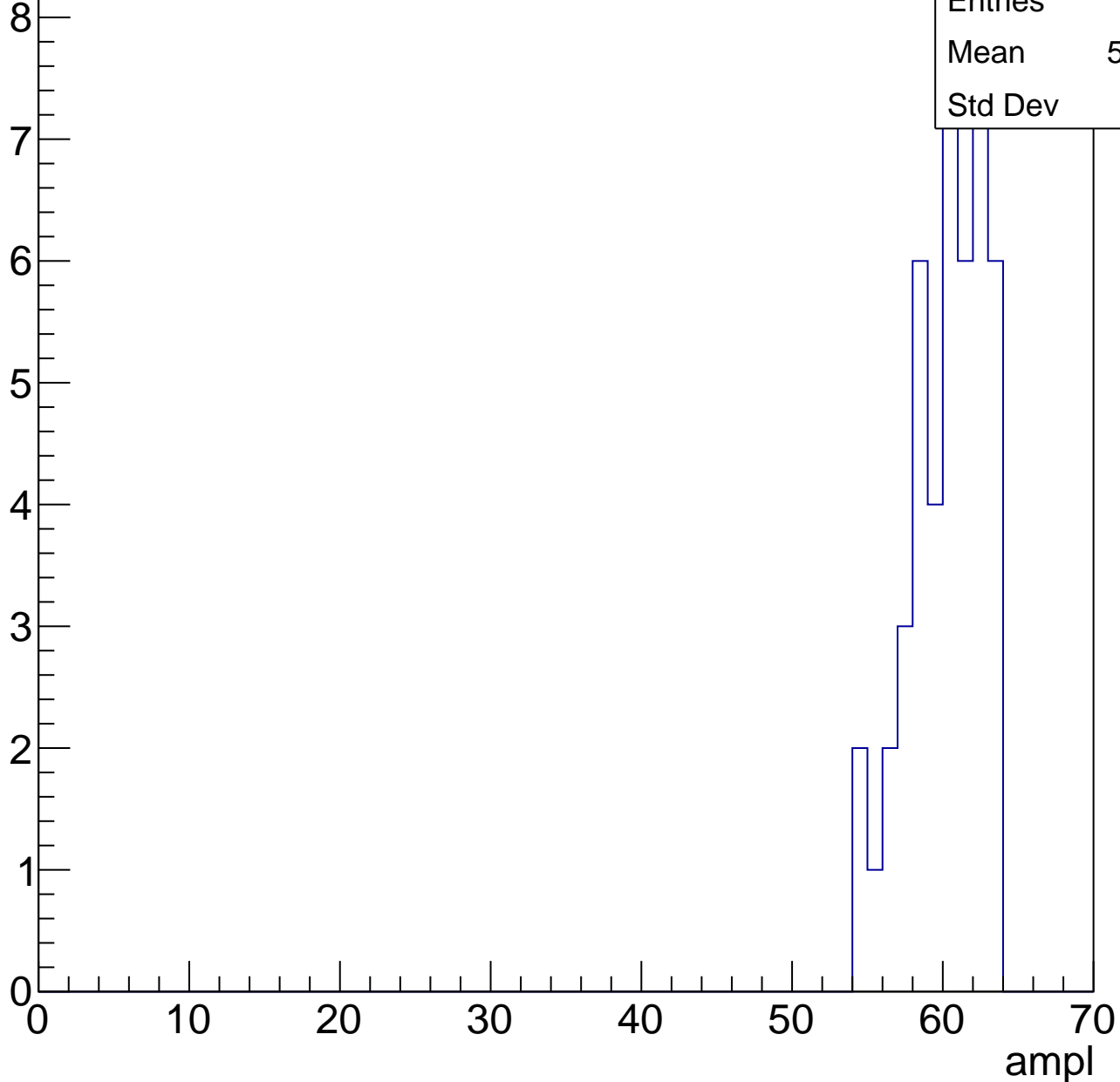


| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.52 |
| Std Dev | 3.522 |

B0L001S, U7-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

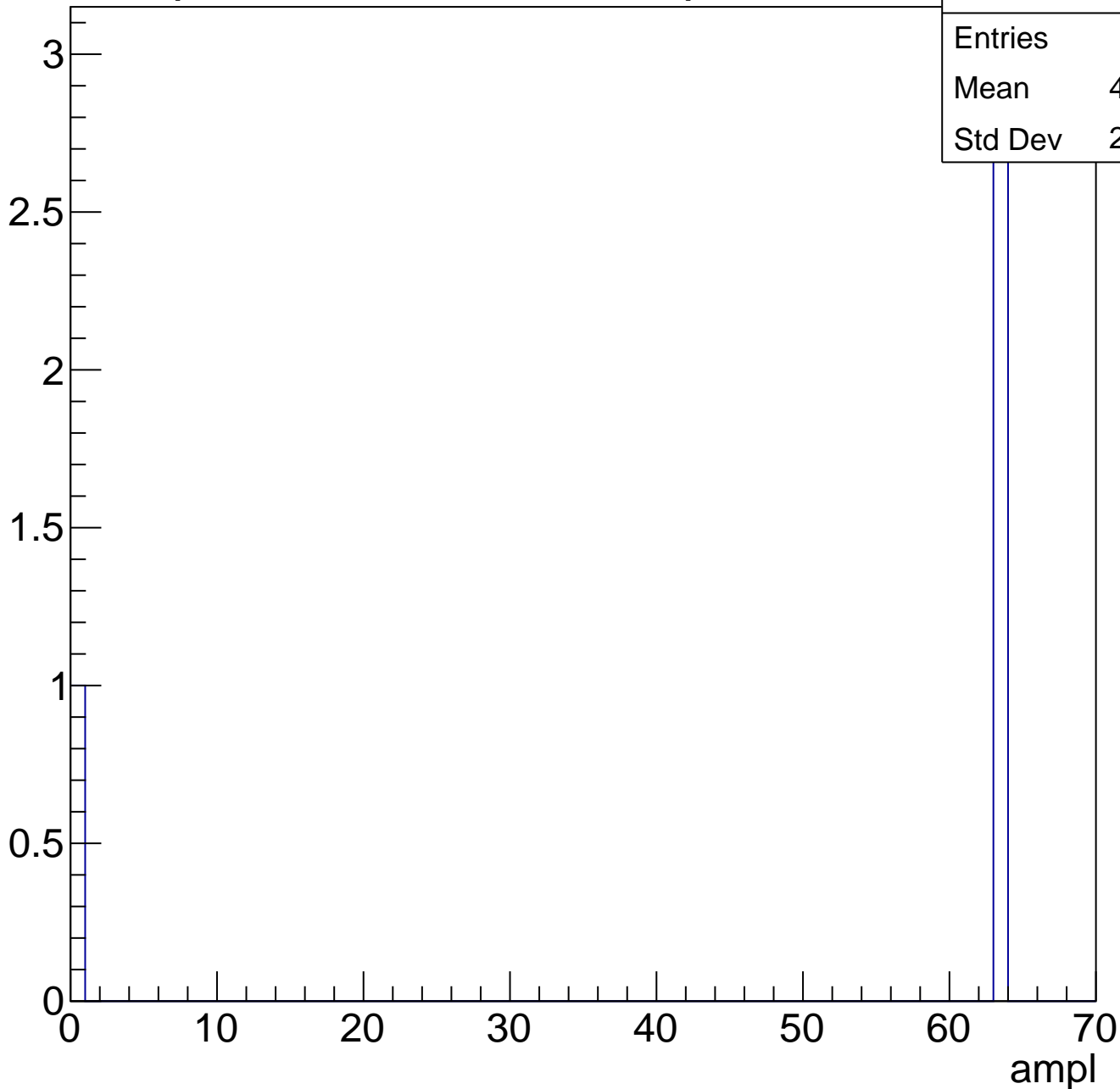
Entry



B0L001S, U7-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 11.5 |
| Std Dev | 11.5 |

B0L001S, U7-ch36, adc0

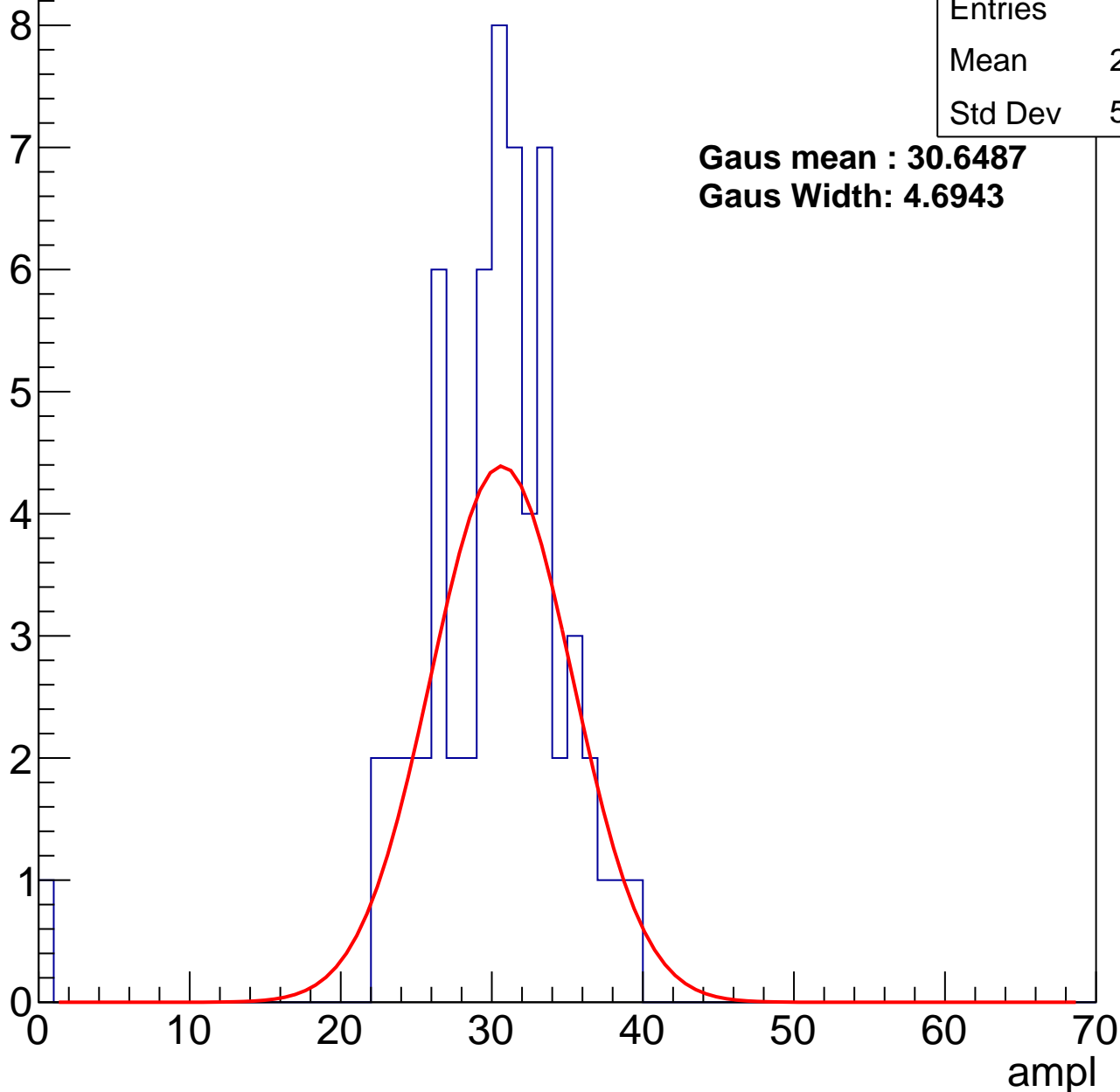
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 29.56 |
| Std Dev | 5.467 |

Gaus mean : 30.6487

Gaus Width: 4.6943



B0L001S, U7-ch36, adc1

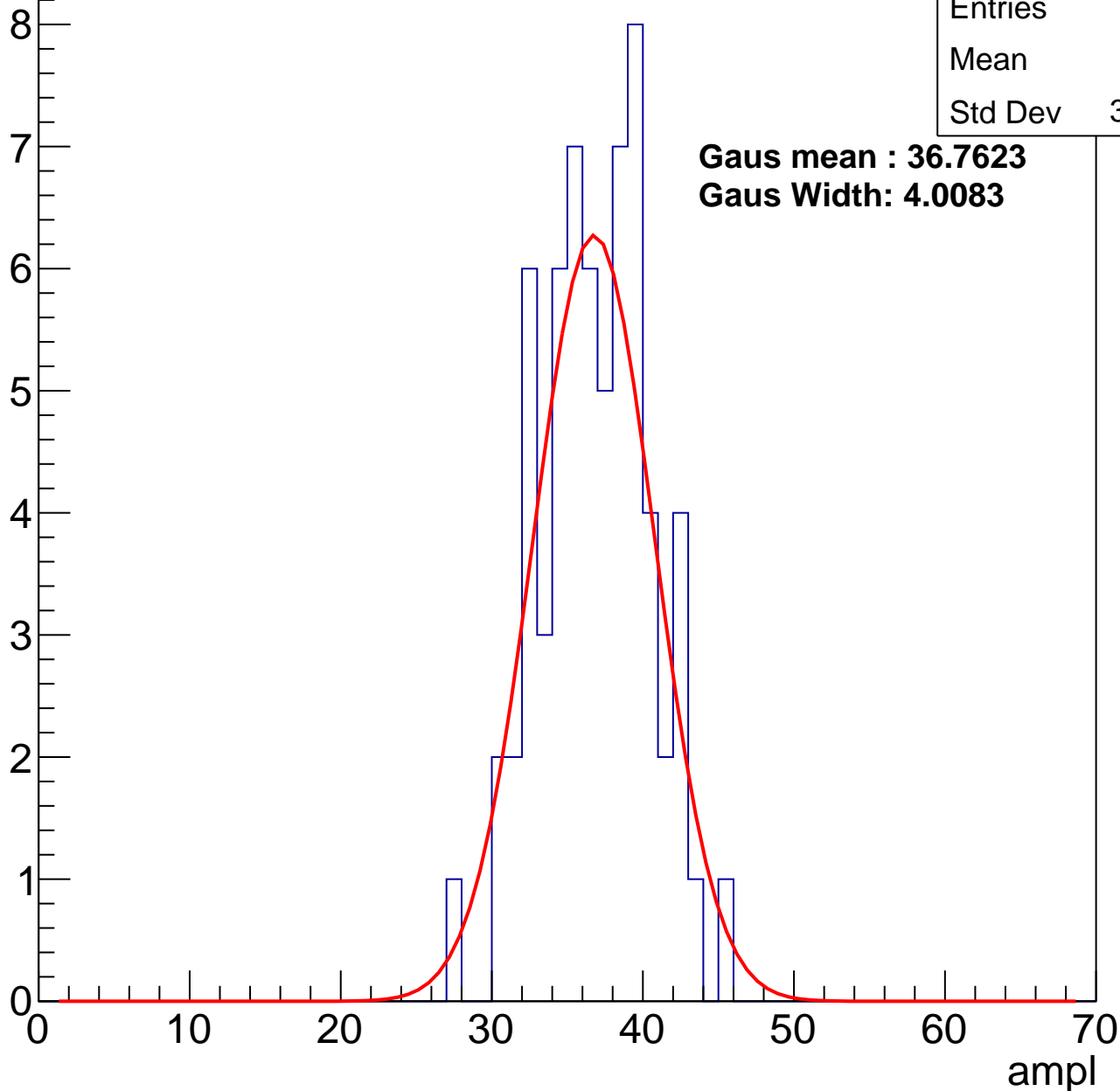
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 36.4 |
| Std Dev | 3.594 |

Gaus mean : 36.7623

Gaus Width: 4.0083



B0L001S, U7-ch36, adc2

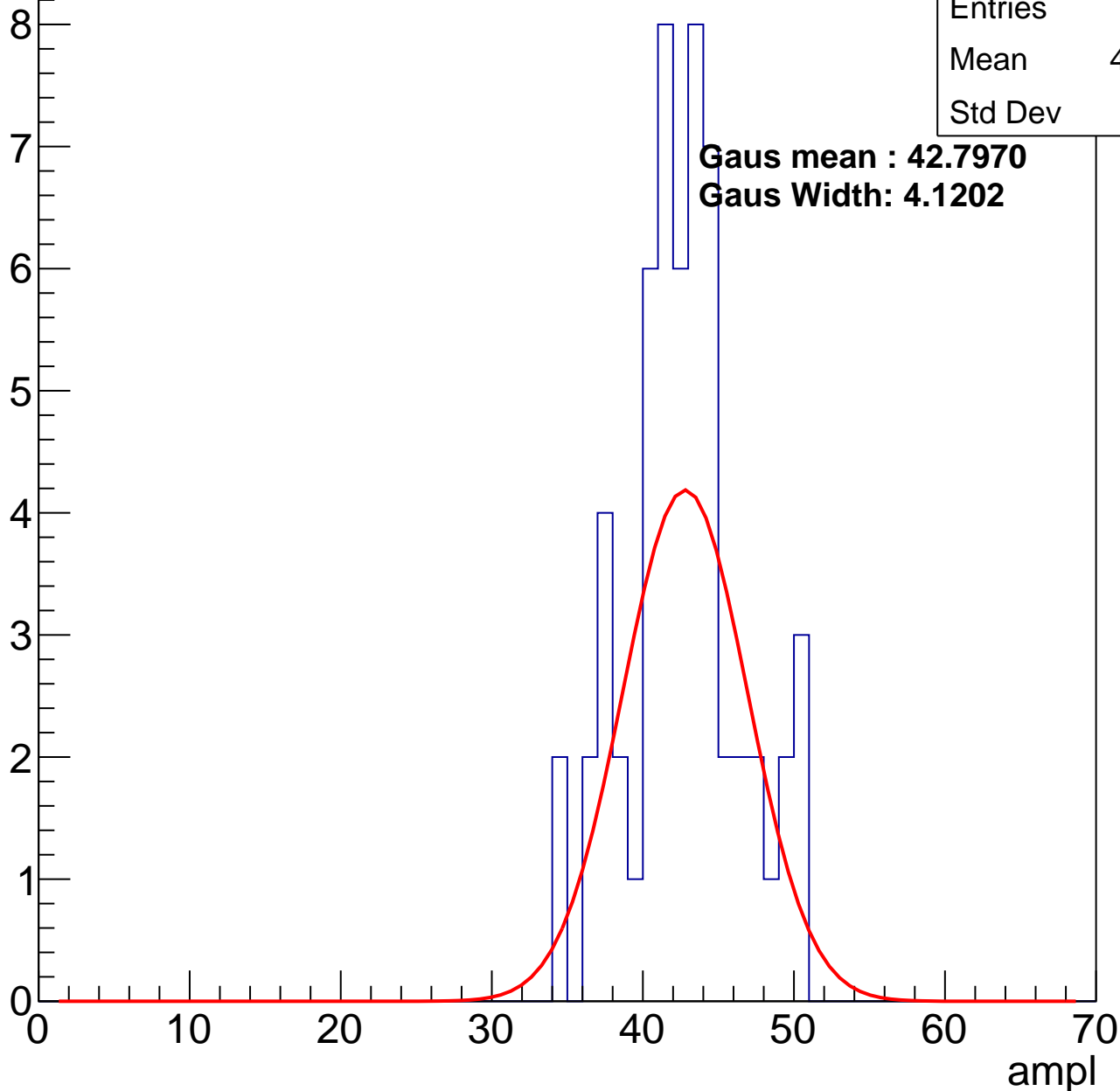
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 42.19 |
| Std Dev | 3.79 |

Gaus mean : 42.7970

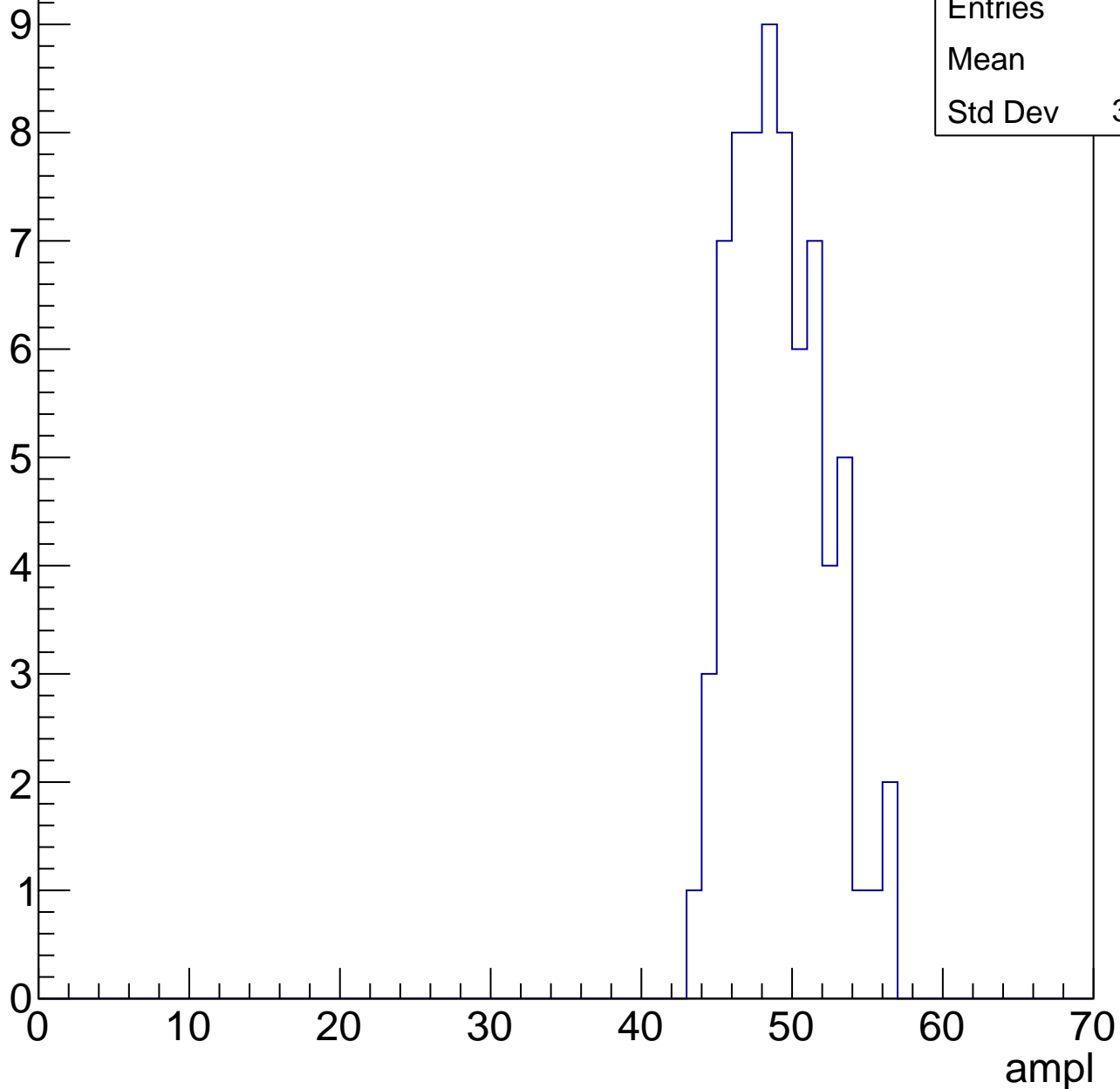
Gaus Width: 4.1202



B0L001S, U7-ch36, adc3

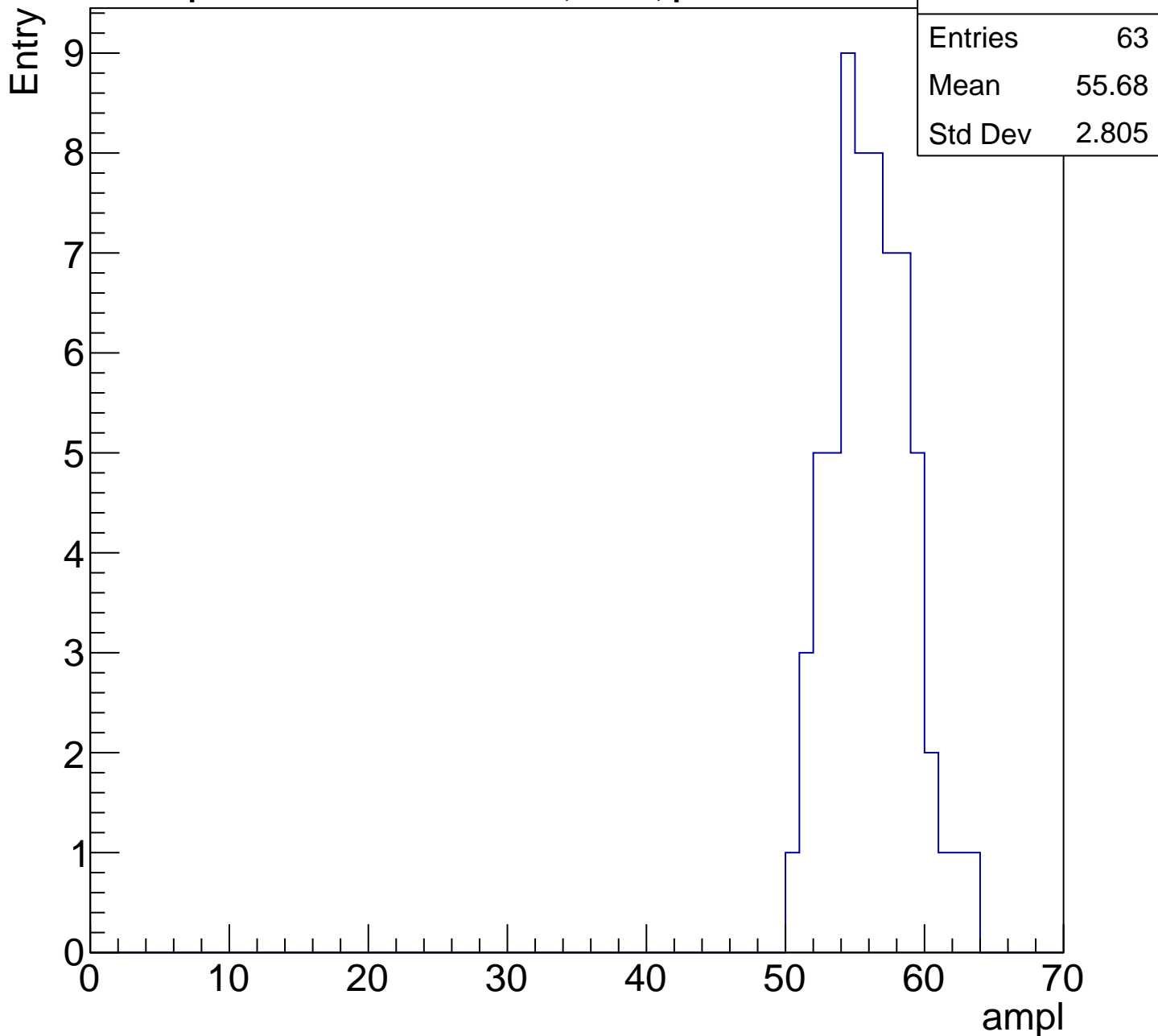
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 59.04 |
| Std Dev | 9.094 |

ampl

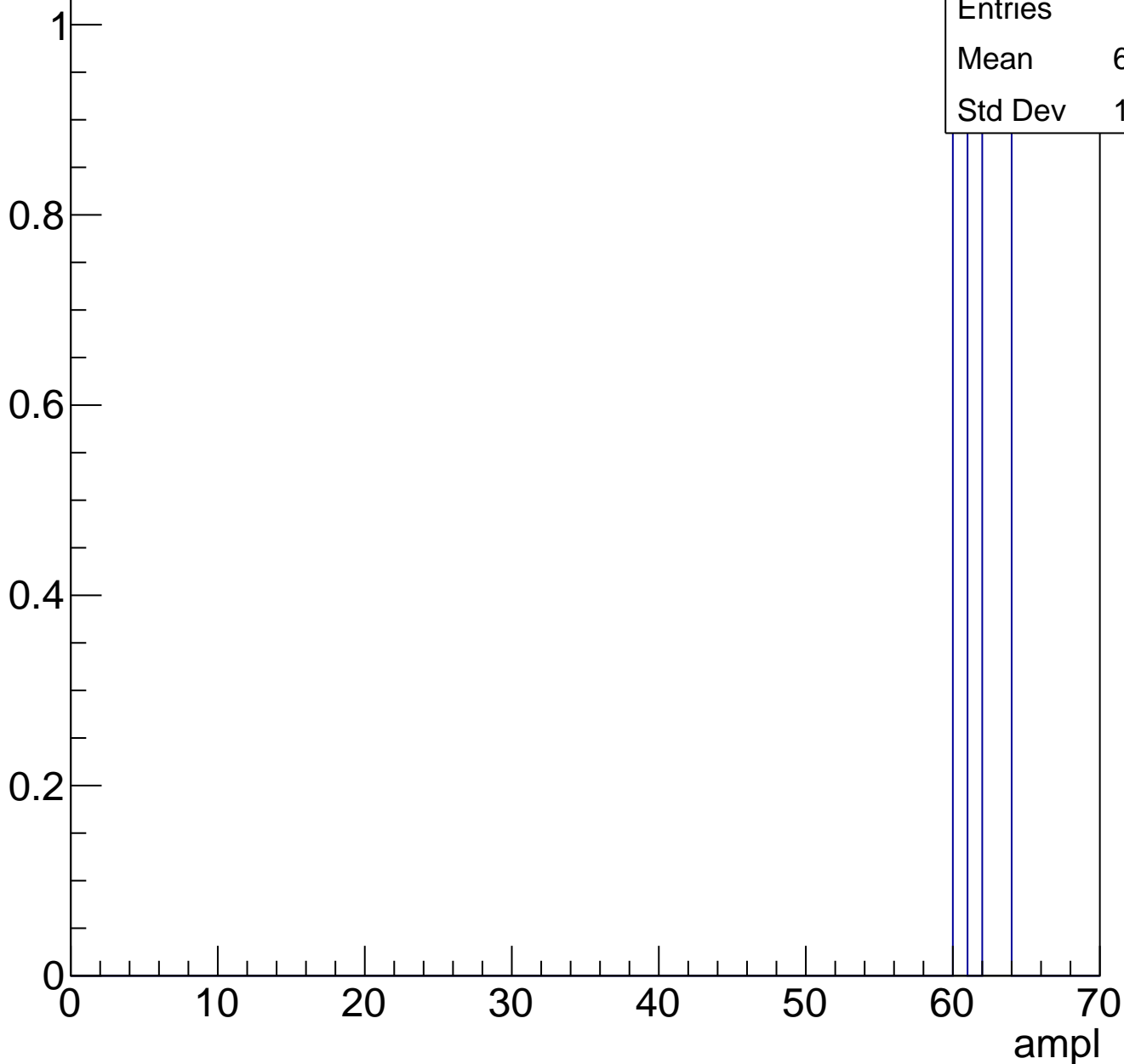
0 10 20 30 40 50 60 70



B0L001S, U7-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch37, adc0

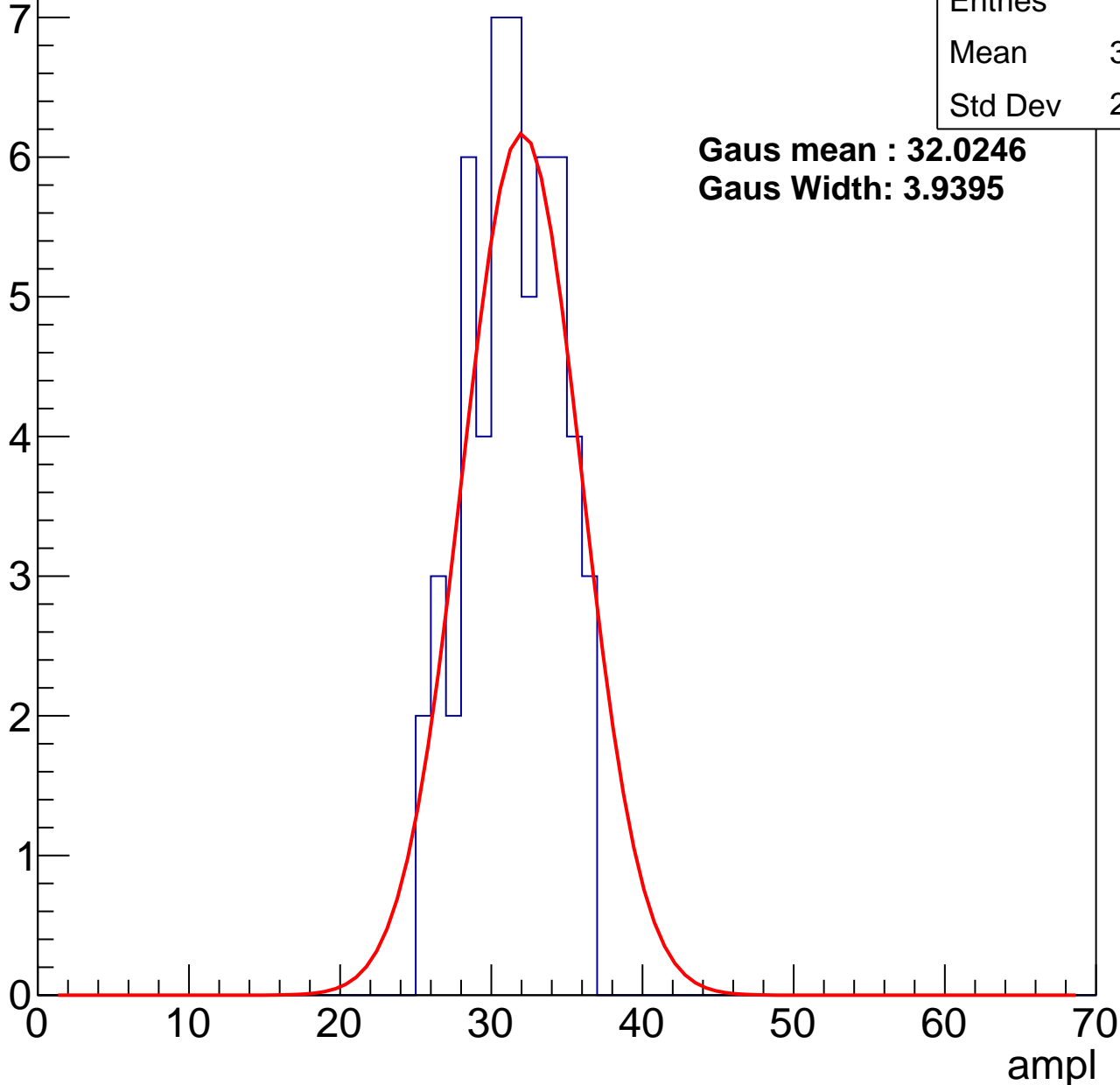
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 30.96 |
| Std Dev | 2.948 |

Gaus mean : 32.0246

Gaus Width: 3.9395



B0L001S, U7-ch37, adc1

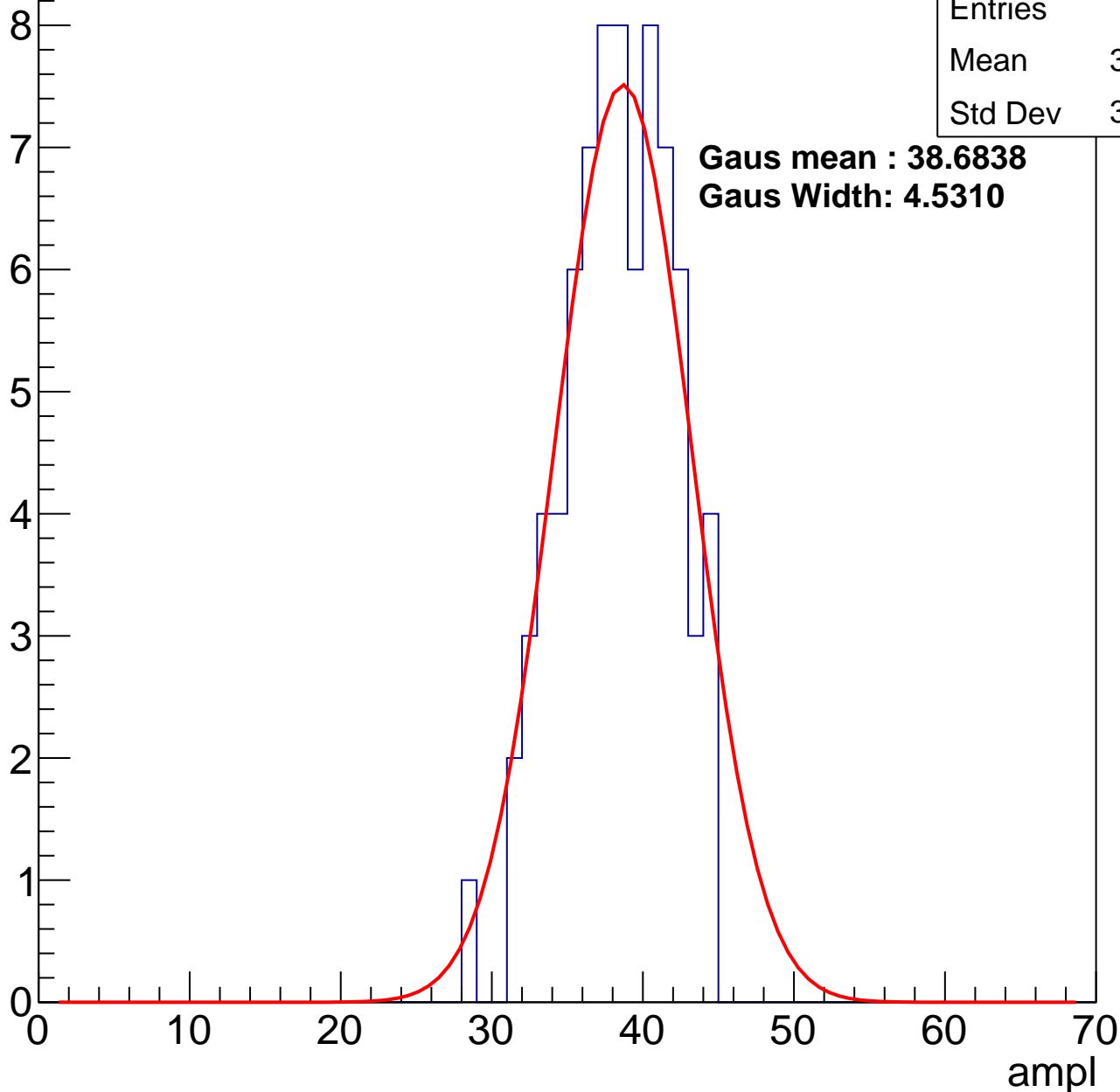
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.84 |
| Std Dev | 3.557 |

Gaus mean : 38.6838

Gaus Width: 4.5310



B0L001S, U7-ch37, adc2

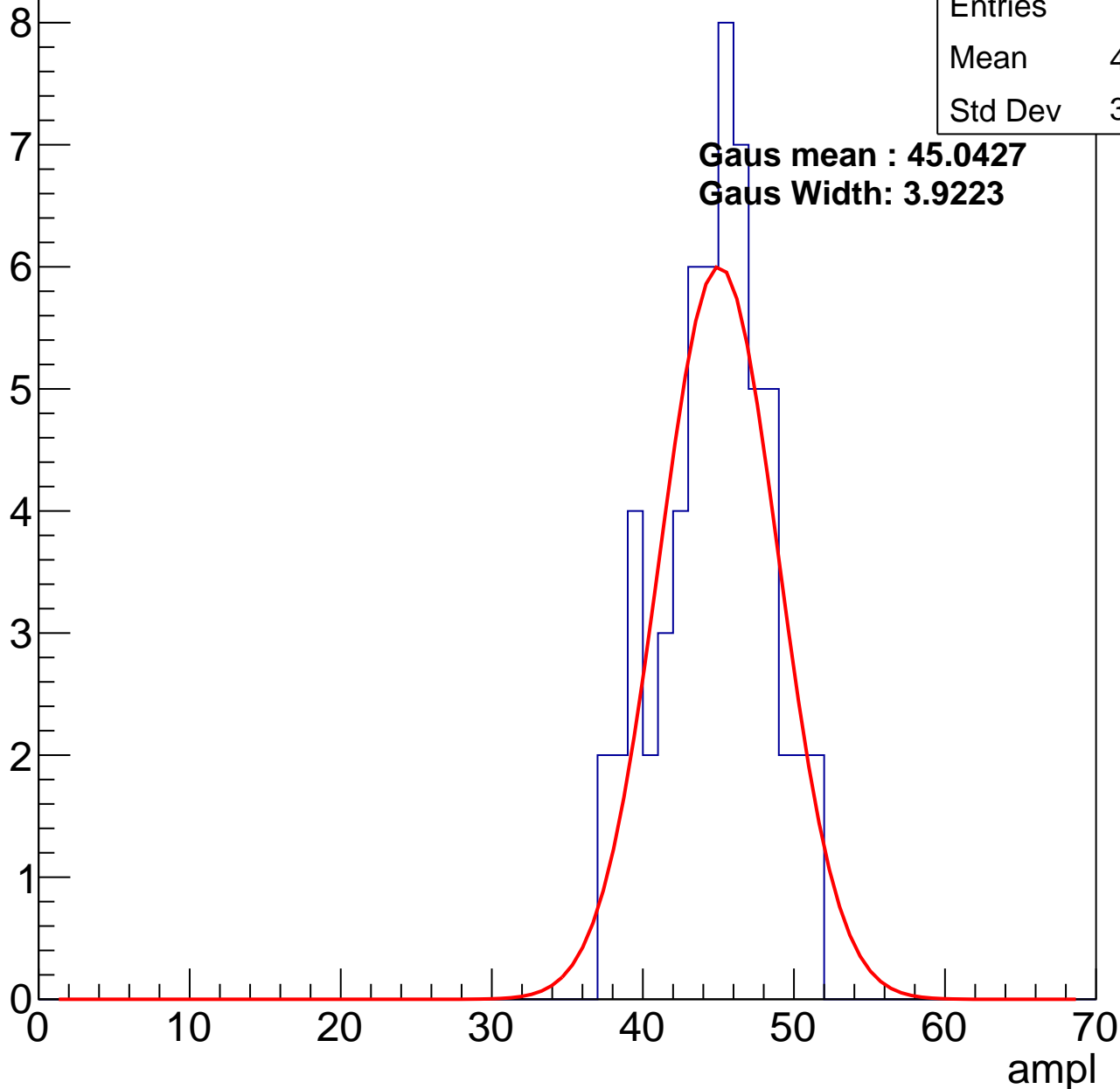
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 44.27 |
| Std Dev | 3.483 |

Gaus mean : 45.0427

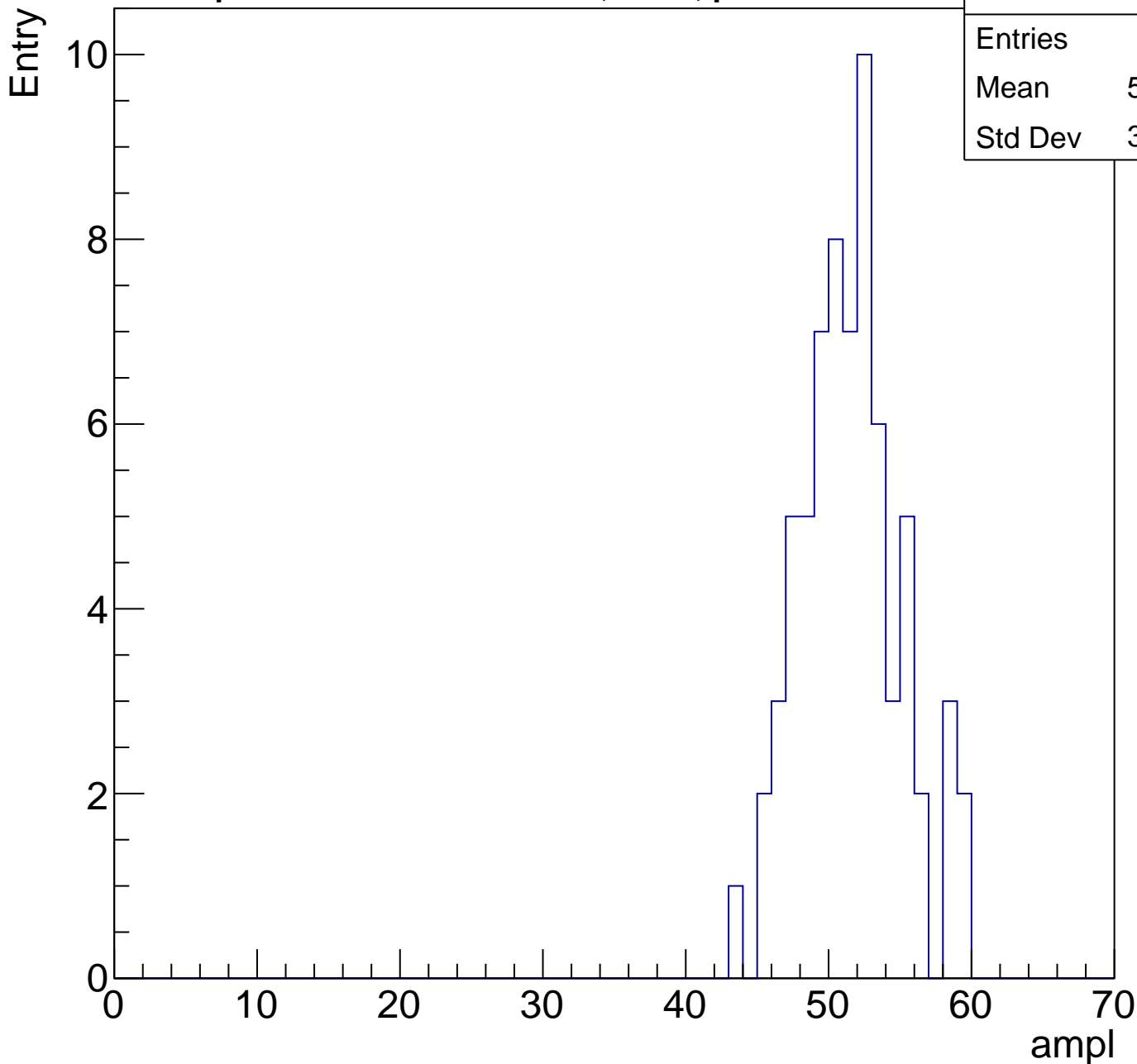
Gaus Width: 3.9223



B0L001S, U7-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 51.09 |
| Std Dev | 3.484 |



B0L001S, U7-ch37, adc4

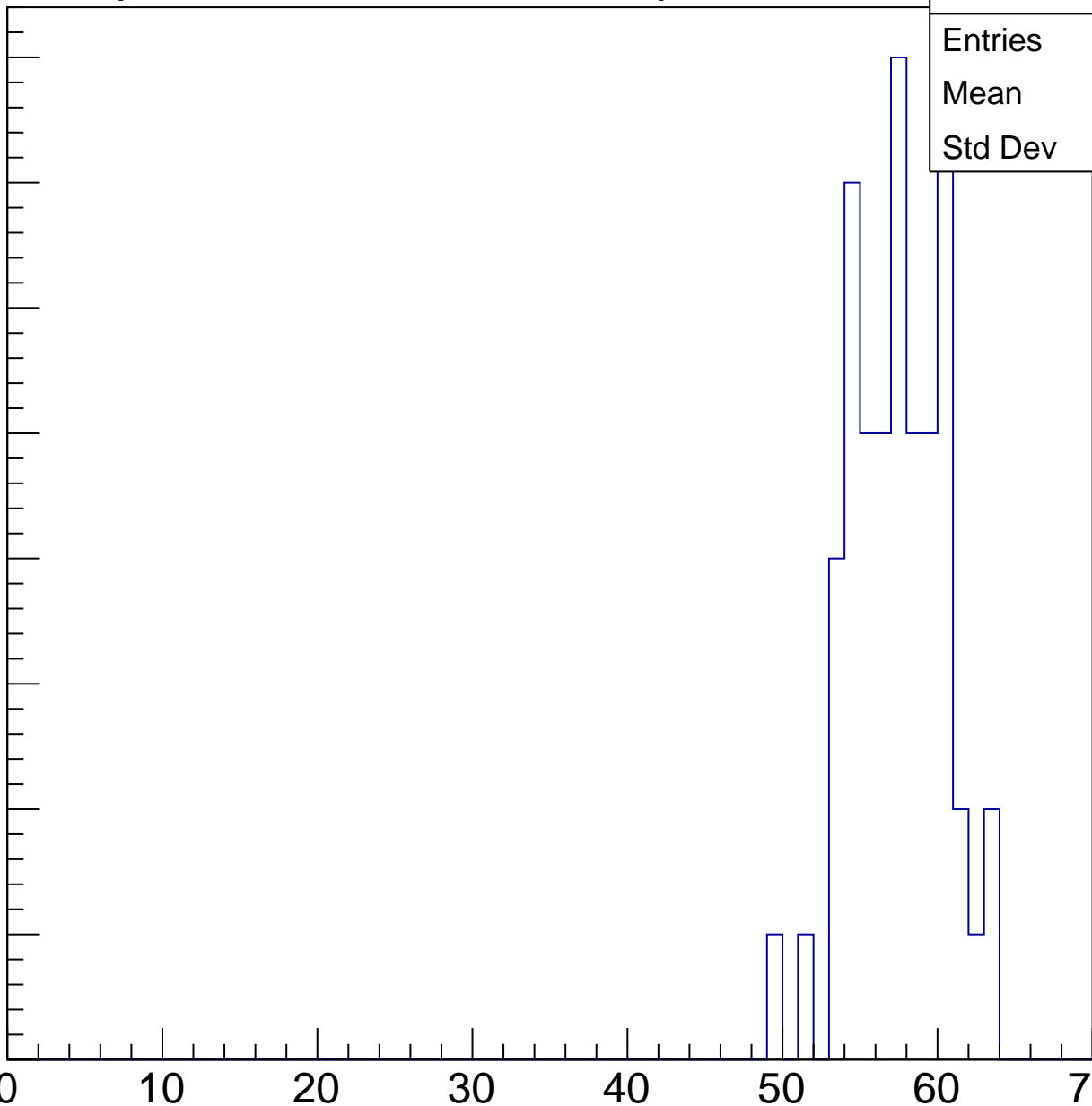
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 54 |
| Mean | 56.96 |
| Std Dev | 2.975 |

ampl



B0L001S, U7-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

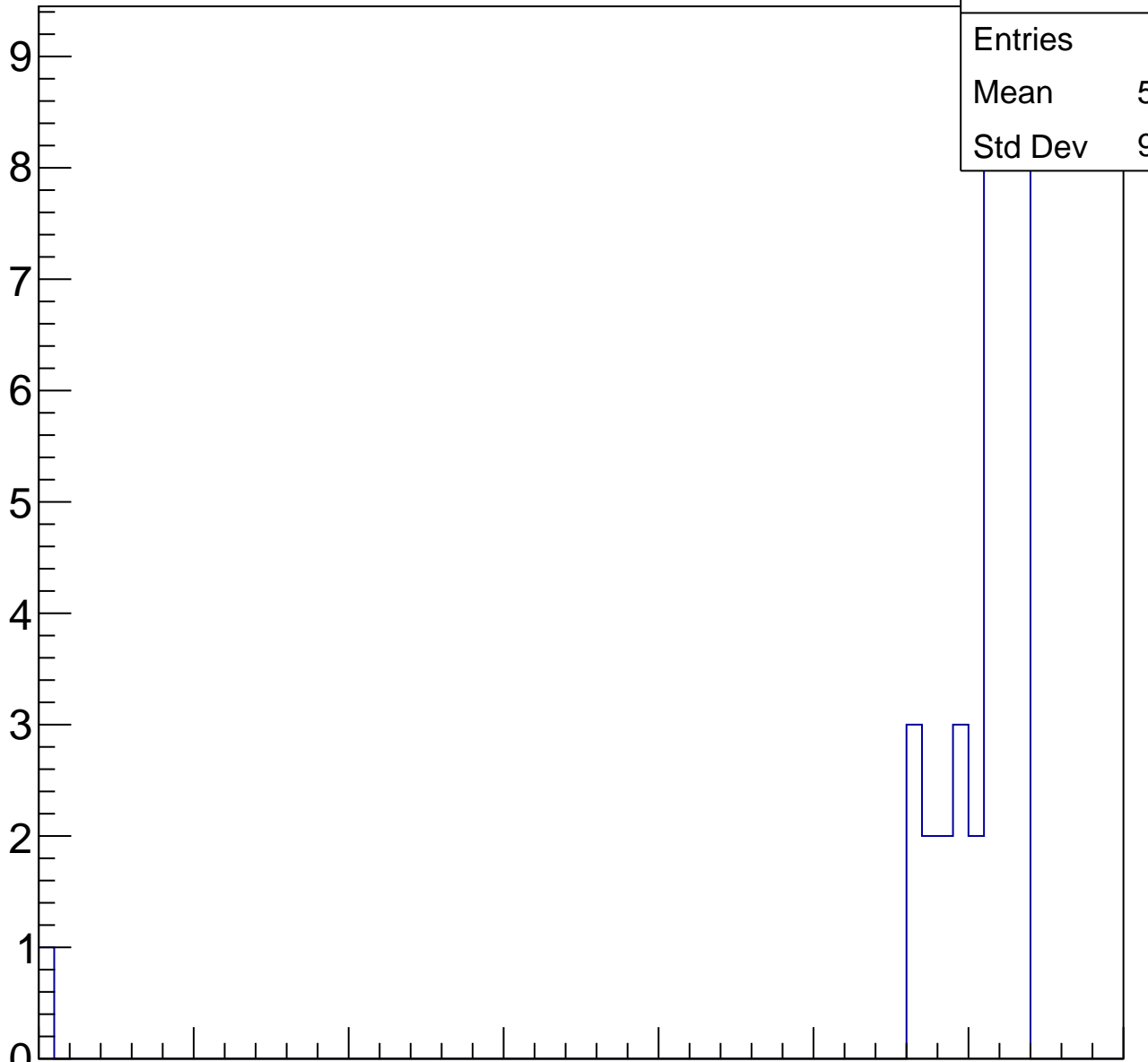
Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 38 |
| Mean | 59.08 |
| Std Dev | 9.948 |

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 25 |
| Std Dev | 0 |

ampl

B0L001S, U7-ch38, adc0

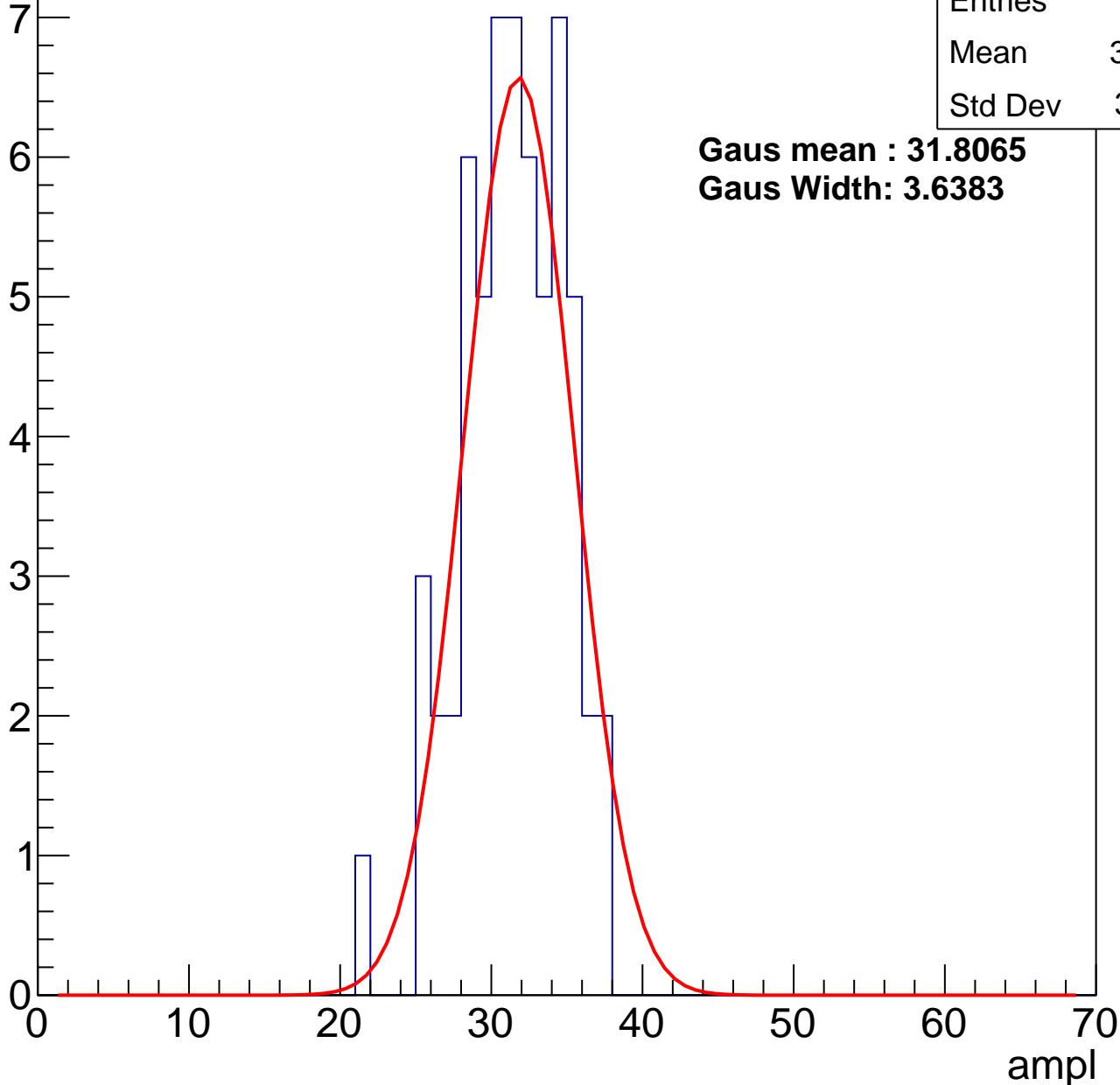
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 30.97 |
| Std Dev | 3.321 |

Gaus mean : 31.8065

Gaus Width: 3.6383



B0L001S, U7-ch38, adc1

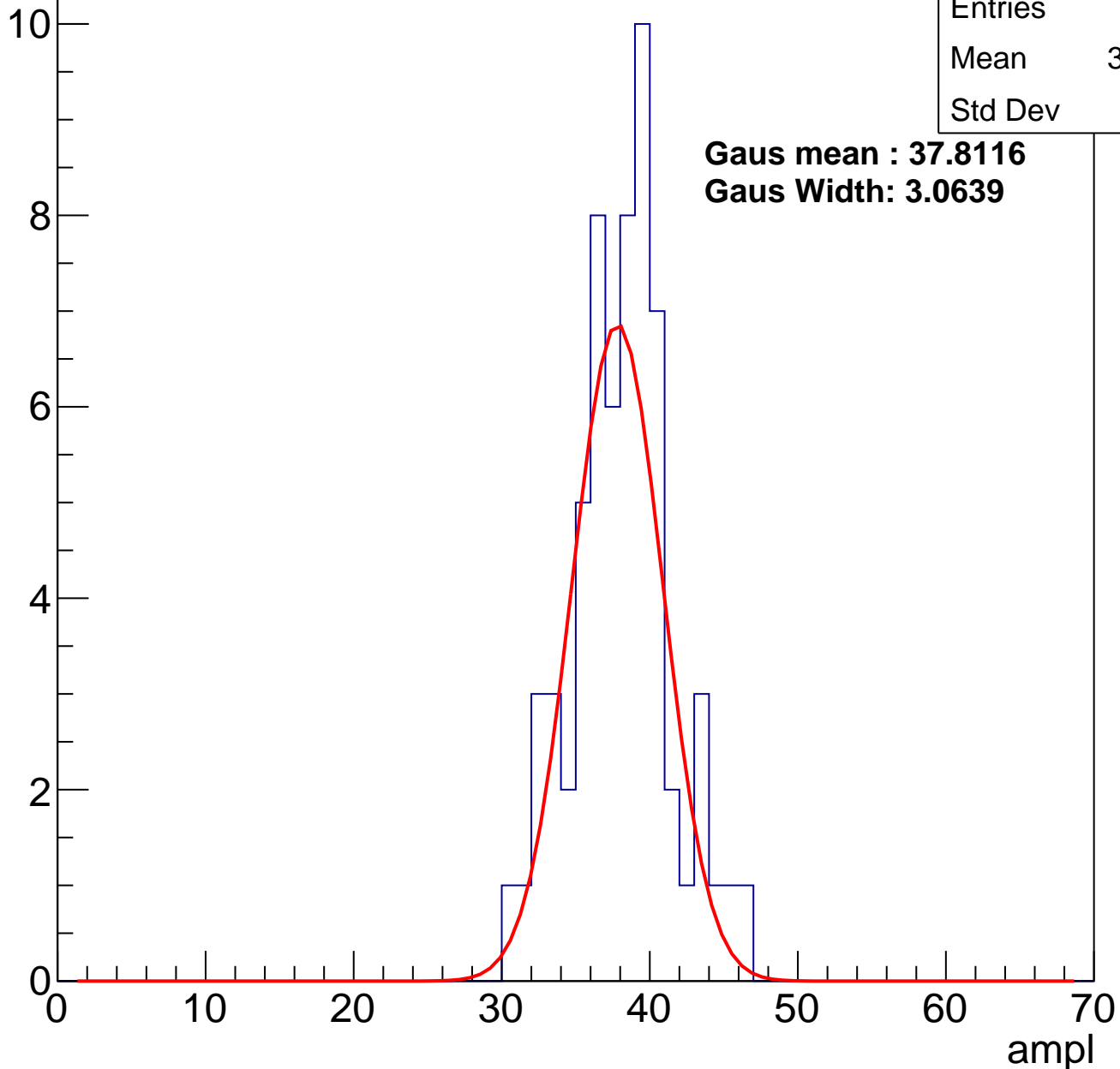
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 63 |
| Mean | 37.63 |
| Std Dev | 3.33 |

Gaus mean : 37.8116

Gaus Width: 3.0639

Entry



B0L001S, U7-ch38, adc2

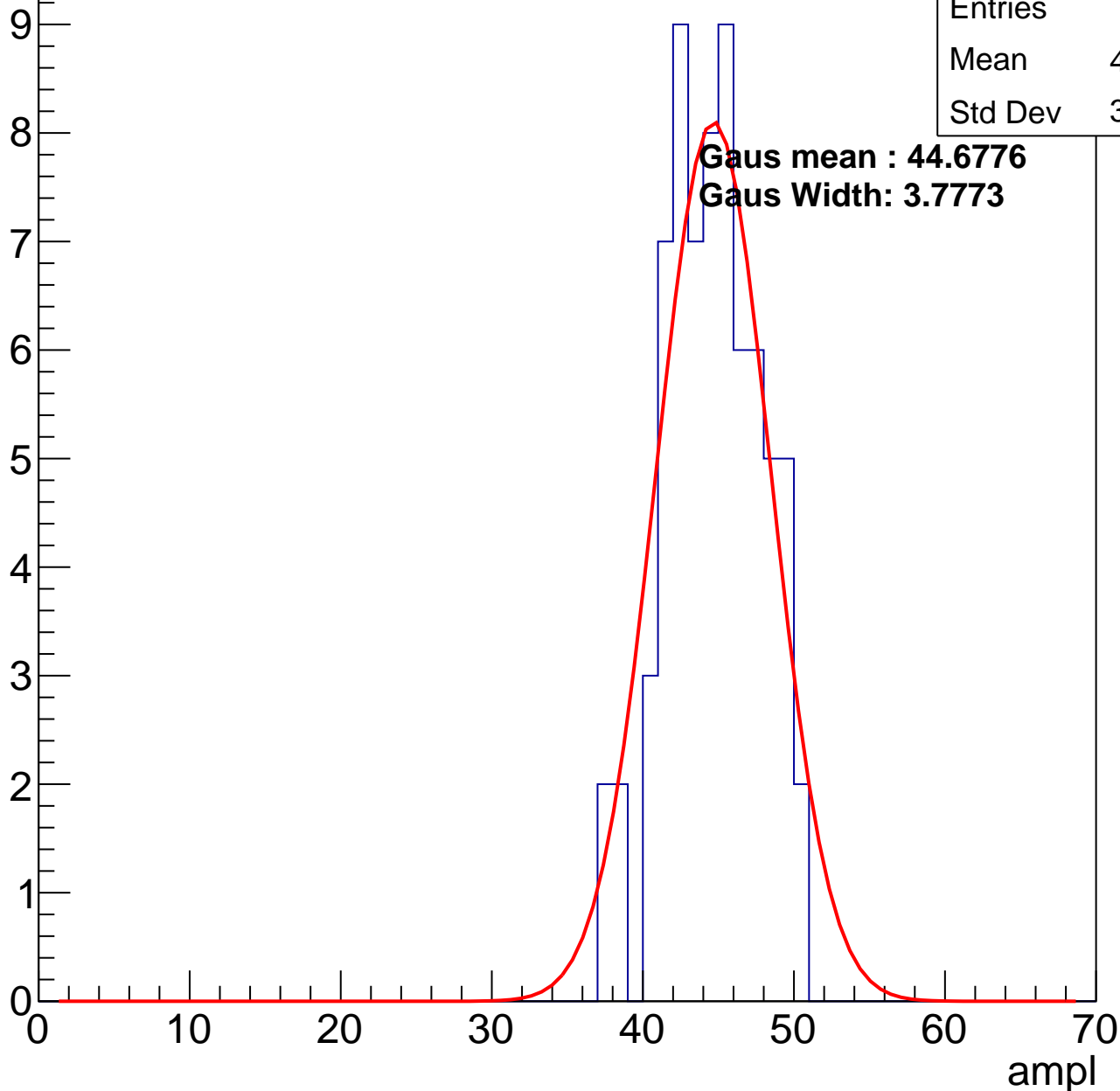
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 44.17 |
| Std Dev | 3.108 |

Gaus mean : 44.6776

Gaus Width: 3.7773

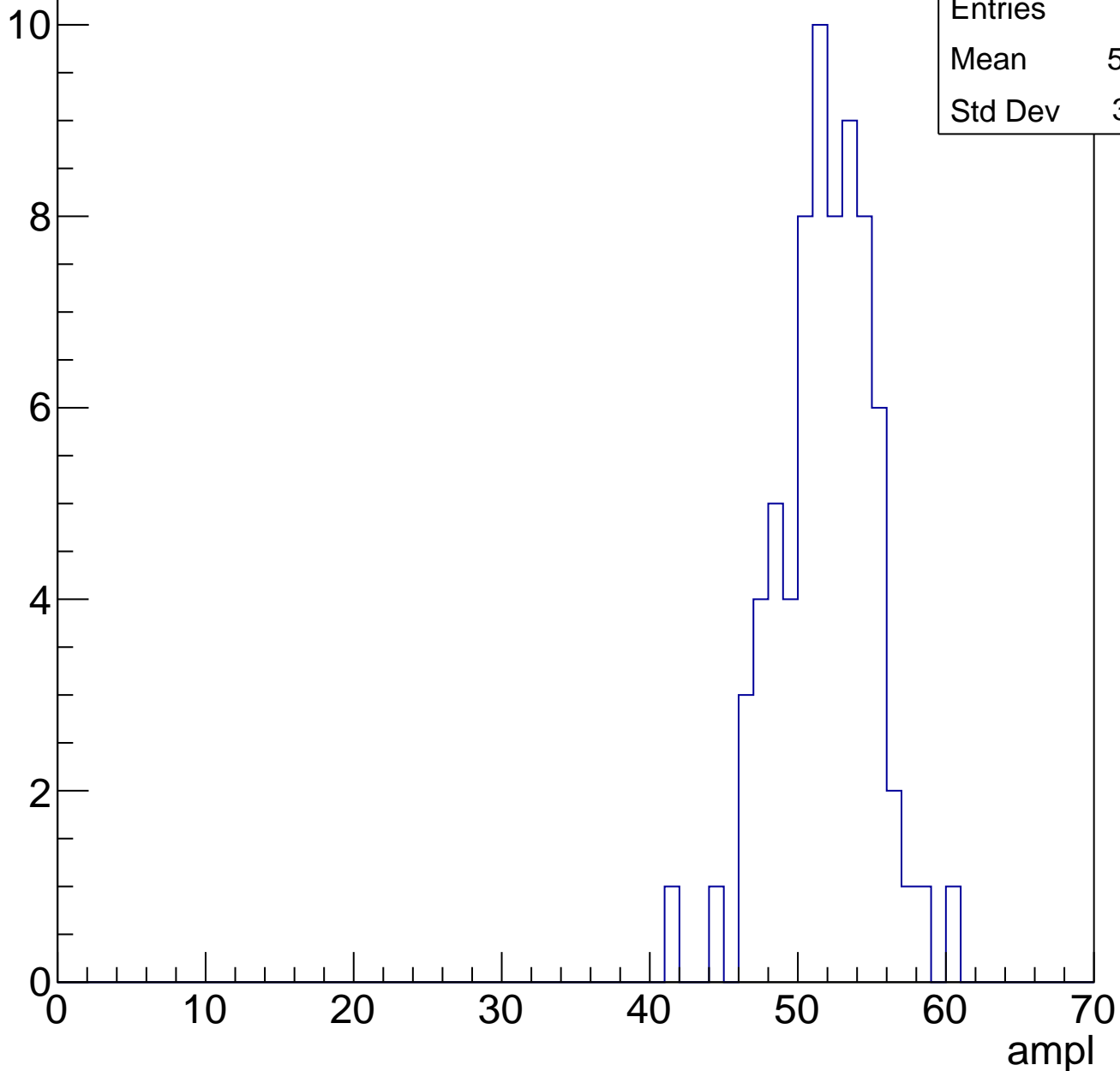


B0L001S, U7-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 51.38 |
| Std Dev | 3.281 |

Entry

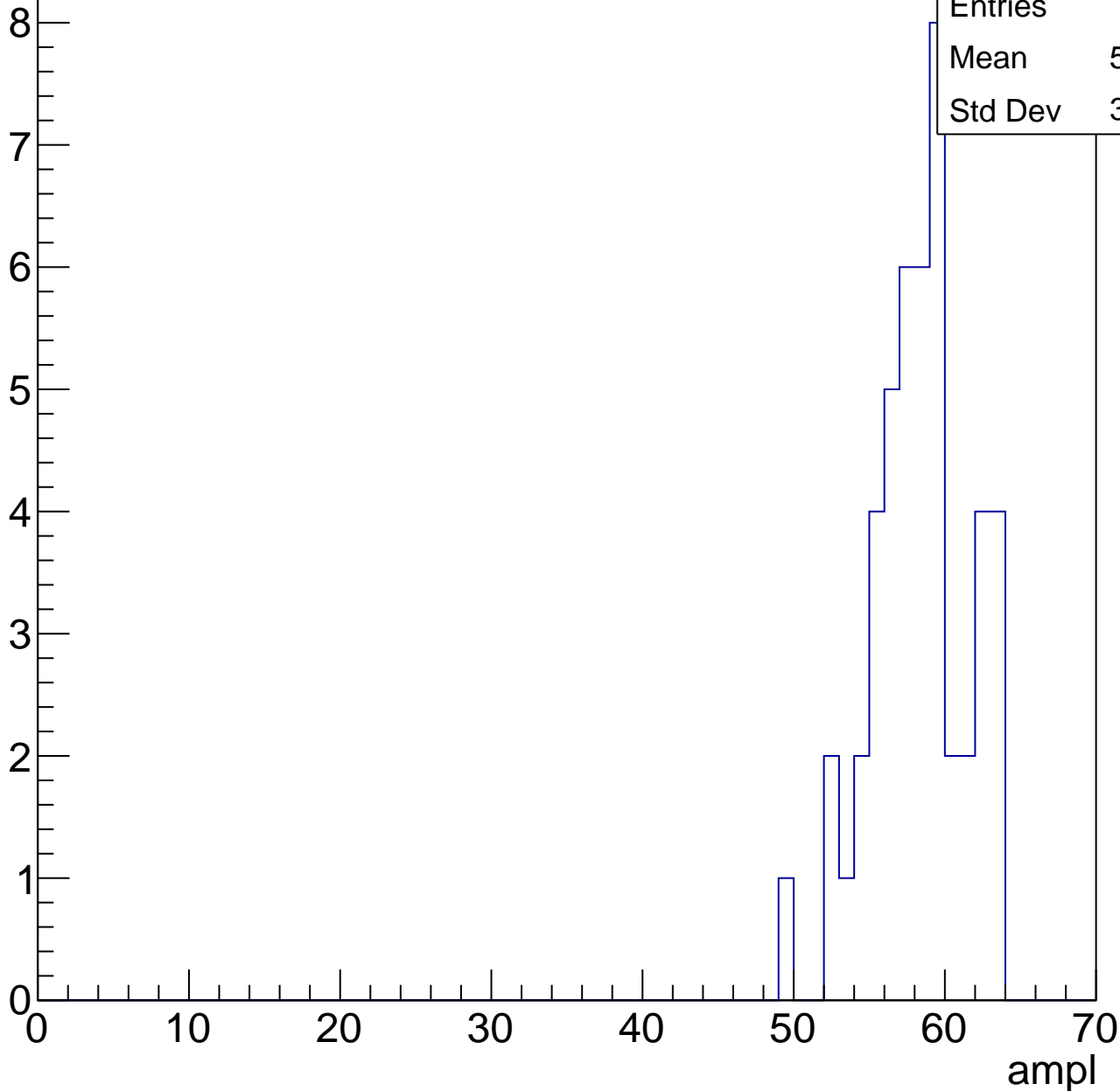


B0L001S, U7-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

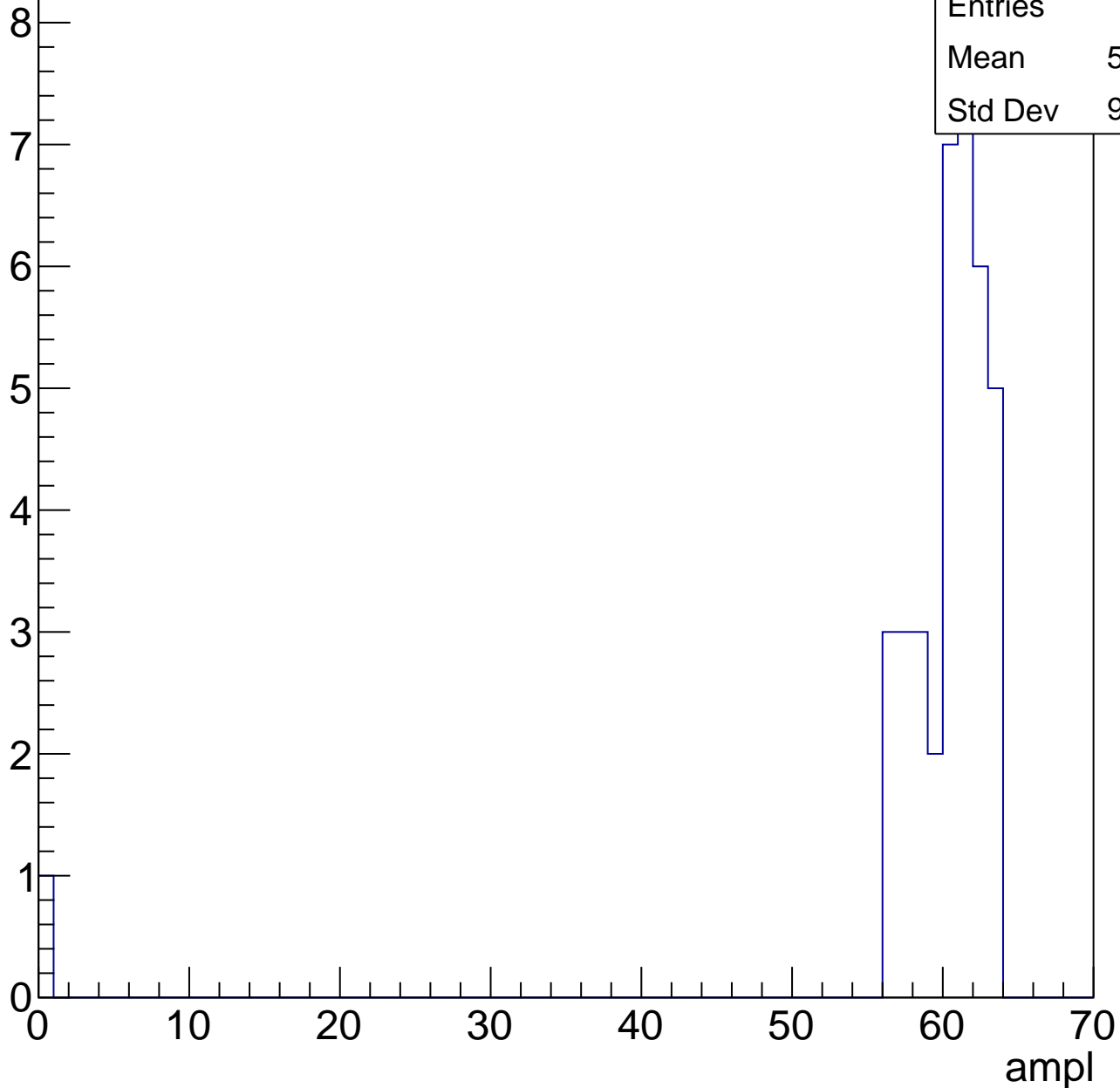
| | |
|---------|-------|
| Entries | 47 |
| Mean | 57.83 |
| Std Dev | 3.158 |



B0L001S, U7-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch39, adc0

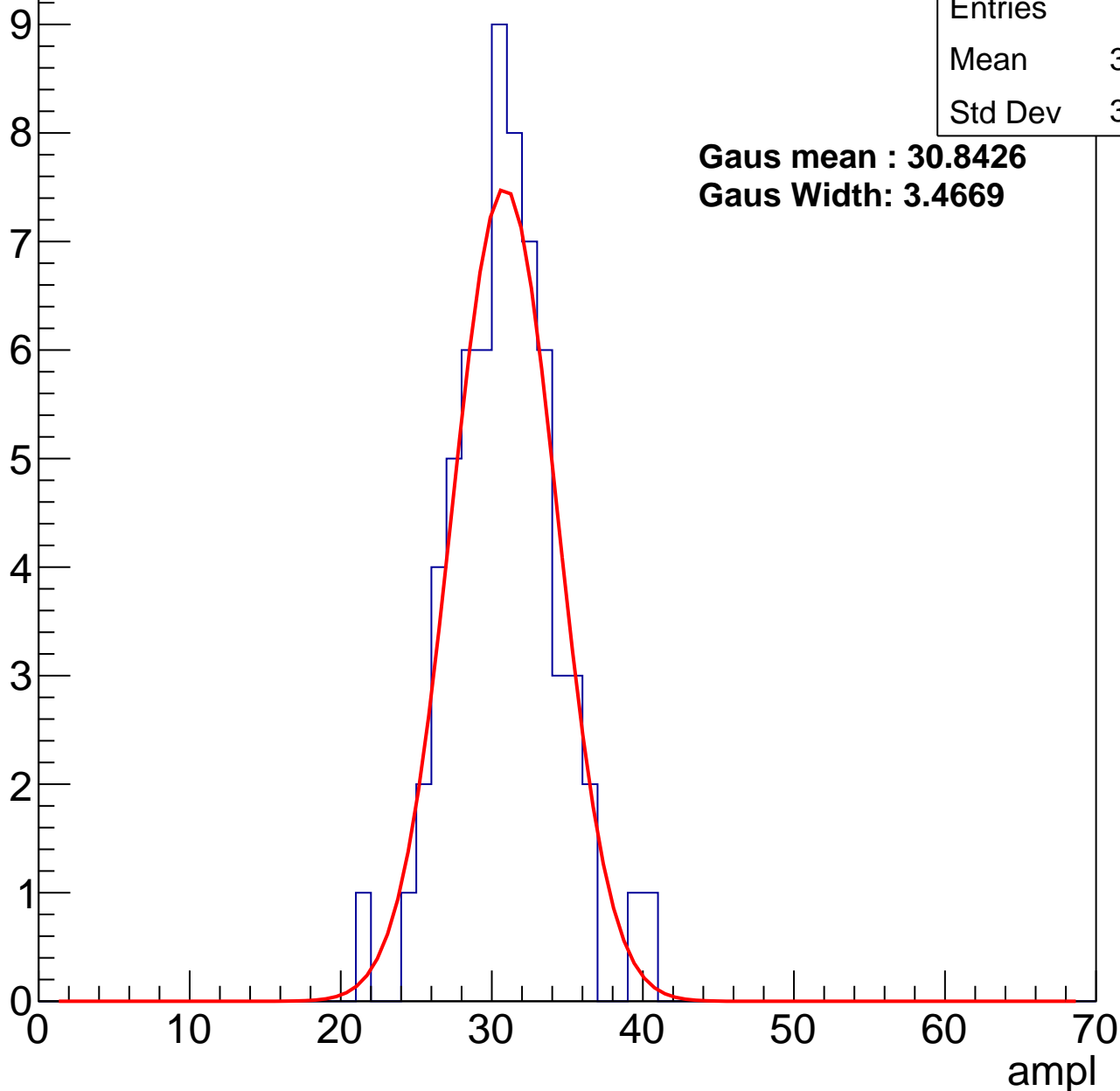
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 30.37 |
| Std Dev | 3.426 |

Gaus mean : 30.8426

Gaus Width: 3.4669

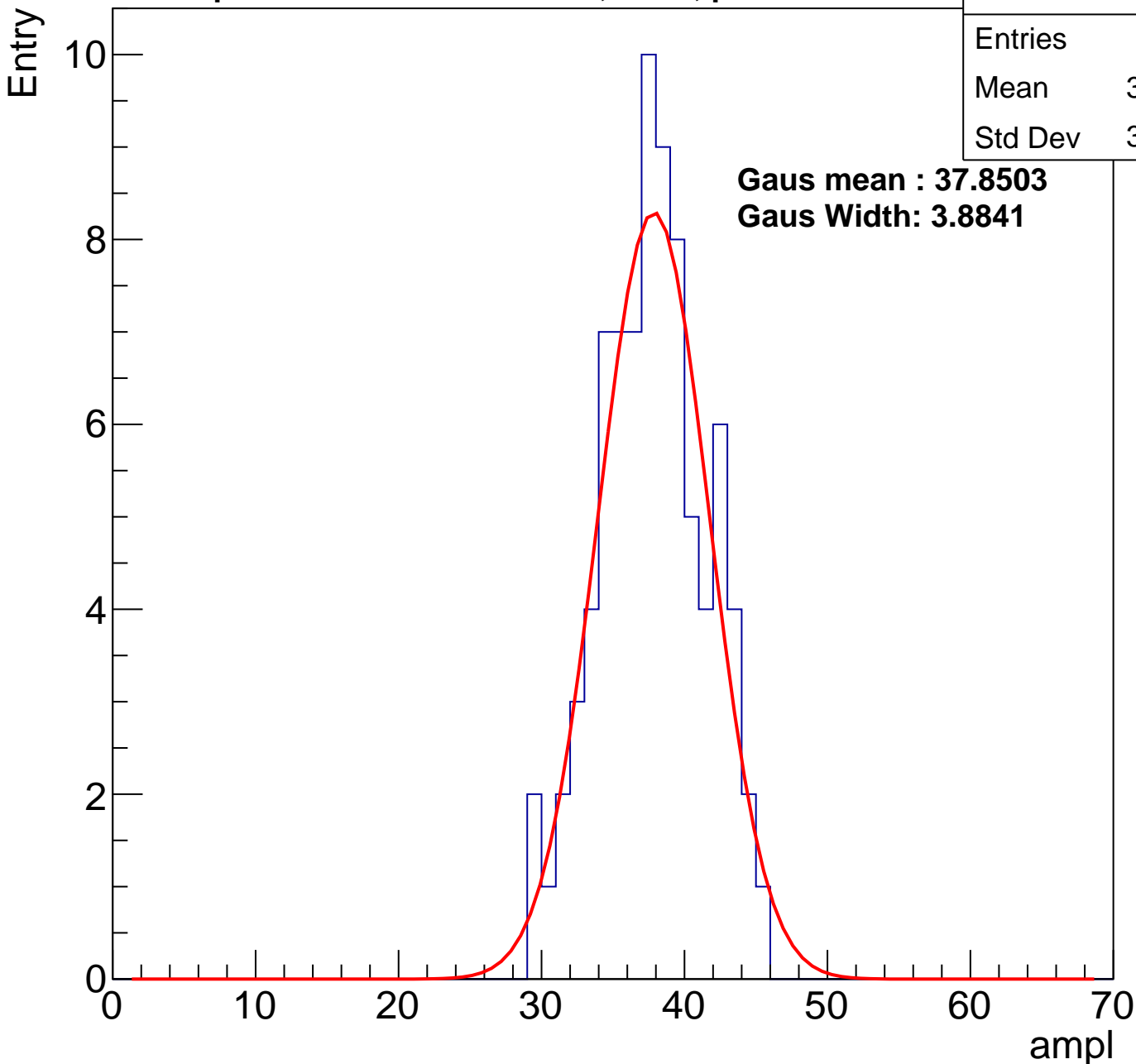


B0L001S, U7-ch39, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 82 |
| Mean | 37.29 |
| Std Dev | 3.638 |

Gaus mean : 37.8503
Gaus Width: 3.8841



B0L001S, U7-ch39, adc2

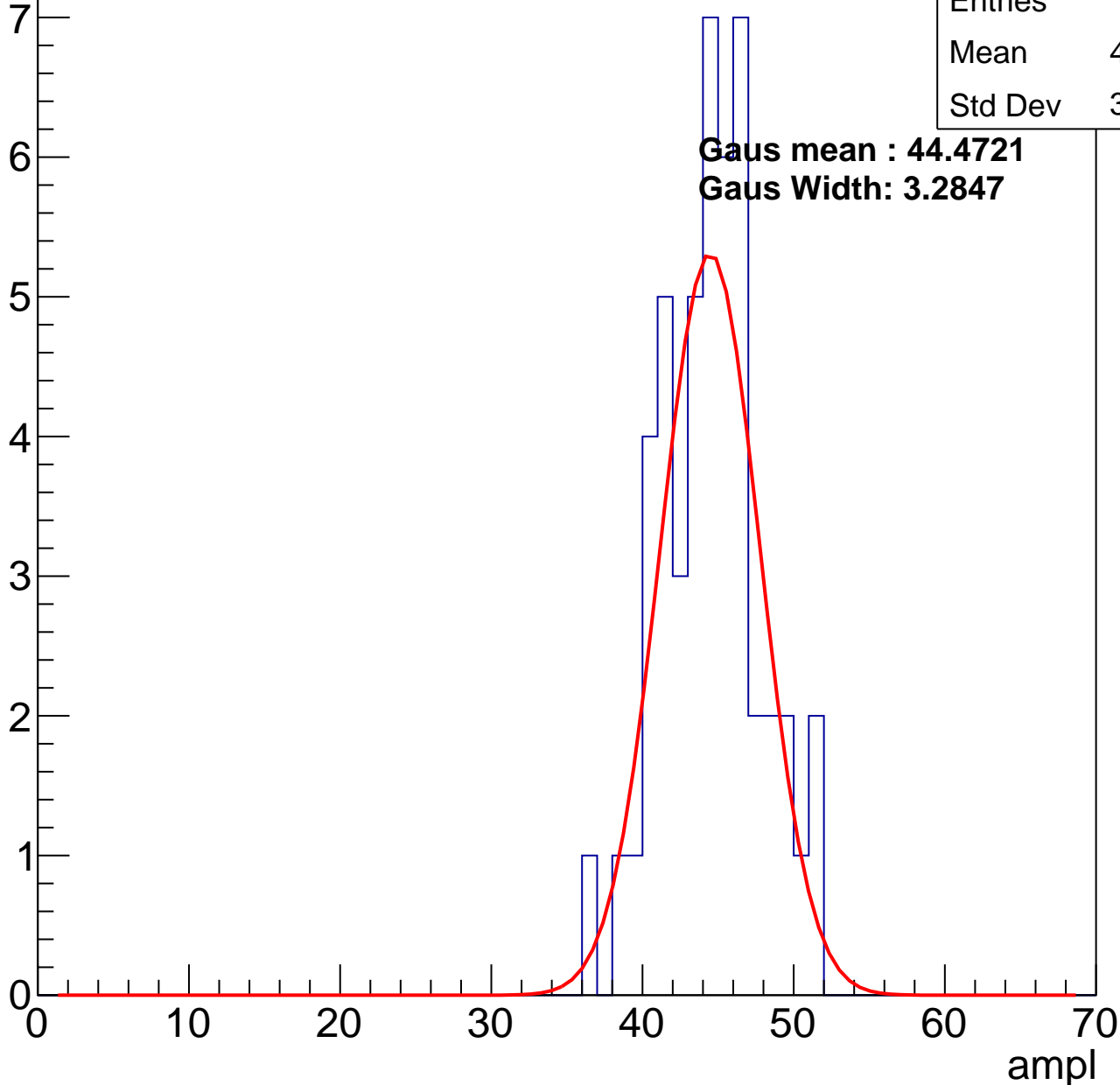
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 44.06 |
| Std Dev | 3.254 |

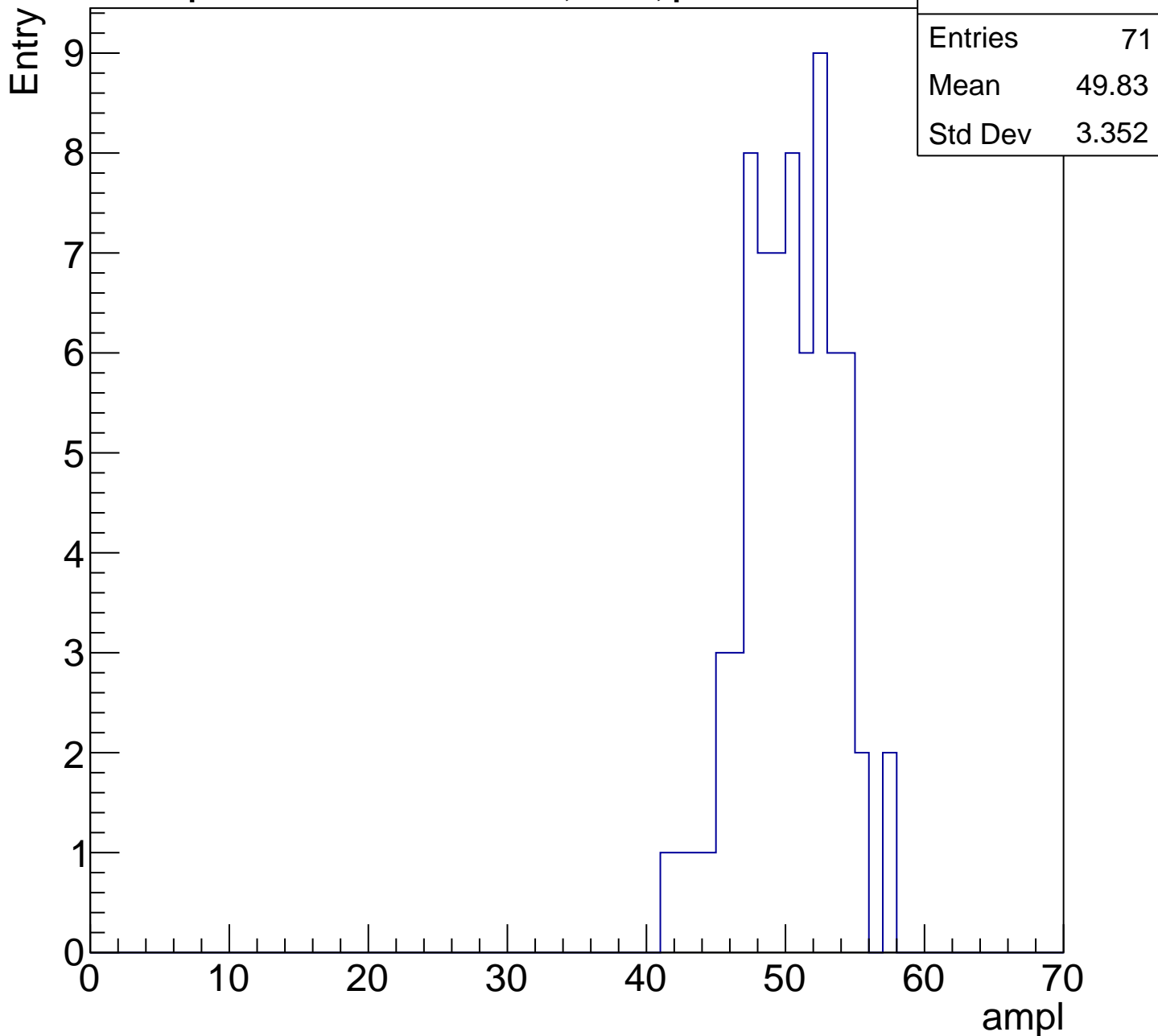
Gaus mean : 44.4721

Gaus Width: 3.2847



B0L001S, U7-ch39, adc3

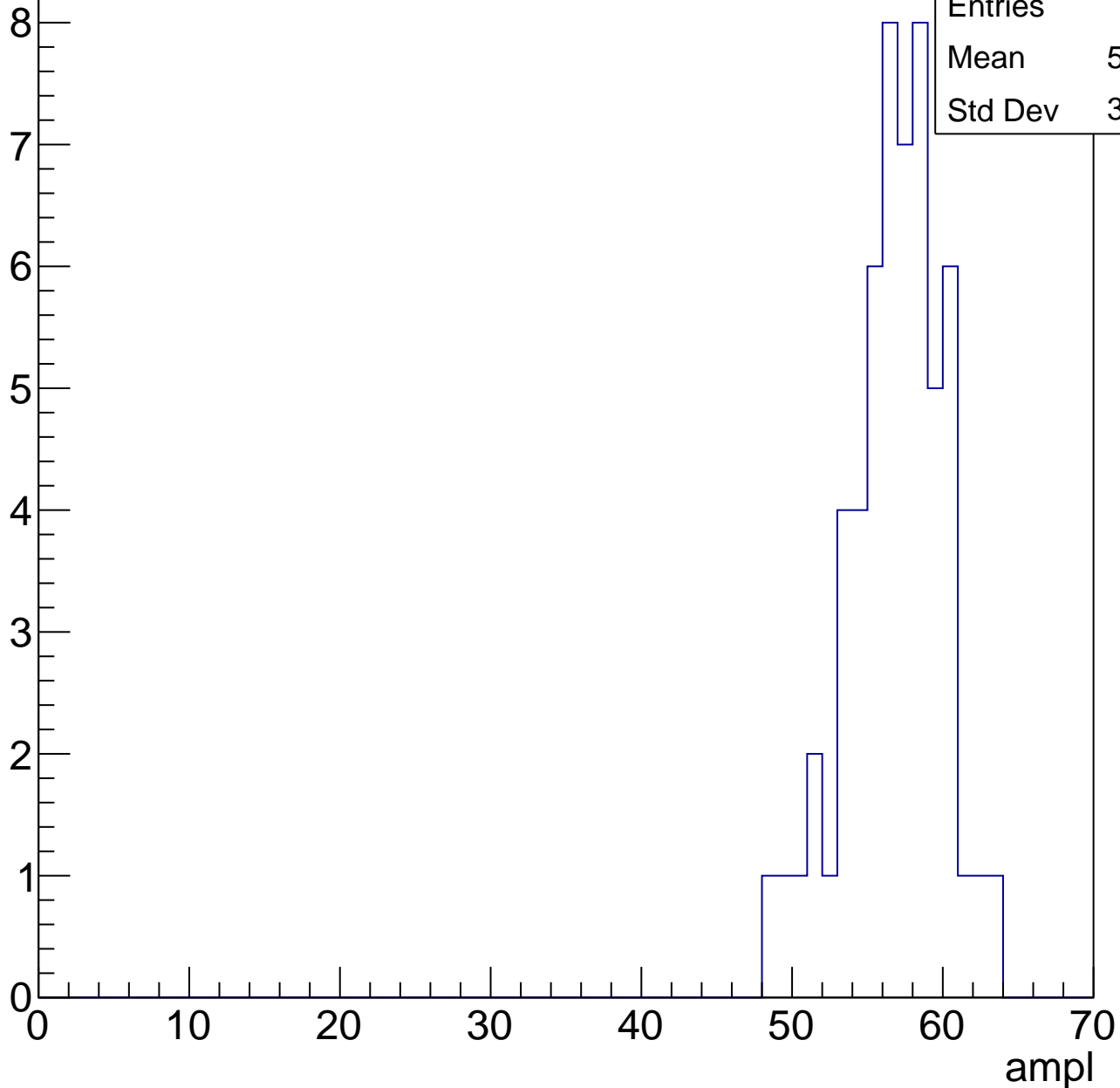
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



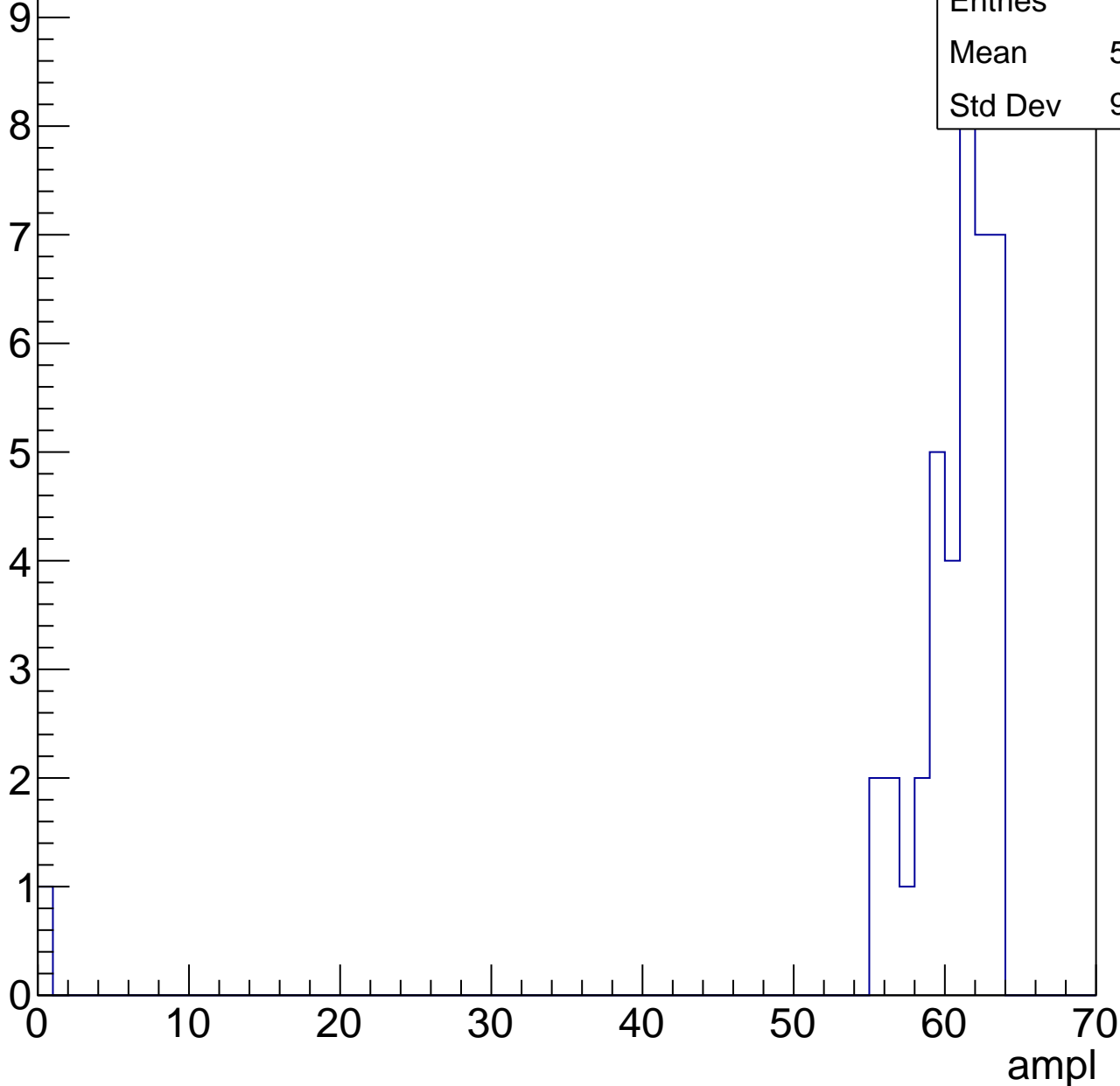
| | |
|---------|-------|
| Entries | 57 |
| Mean | 56.33 |
| Std Dev | 3.125 |

B0L001S, U7-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

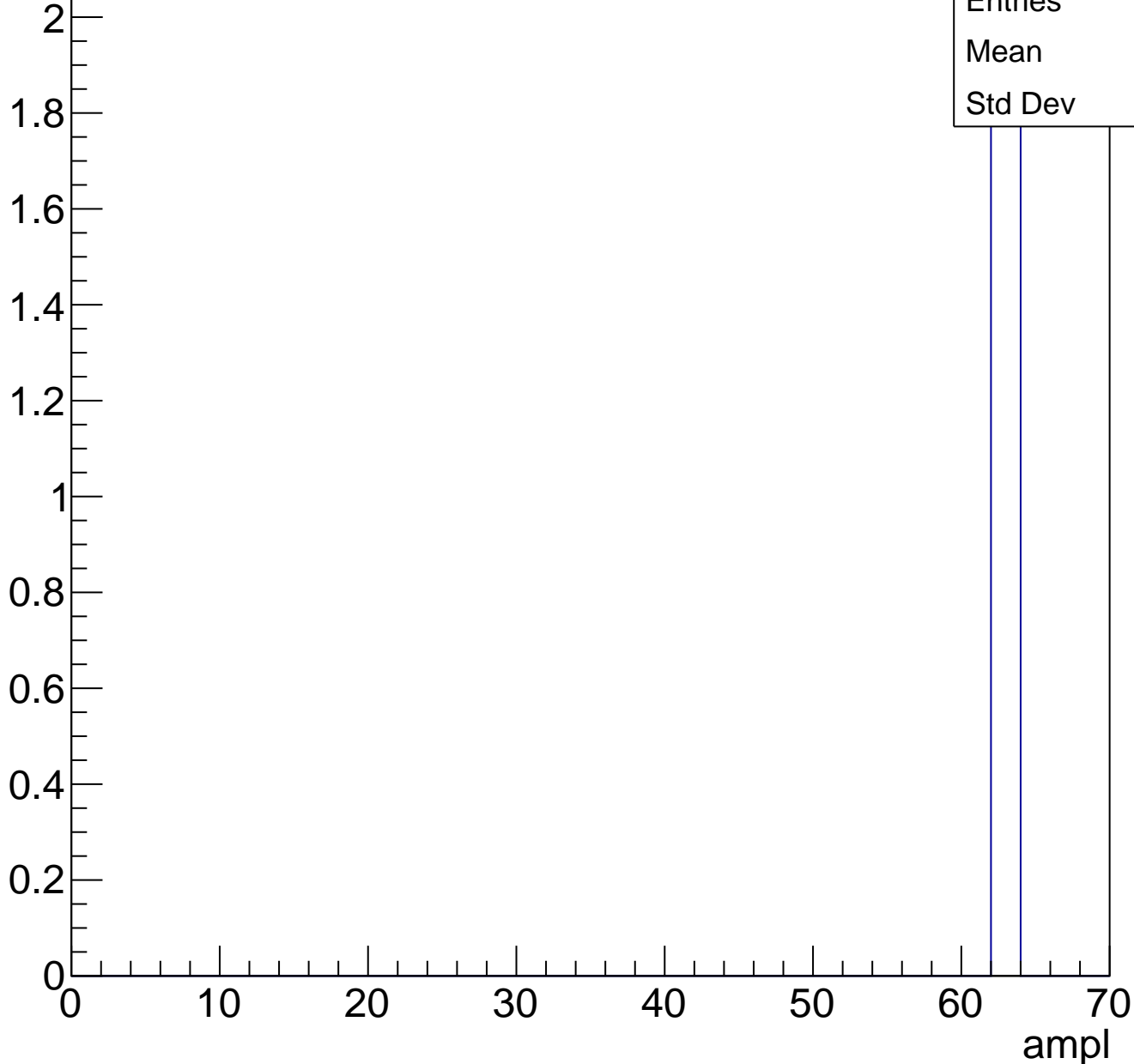
| | |
|---------|-------|
| Entries | 40 |
| Mean | 58.85 |
| Std Dev | 9.684 |



B0L001S, U7-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch40, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 31.2 |
| Std Dev | 4.869 |

Gaus mean : 32.0874

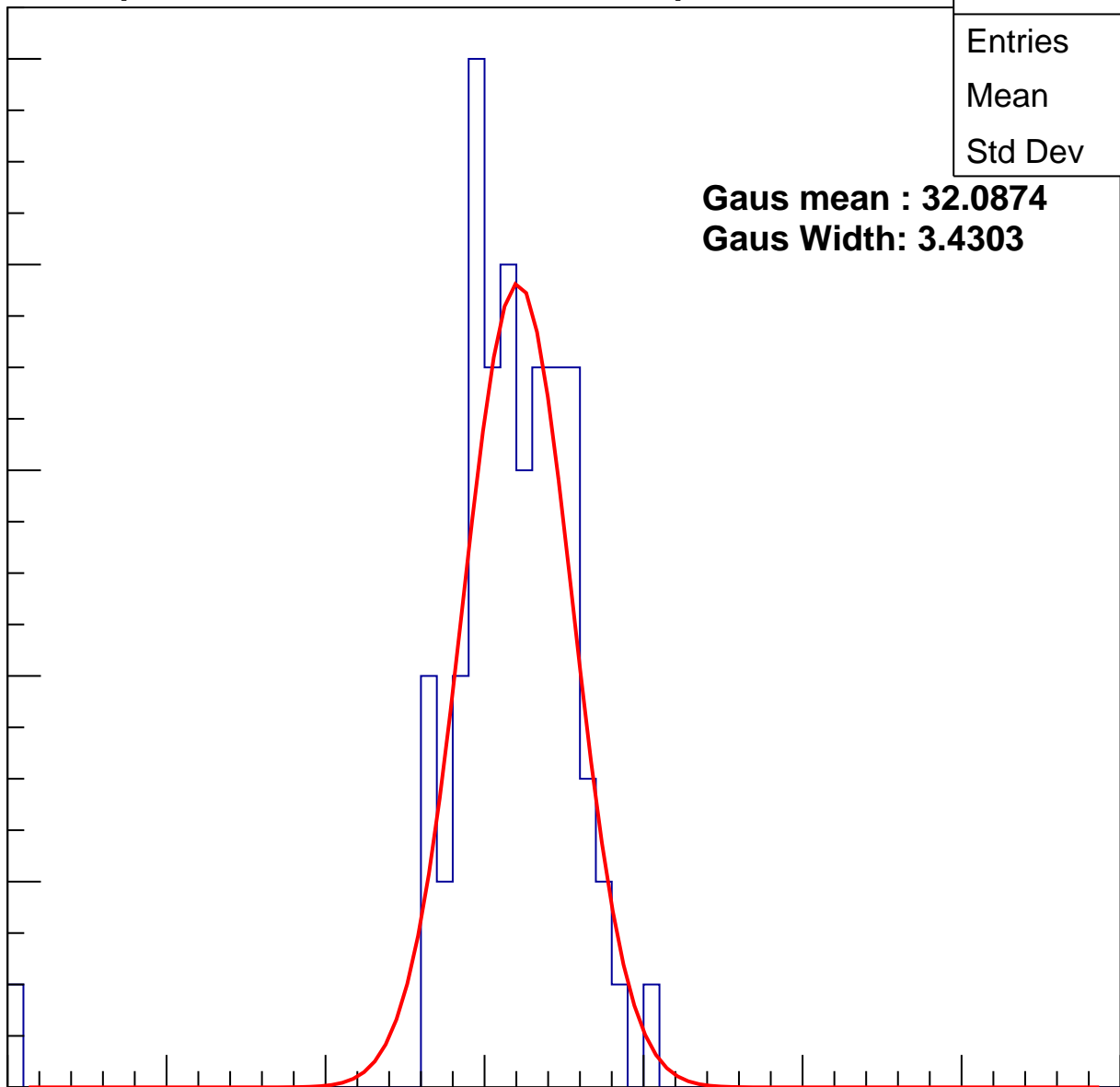
Gaus Width: 3.4303

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch40, adc1

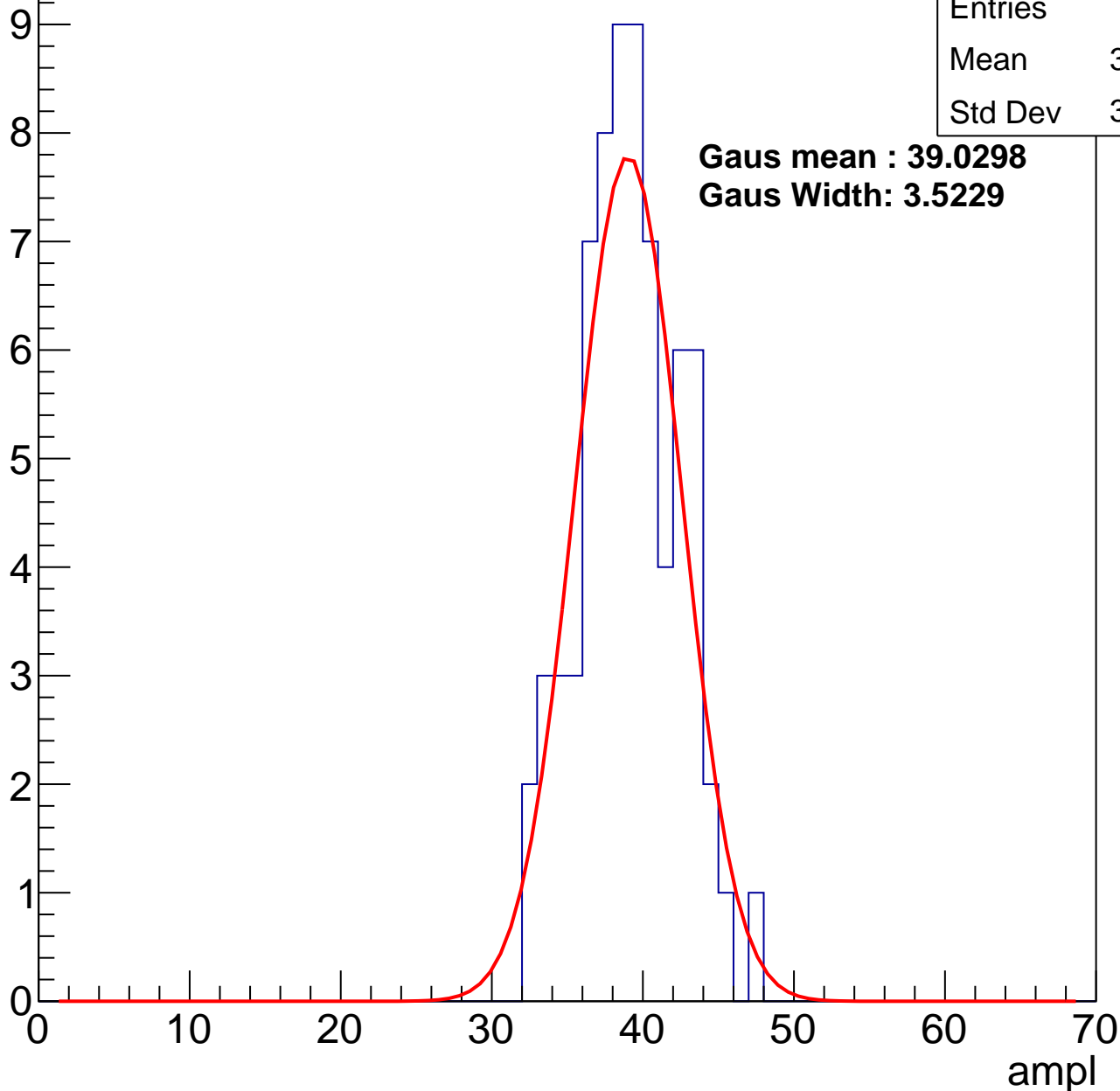
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 38.66 |
| Std Dev | 3.246 |

Gaus mean : 39.0298

Gaus Width: 3.5229



B0L001S, U7-ch40, adc2

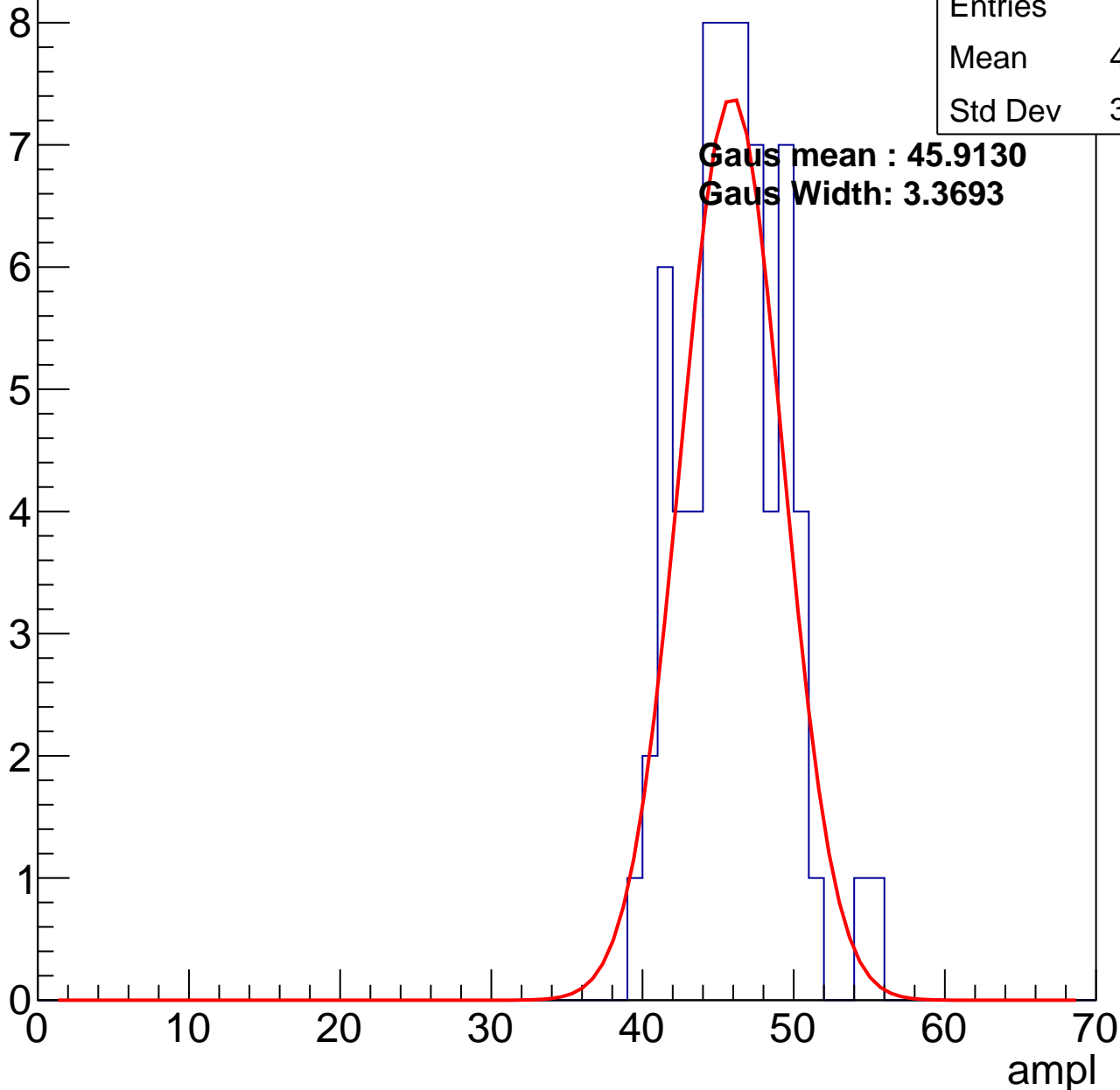
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 45.59 |
| Std Dev | 3.303 |

Gaus mean : 45.9130

Gaus Width: 3.3693

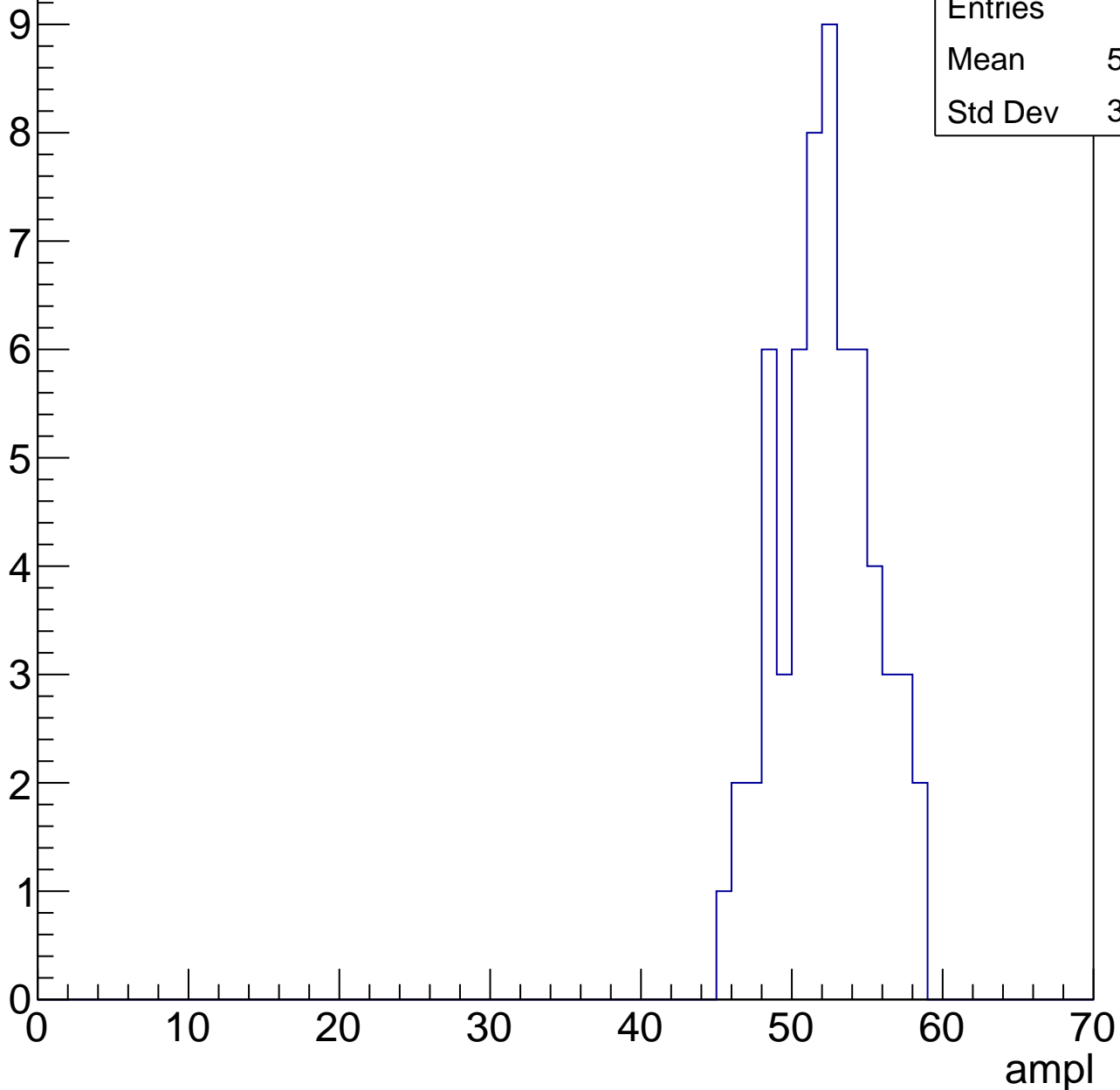


B0L001S, U7-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

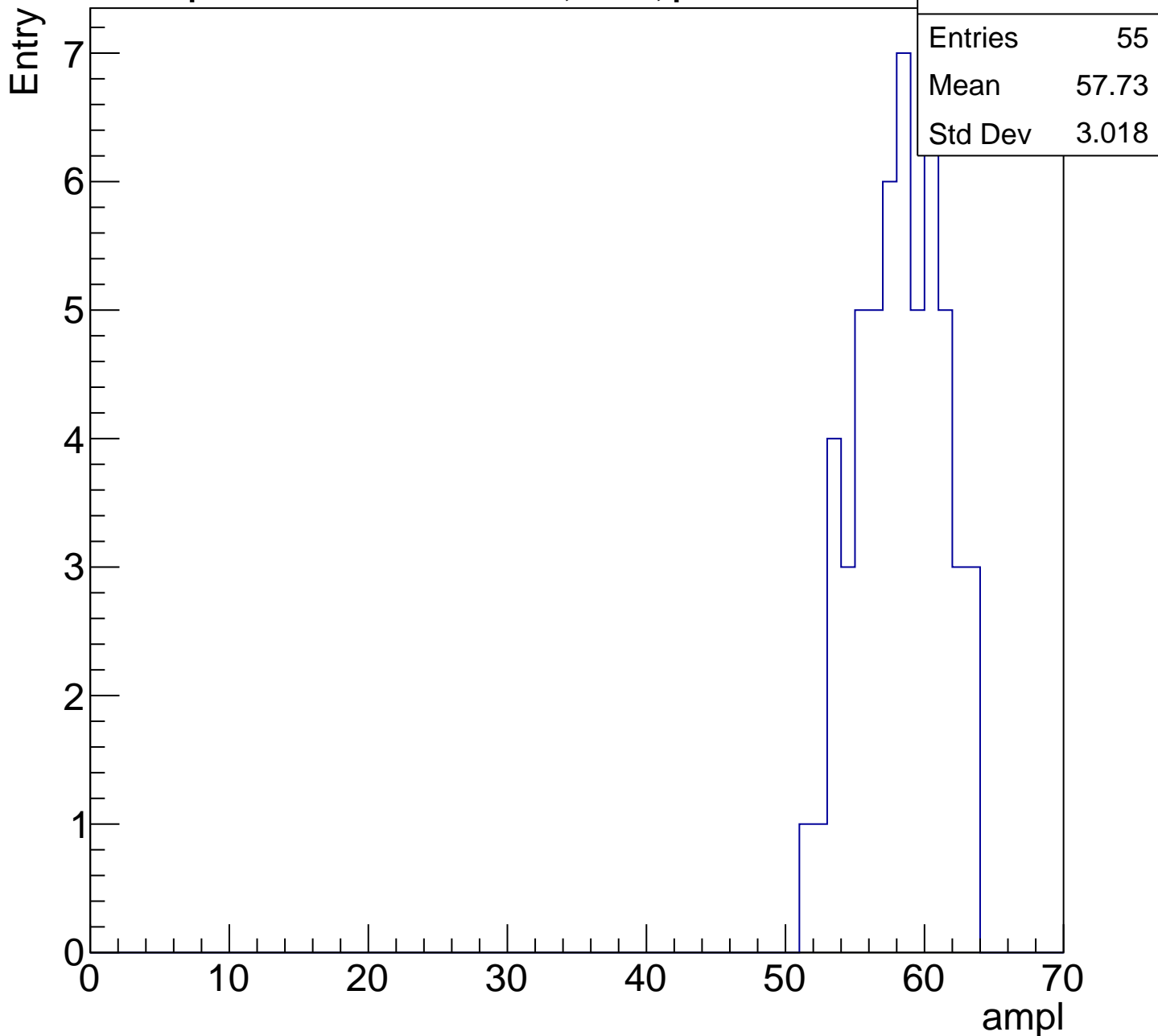
Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 51.79 |
| Std Dev | 3.095 |



B0L001S, U7-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

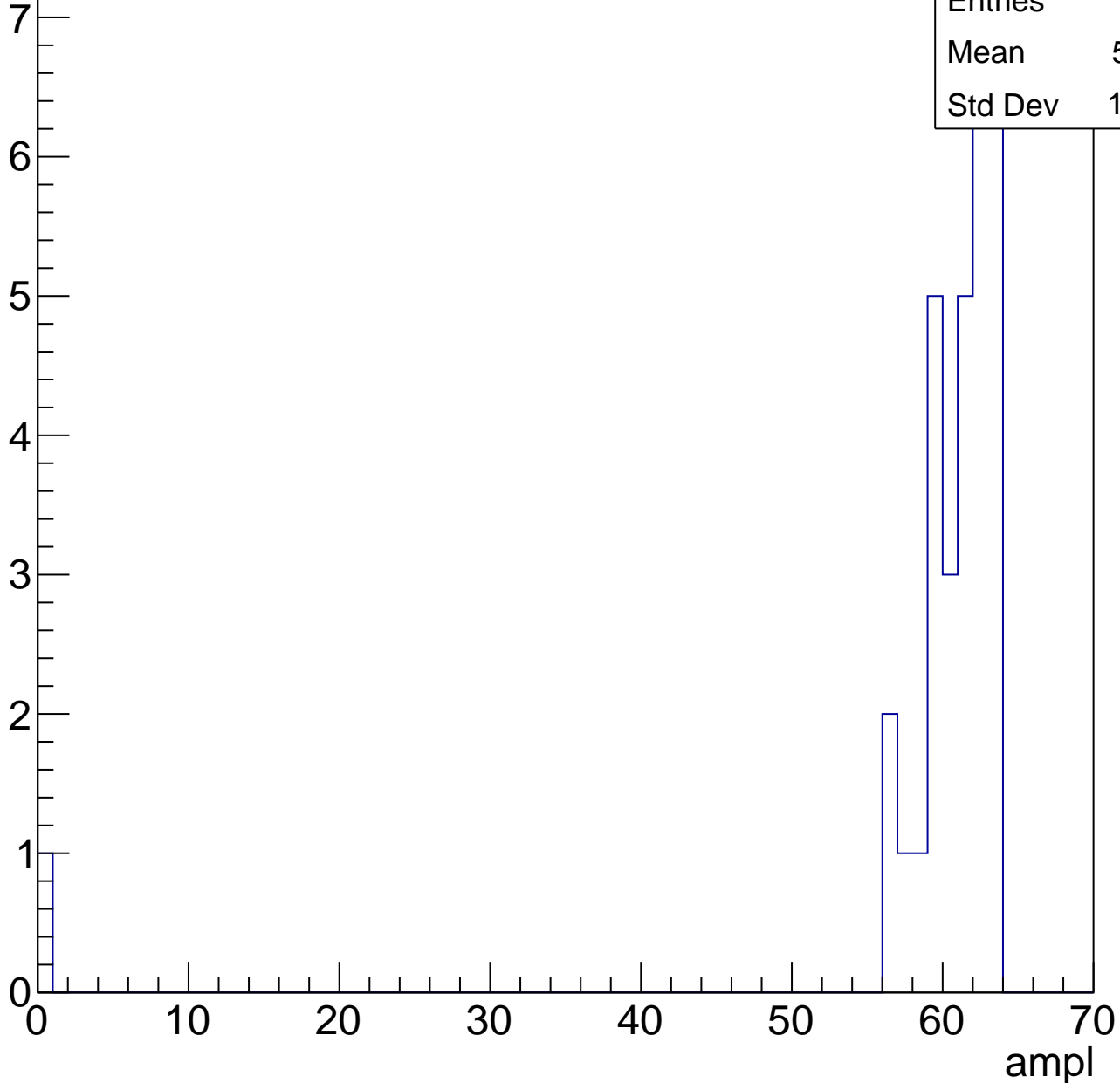


B0L001S, U7-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.81 |
| Std Dev | 10.75 |



B0L001S, U7-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch41, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.42 |
| Std Dev | 4.881 |

Gaus mean : 29.0090

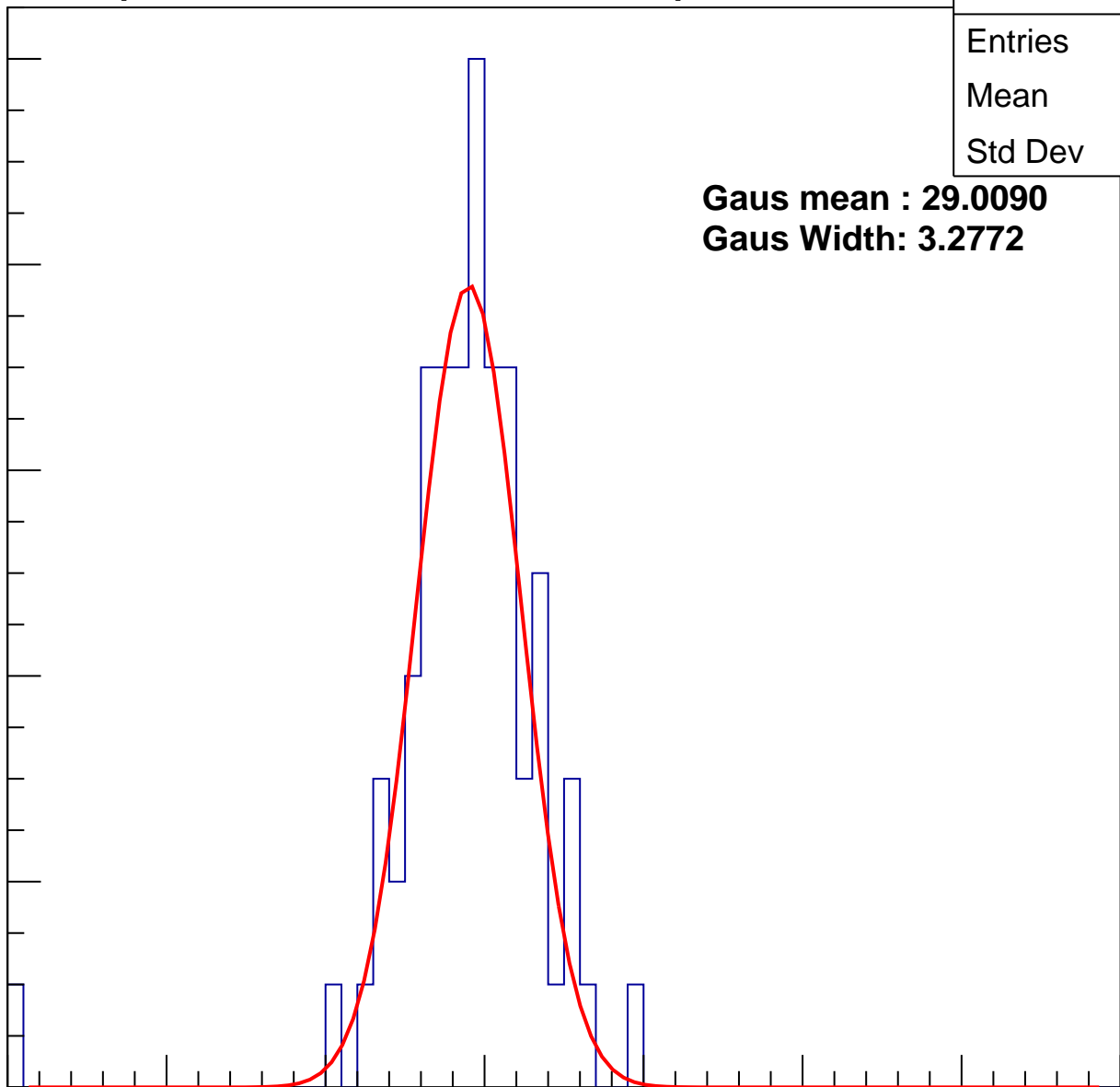
Gaus Width: 3.2772

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch41, adc1

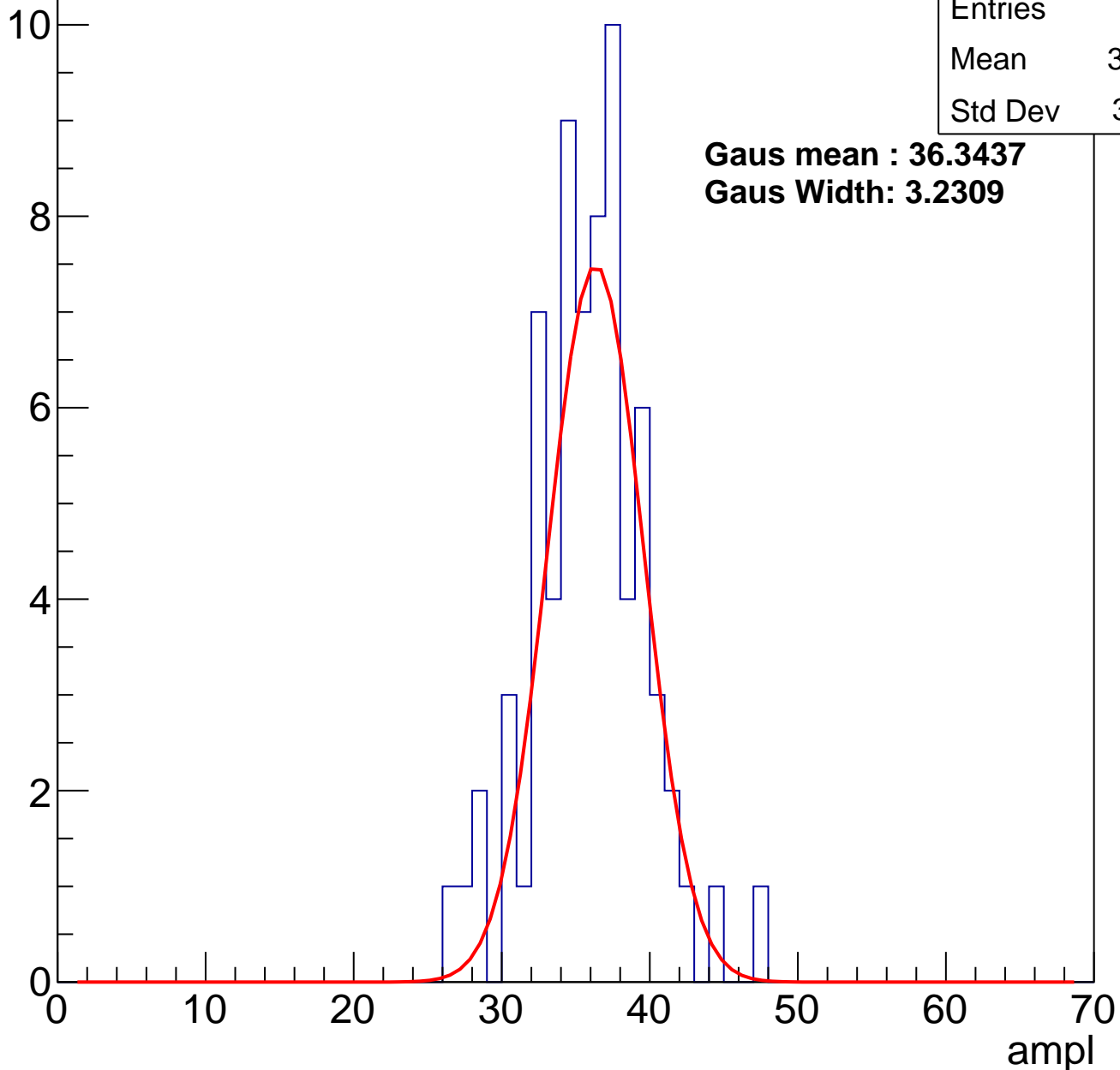
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.44 |
| Std Dev | 3.771 |

Gaus mean : 36.3437

Gaus Width: 3.2309

Entry



B0L001S, U7-ch41, adc2

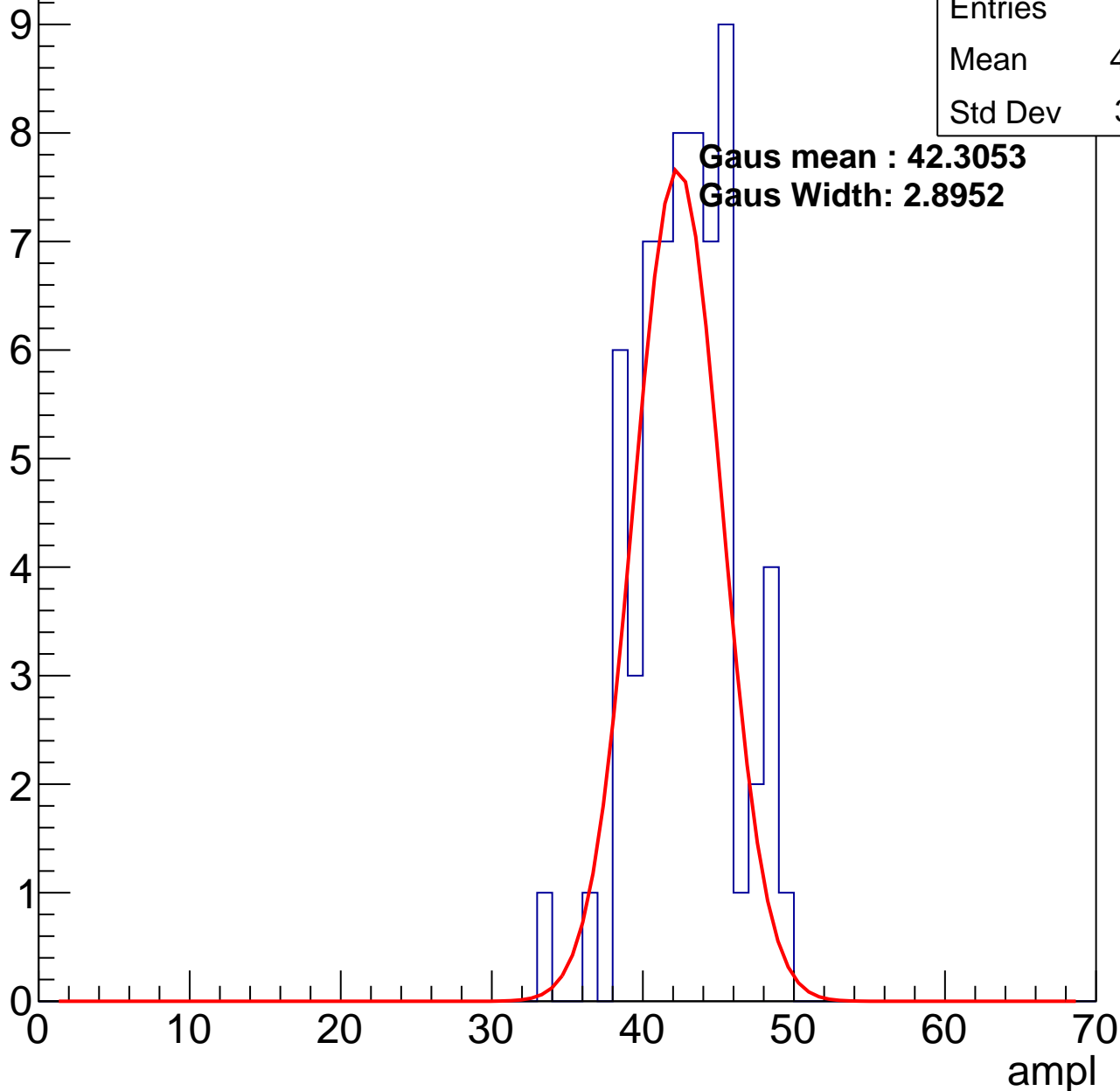
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 42.38 |
| Std Dev | 3.141 |

Gaus mean : 42.3053

Gaus Width: 2.8952

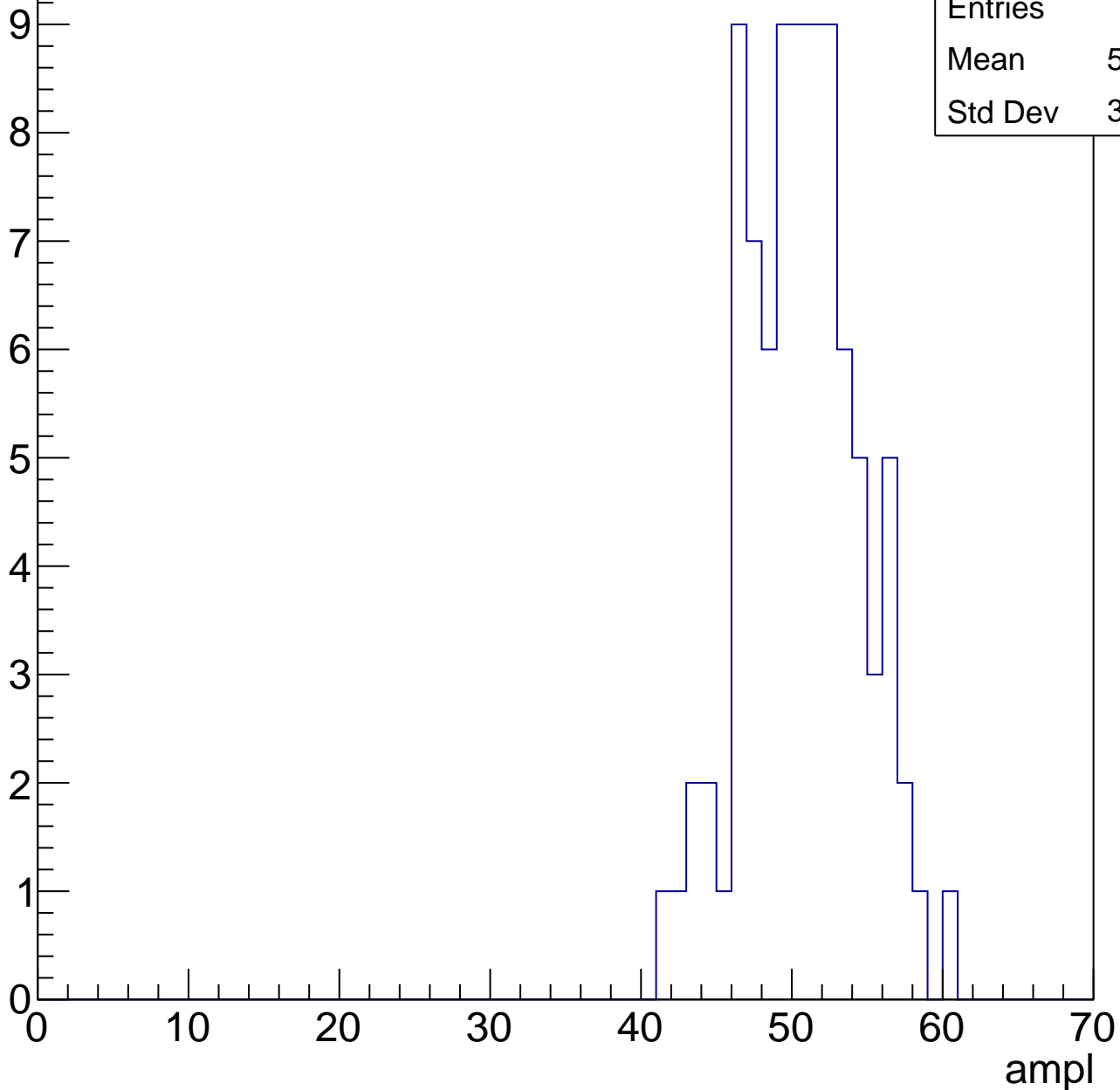


B0L001S, U7-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 50.18 |
| Std Dev | 3.804 |

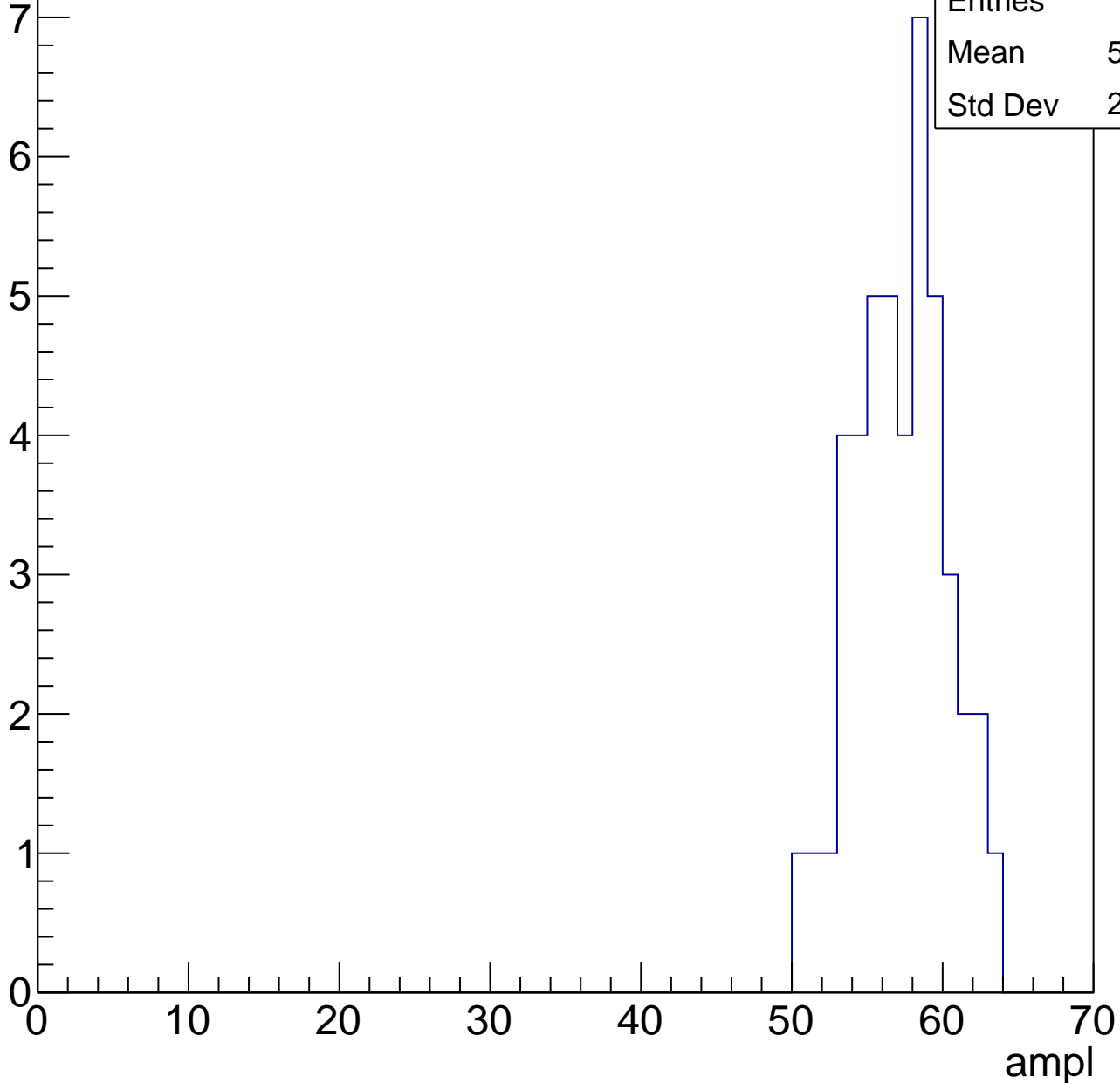


B0L001S, U7-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 56.76 |
| Std Dev | 2.997 |

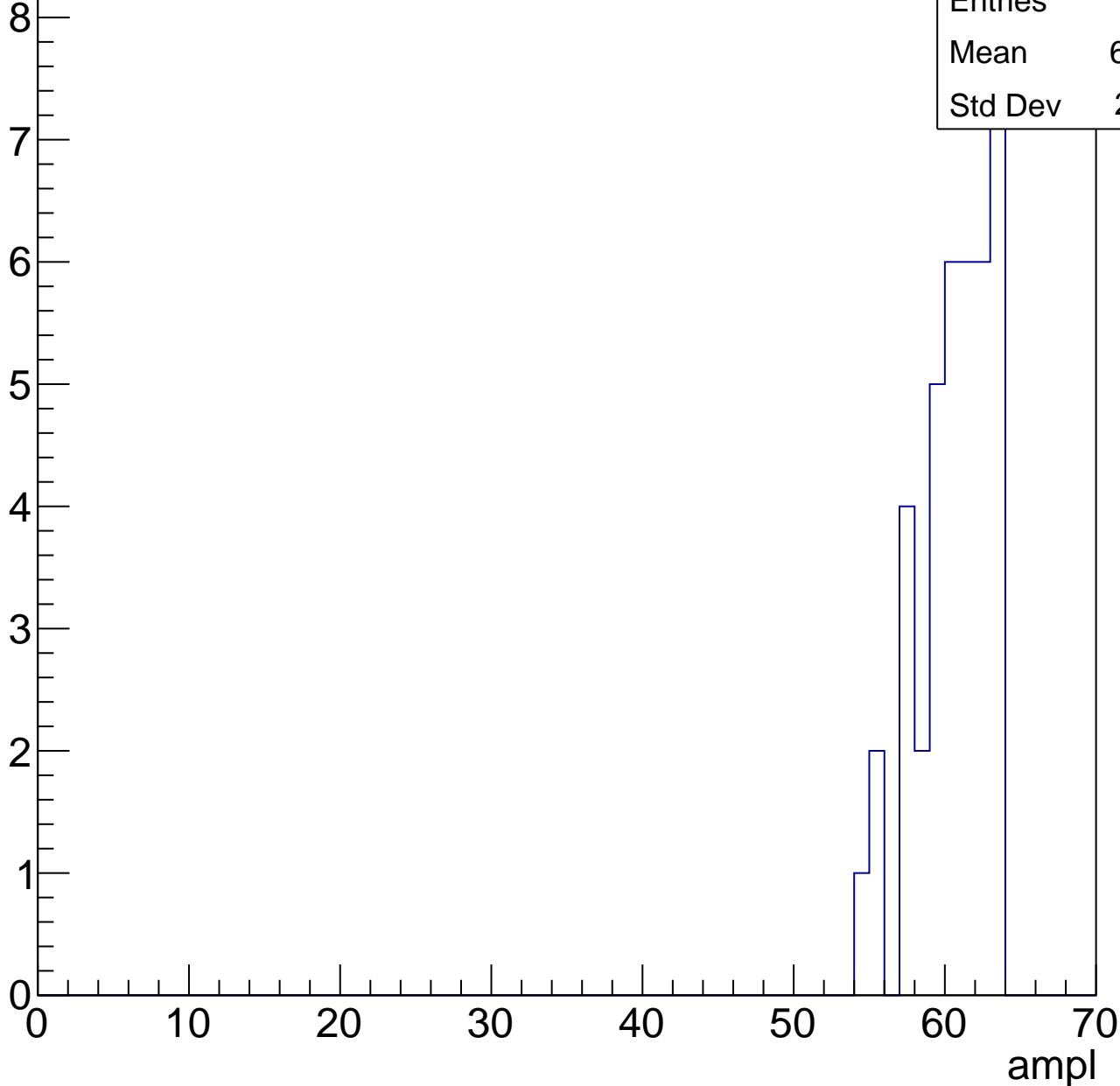


B0L001S, U7-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 60.12 |
| Std Dev | 2.431 |



B0L001S, U7-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

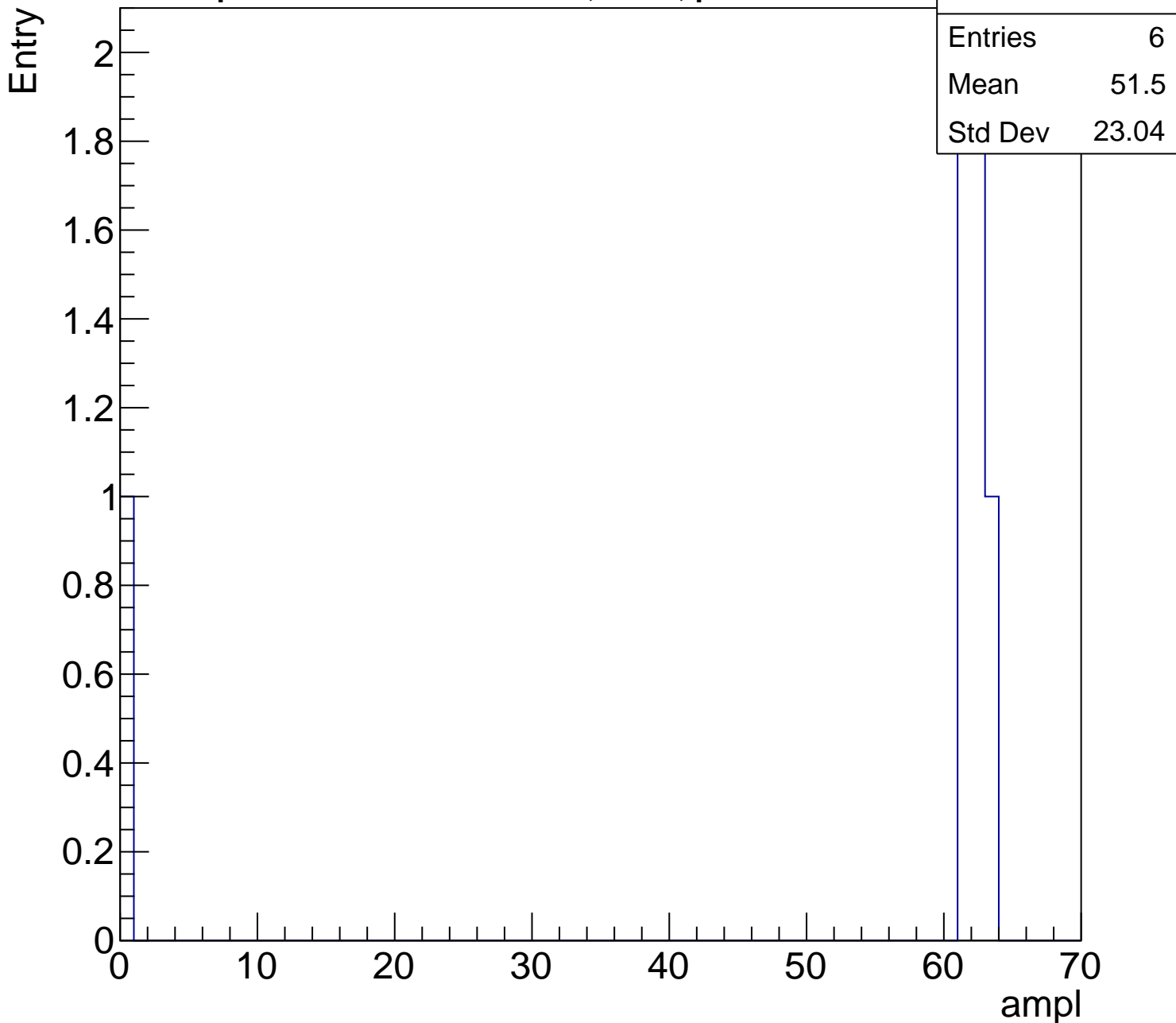
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.5 |
| Std Dev | 23.04 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch42, adc0

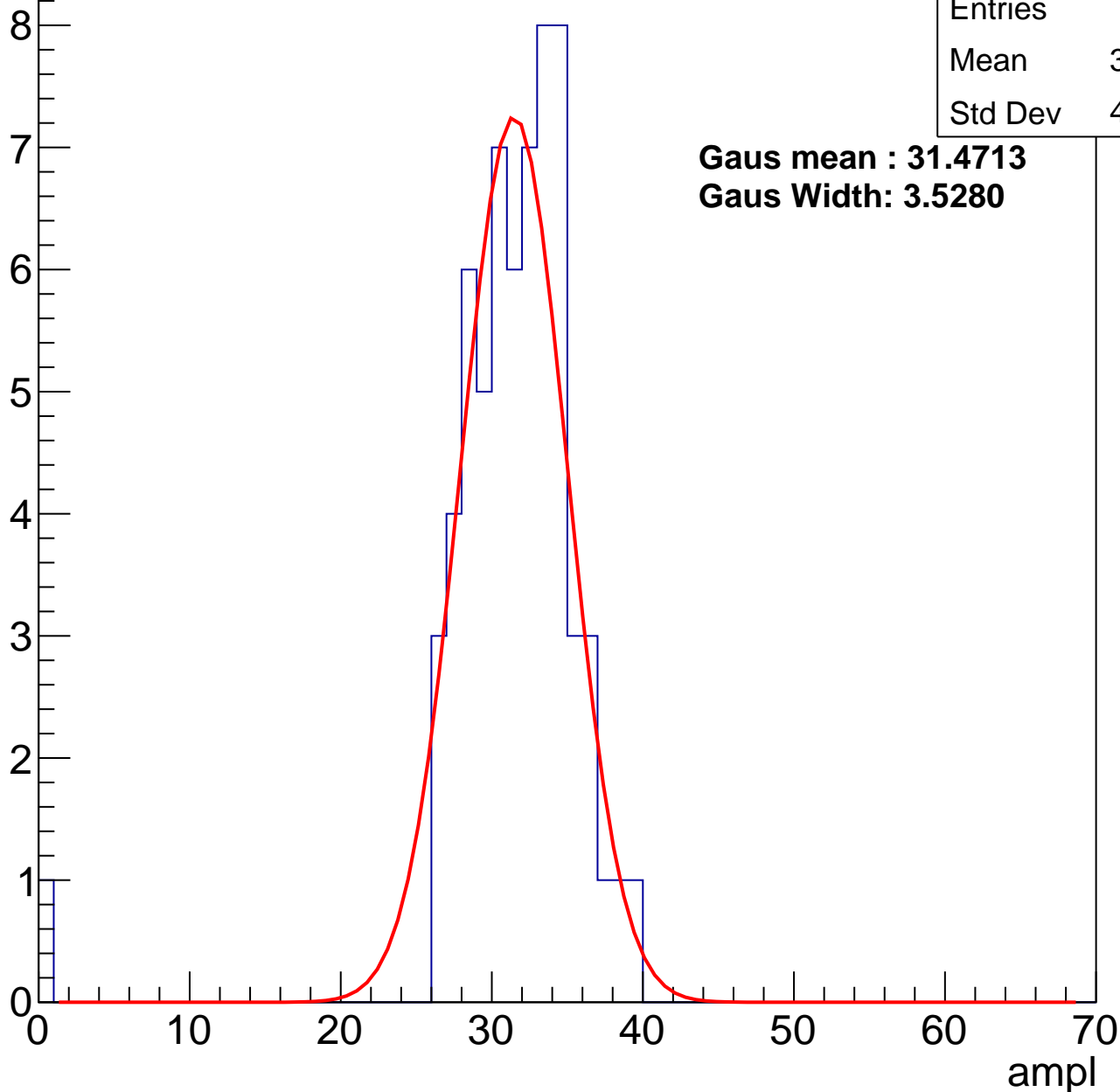
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 30.97 |
| Std Dev | 4.943 |

Gaus mean : 31.4713

Gaus Width: 3.5280



B0L001S, U7-ch42, adc1

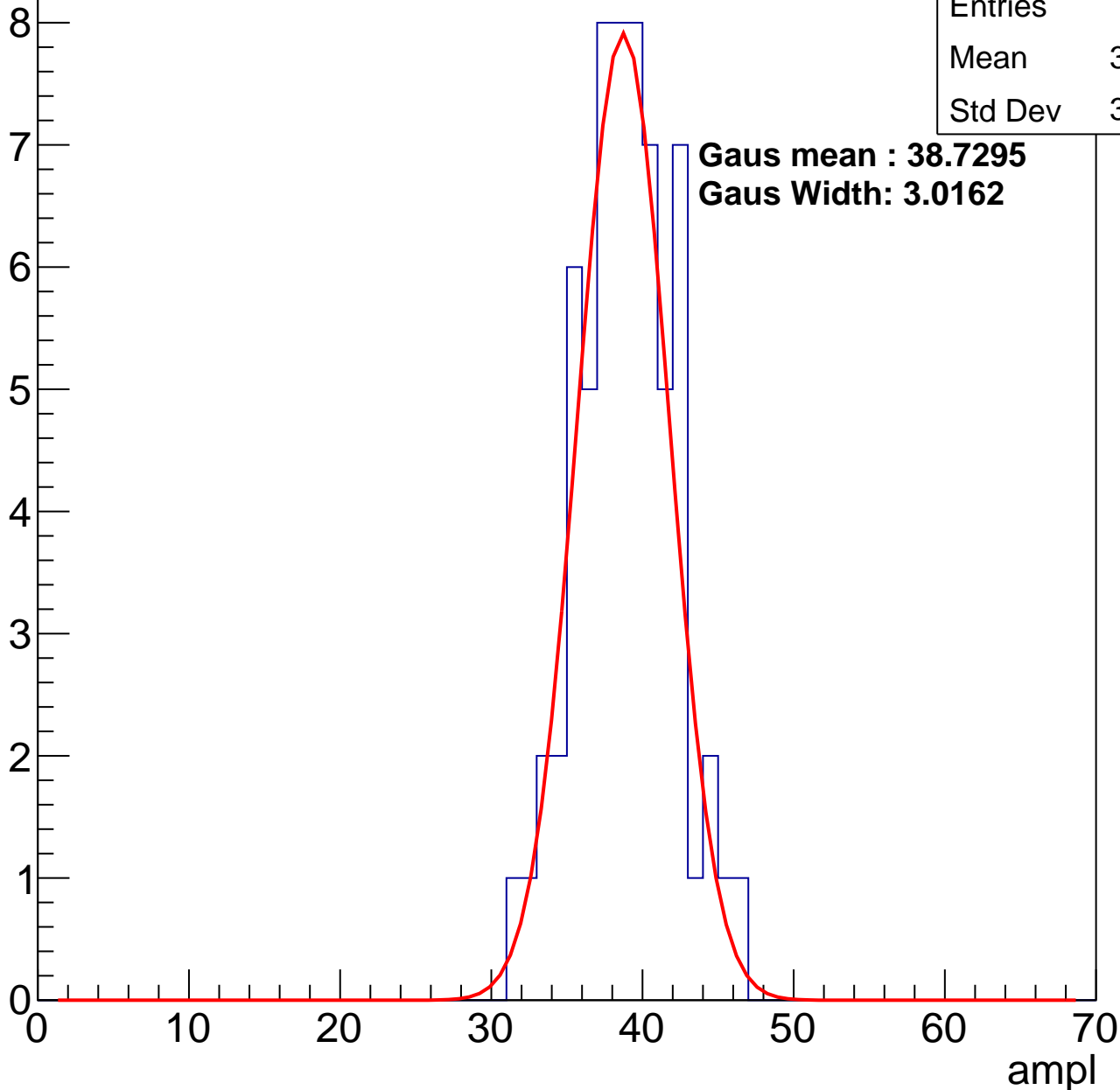
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 38.46 |
| Std Dev | 3.133 |

Gaus mean : 38.7295

Gaus Width: 3.0162



B0L001S, U7-ch42, adc2

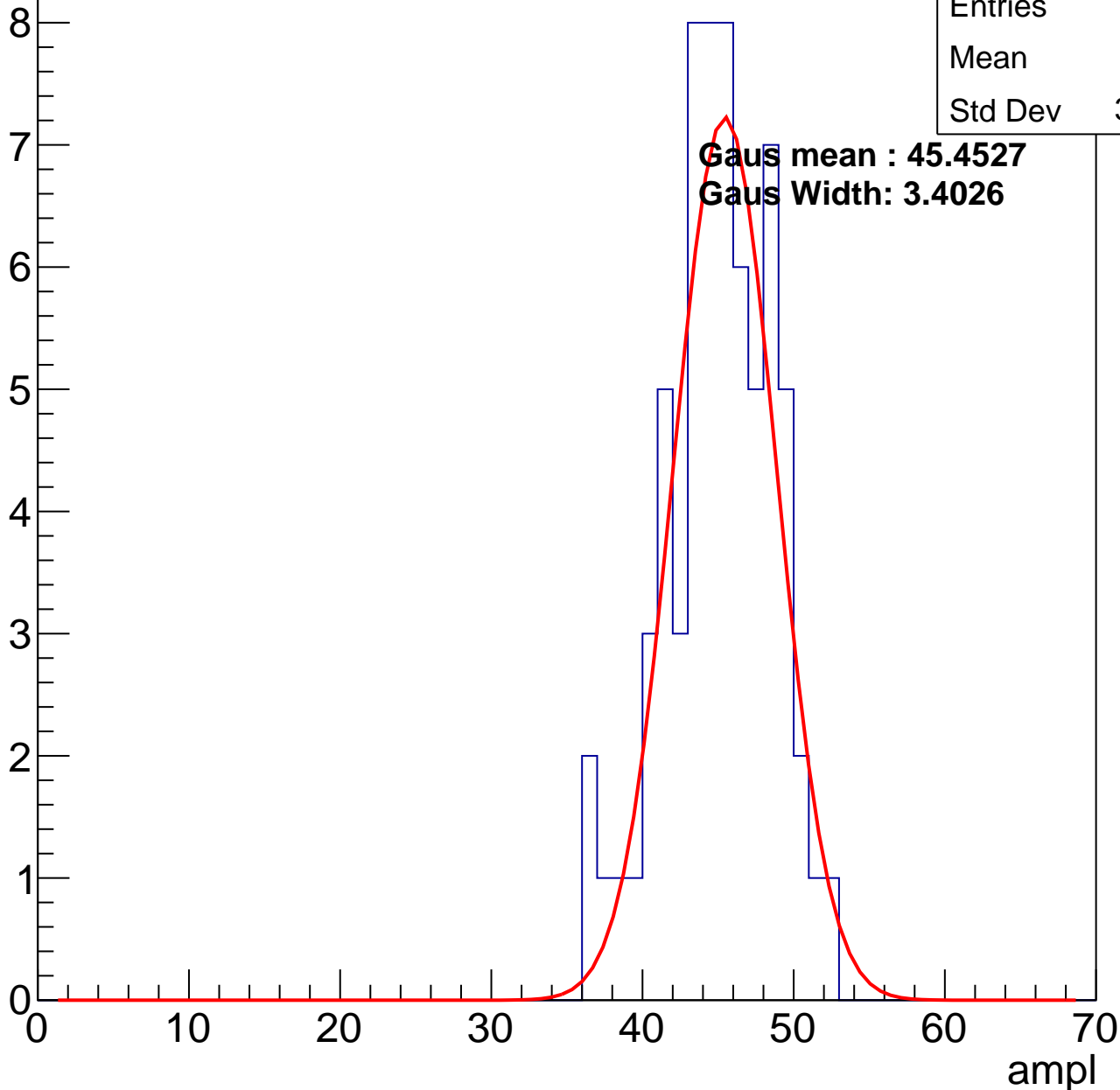
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 44.6 |
| Std Dev | 3.511 |

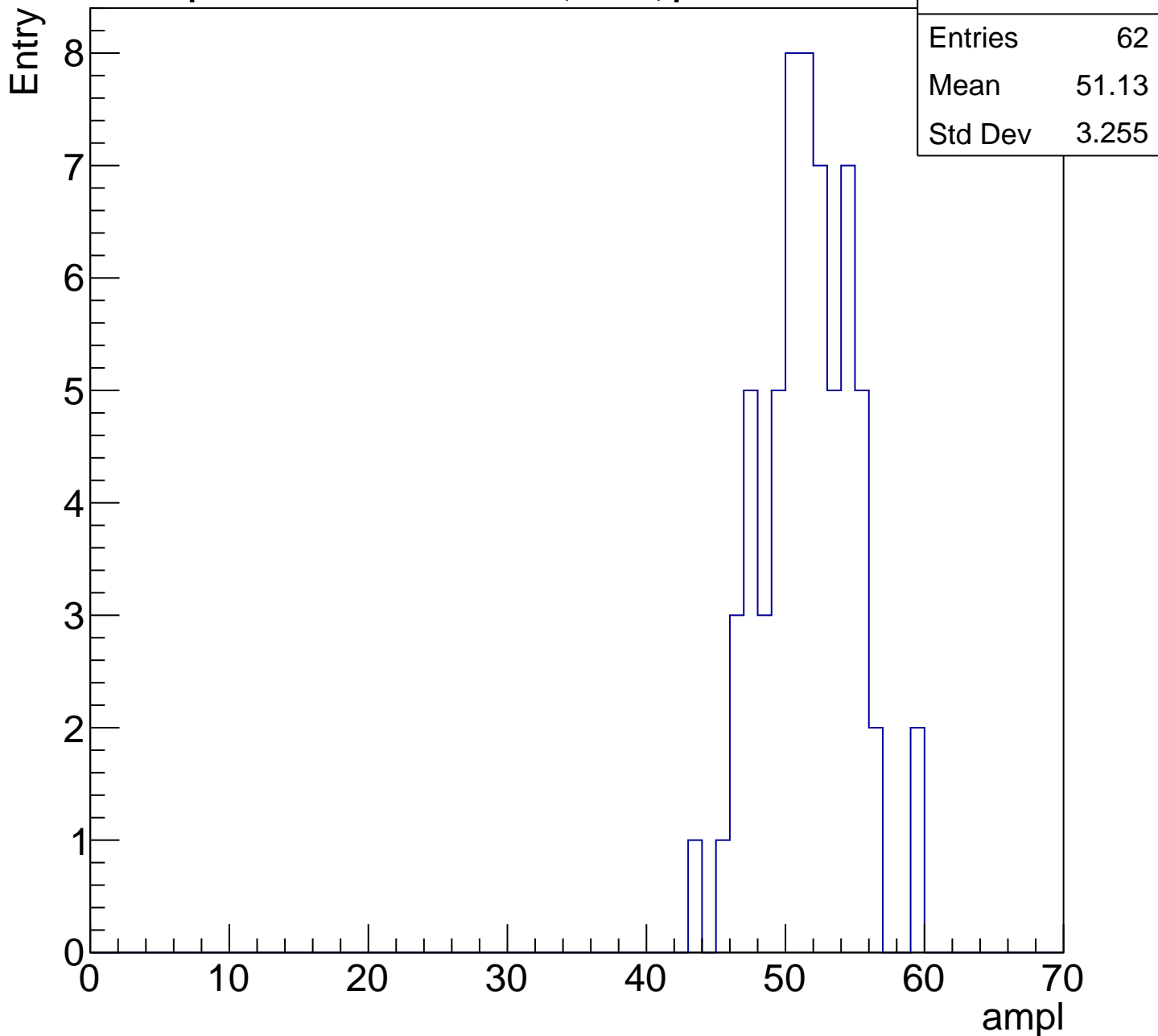
Gaus mean : 45.4527

Gaus Width: 3.4026



B0L001S, U7-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

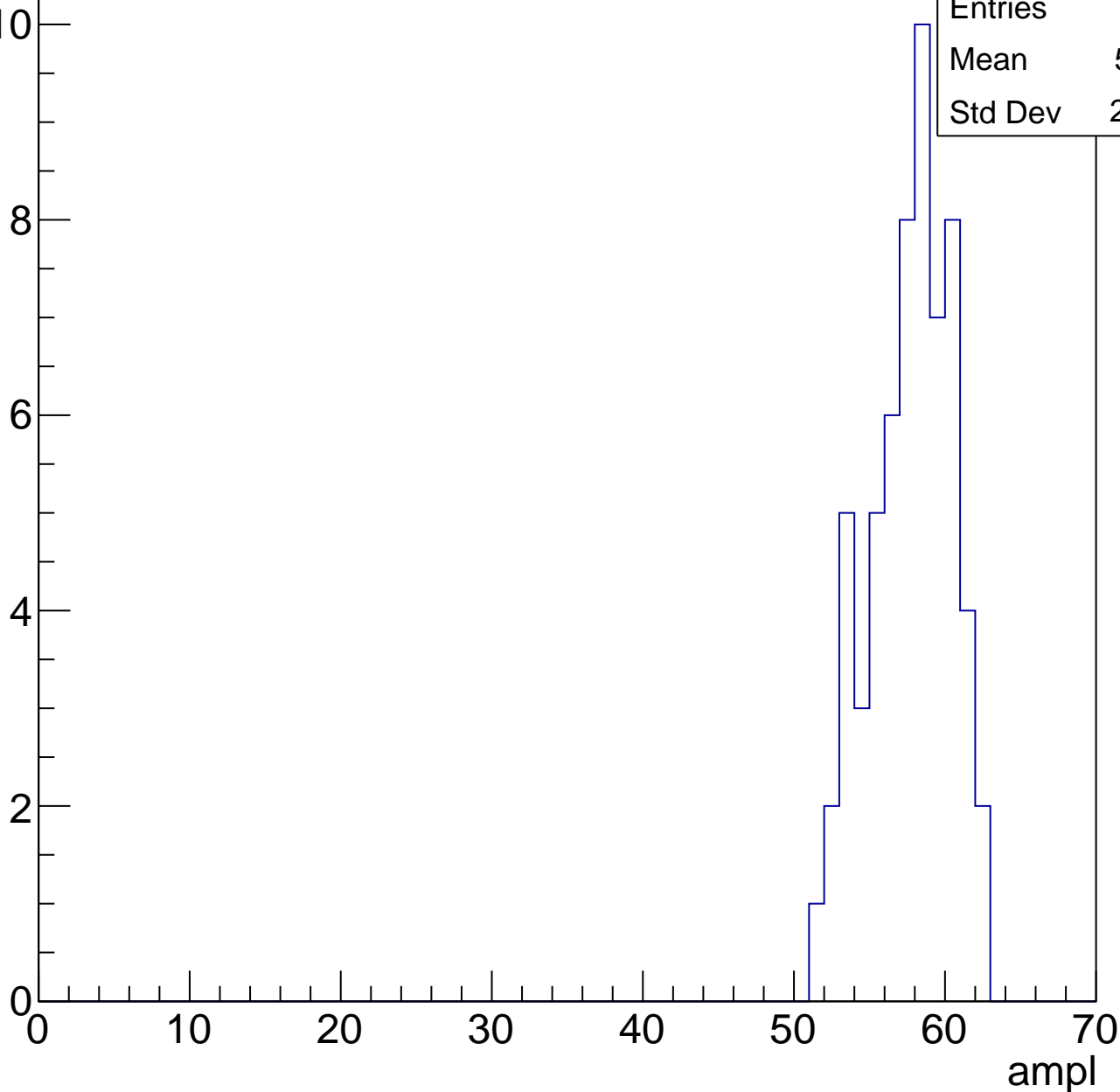


B0L001S, U7-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 57.21 |
| Std Dev | 2.686 |



B0L001S, U7-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 31 |
| Mean | 59.19 |
| Std Dev | 11.01 |

Entry

10

8

6

4

2

0

0

10

20

30

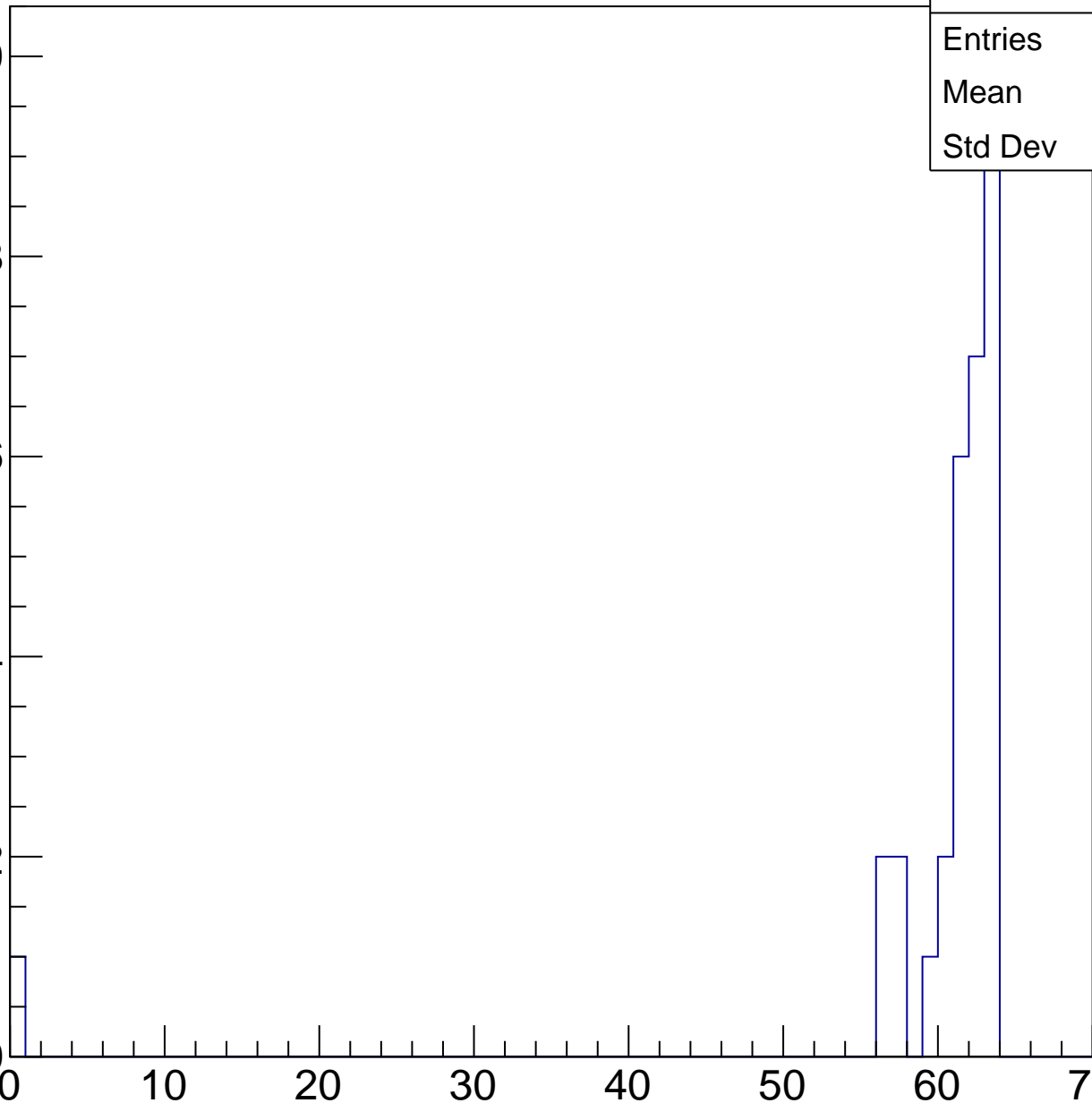
40

50

60

ampl

70



B0L001S, U7-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch43, adc0

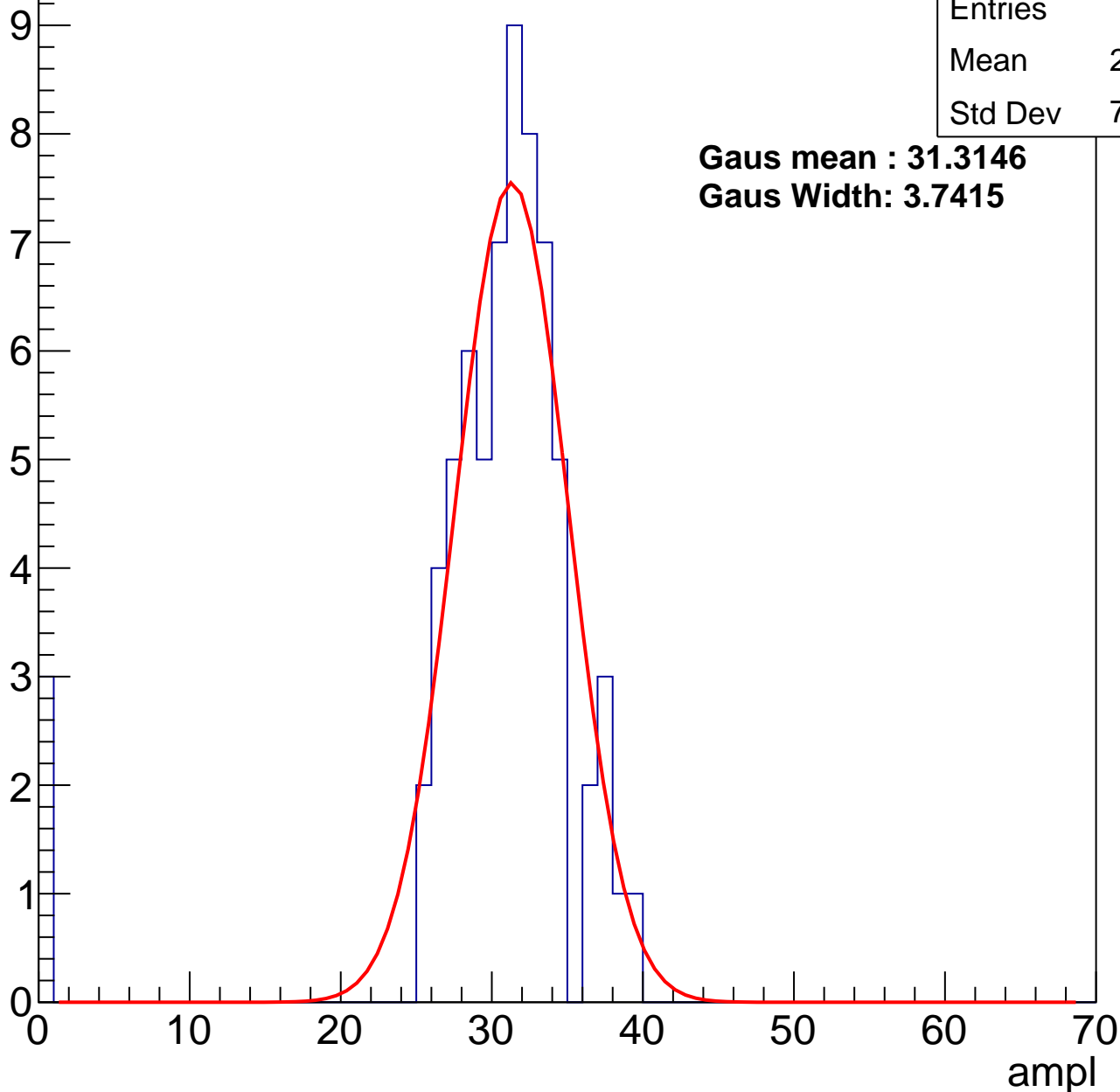
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.53 |
| Std Dev | 7.093 |

Gaus mean : 31.3146

Gaus Width: 3.7415



B0L001S, U7-ch43, adc1

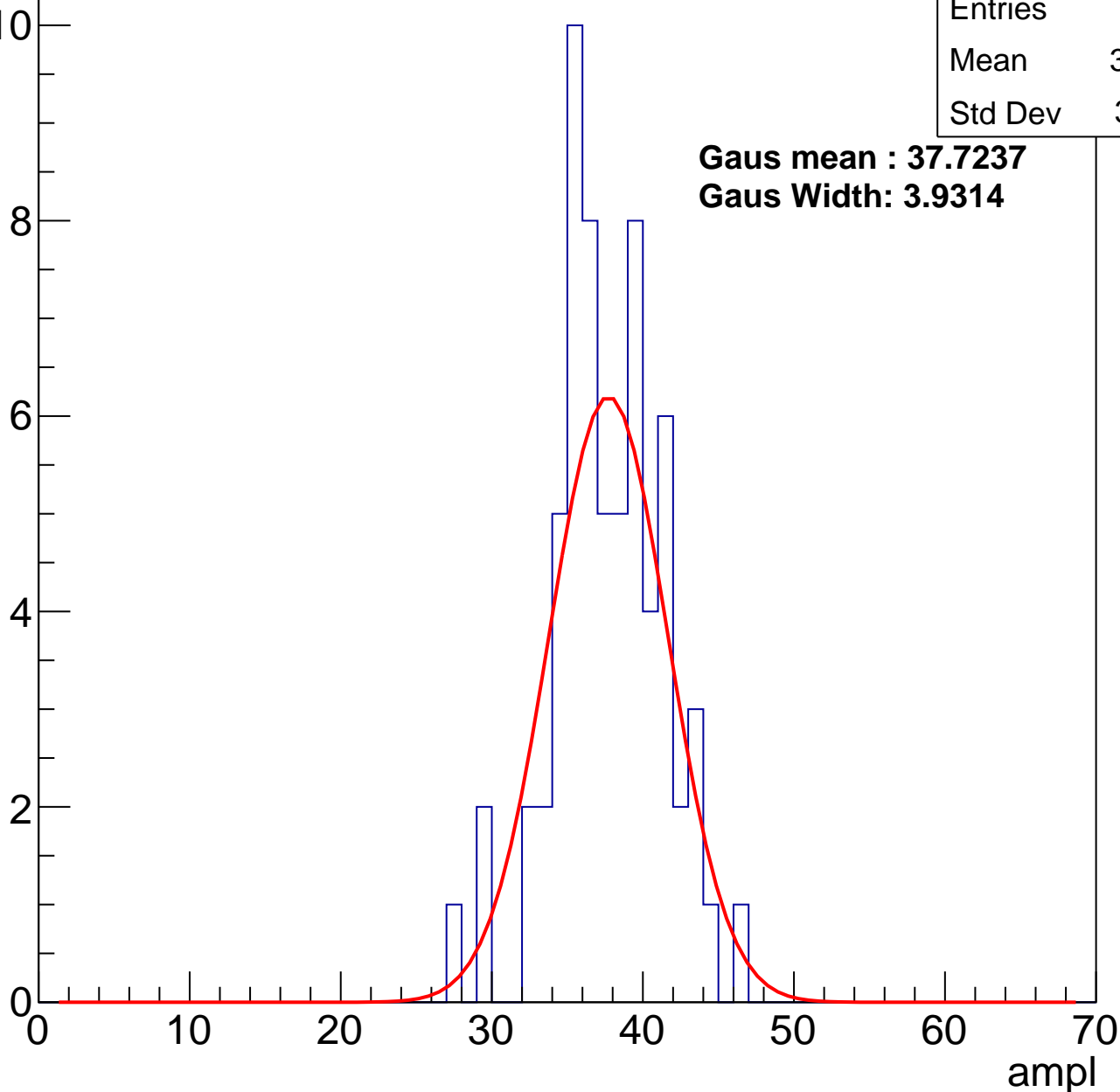
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 37.22 |
| Std Dev | 3.631 |

Gaus mean : 37.7237

Gaus Width: 3.9314



B0L001S, U7-ch43, adc2

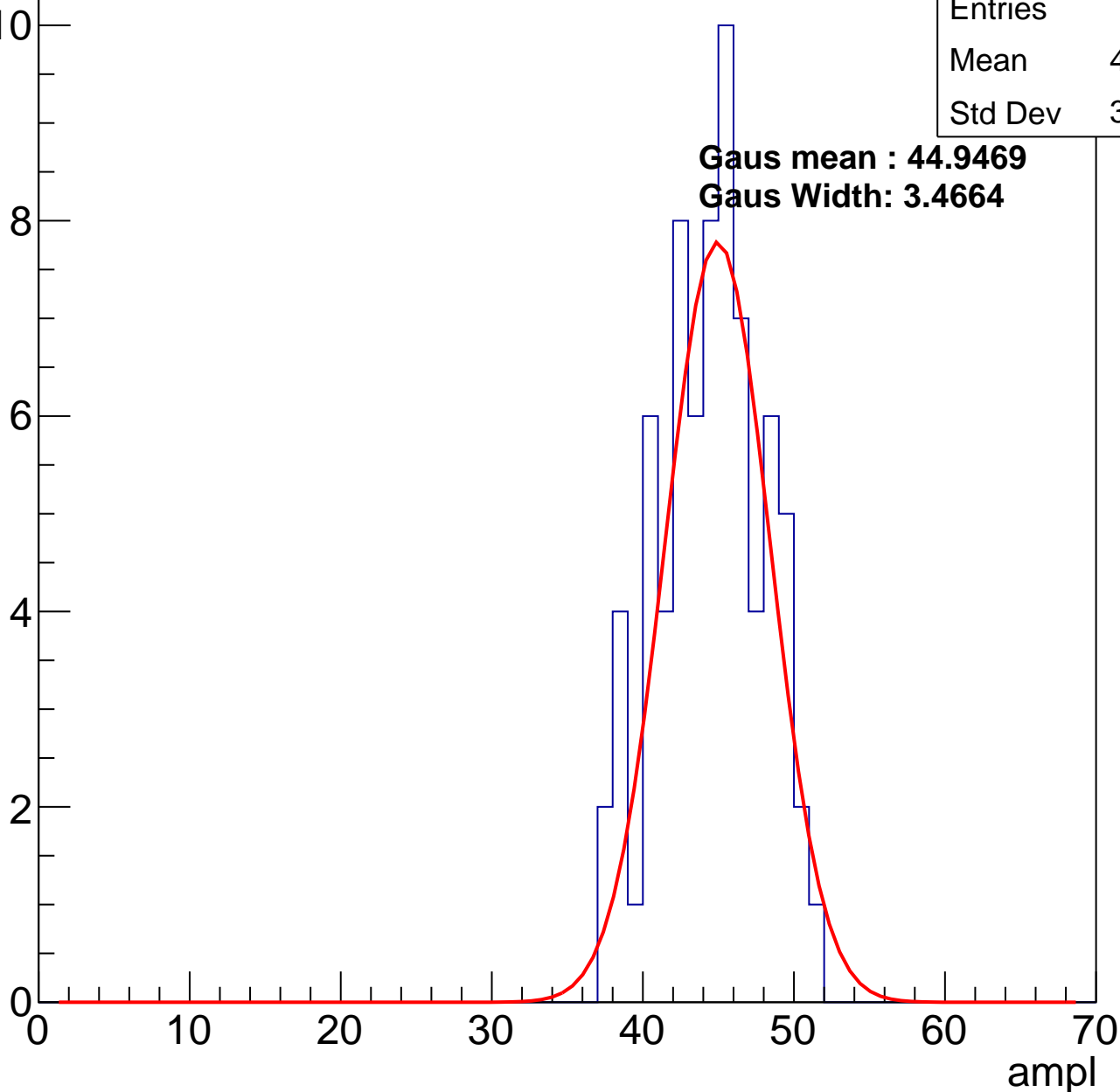
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 44.04 |
| Std Dev | 3.395 |

Gaus mean : 44.9469

Gaus Width: 3.4664

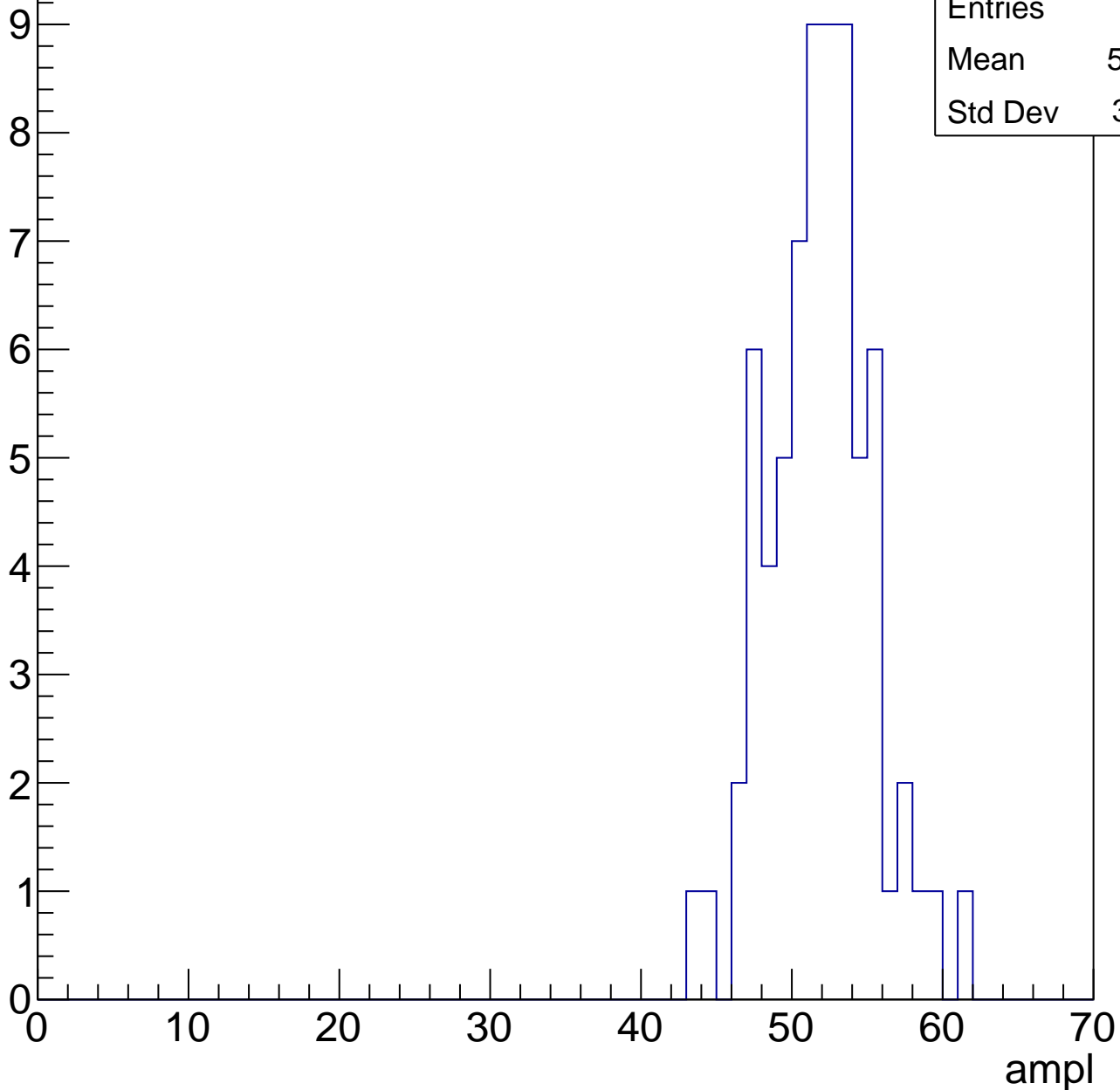


B0L001S, U7-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

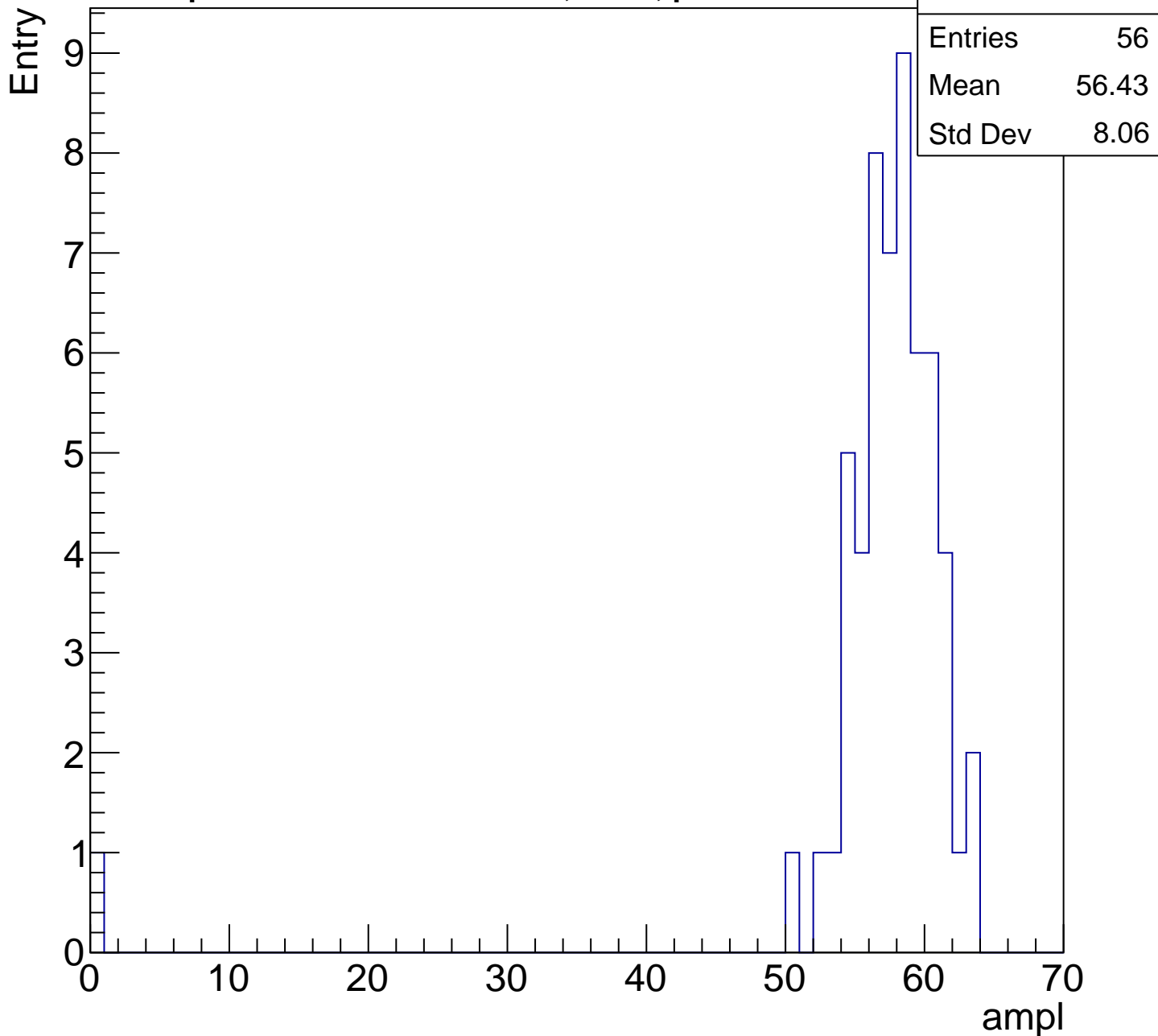
Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 51.43 |
| Std Dev | 3.391 |



B0L001S, U7-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

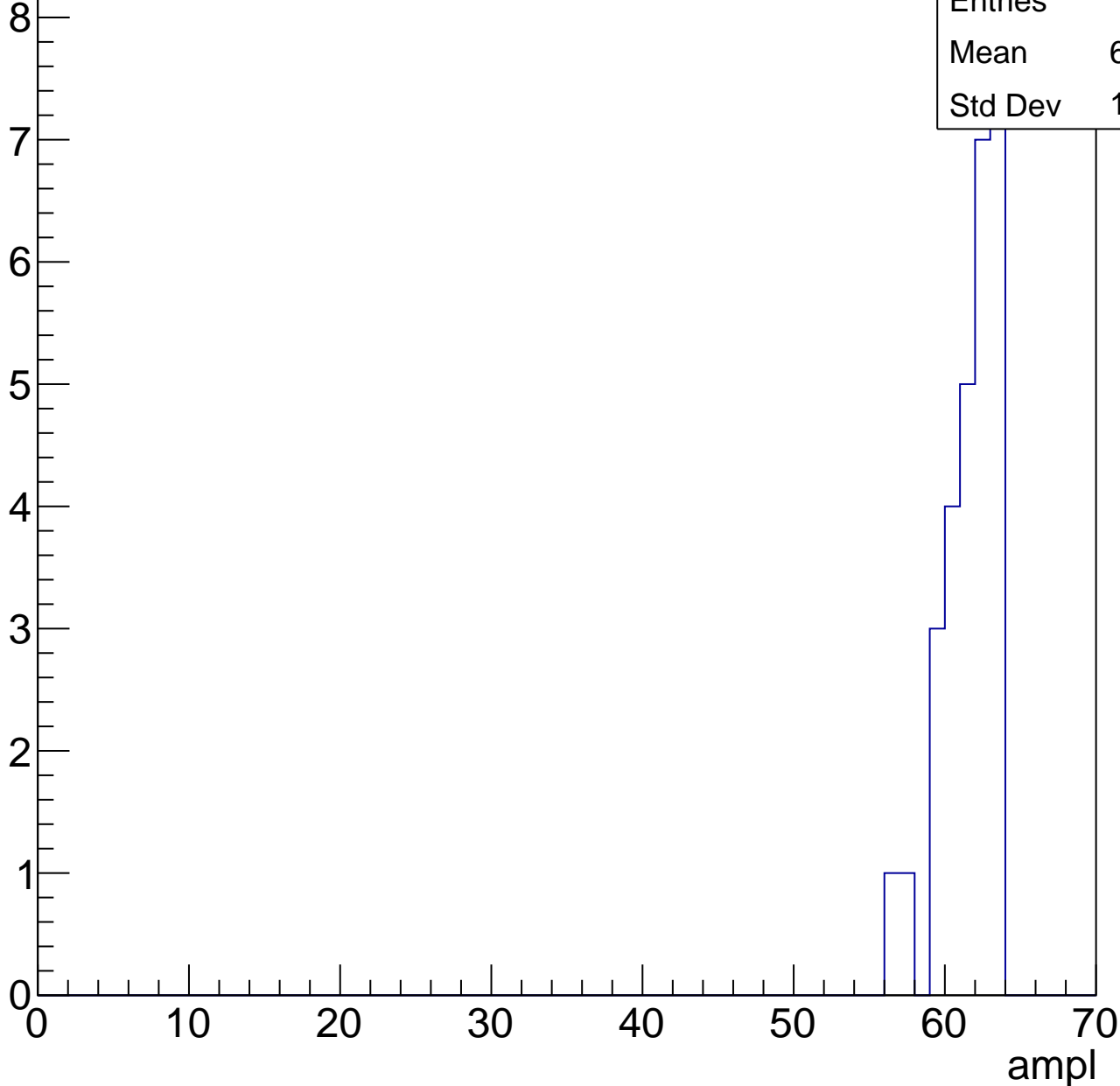


B0L001S, U7-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 61.14 |
| Std Dev | 1.814 |



B0L001S, U7-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch44, adc0

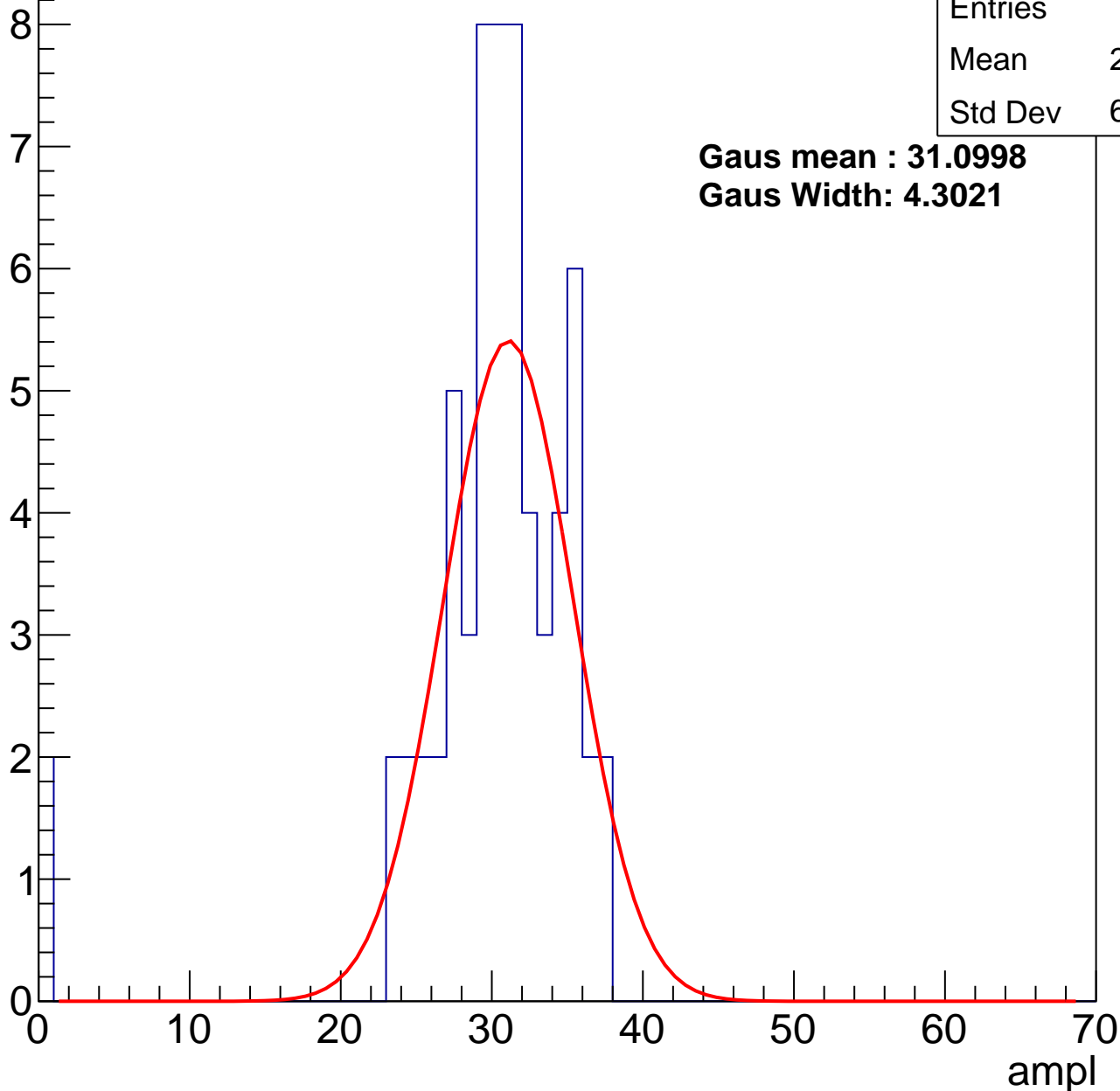
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 29.43 |
| Std Dev | 6.339 |

Gaus mean : 31.0998

Gaus Width: 4.3021



B0L001S, U7-ch44, adc1

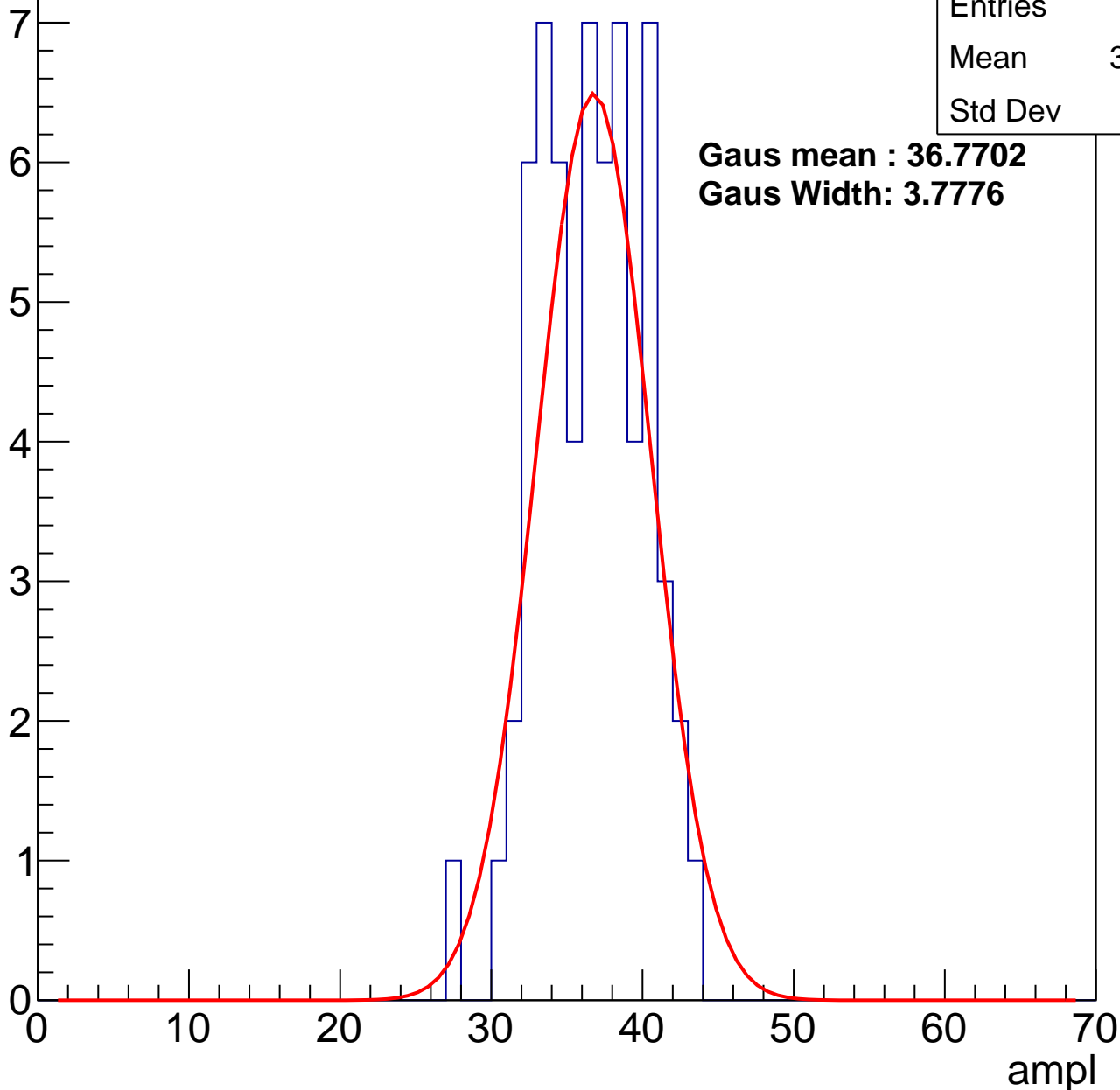
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.12 |
| Std Dev | 3.38 |

Gaus mean : 36.7702

Gaus Width: 3.7776



B0L001S, U7-ch44, adc2

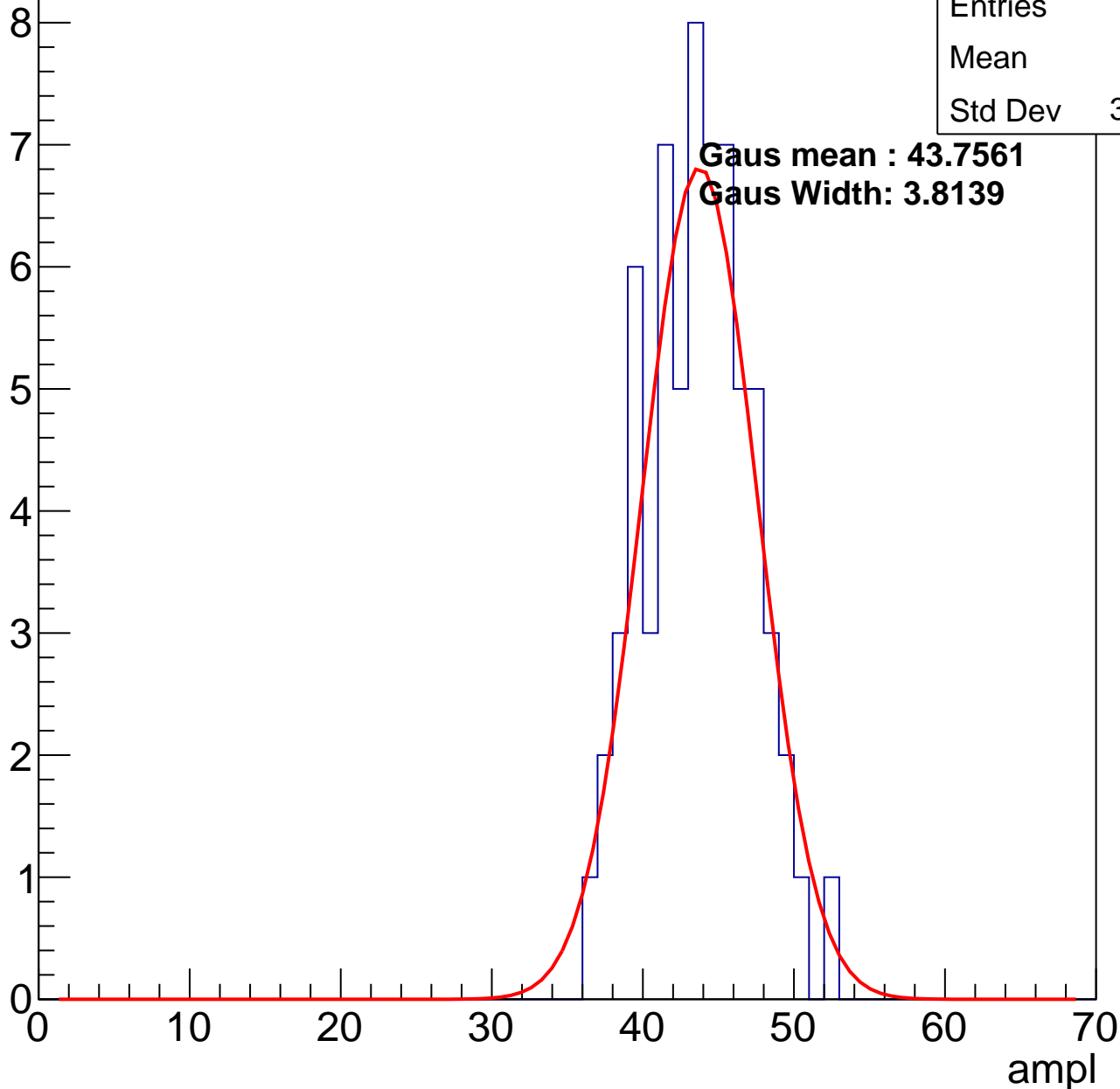
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 43.2 |
| Std Dev | 3.452 |

Gaus mean : 43.7561

Gaus Width: 3.8139

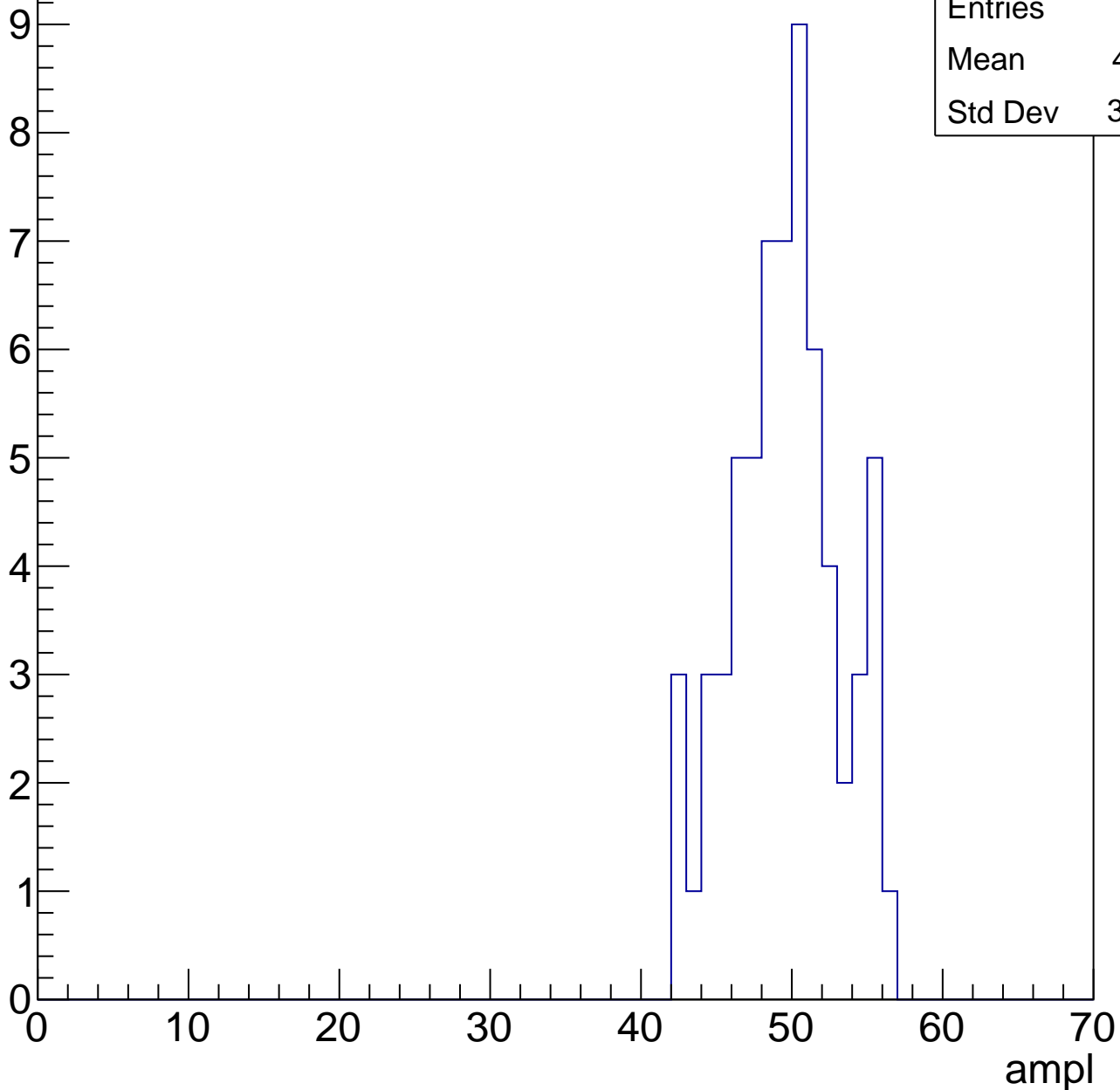


B0L001S, U7-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 49.11 |
| Std Dev | 3.496 |

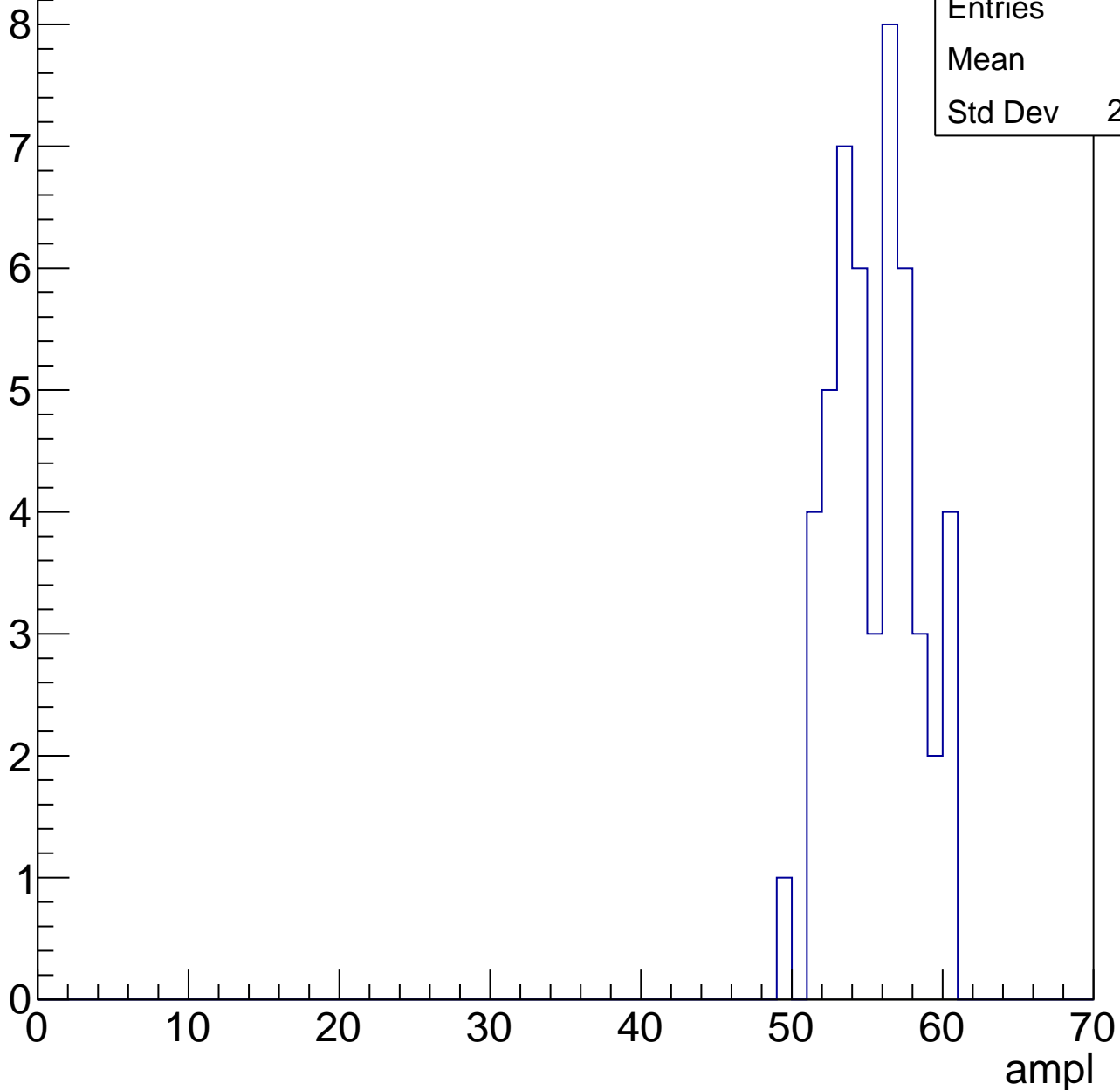


B0L001S, U7-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 55 |
| Std Dev | 2.748 |

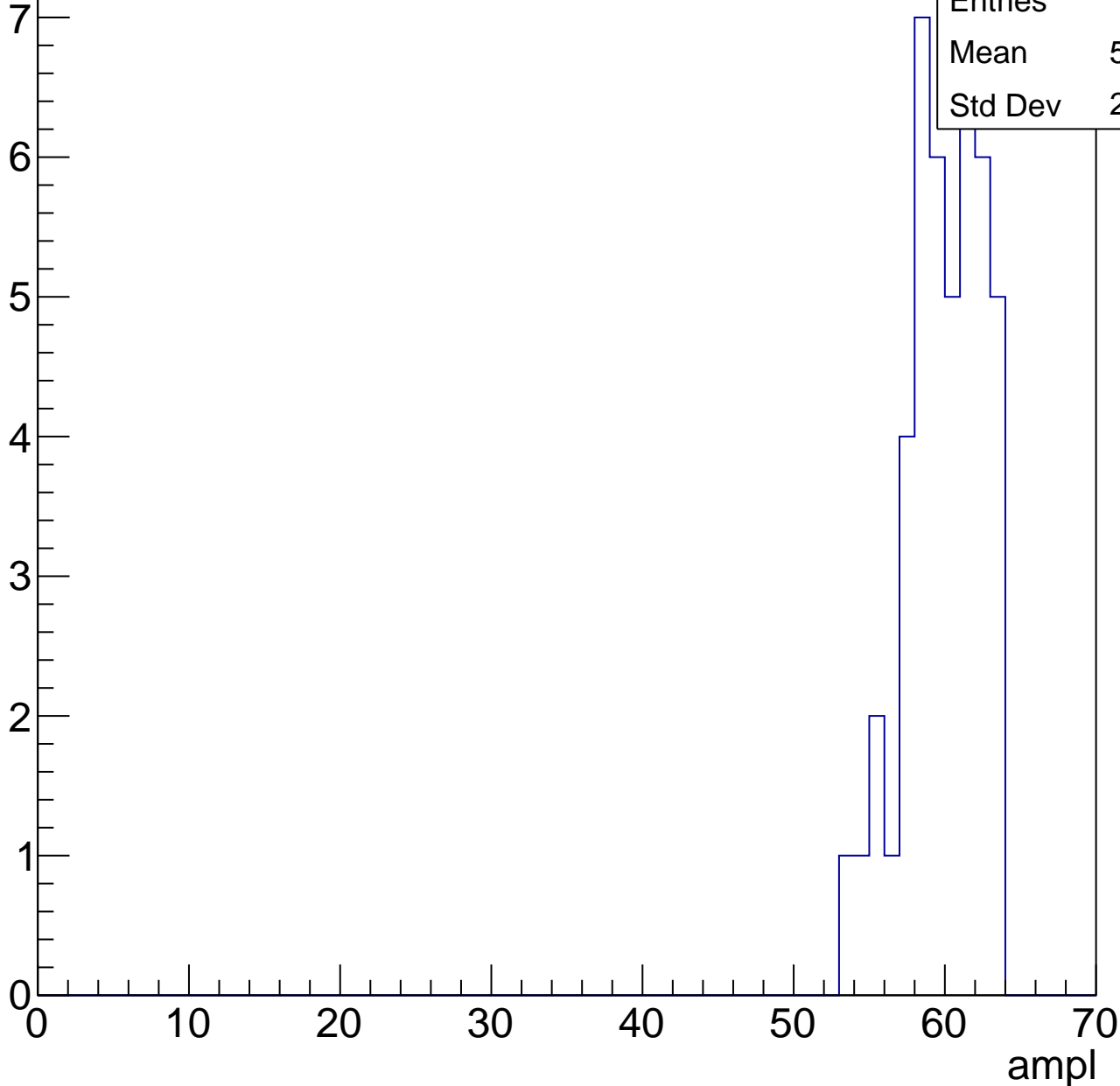


B0L001S, U7-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

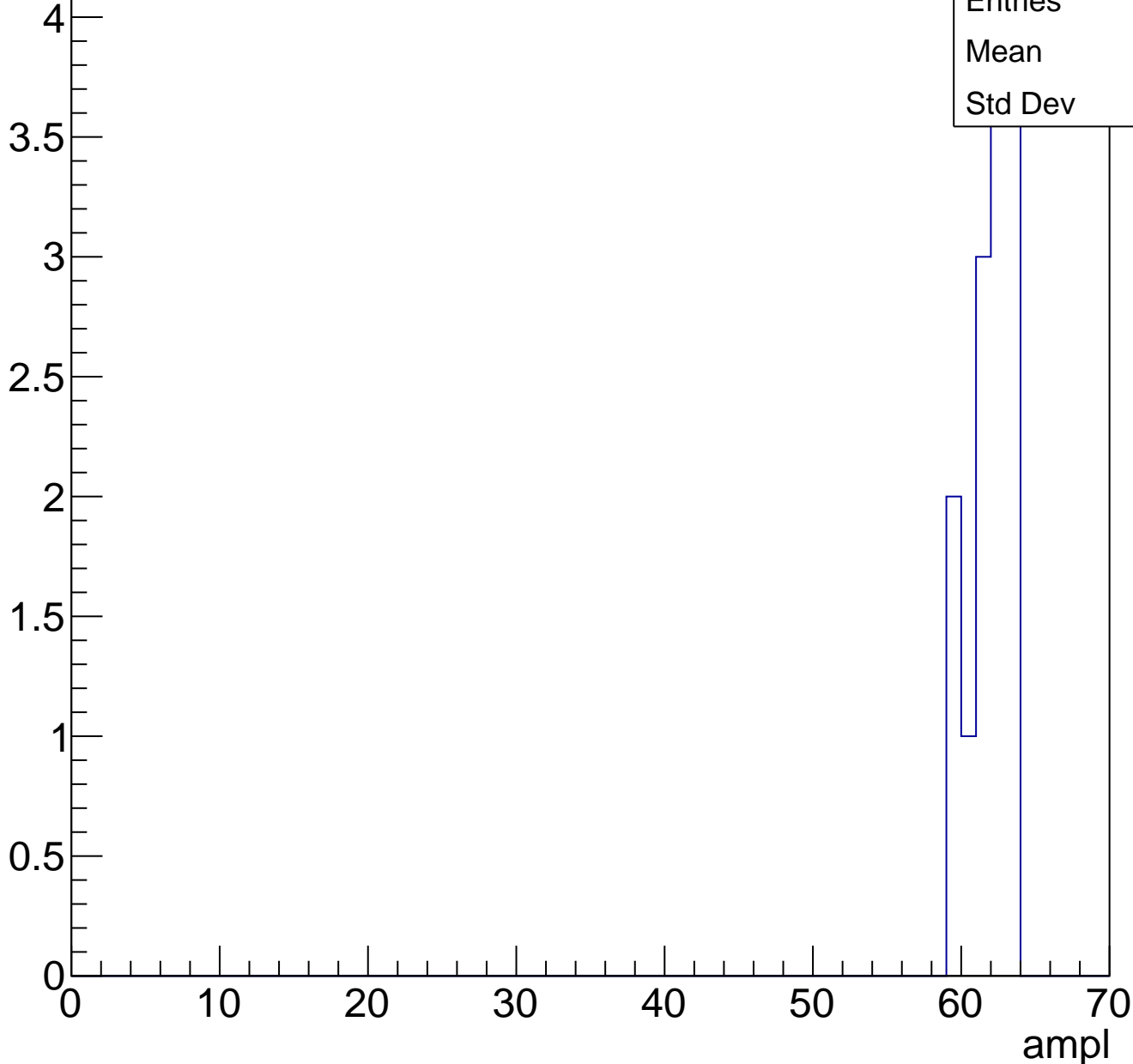
| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.44 |
| Std Dev | 2.508 |



B0L001S, U7-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch45, adc0

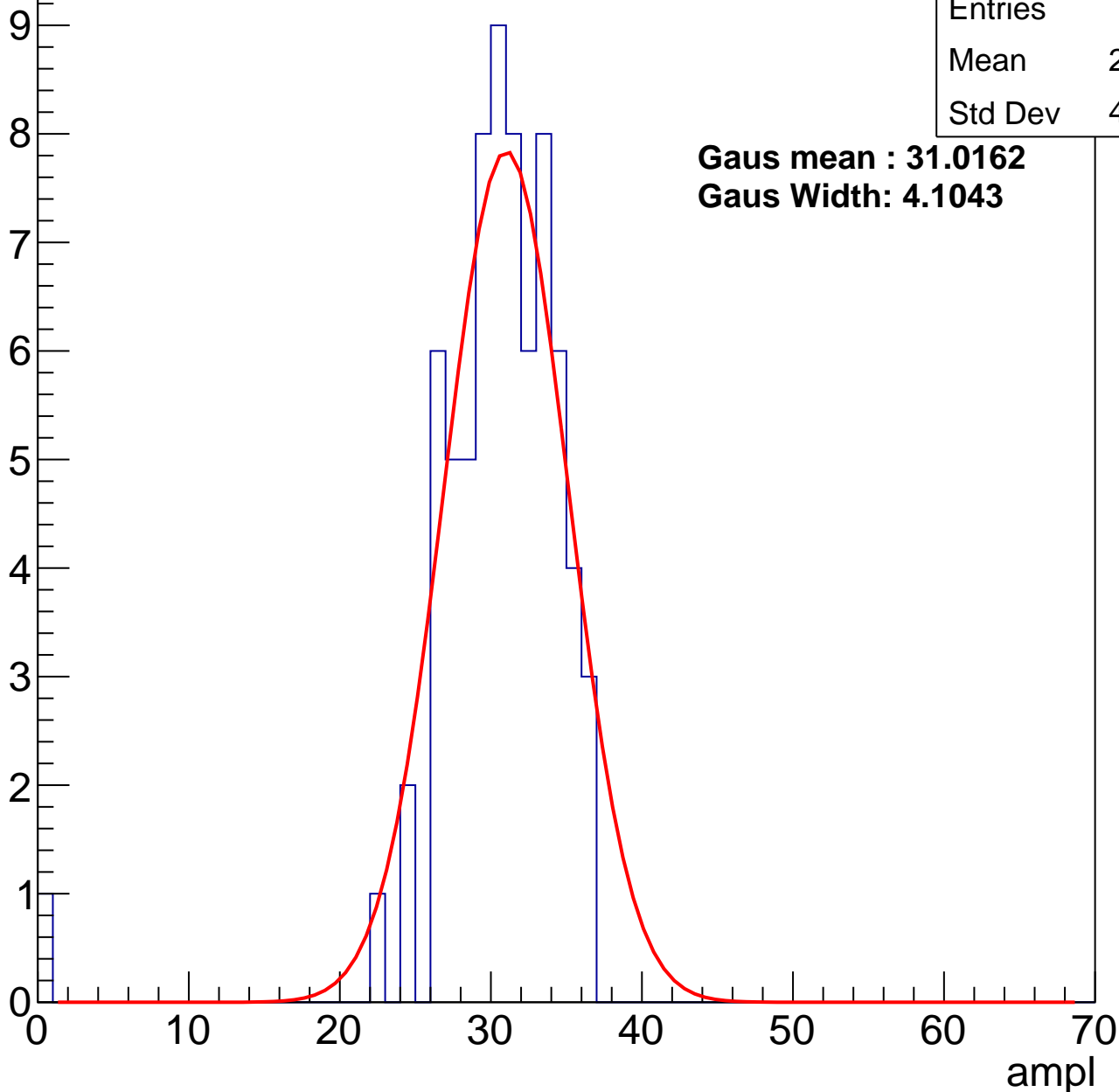
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 29.99 |
| Std Dev | 4.733 |

Gaus mean : 31.0162

Gaus Width: 4.1043

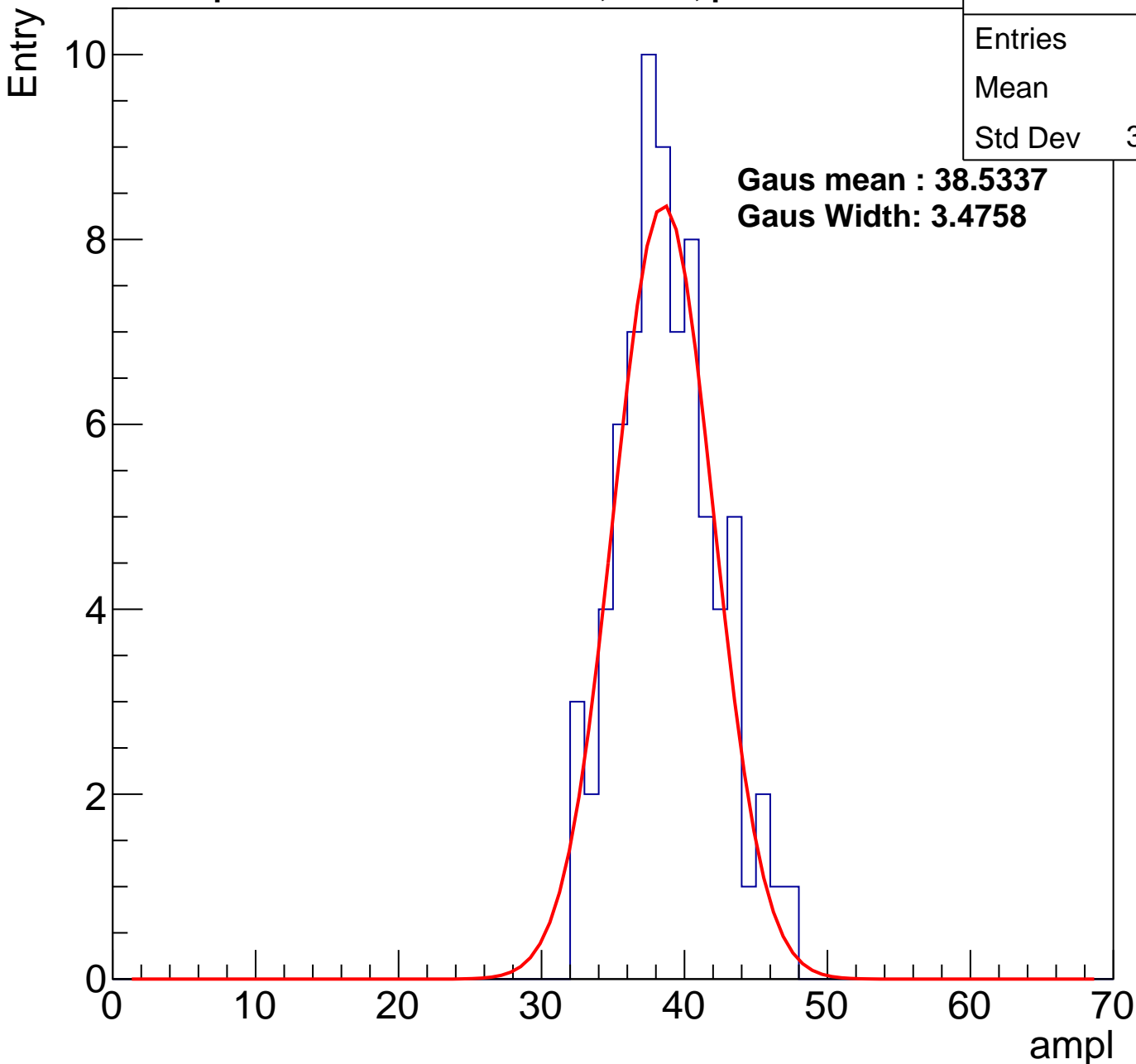


B0L001S, U7-ch45, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 38.4 |
| Std Dev | 3.374 |

Gaus mean : 38.5337
Gaus Width: 3.4758



B0L001S, U7-ch45, adc2

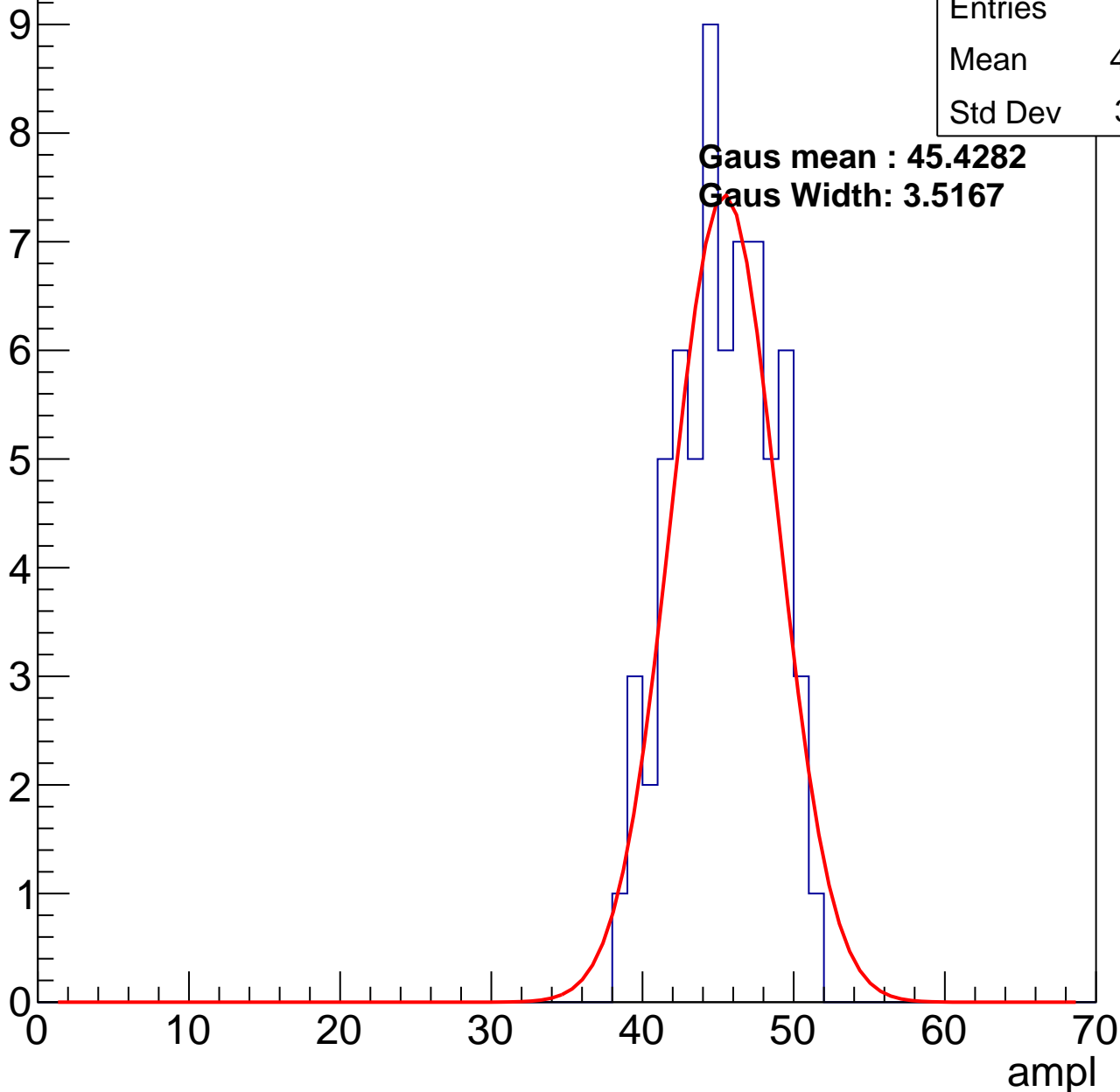
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44.83 |
| Std Dev | 3.151 |

Gaus mean : 45.4282

Gaus Width: 3.5167

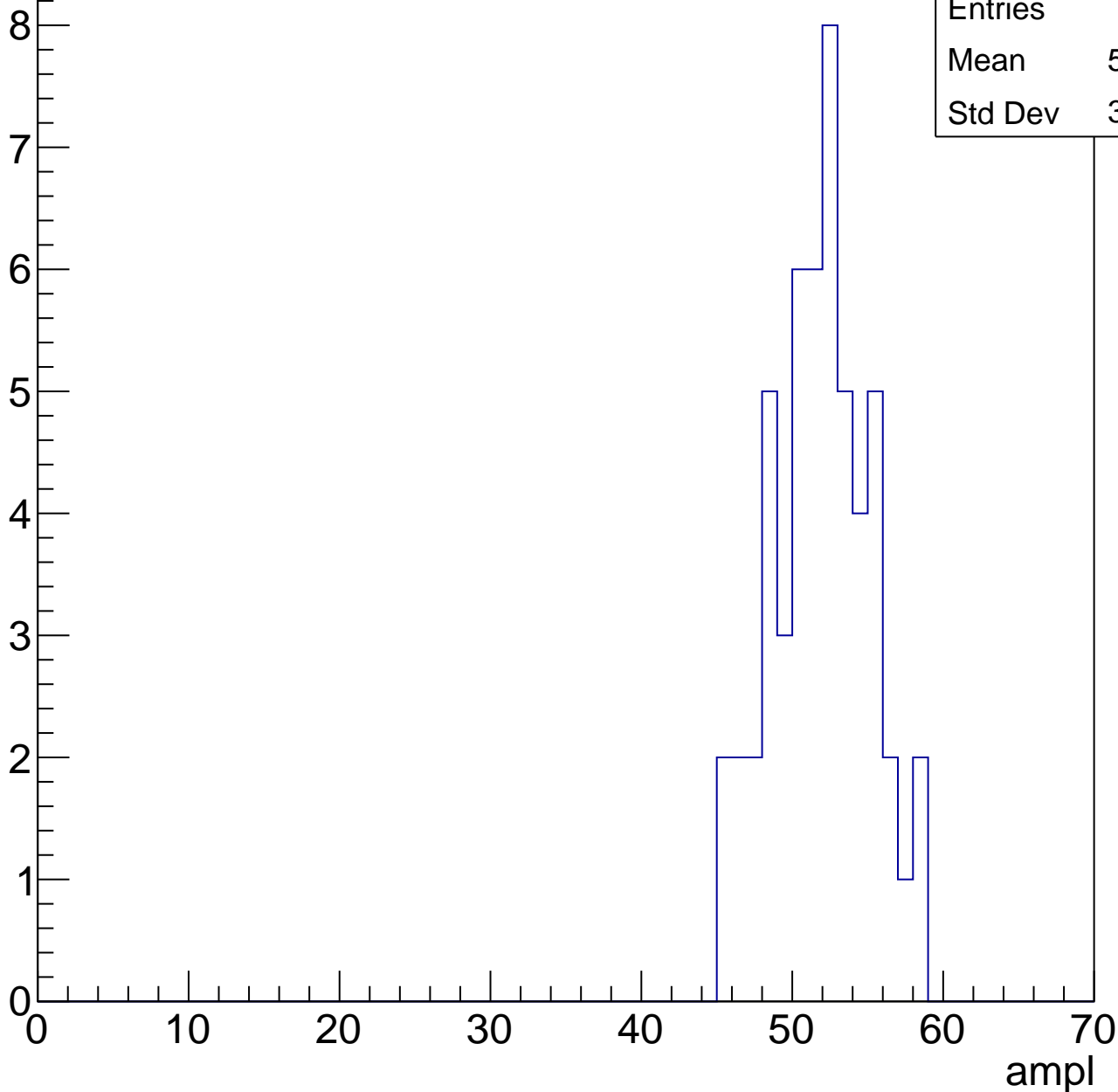


B0L001S, U7-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 51.43 |
| Std Dev | 3.177 |

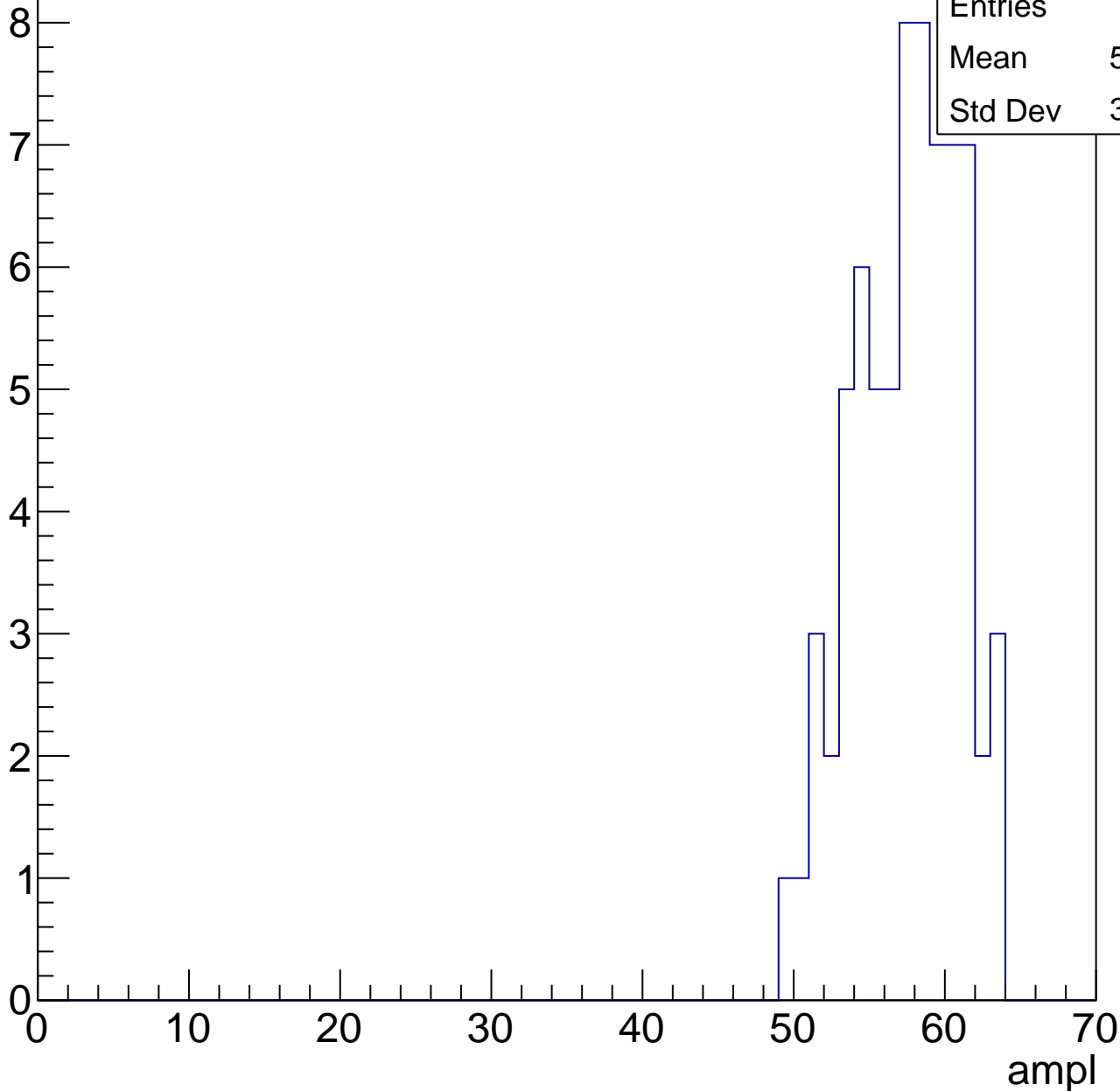


B0L001S, U7-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 57.04 |
| Std Dev | 3.378 |

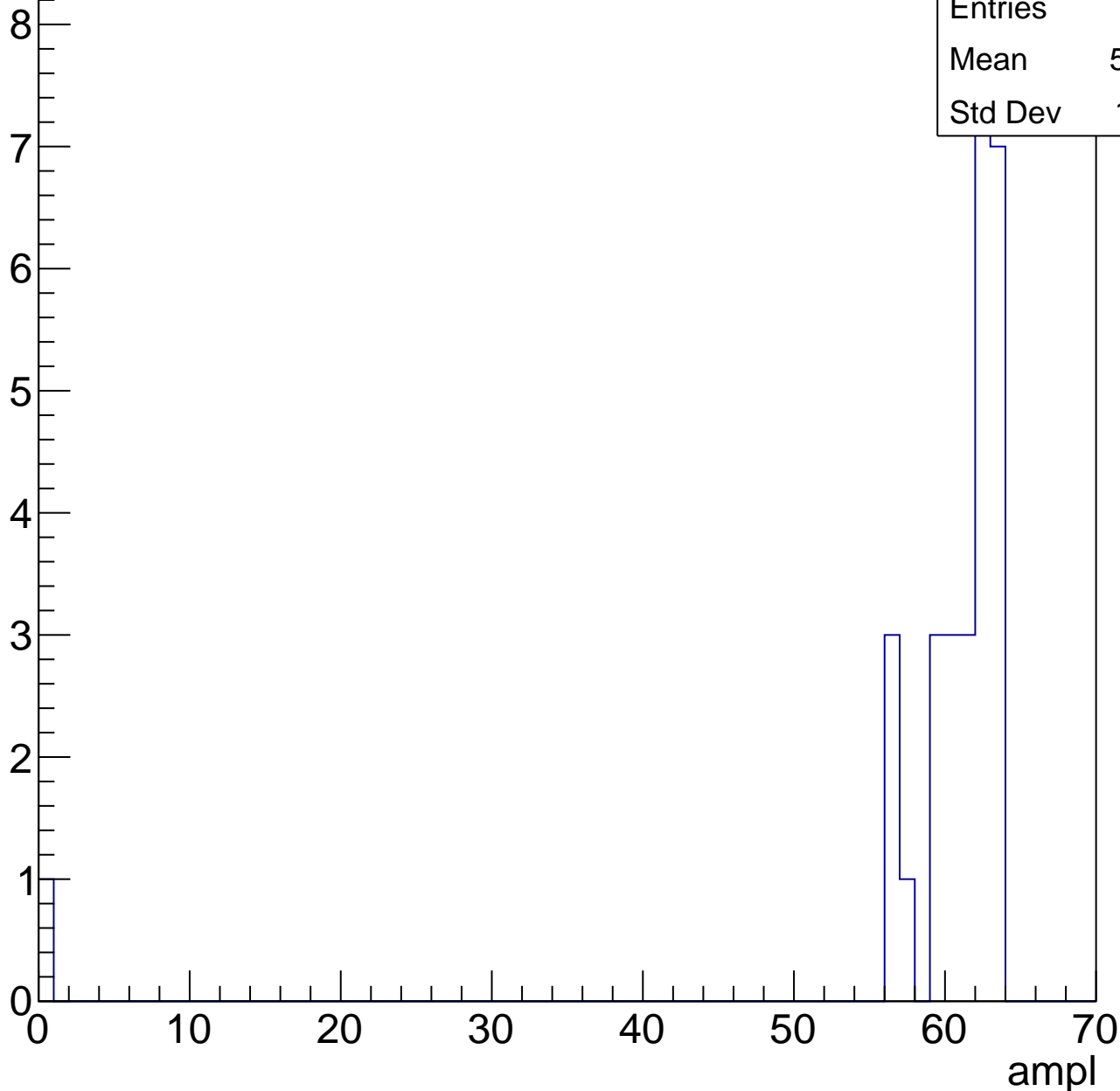


B0L001S, U7-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.69 |
| Std Dev | 11.31 |



B0L001S, U7-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch46, adc0

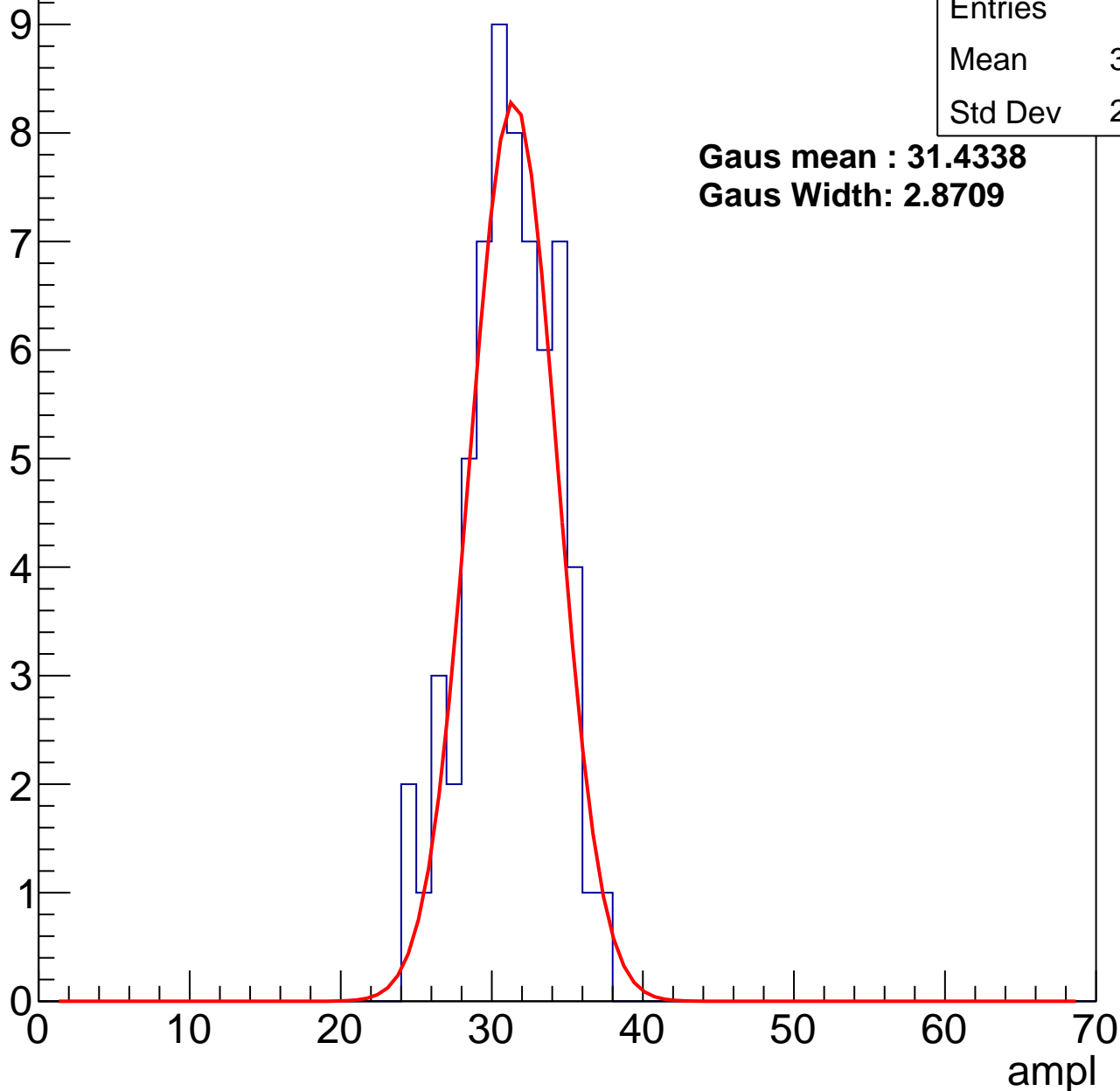
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 30.78 |
| Std Dev | 2.925 |

Gaus mean : 31.4338

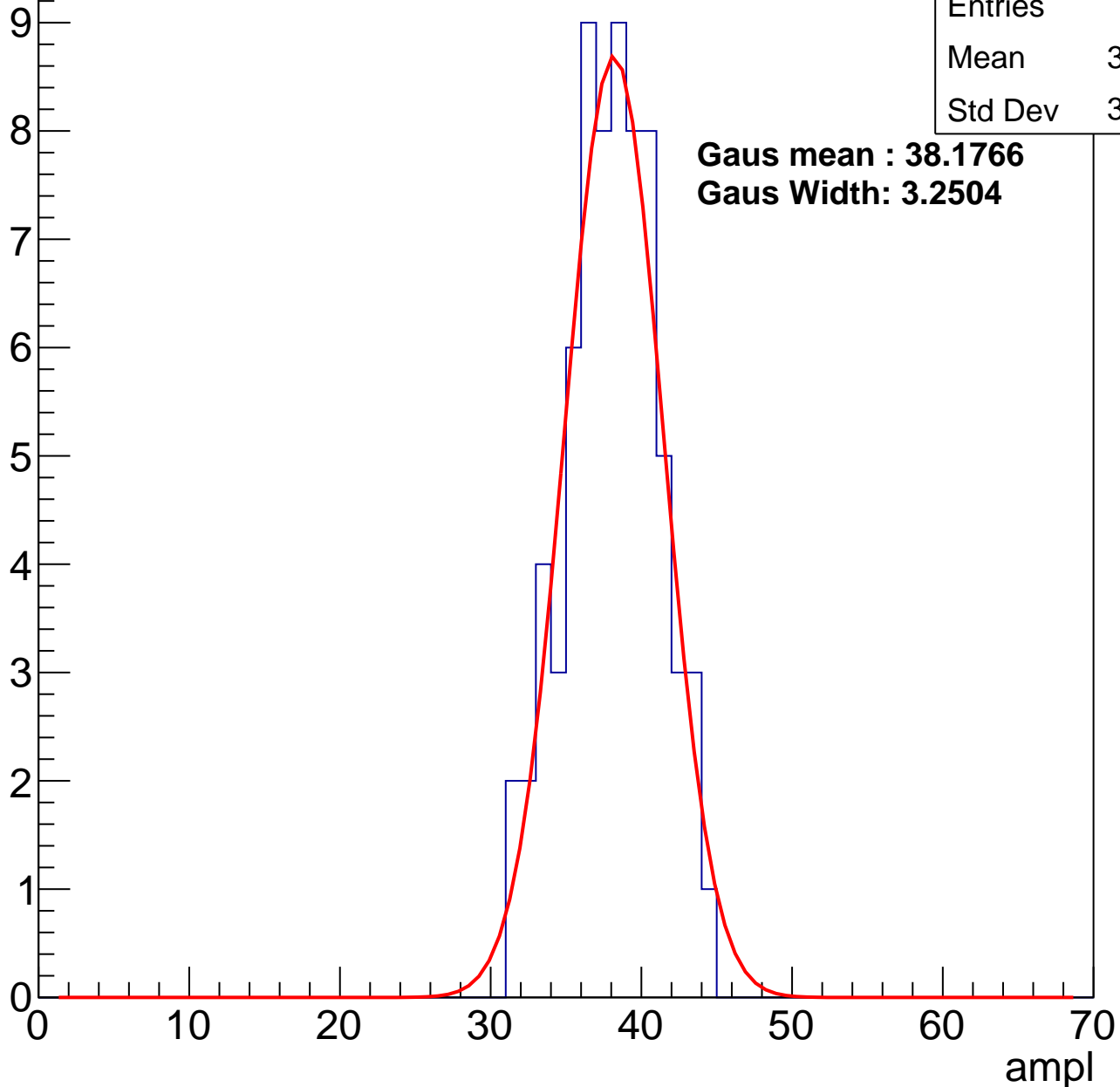
Gaus Width: 2.8709



B0L001S, U7-ch46, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch46, adc2

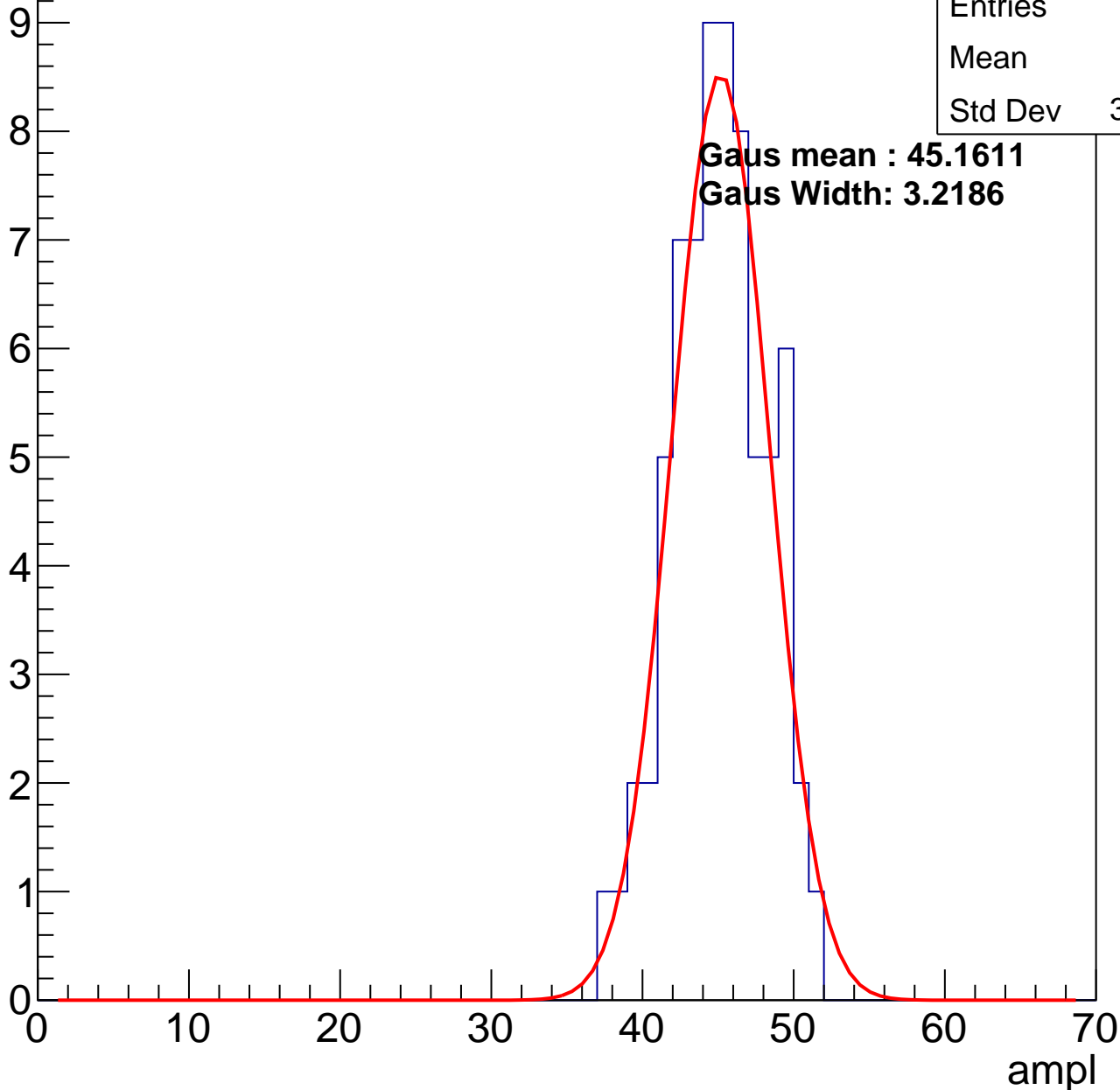
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.6 |
| Std Dev | 3.068 |

Gaus mean : 45.1611

Gaus Width: 3.2186

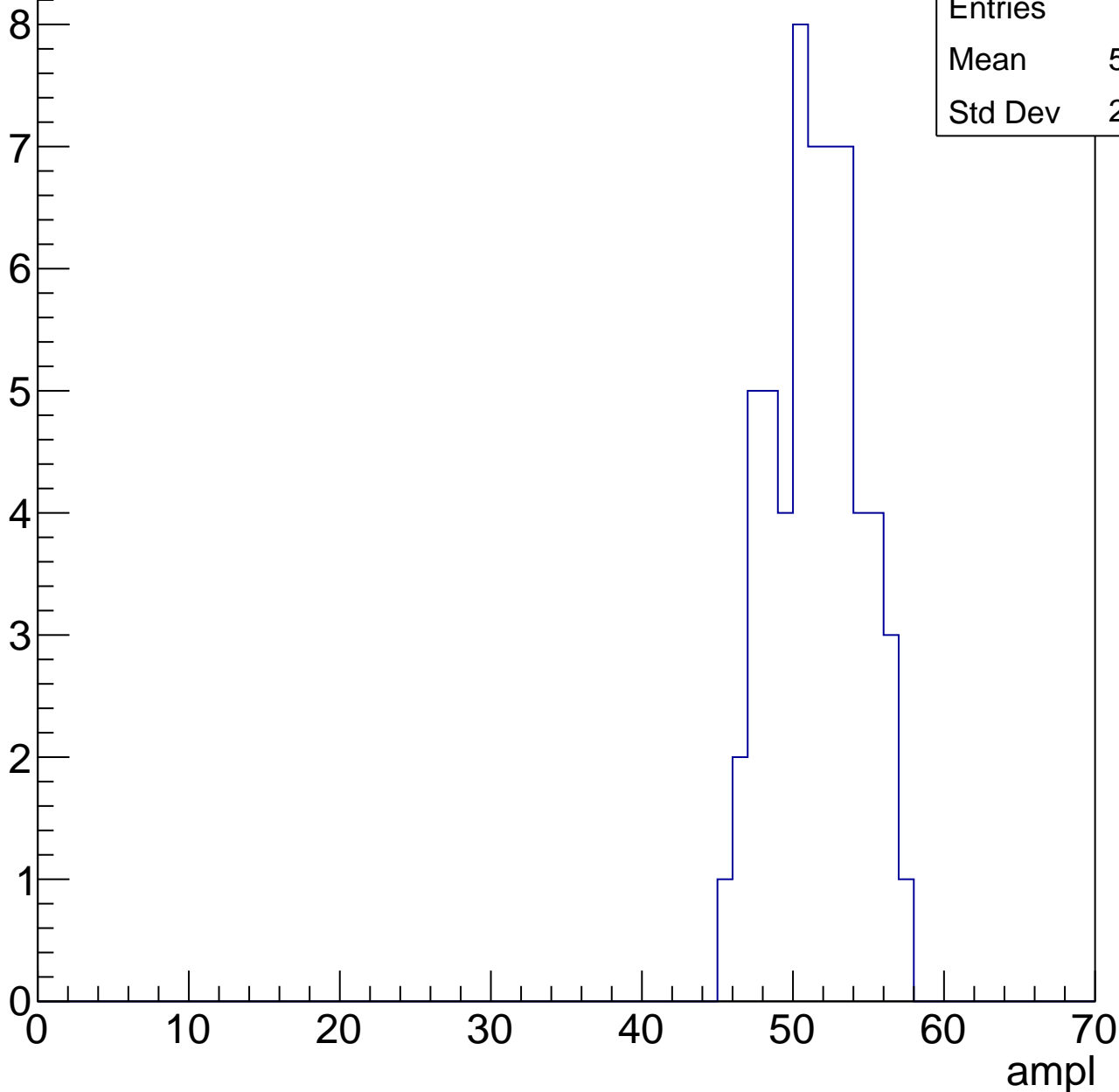


B0L001S, U7-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

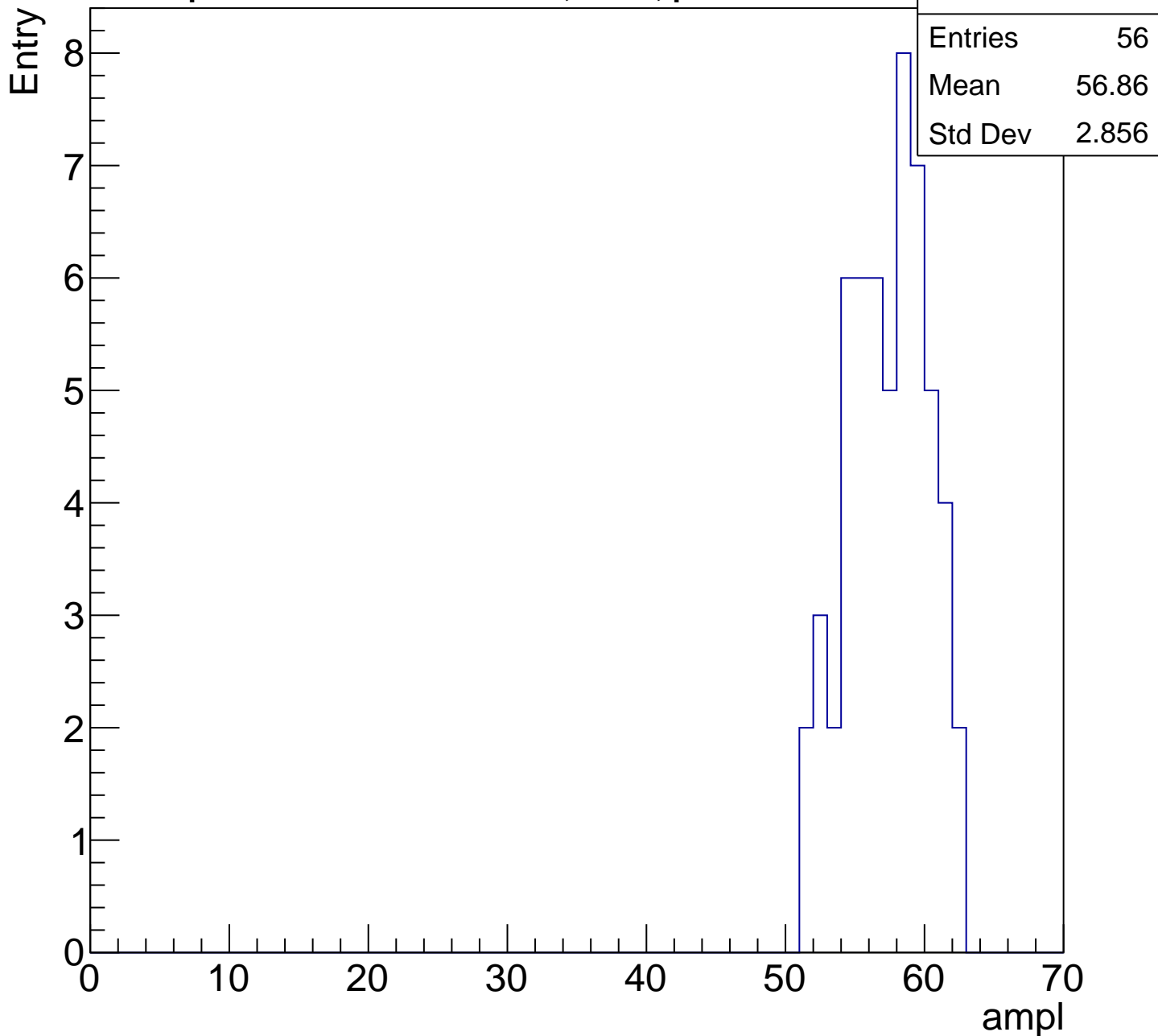
Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 51.05 |
| Std Dev | 2.879 |



B0L001S, U7-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

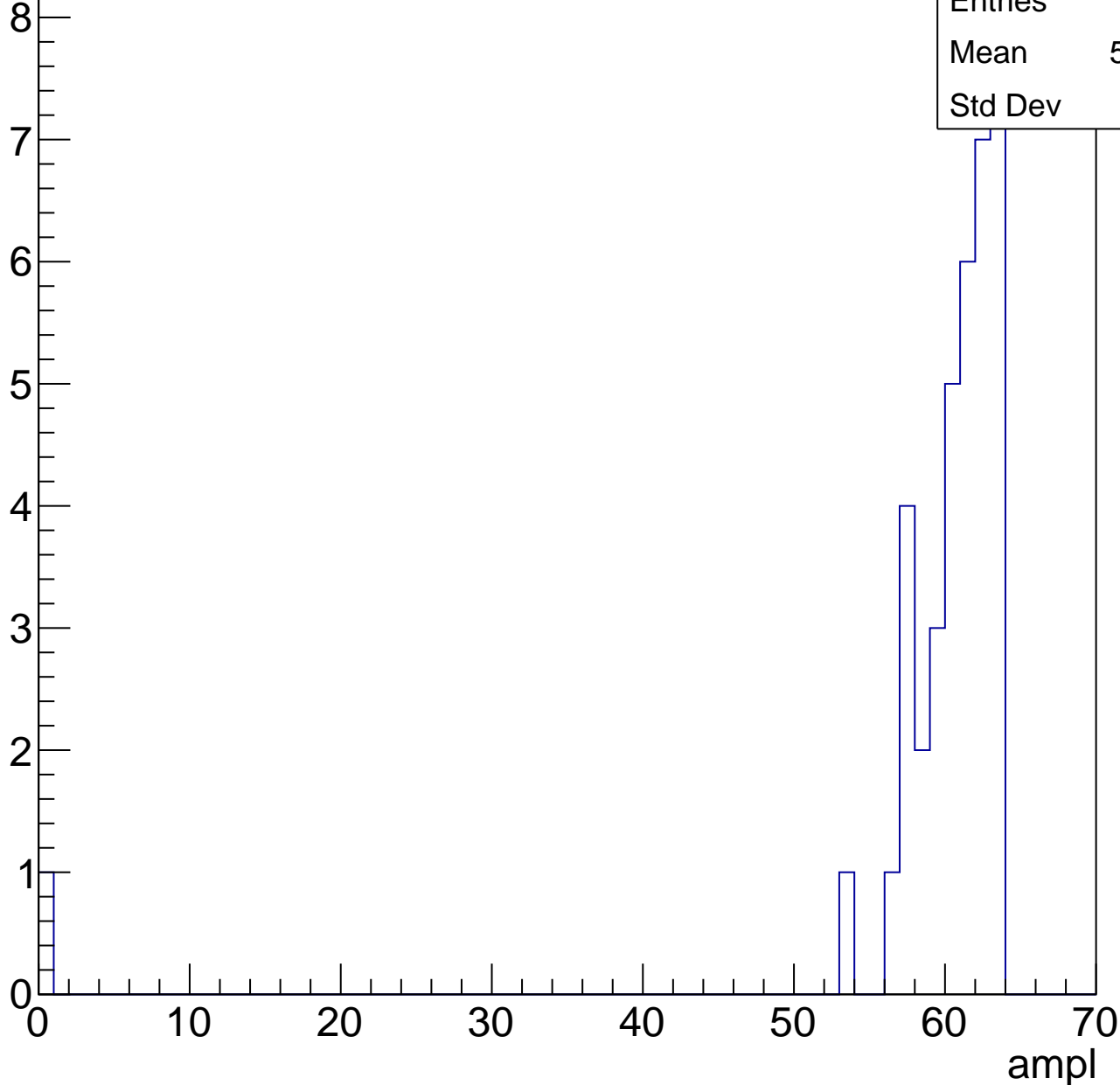


B0L001S, U7-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 58.79 |
| Std Dev | 9.95 |



B0L001S, U7-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch47, adc0

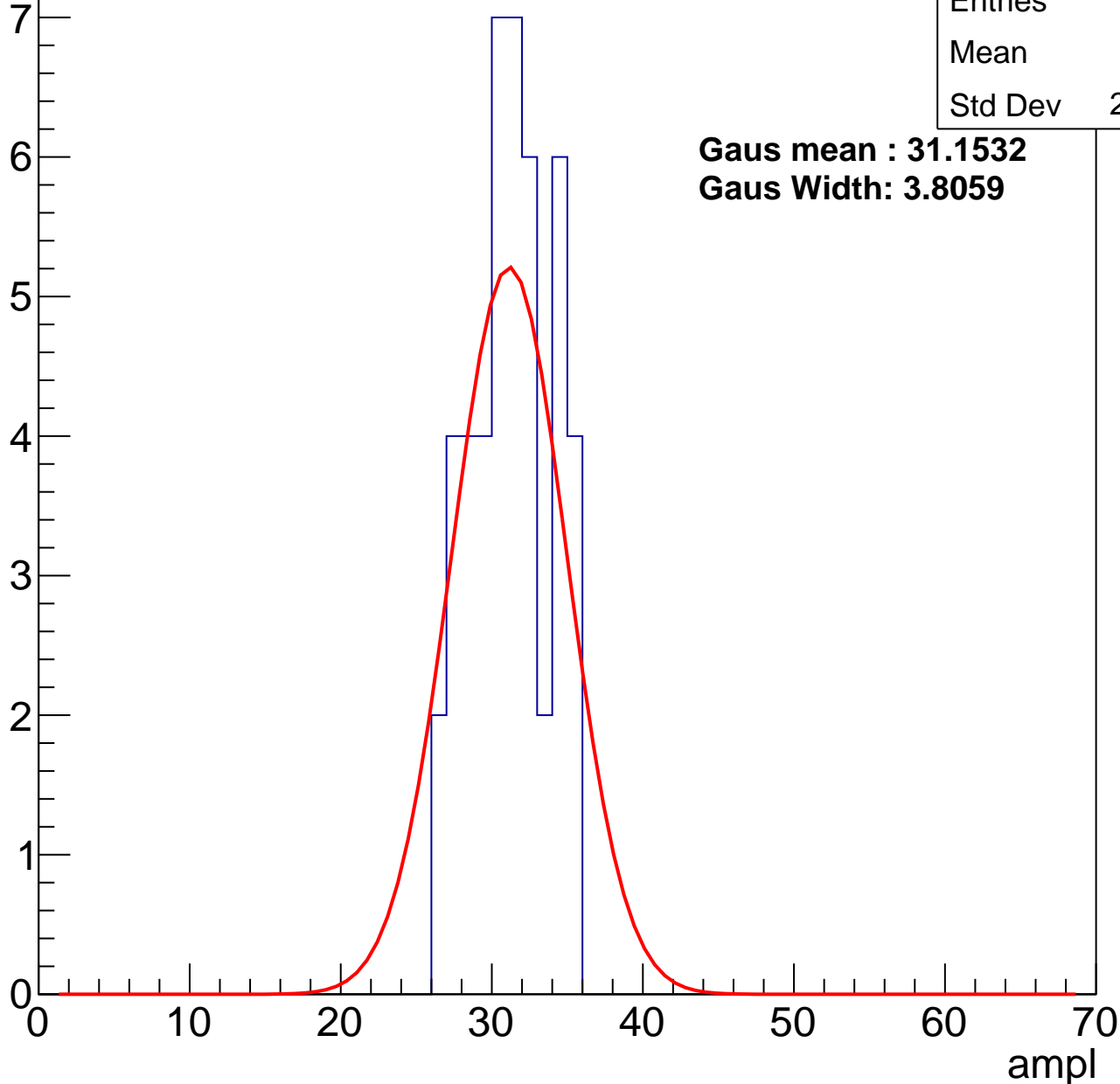
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 30.8 |
| Std Dev | 2.568 |

Gaus mean : 31.1532

Gaus Width: 3.8059



B0L001S, U7-ch47, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.67 |
| Std Dev | 3.197 |

Gaus mean : 37.1668

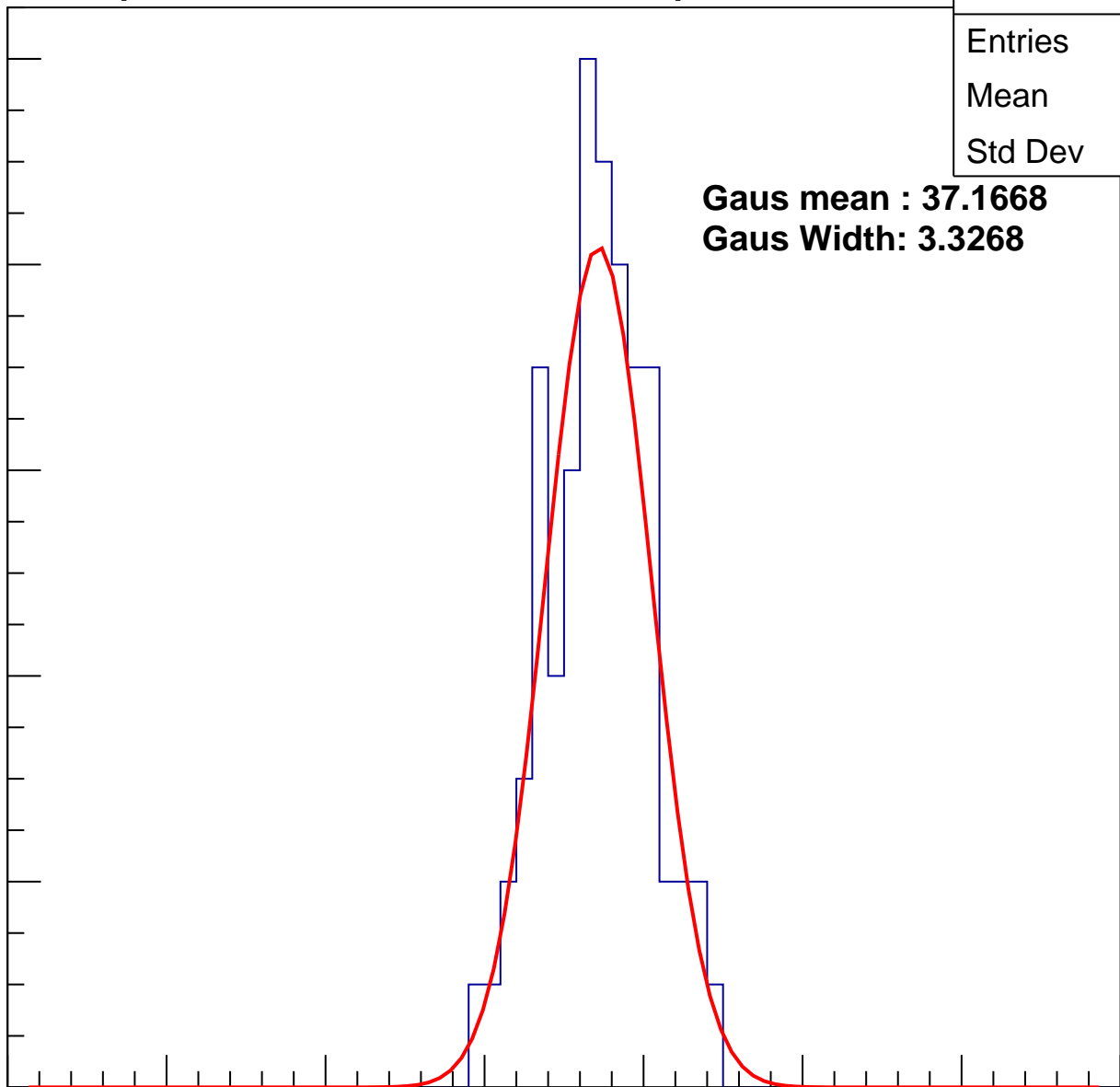
Gaus Width: 3.3268

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

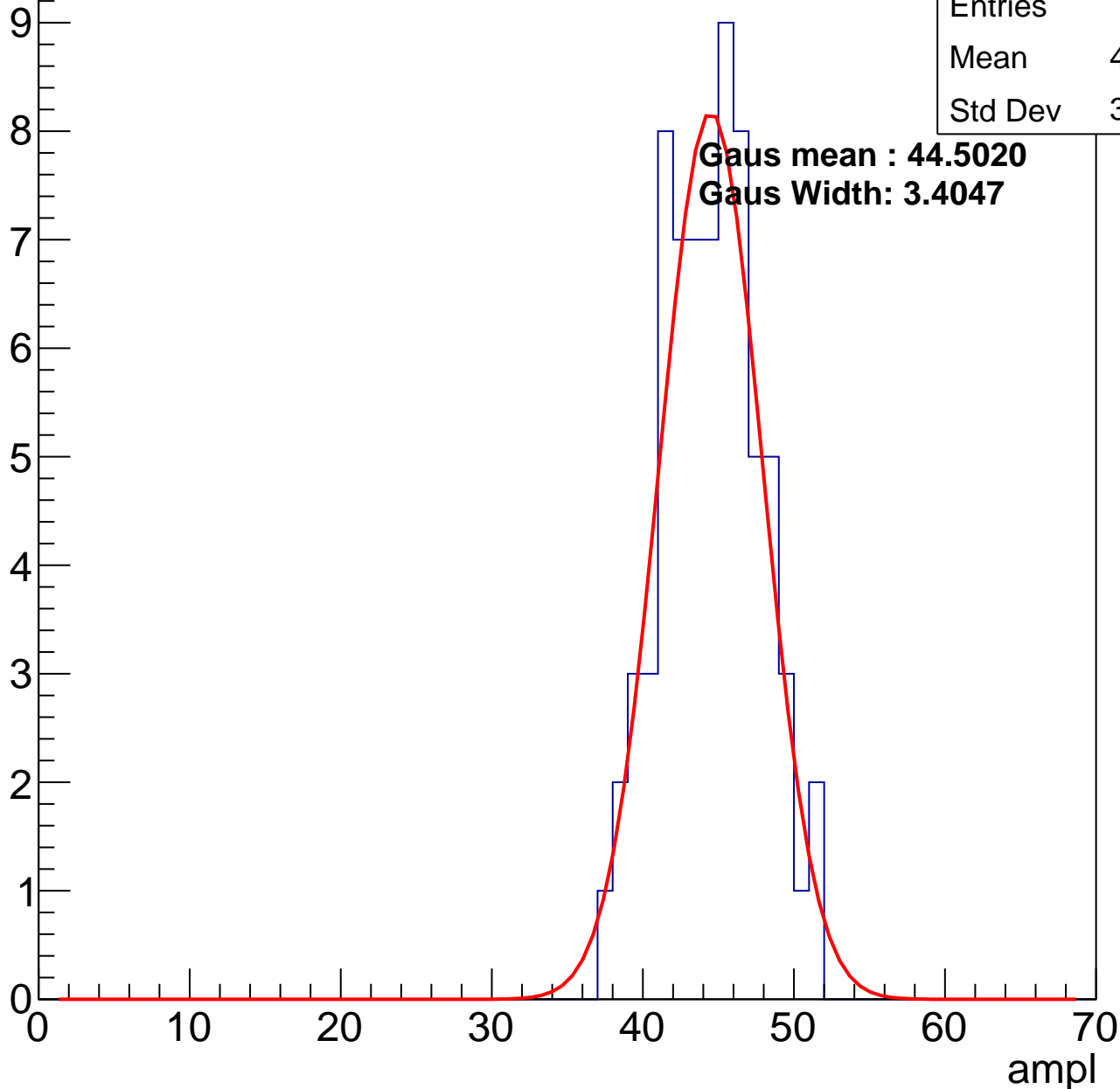
ampl



B0L001S, U7-ch47, adc2

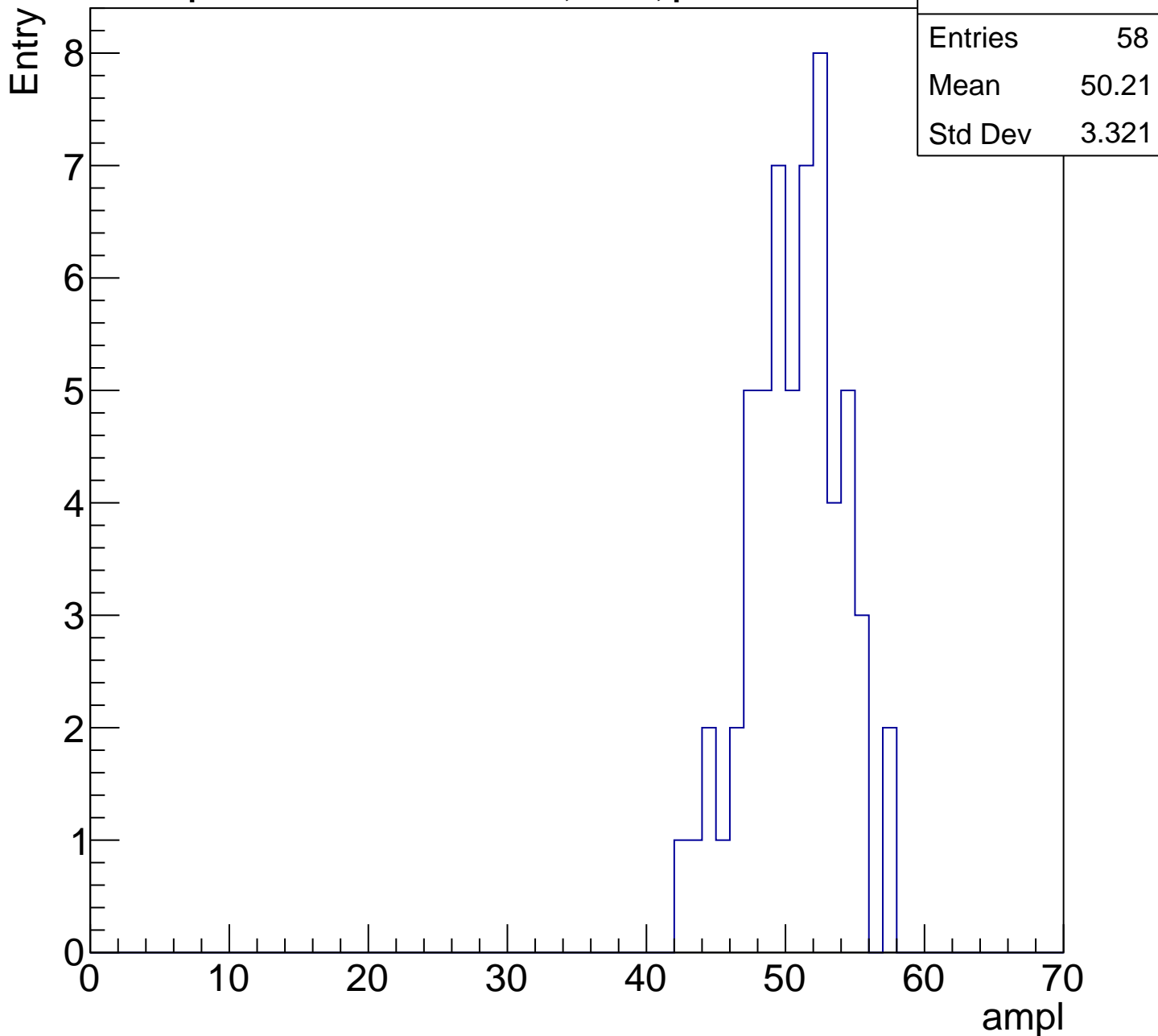
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

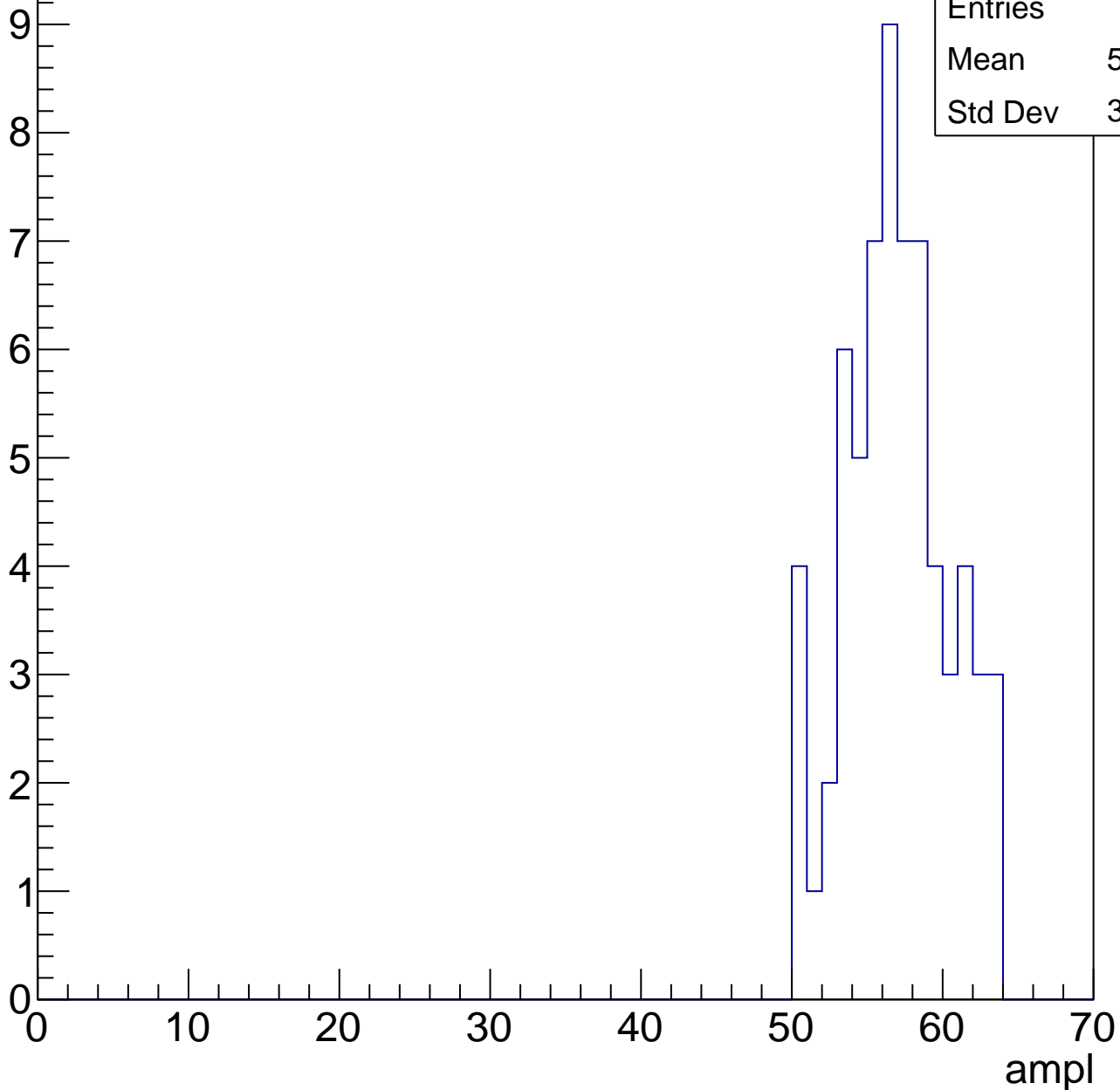


B0L001S, U7-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

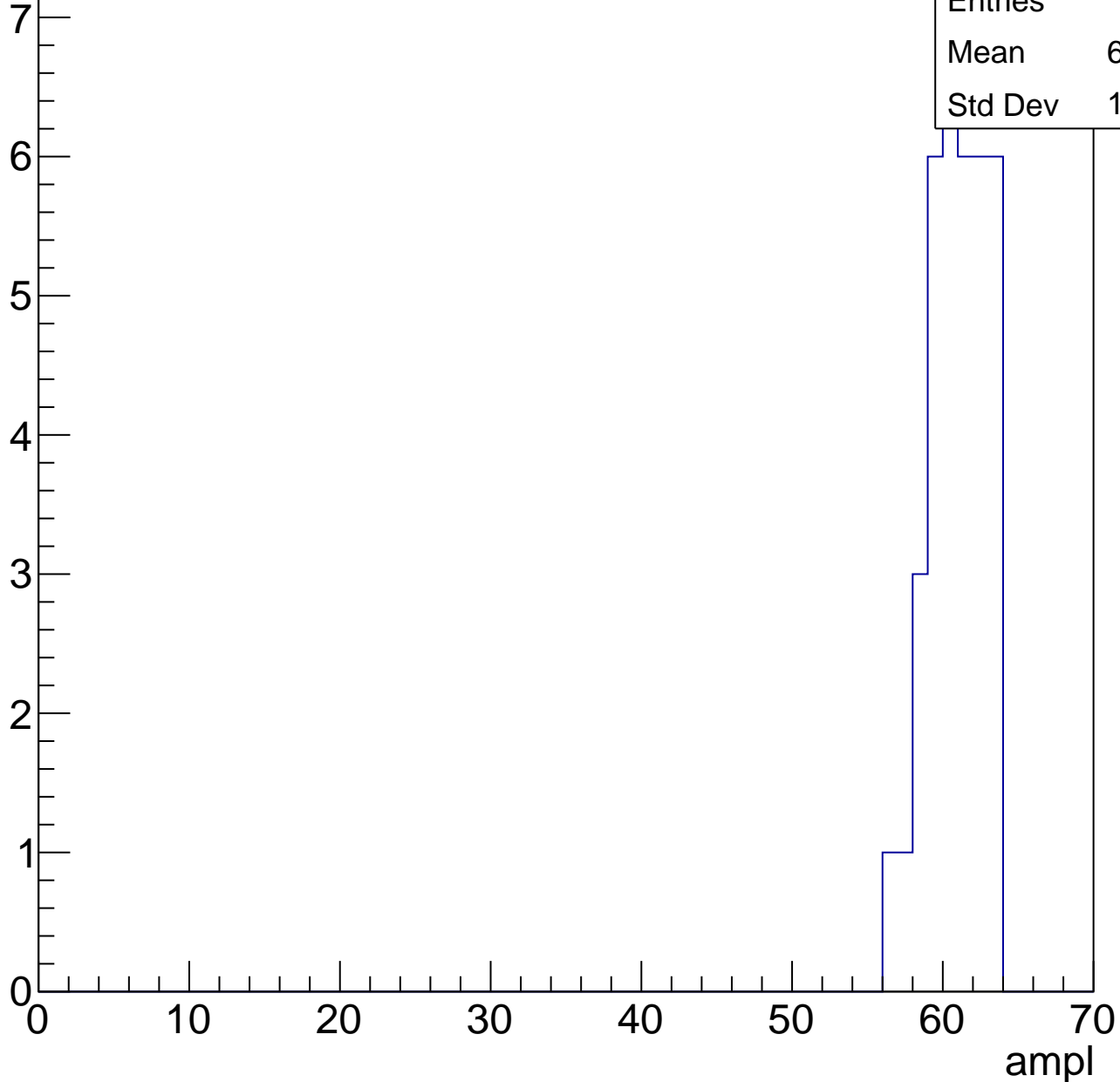
| | |
|---------|-------|
| Entries | 65 |
| Mean | 56.49 |
| Std Dev | 3.375 |



B0L001S, U7-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

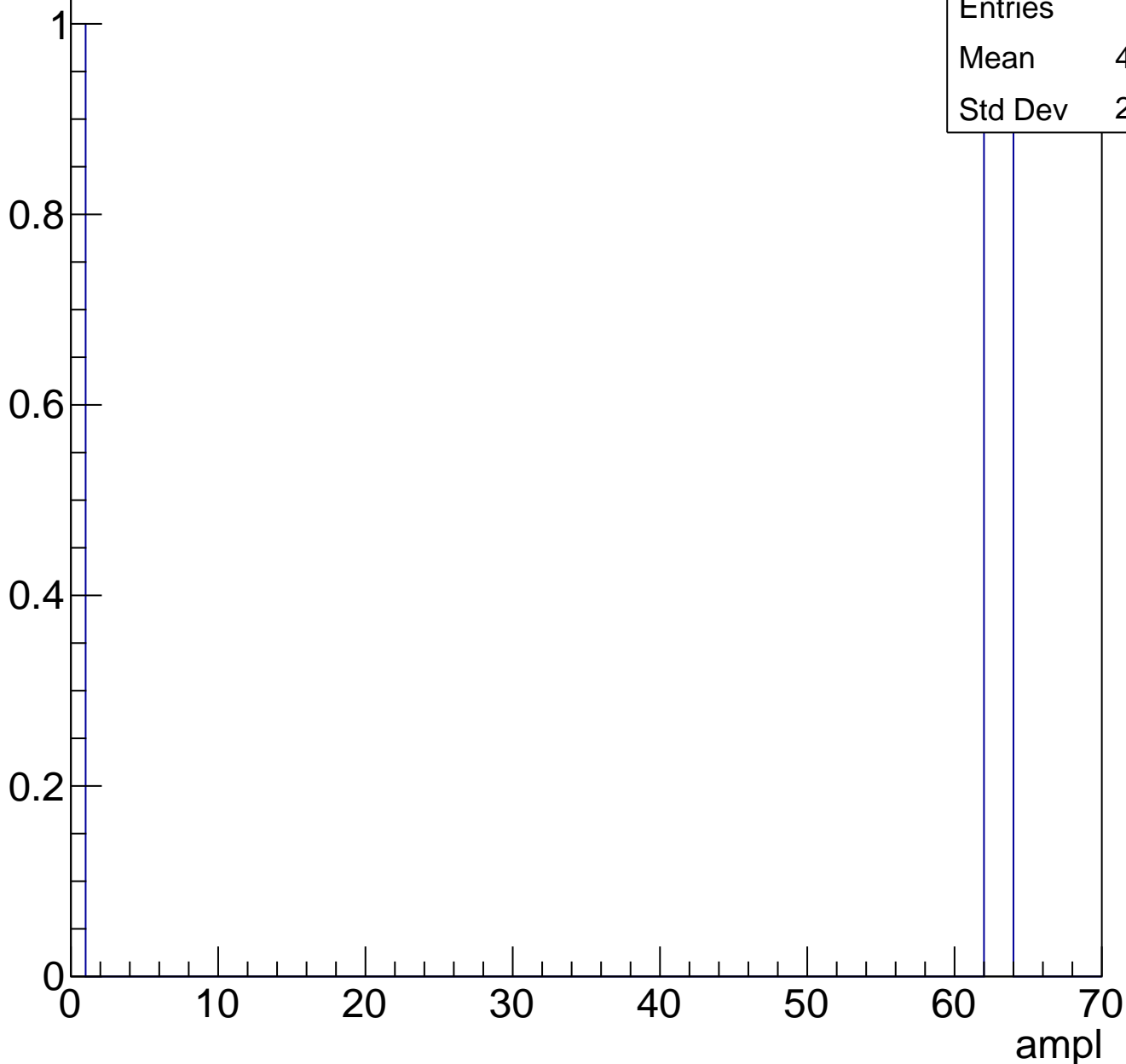
Entry



B0L001S, U7-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|------|
| Entries | 2 |
| Mean | 12.5 |
| Std Dev | 12.5 |

B0L001S, U7-ch48, adc0

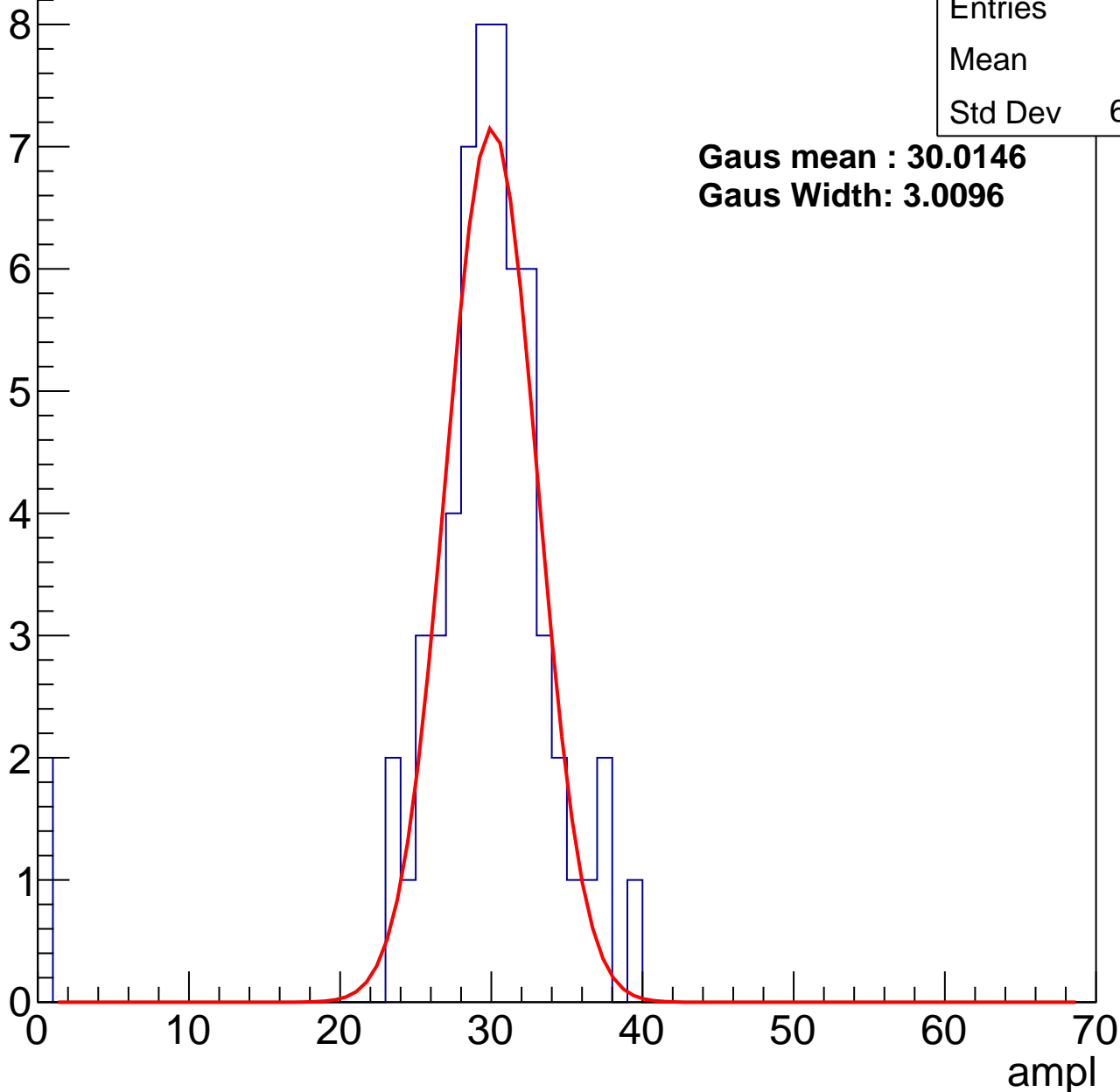
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 28.8 |
| Std Dev | 6.287 |

Gaus mean : 30.0146

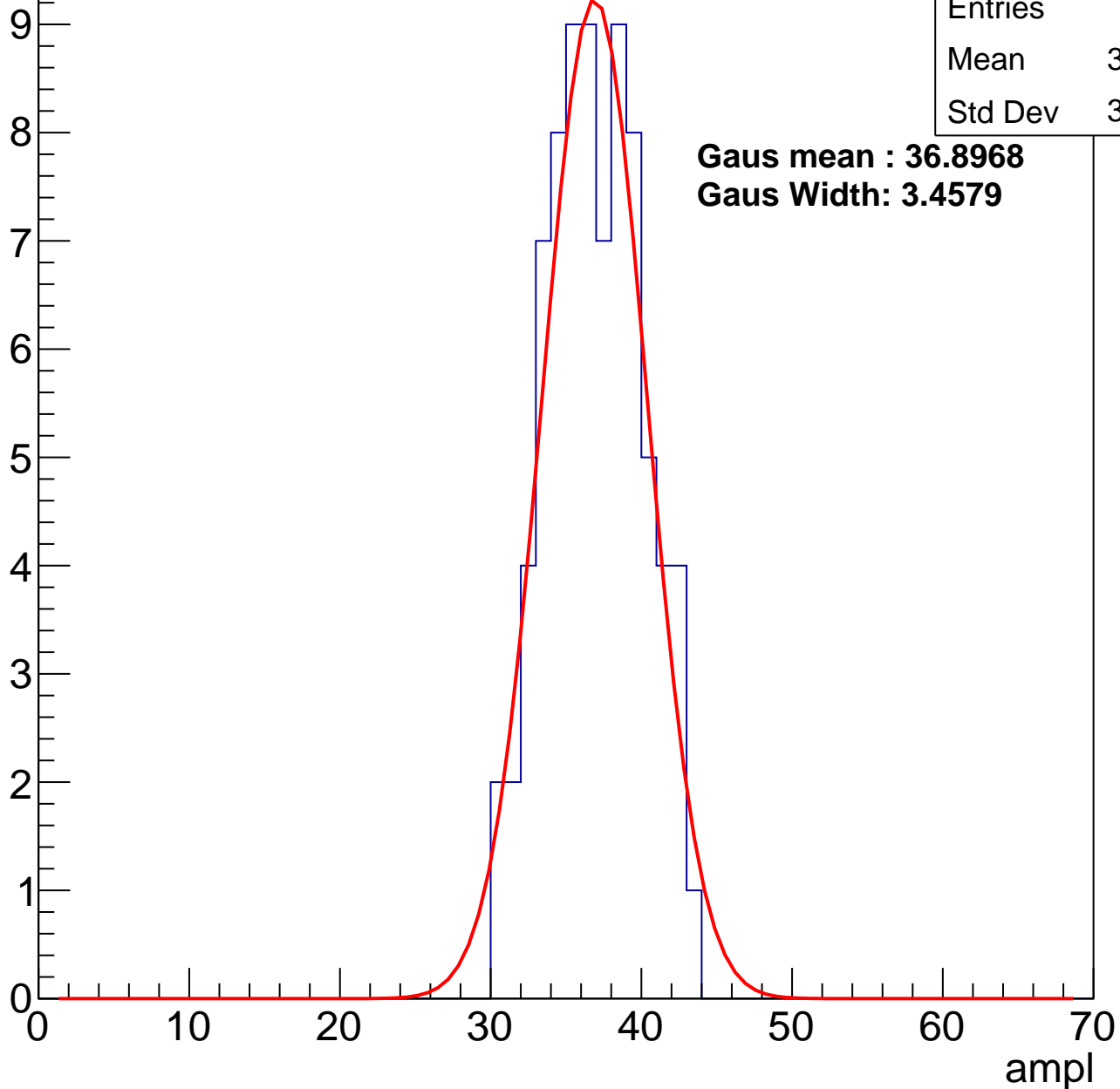
Gaus Width: 3.0096



B0L001S, U7-ch48, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch48, adc2

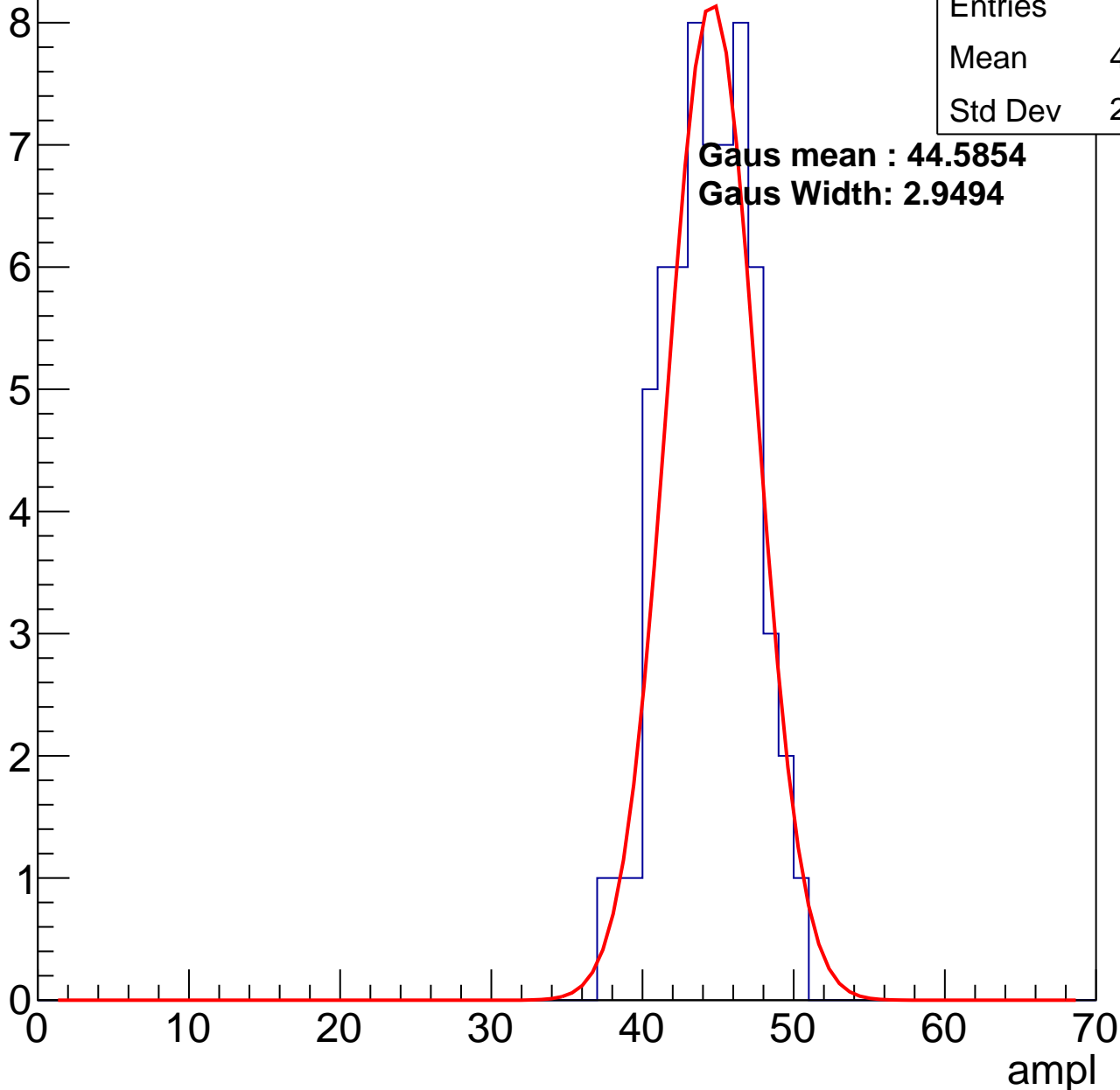
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 43.89 |
| Std Dev | 2.846 |

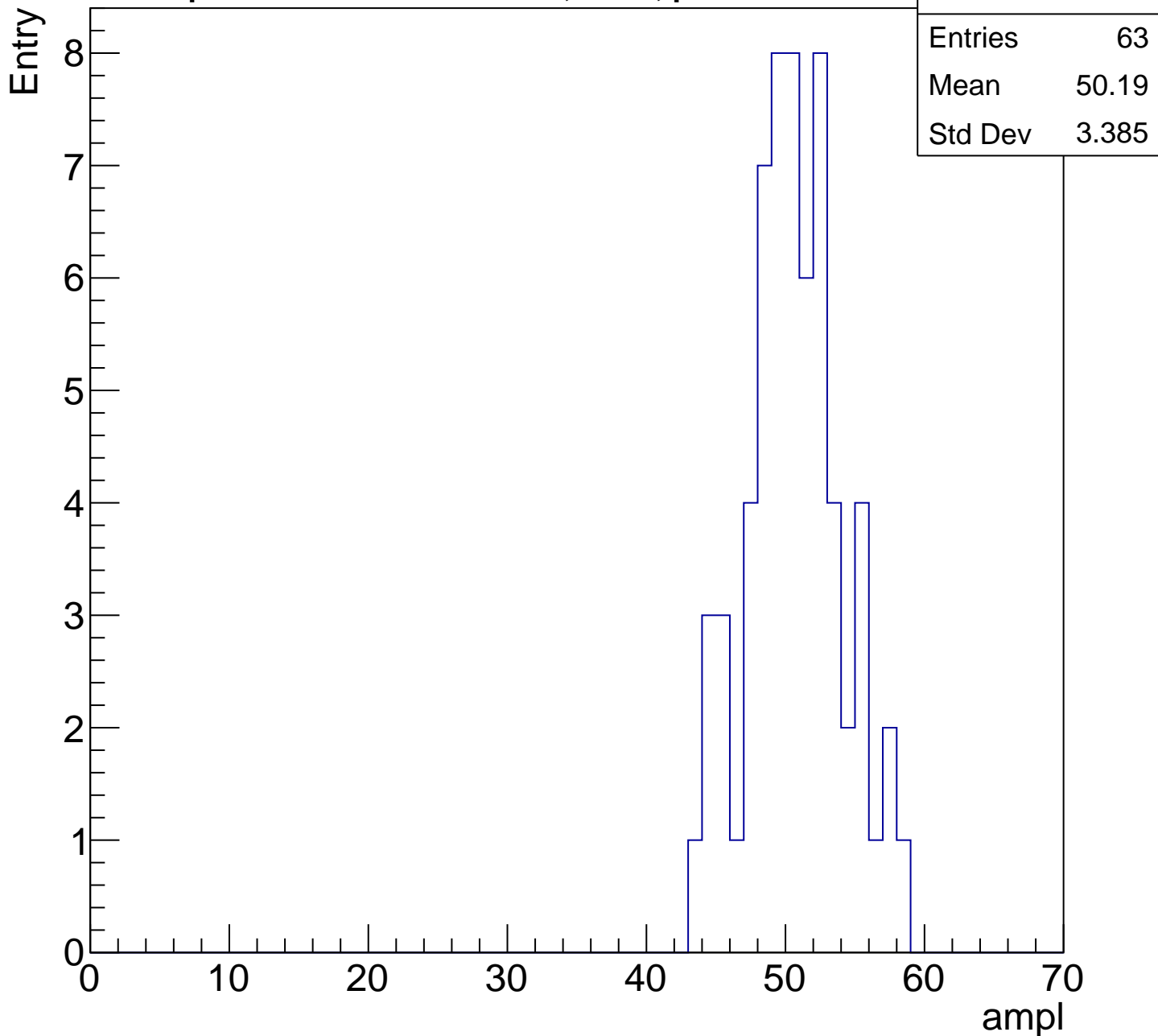
Gaus mean : 44.5854

Gaus Width: 2.9494



B0L001S, U7-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

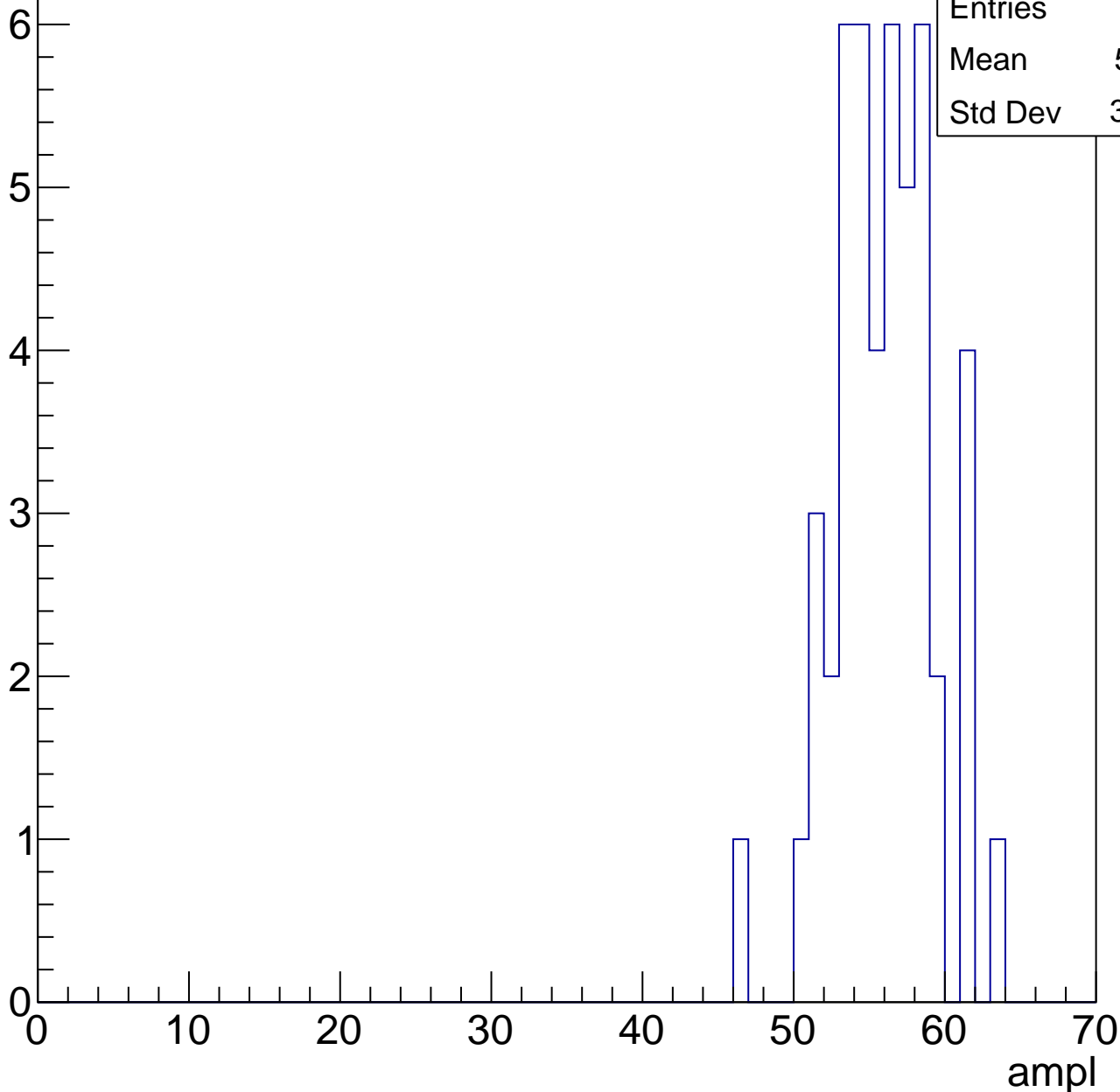


B0L001S, U7-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.51 |
| Std Dev | 3.293 |

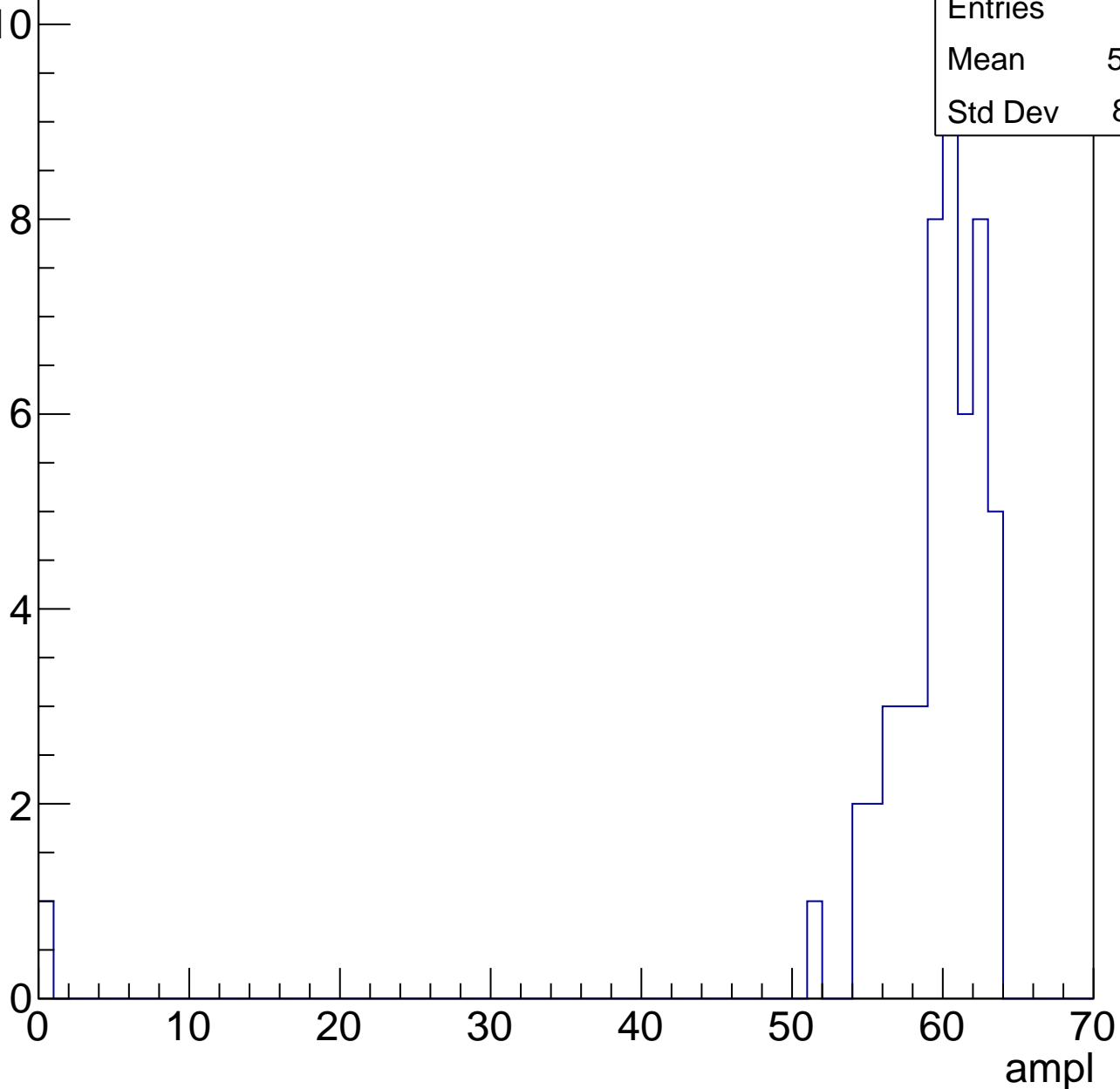


B0L001S, U7-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

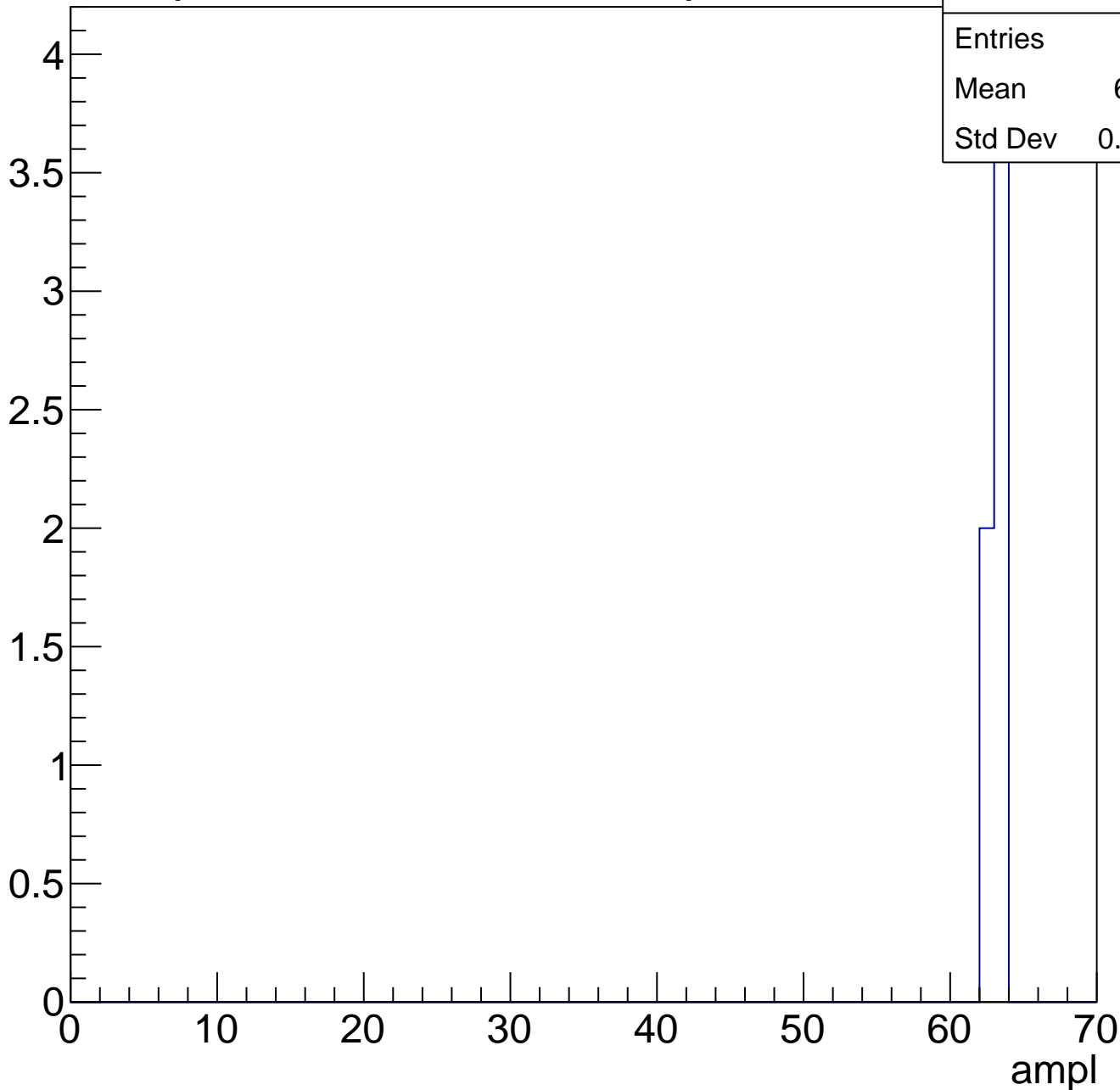
| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.29 |
| Std Dev | 8.581 |



B0L001S, U7-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch49, adc0

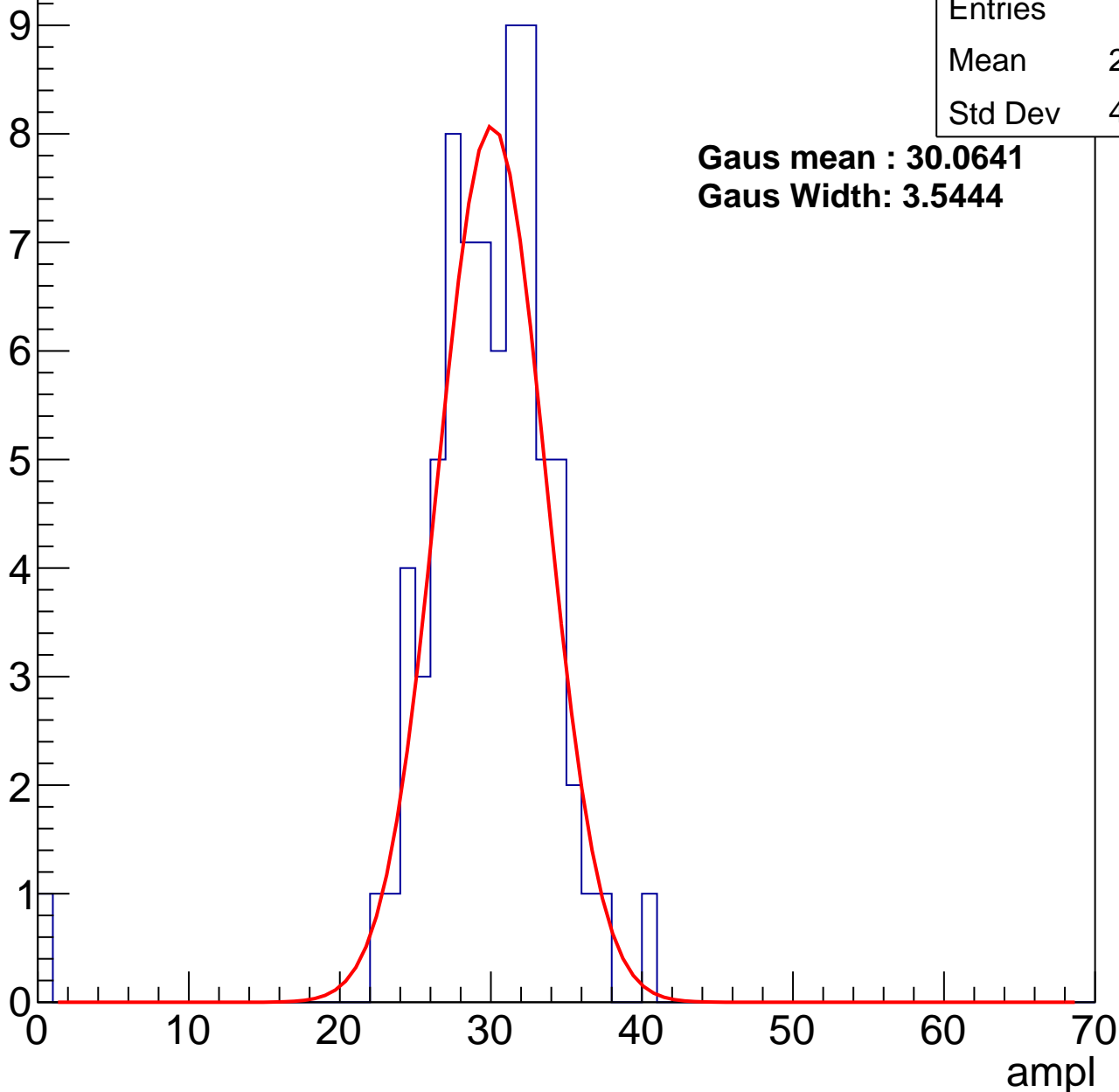
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.29 |
| Std Dev | 4.847 |

Gaus mean : 30.0641

Gaus Width: 3.5444



B0L001S, U7-ch49, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 37.15 |
| Std Dev | 3.233 |

Gaus mean : 37.7303

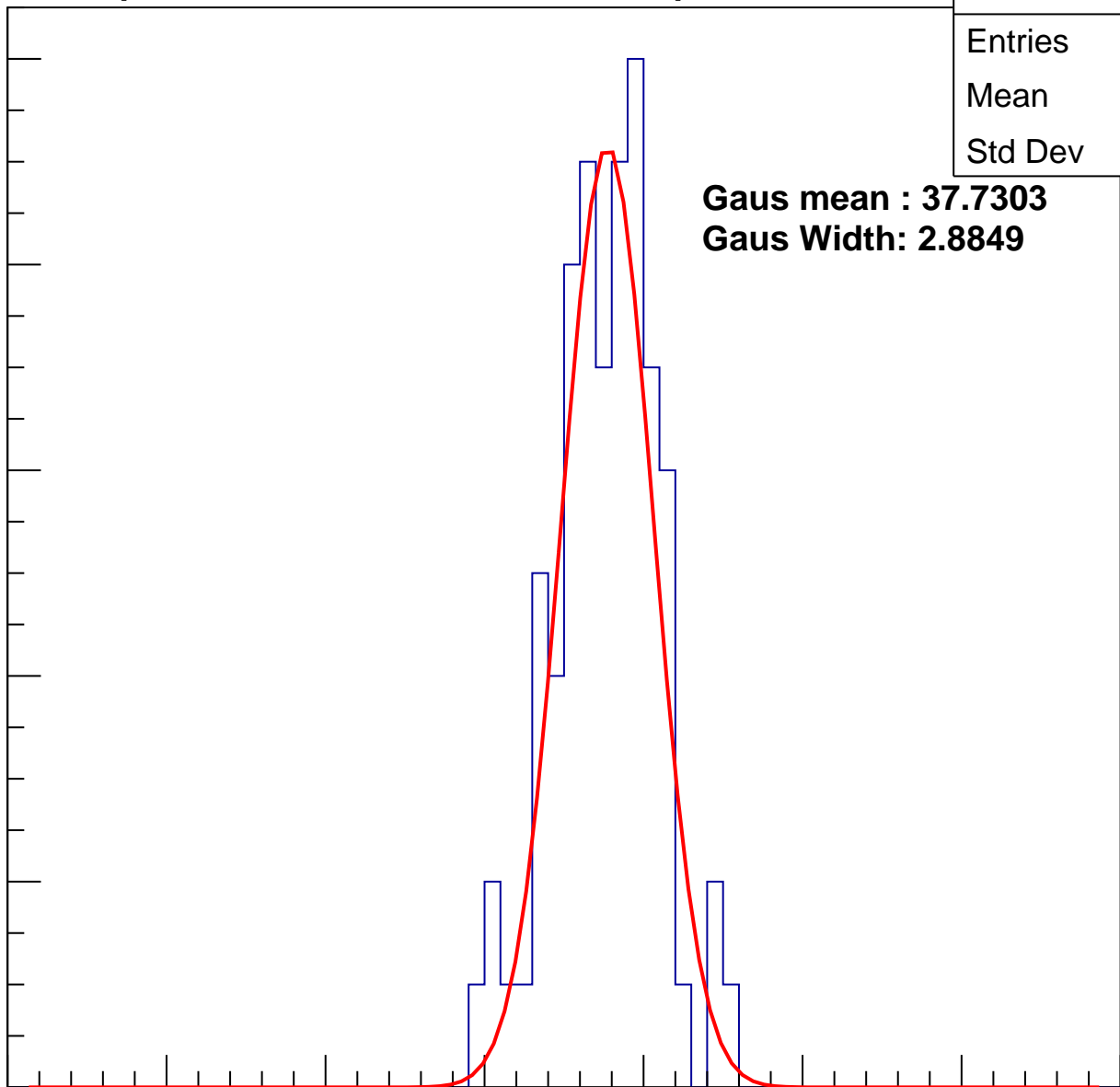
Gaus Width: 2.8849

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch49, adc2

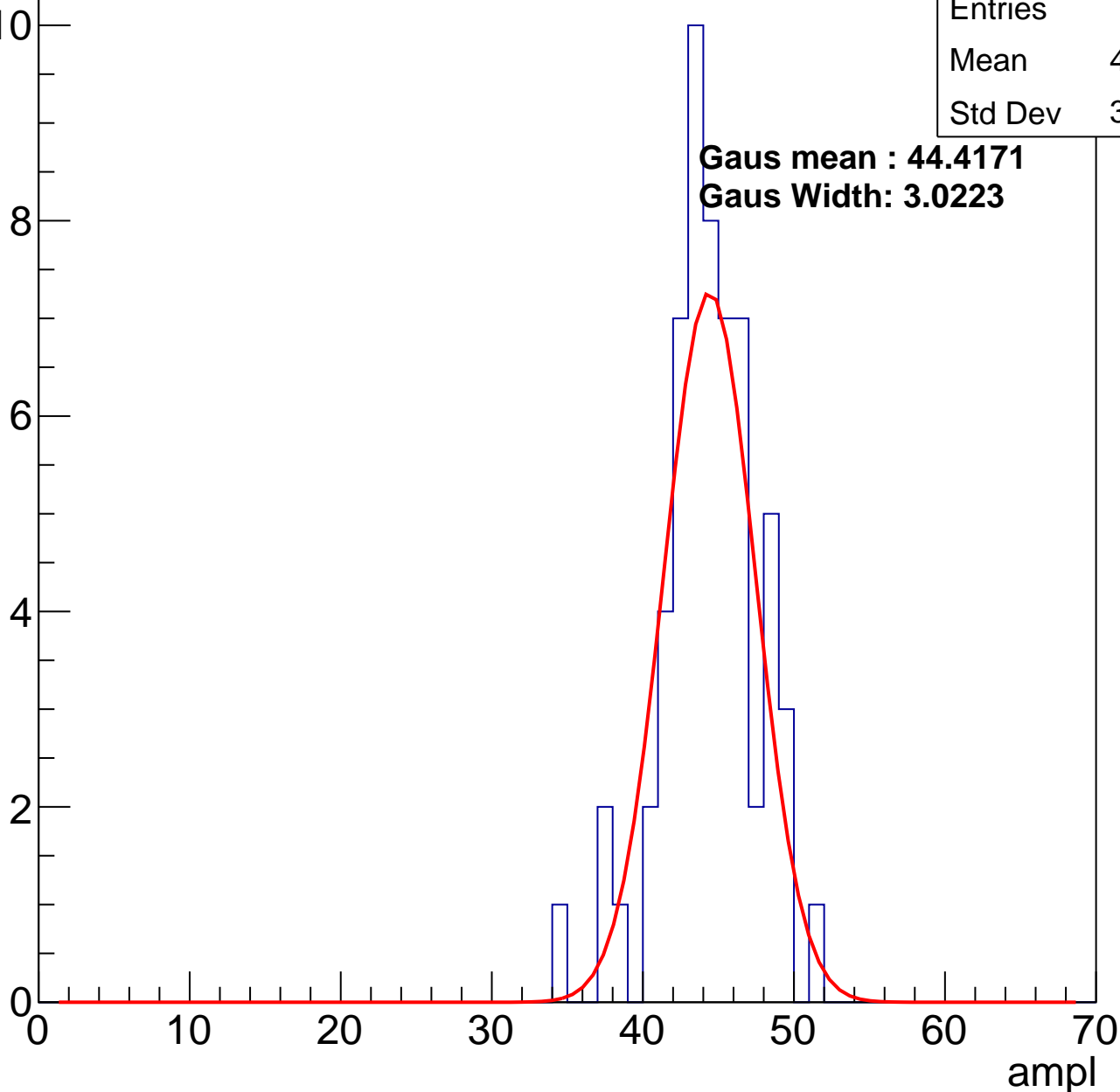
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 43.92 |
| Std Dev | 3.153 |

Gaus mean : 44.4171

Gaus Width: 3.0223



B0L001S, U7-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.65 |
| Std Dev | 3.27 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

2

2

3

5

7

9

7

6

7

1

1

0

0

0

0

0

0

0

0

0

0

0

0

0

3

3

4

6

8

10

8

7

6

1

1

0

0

0

0

0

0

0

0

0

0

0

0

0

3

3

4

6

8

10

8

7

6

1

1

0

0

0

0

0

0

0

0

0

0

0

0

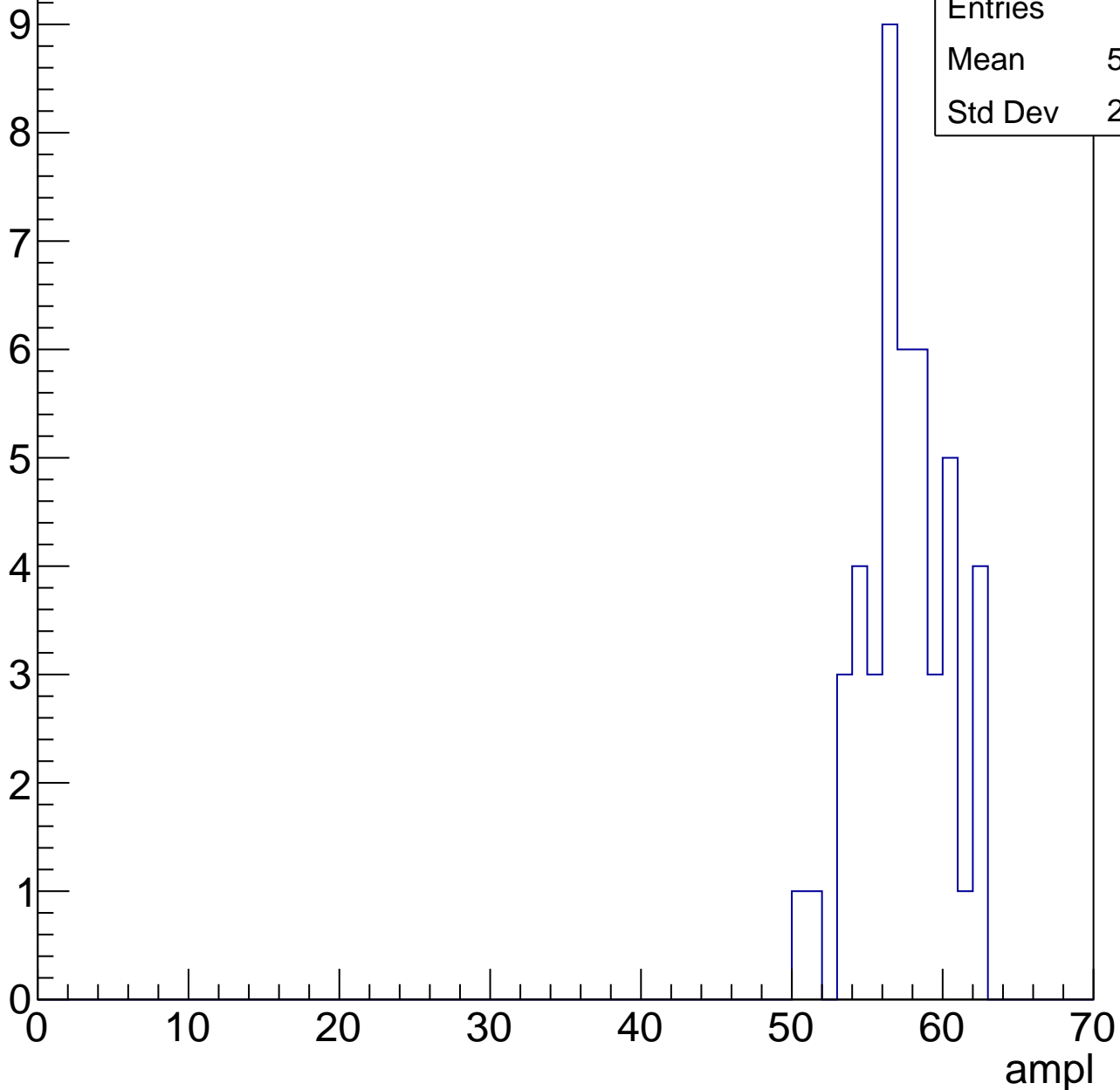
0

B0L001S, U7-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 56.98 |
| Std Dev | 2.832 |

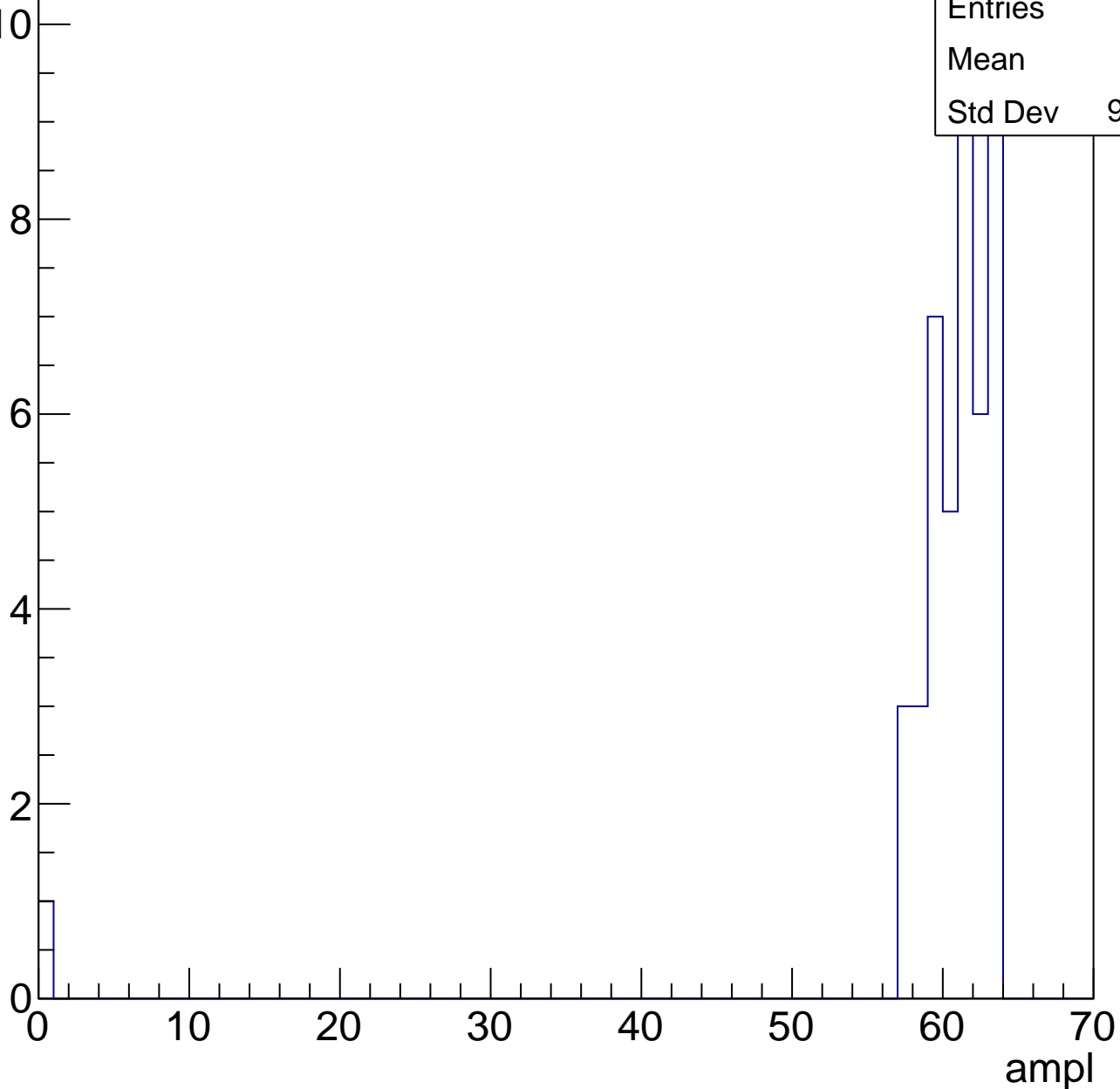


B0L001S, U7-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 59.3 |
| Std Dev | 9.228 |



B0L001S, U7-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch50, adc0

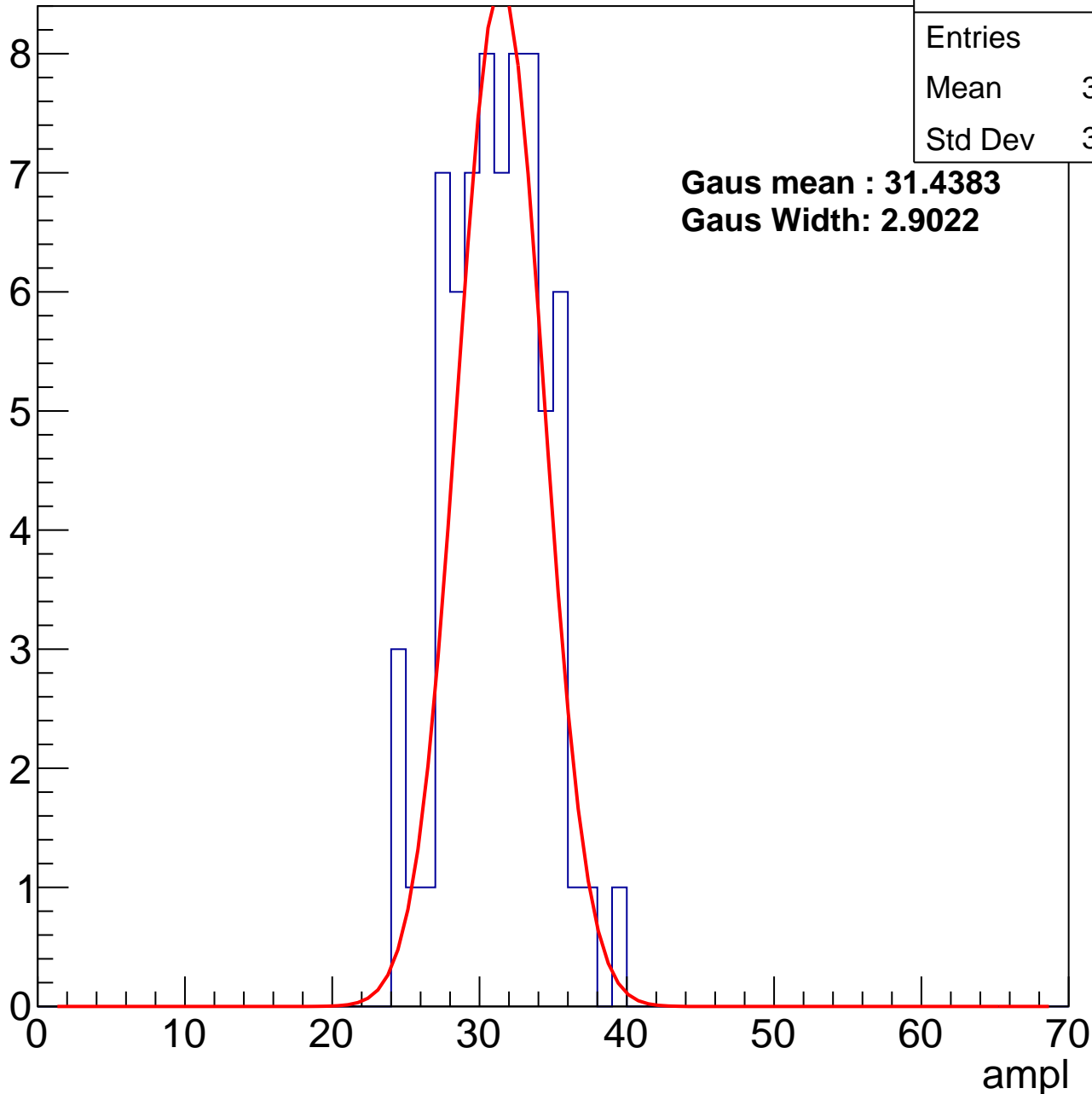
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 30.74 |
| Std Dev | 3.188 |

Gaus mean : 31.4383

Gaus Width: 2.9022



B0L001S, U7-ch50, adc1

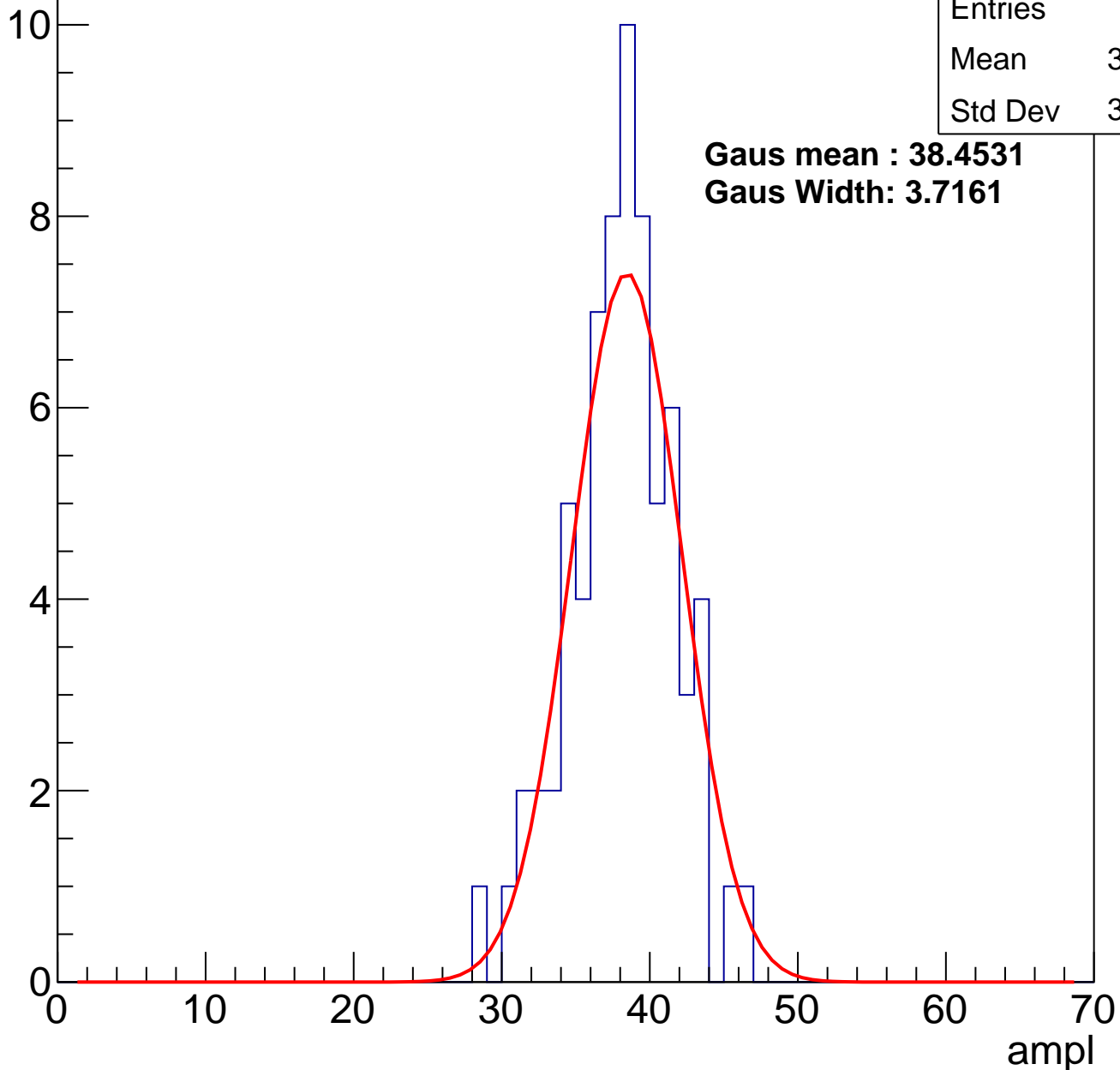
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.64 |
| Std Dev | 3.526 |

Gaus mean : 38.4531

Gaus Width: 3.7161

Entry



B0L001S, U7-ch50, adc2

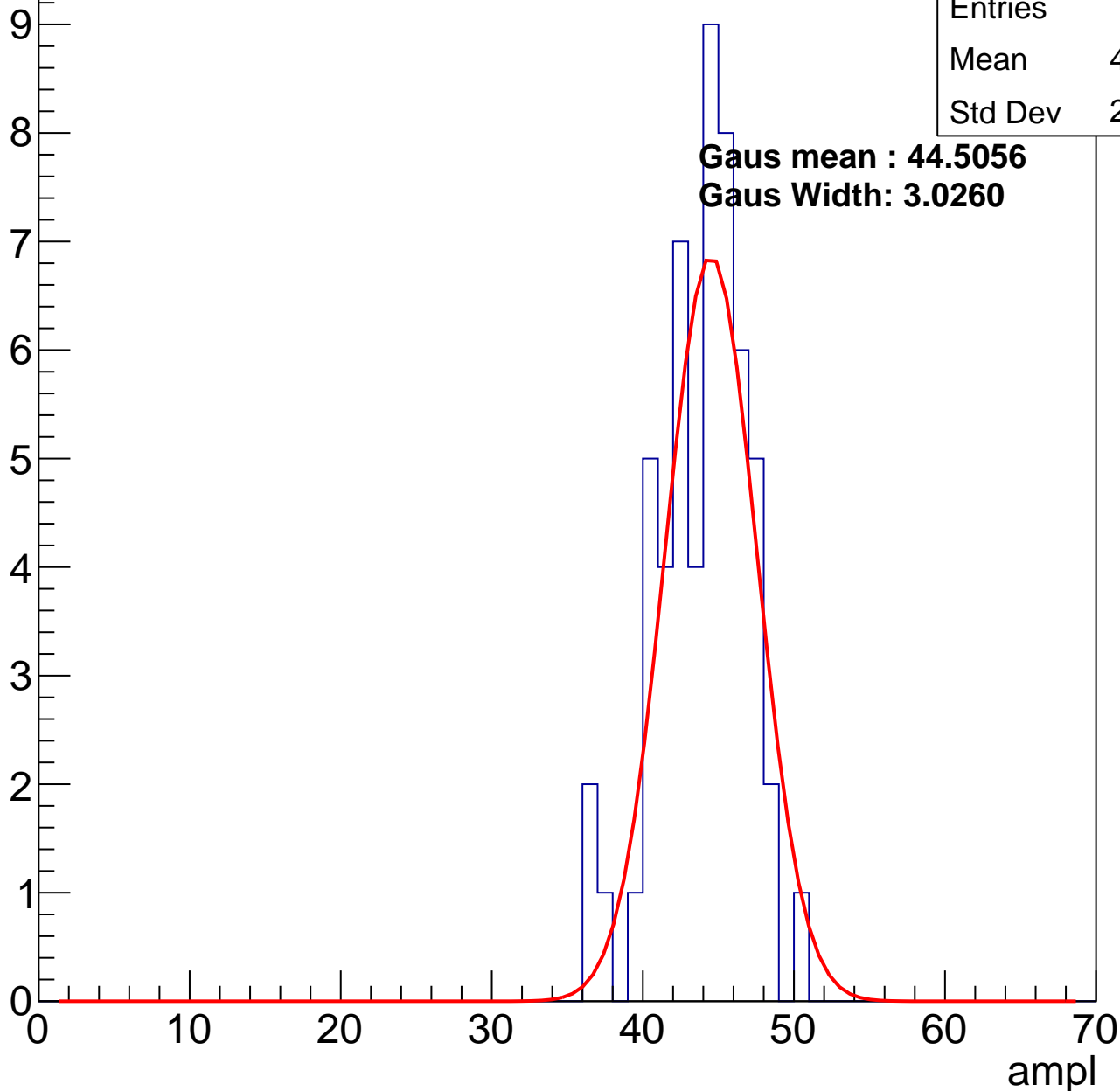
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 43.47 |
| Std Dev | 2.953 |

Gaus mean : 44.5056

Gaus Width: 3.0260

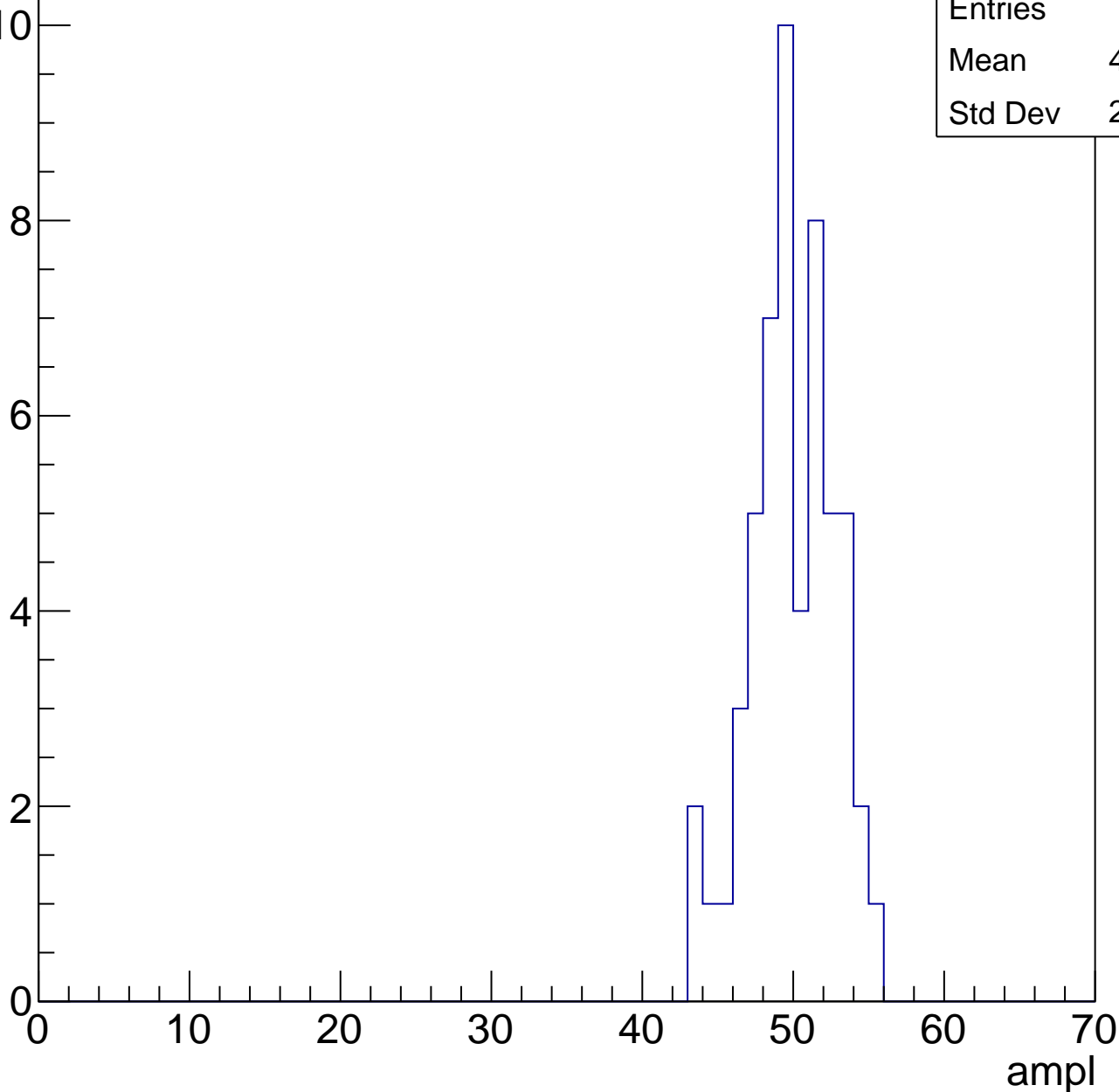


B0L001S, U7-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 49.44 |
| Std Dev | 2.733 |

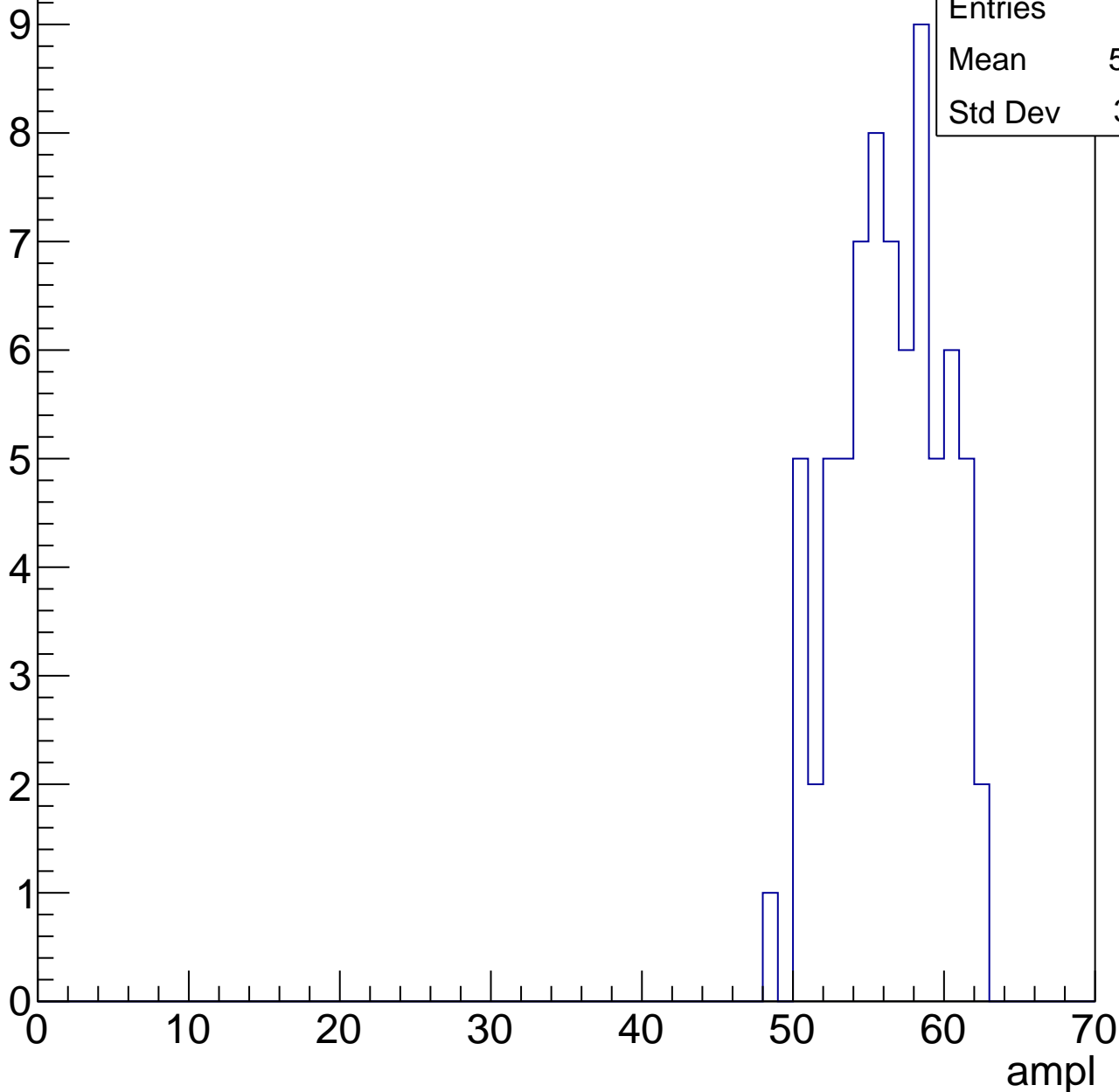


B0L001S, U7-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 55.93 |
| Std Dev | 3.381 |

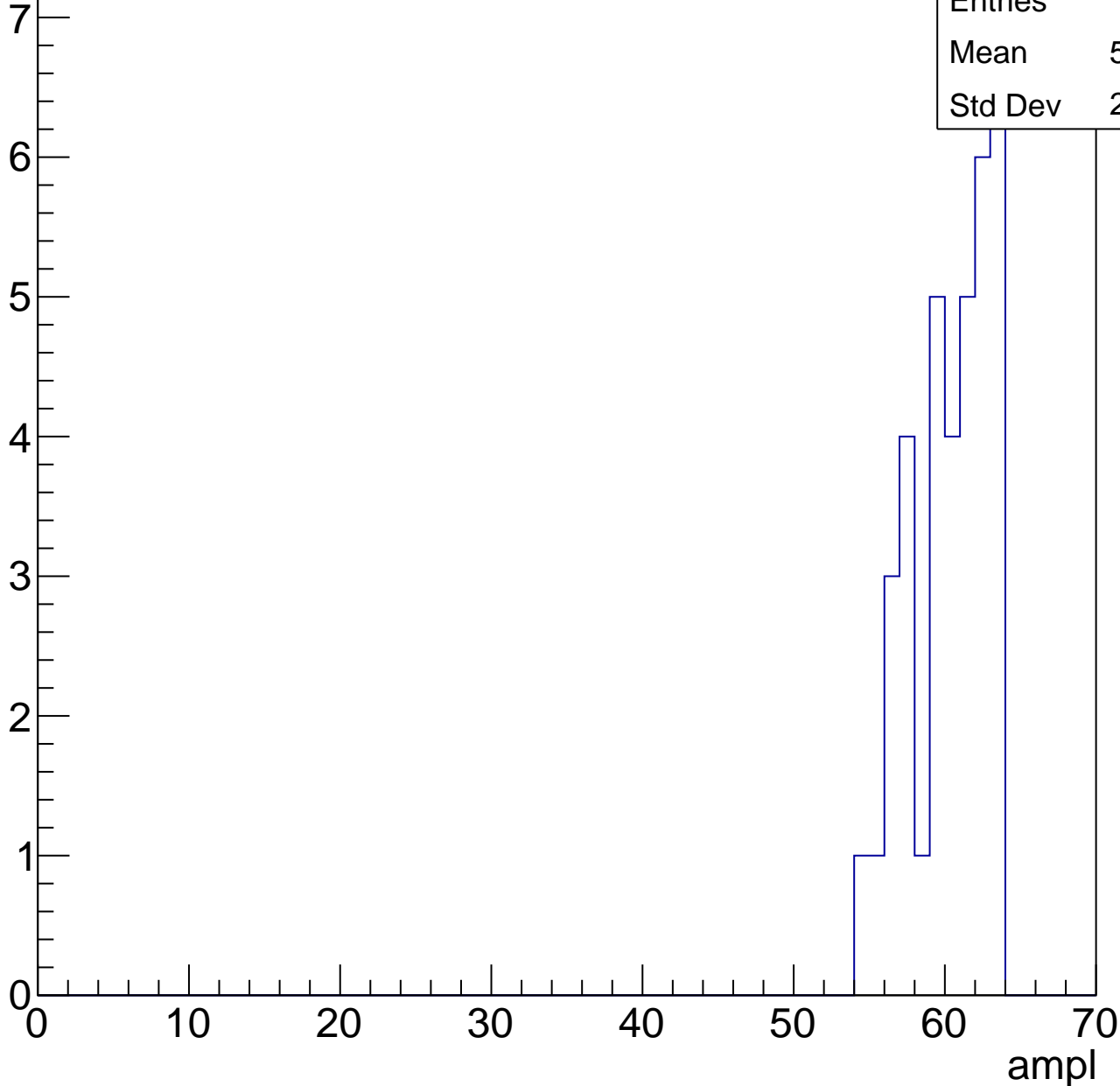


B0L001S, U7-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

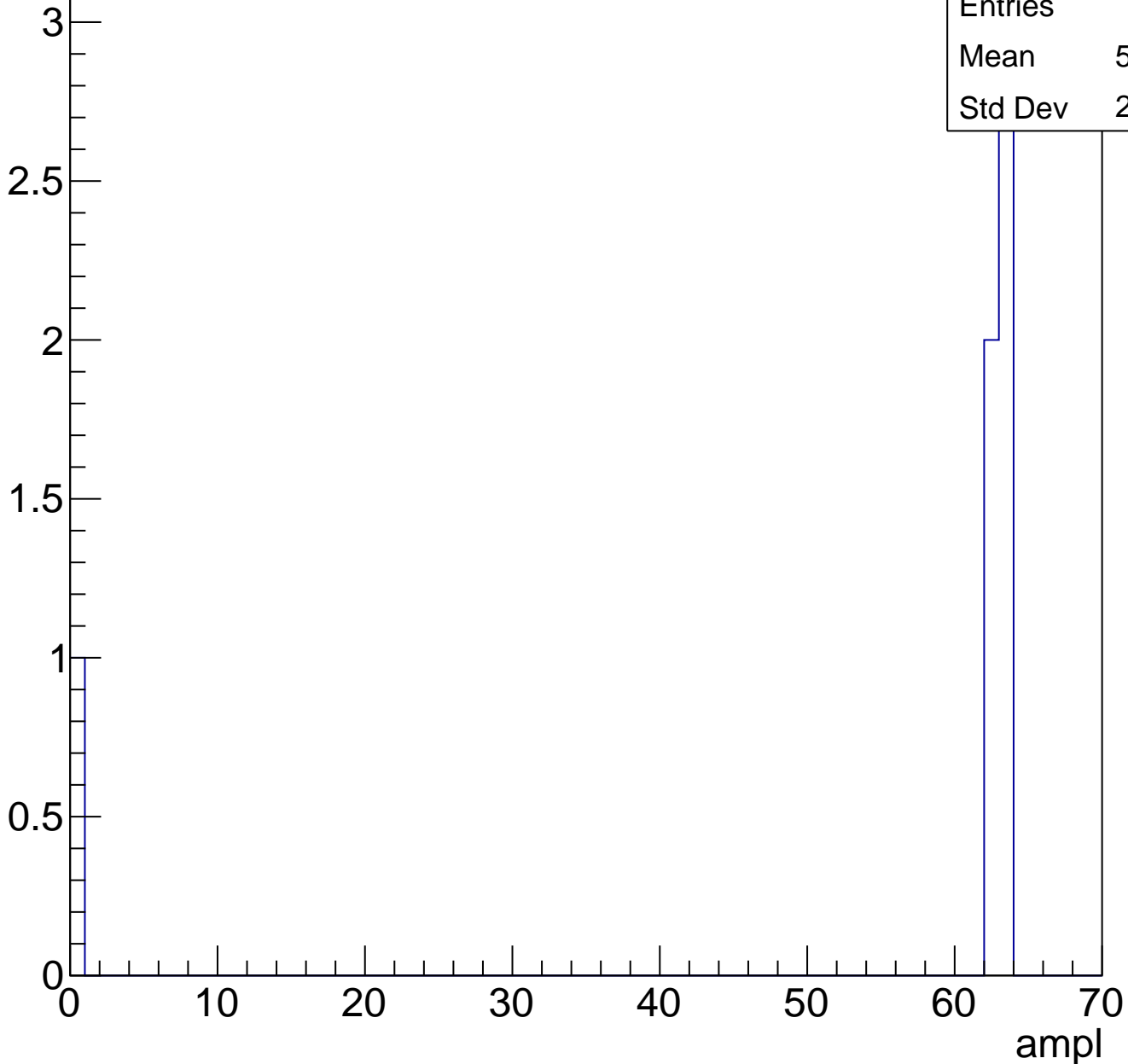
| | |
|---------|-------|
| Entries | 37 |
| Mean | 59.89 |
| Std Dev | 2.576 |



B0L001S, U7-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch51, adc0

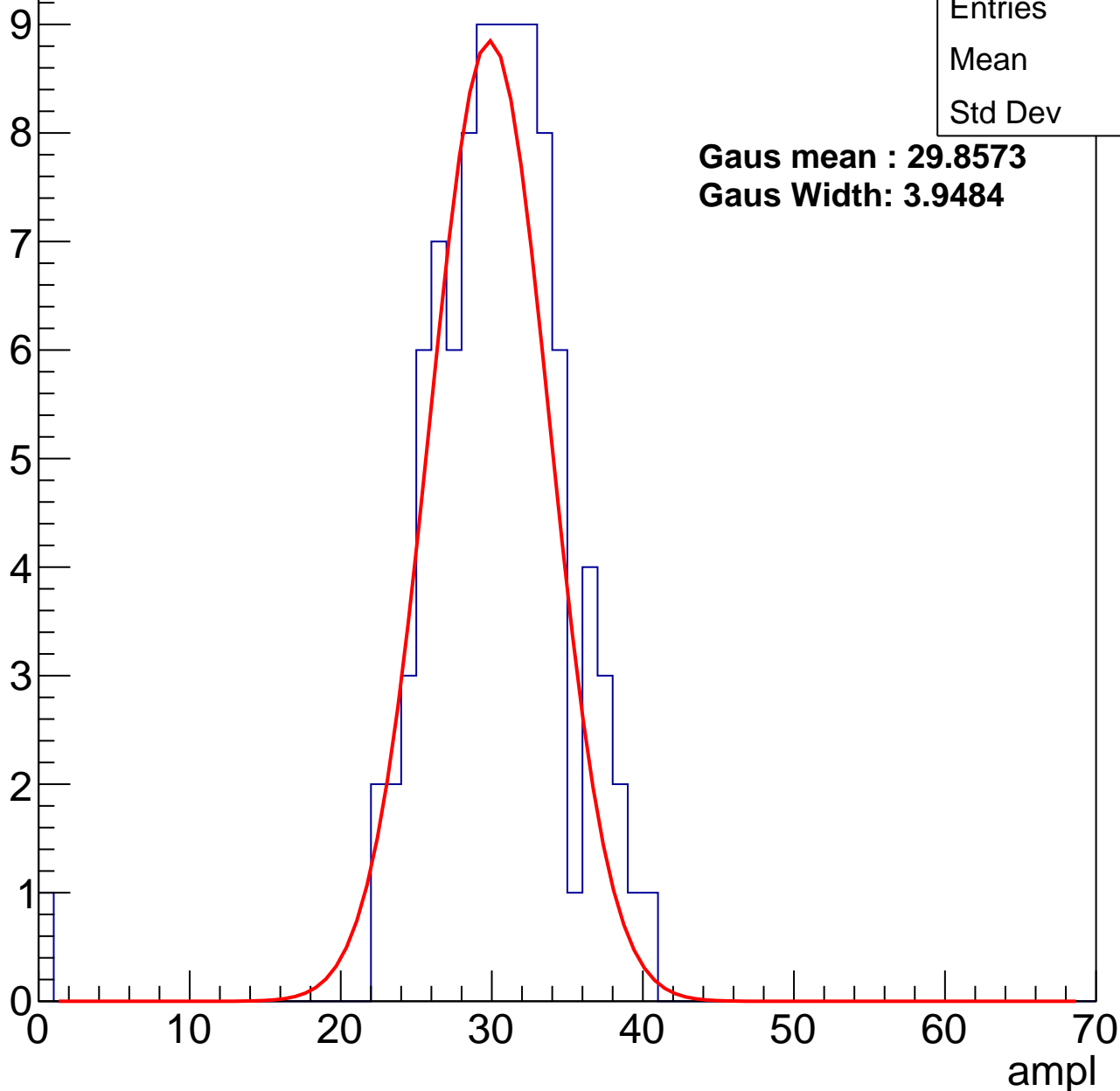
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 97 |
| Mean | 29.8 |
| Std Dev | 5 |

Gaus mean : 29.8573

Gaus Width: 3.9484



B0L001S, U7-ch51, adc1

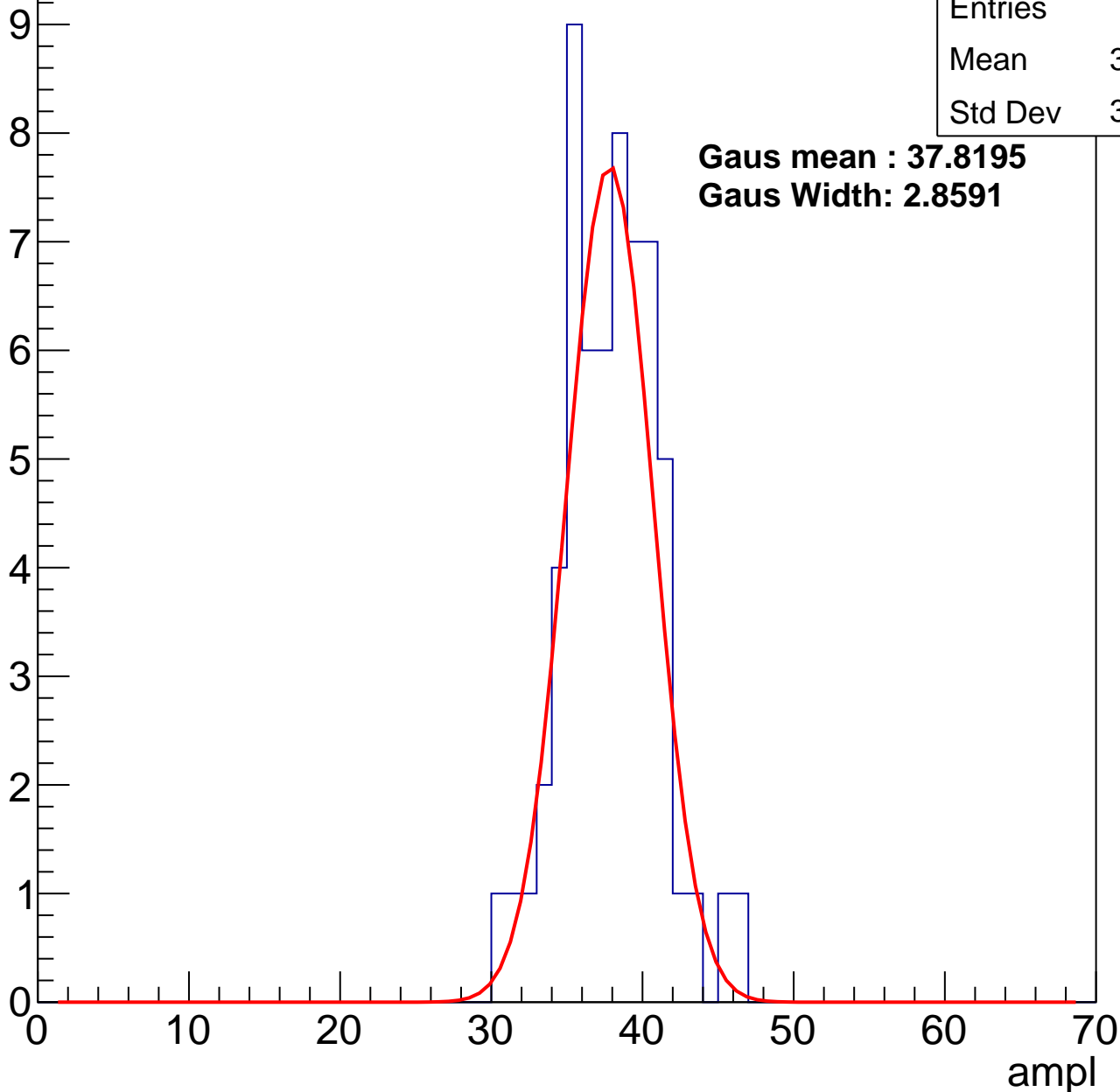
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 37.48 |
| Std Dev | 3.113 |

Gaus mean : 37.8195

Gaus Width: 2.8591



B0L001S, U7-ch51, adc2

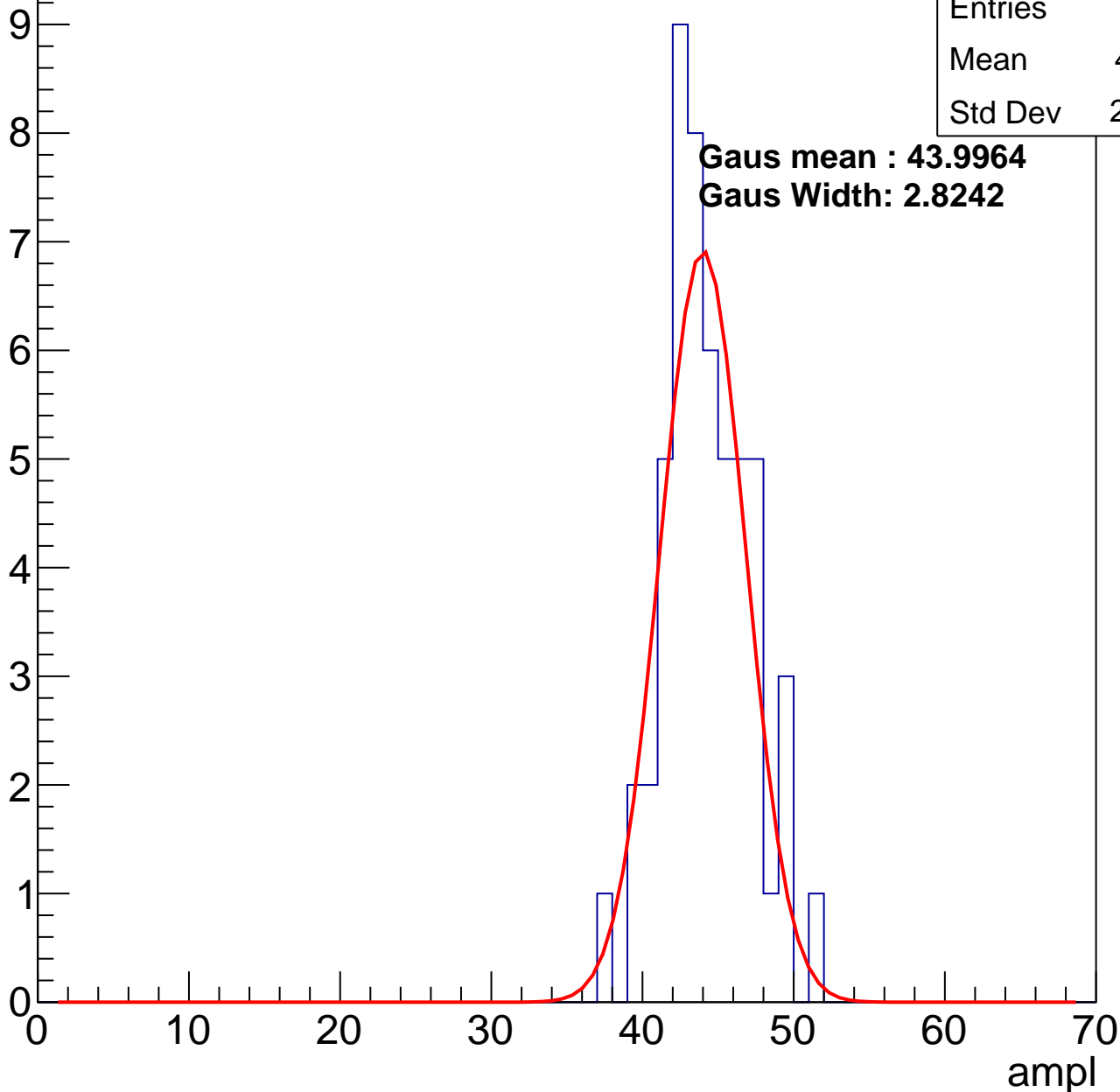
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 43.81 |
| Std Dev | 2.842 |

Gaus mean : 43.9964

Gaus Width: 2.8242



B0L001S, U7-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

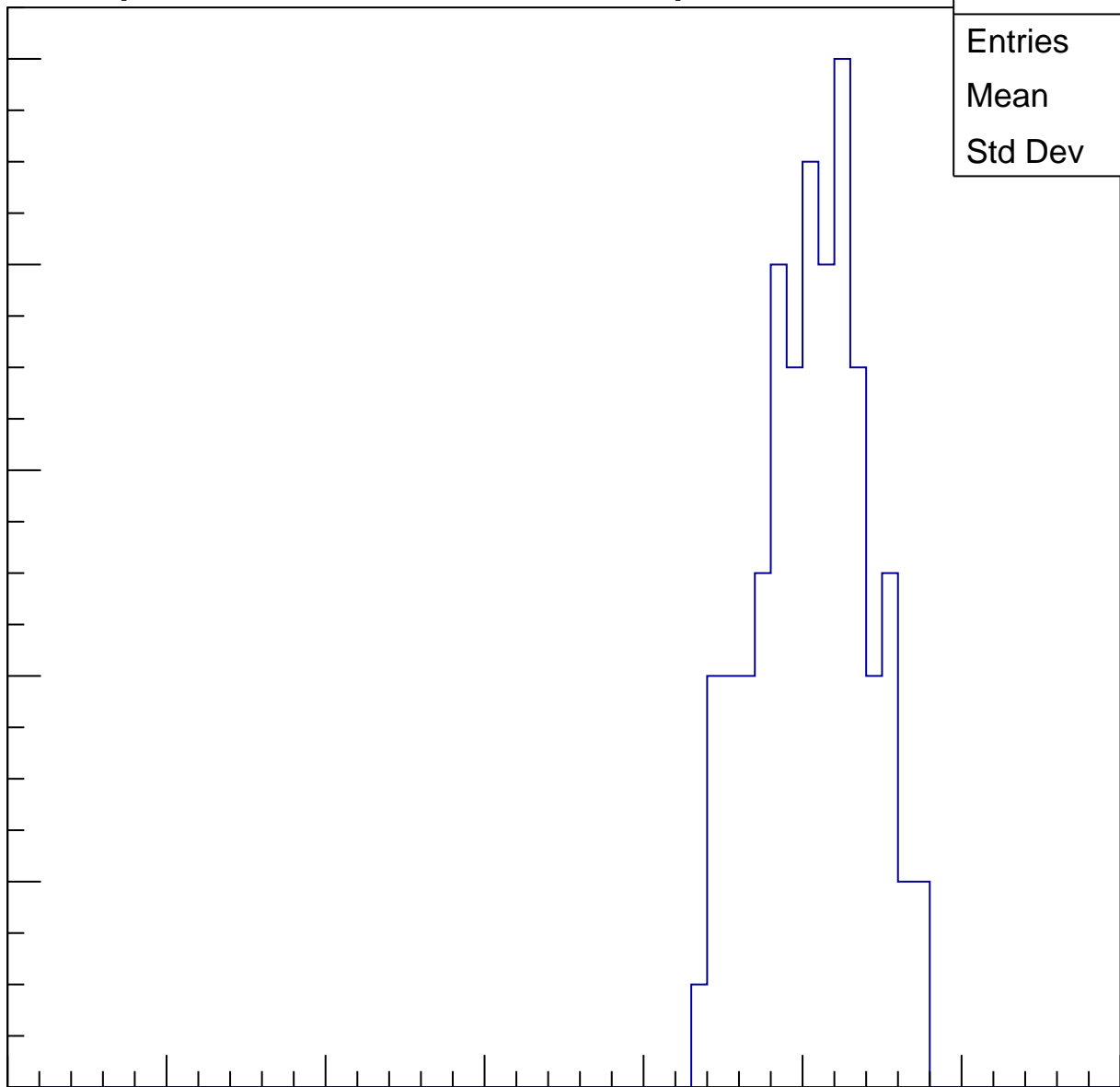
| | |
|---------|-------|
| Entries | 80 |
| Mean | 50.14 |
| Std Dev | 3.372 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

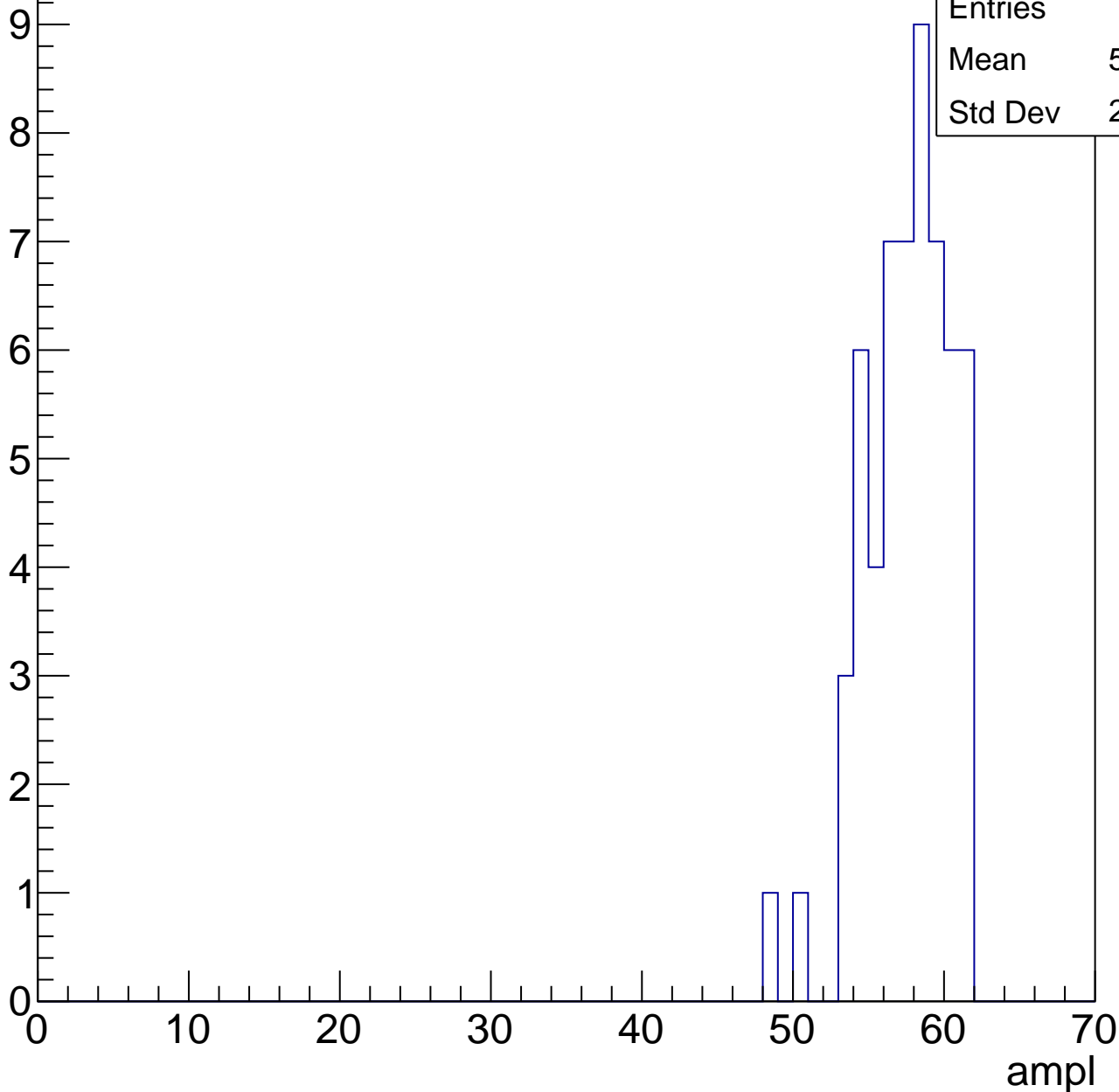


B0L001S, U7-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.07 |
| Std Dev | 2.784 |

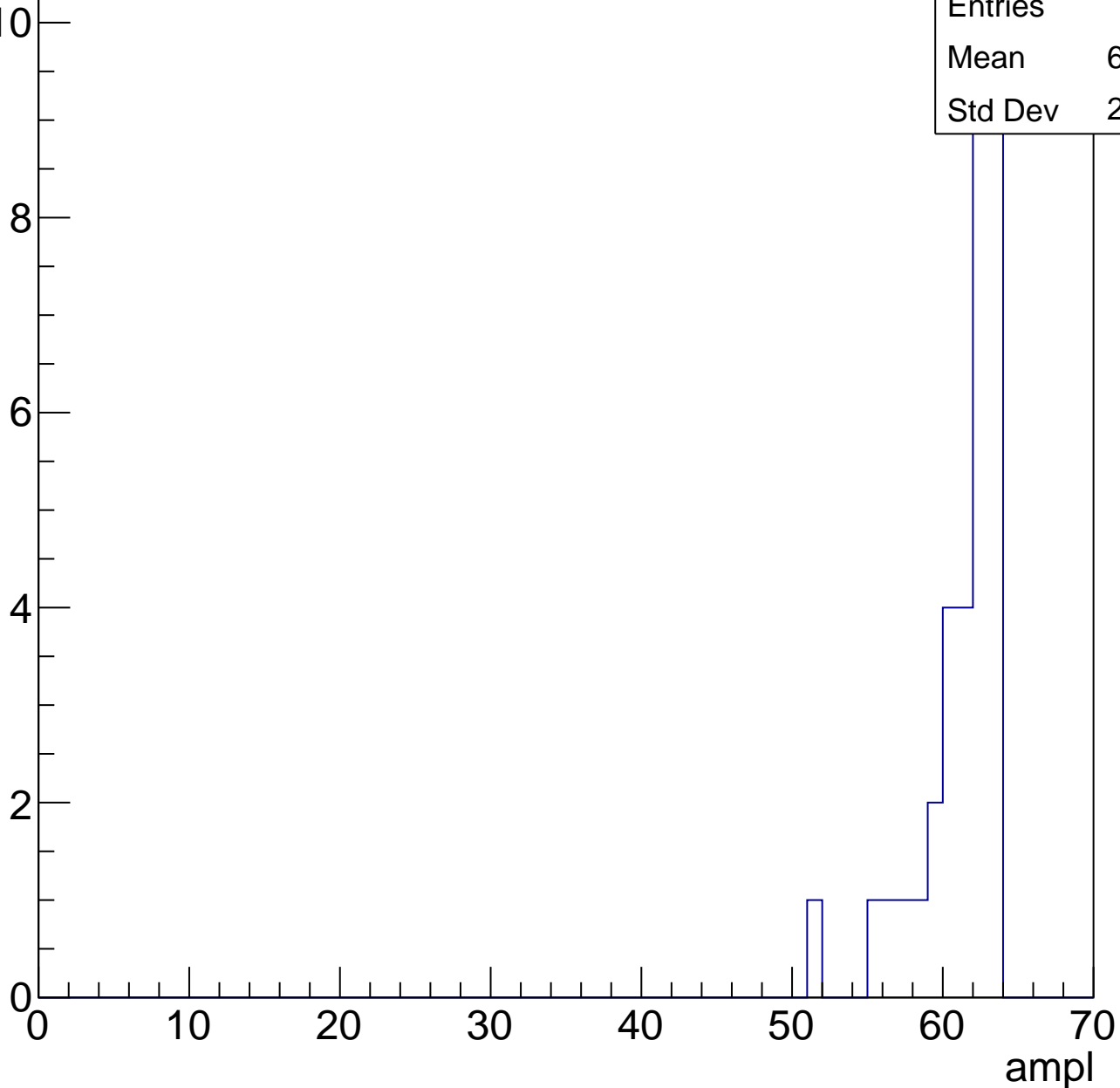


B0L001S, U7-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

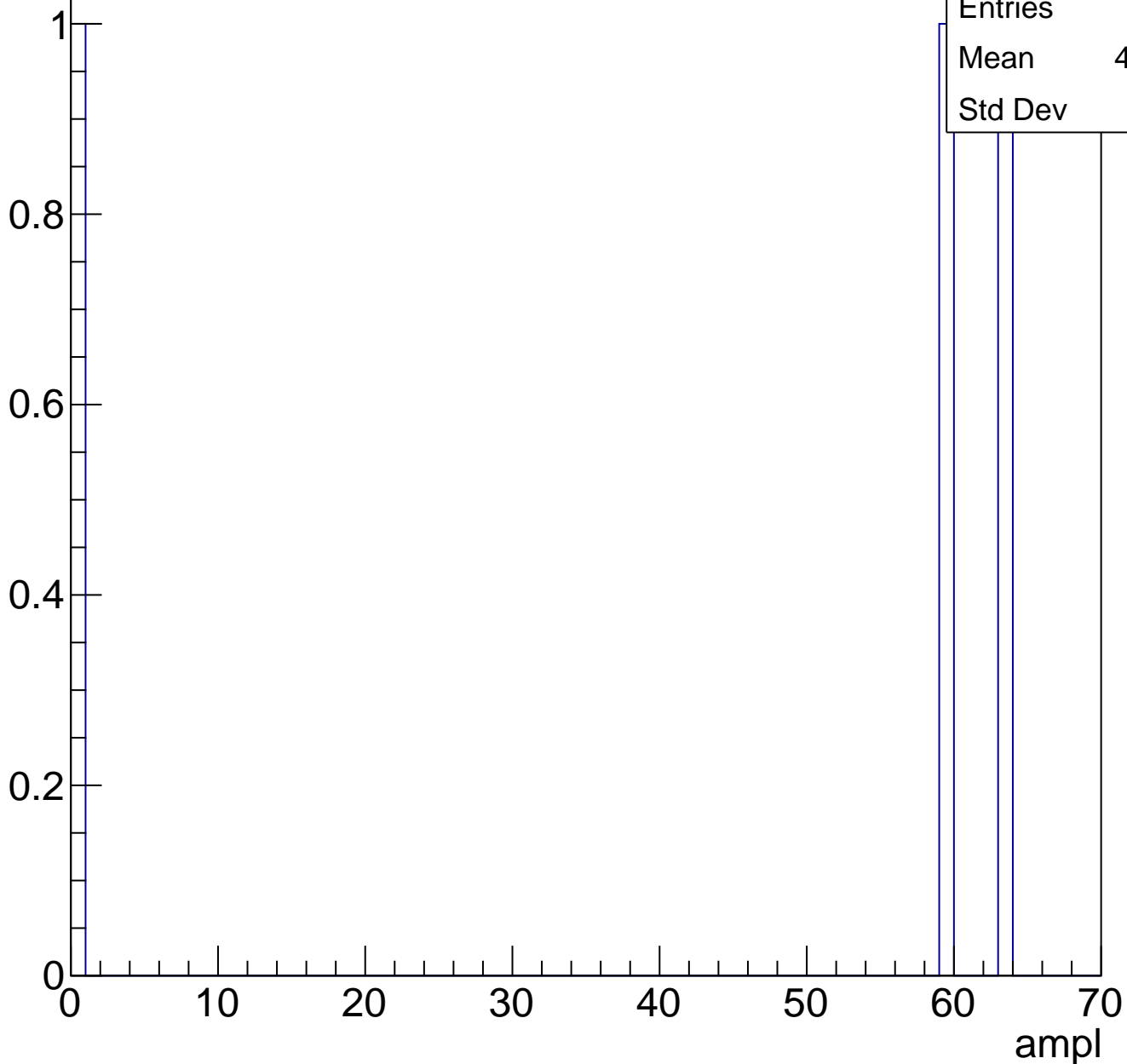
| | |
|---------|-------|
| Entries | 34 |
| Mean | 60.76 |
| Std Dev | 2.669 |



B0L001S, U7-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

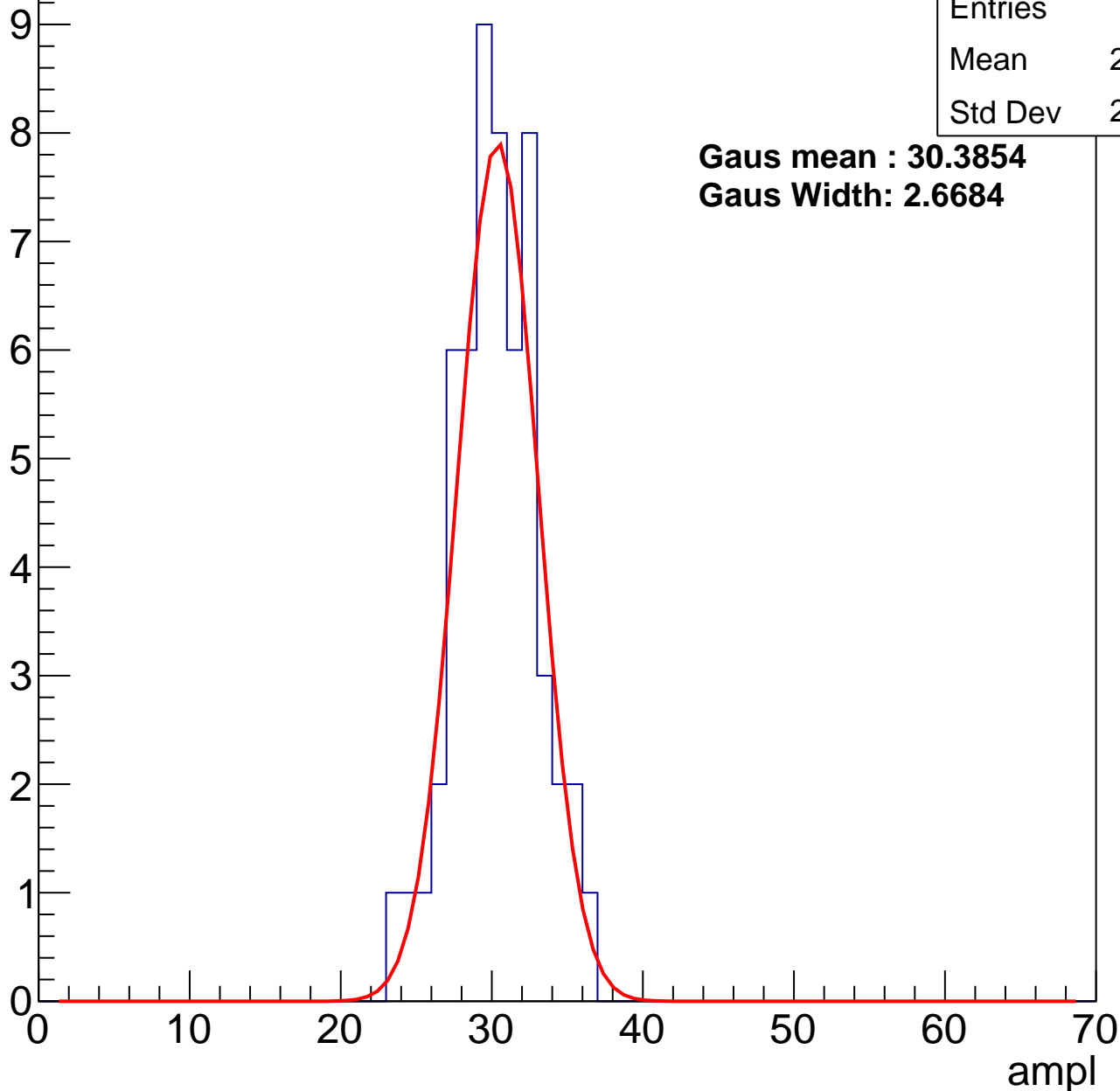


| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch52, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

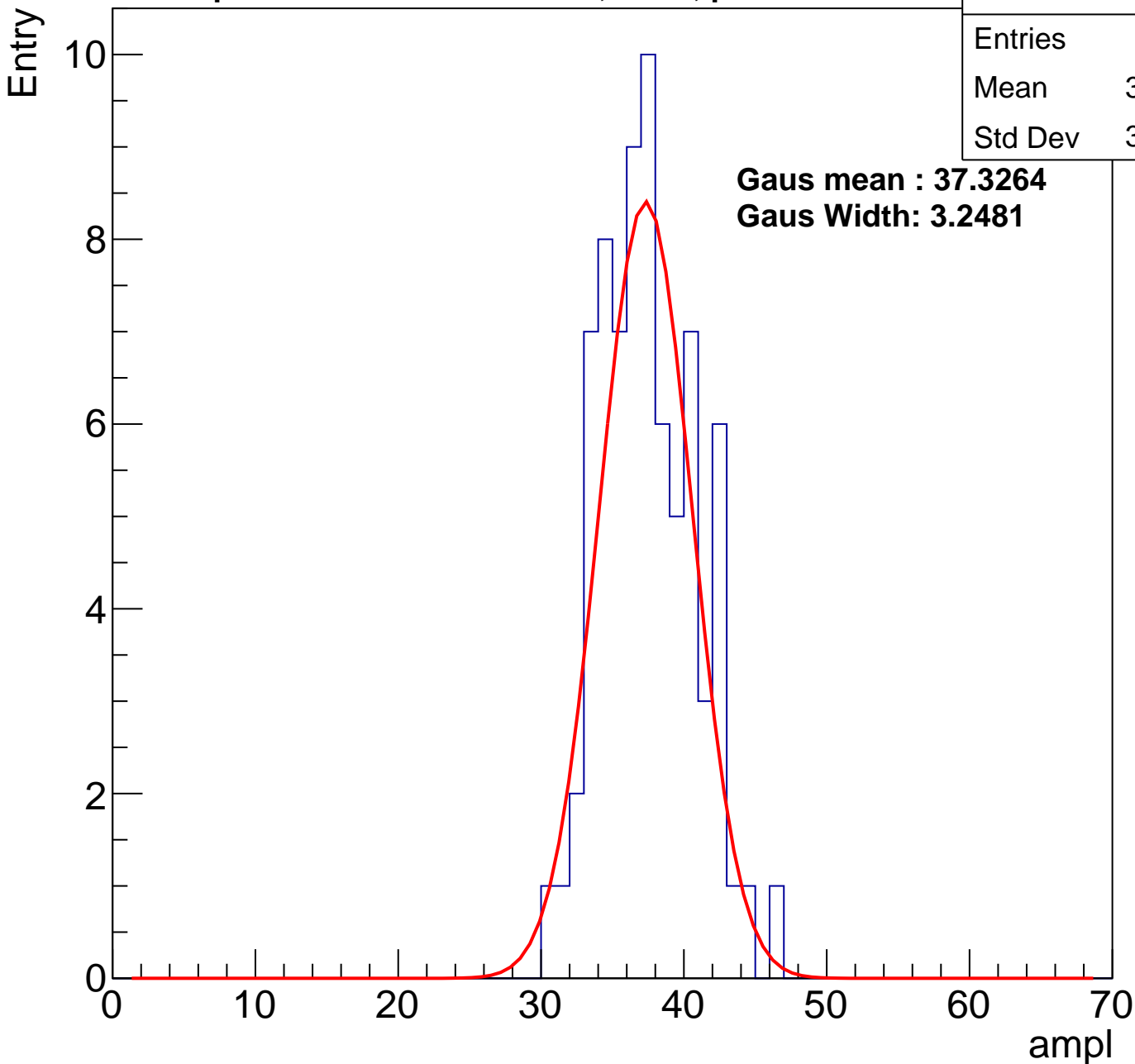


B0L001S, U7-ch52, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 75 |
| Mean | 37.04 |
| Std Dev | 3.284 |

Gaus mean : 37.3264
Gaus Width: 3.2481



B0L001S, U7-ch52, adc2

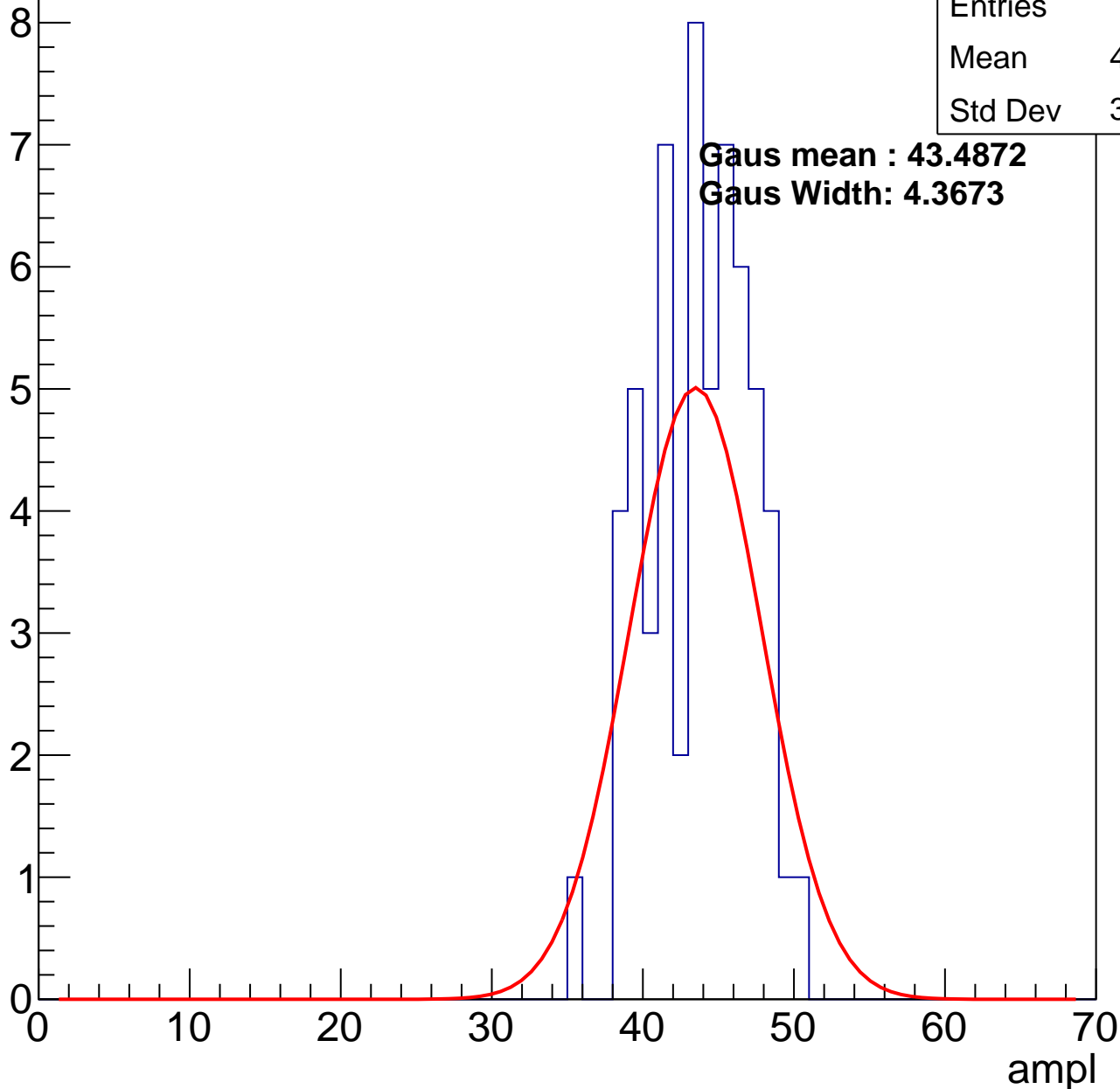
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 43.29 |
| Std Dev | 3.314 |

Gaus mean : 43.4872

Gaus Width: 4.3673

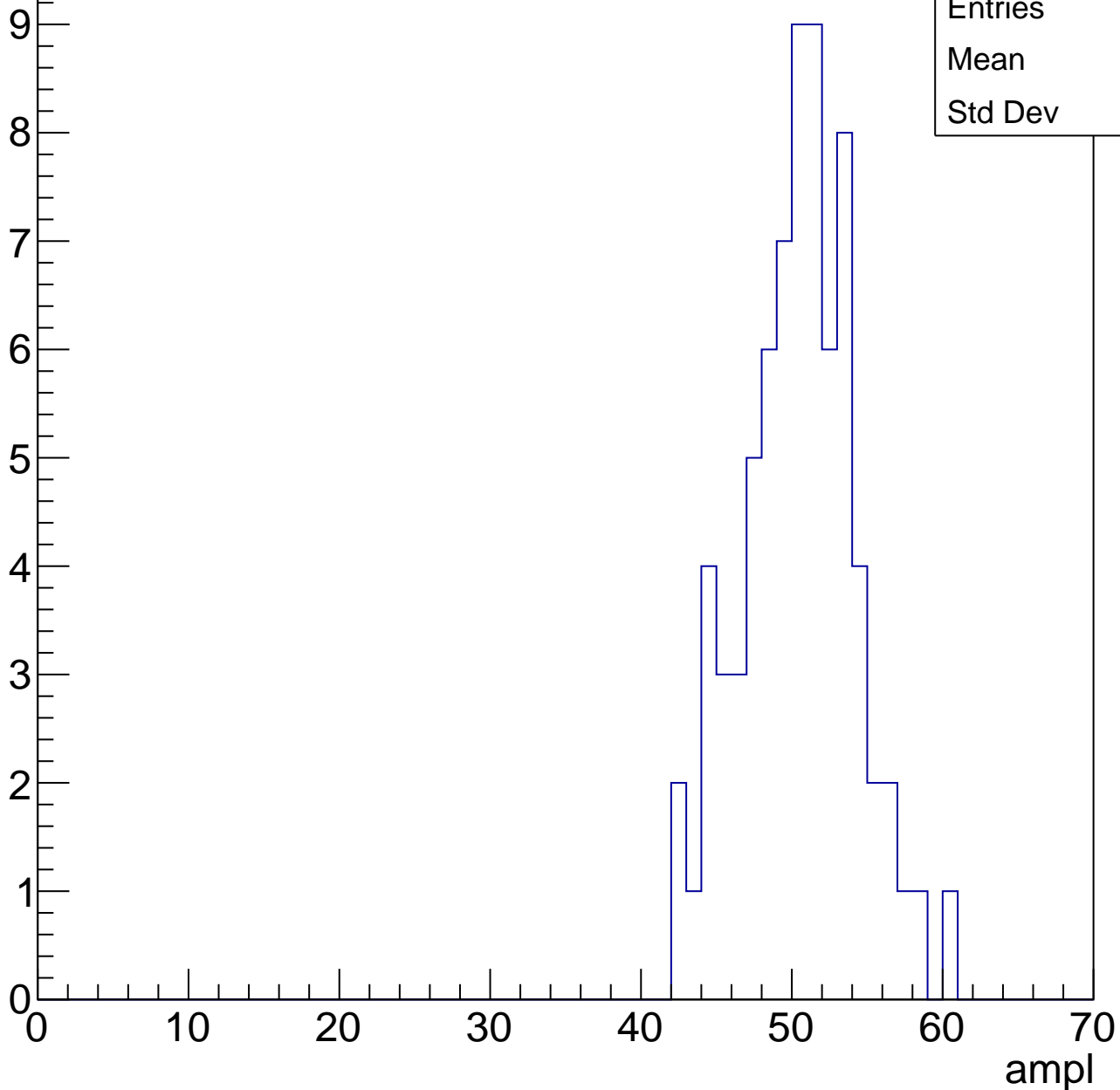


B0L001S, U7-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 74 |
| Mean | 50 |
| Std Dev | 3.72 |

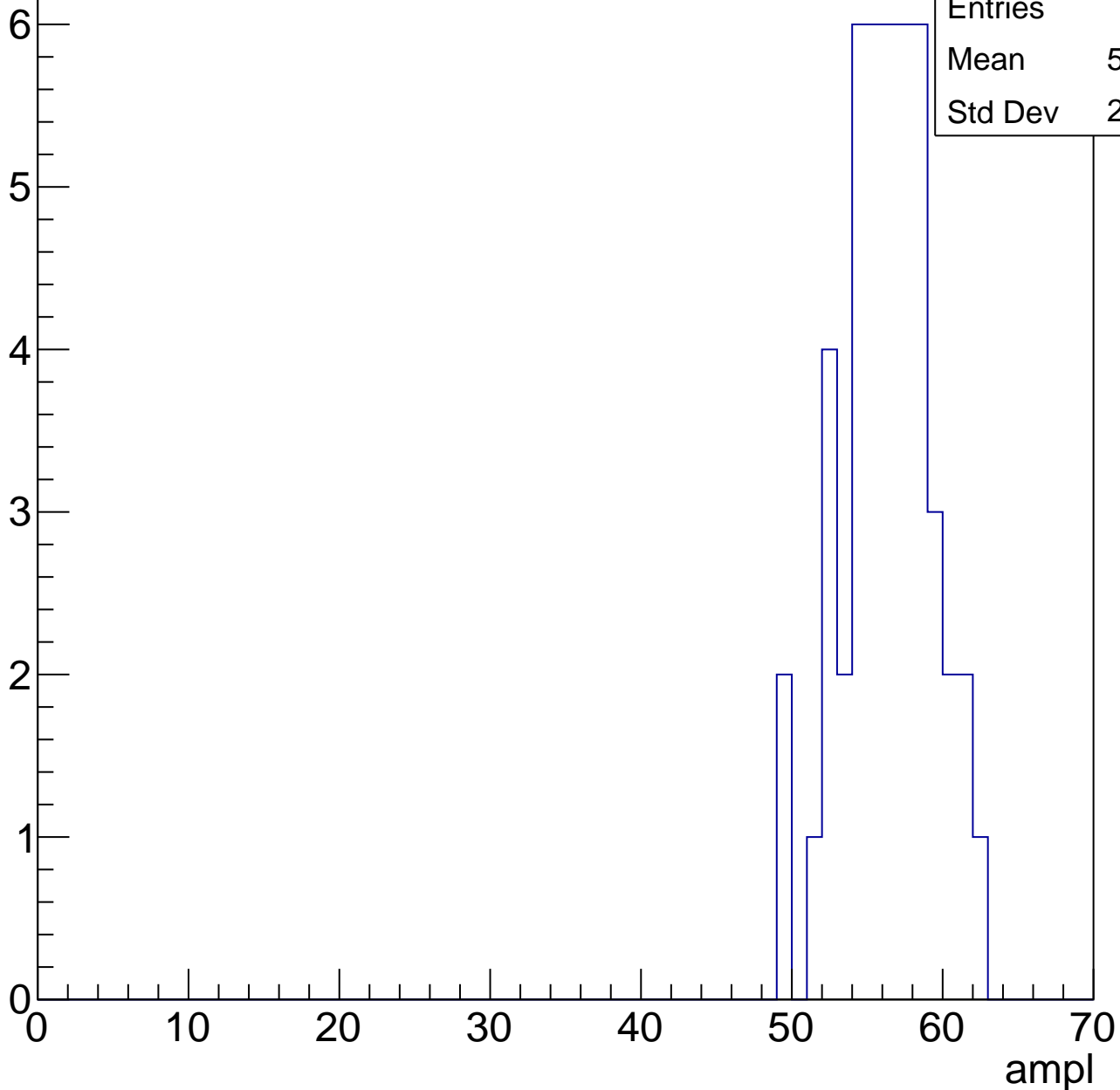


B0L001S, U7-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.83 |
| Std Dev | 2.949 |

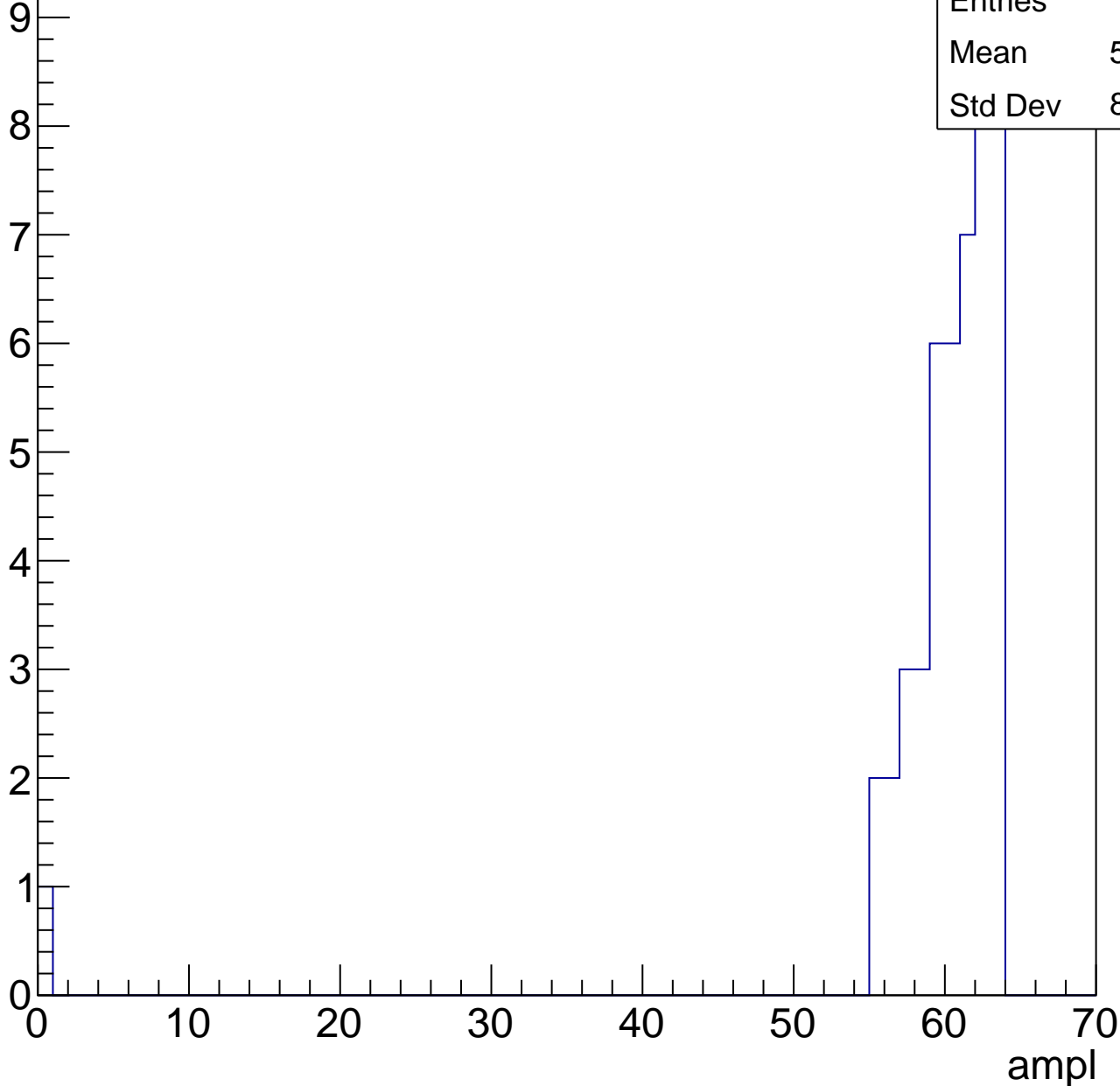


B0L001S, U7-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

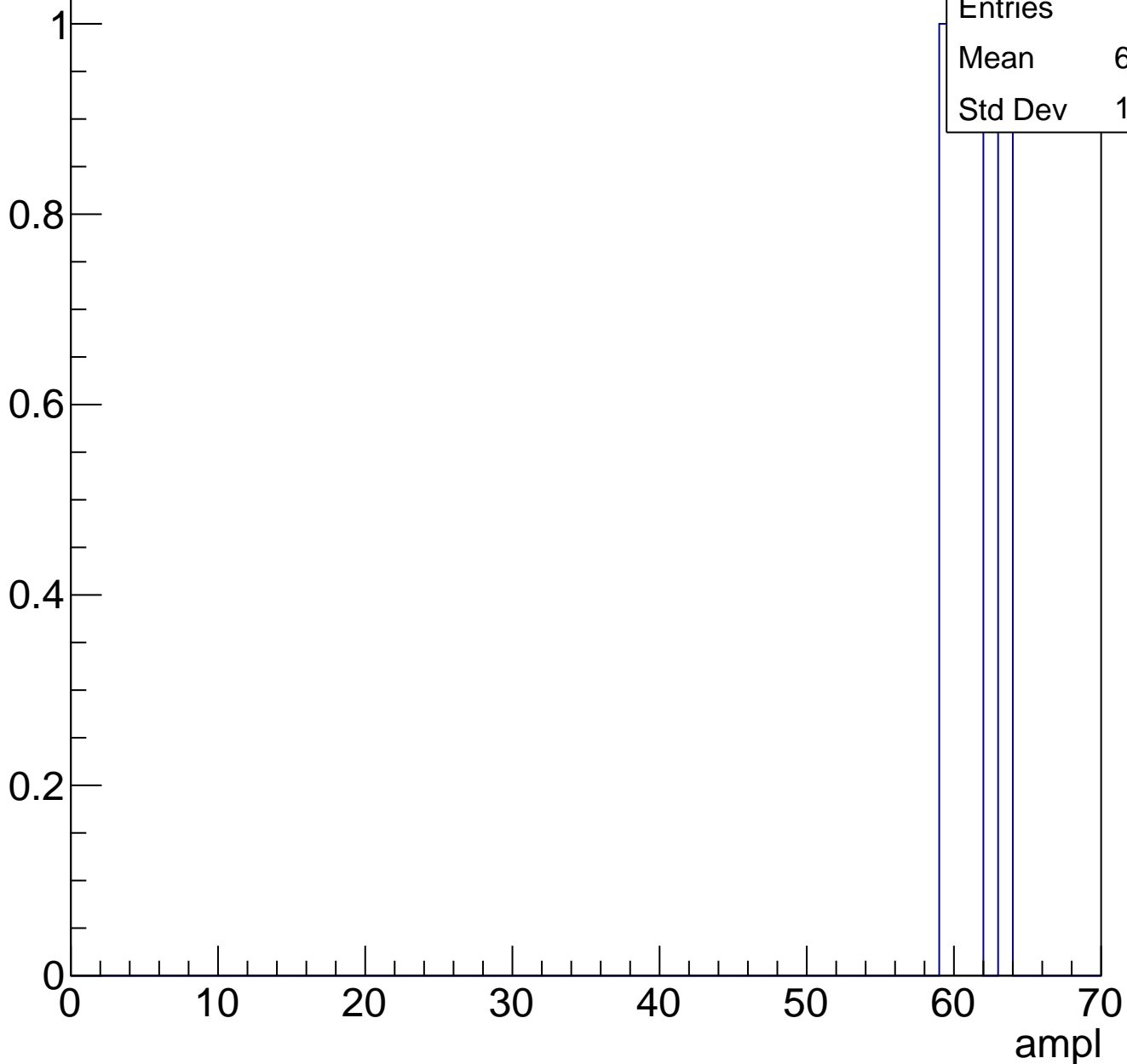
| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.02 |
| Std Dev | 8.903 |



B0L001S, U7-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch53, adc0

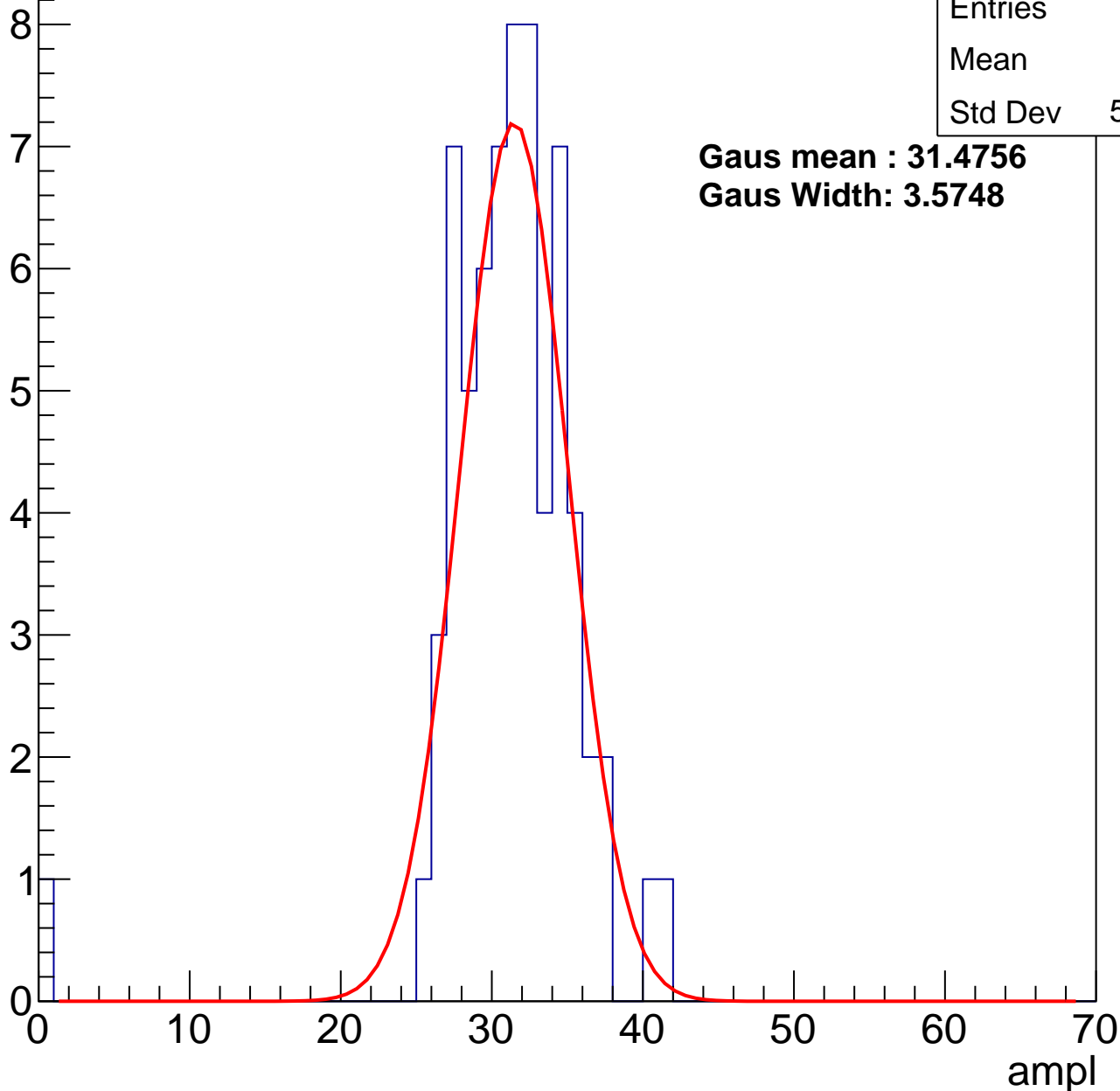
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.7 |
| Std Dev | 5.049 |

Gaus mean : 31.4756

Gaus Width: 3.5748



B0L001S, U7-ch53, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.57 |
| Std Dev | 3.421 |

Gaus mean : 38.1675

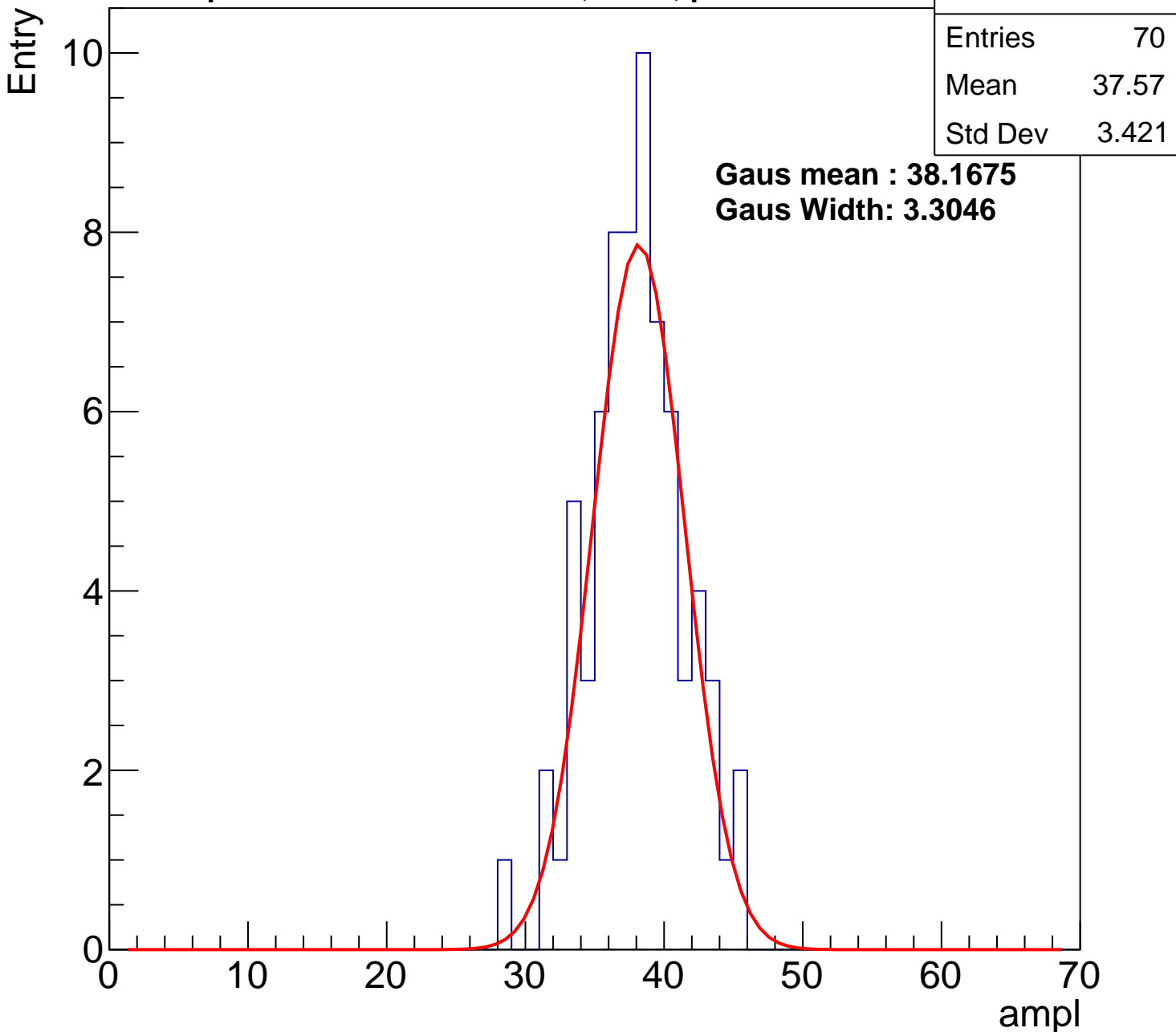
Gaus Width: 3.3046

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch53, adc2

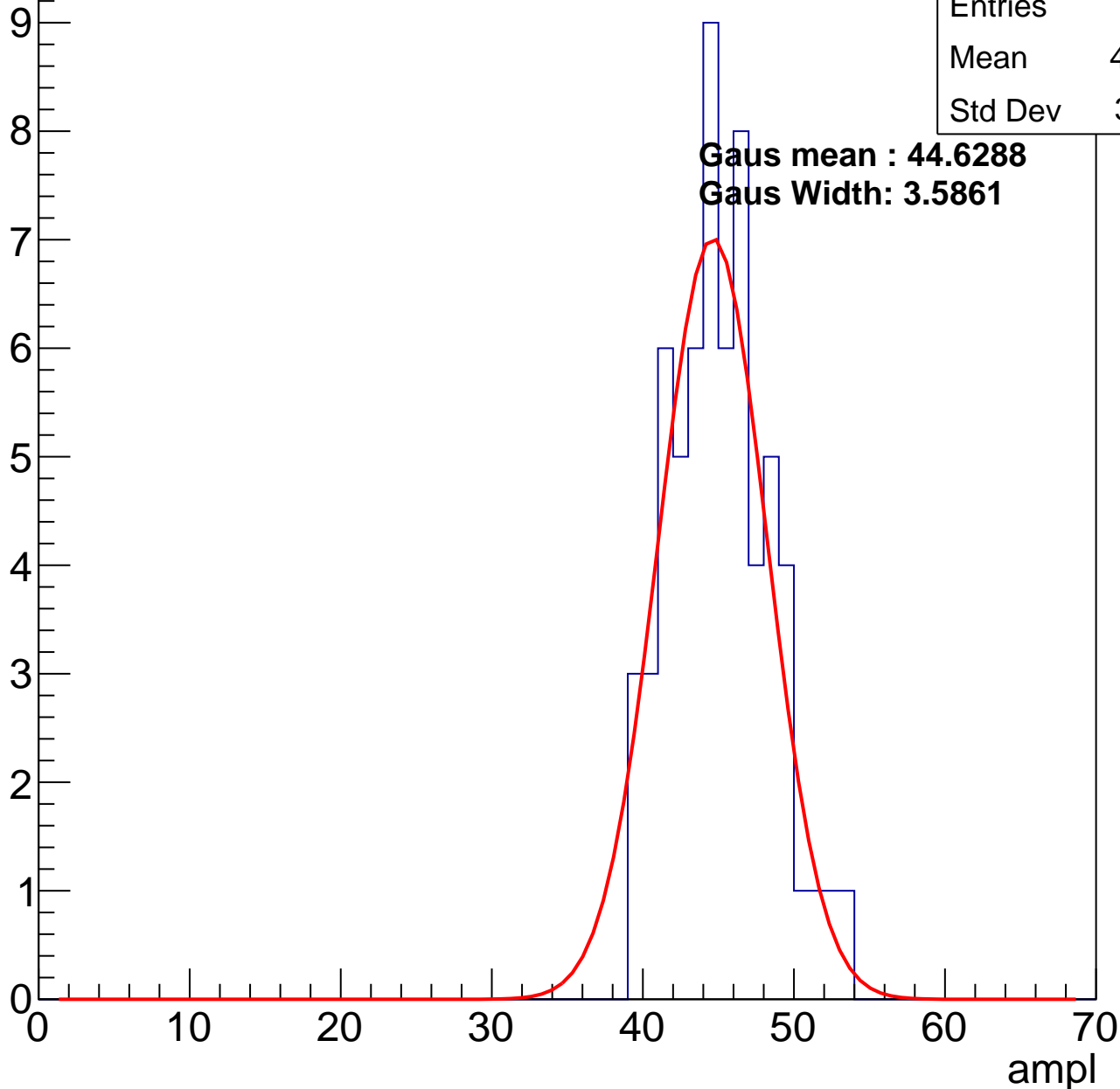
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 44.68 |
| Std Dev | 3.231 |

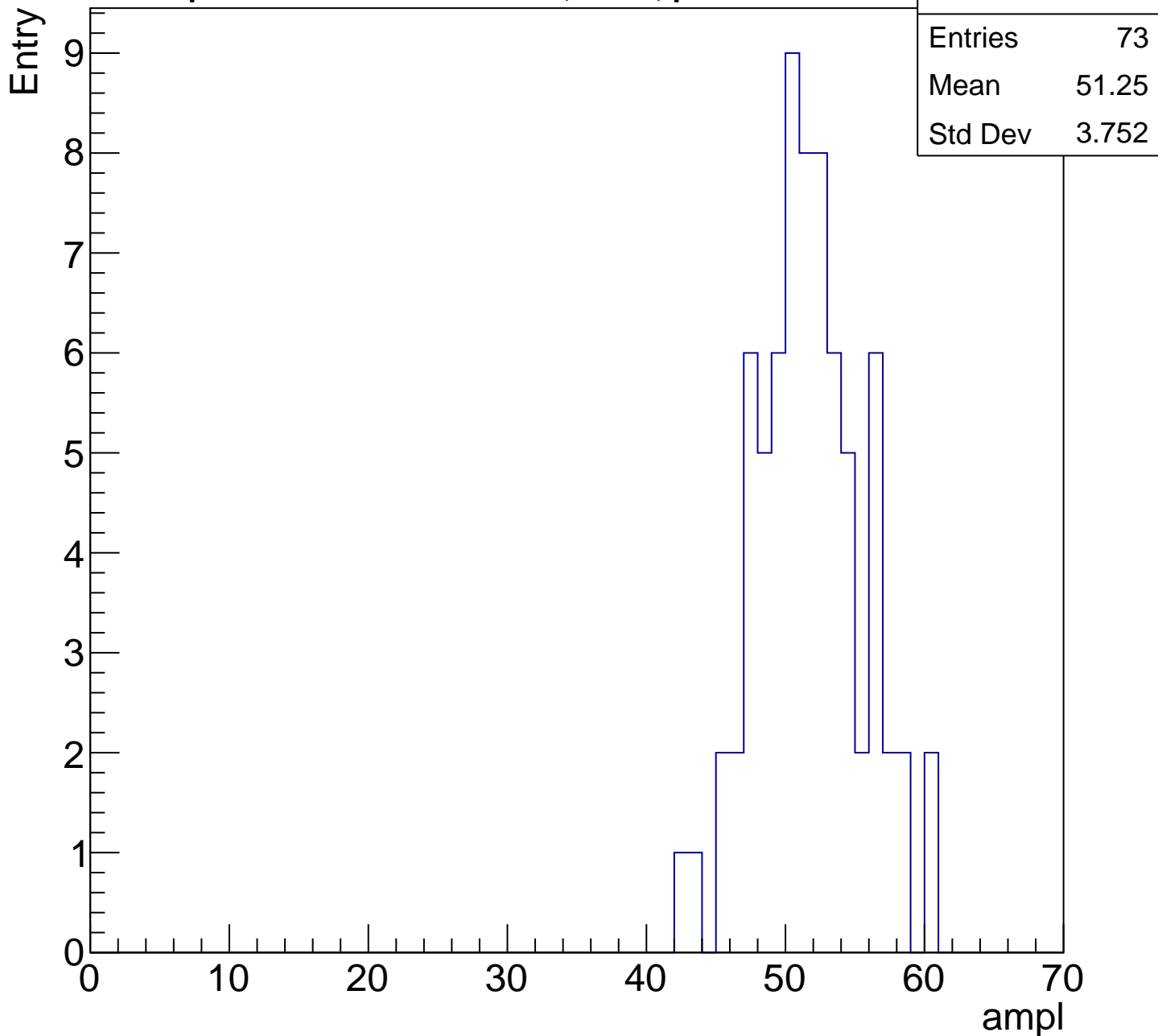
Gaus mean : 44.6288

Gaus Width: 3.5861



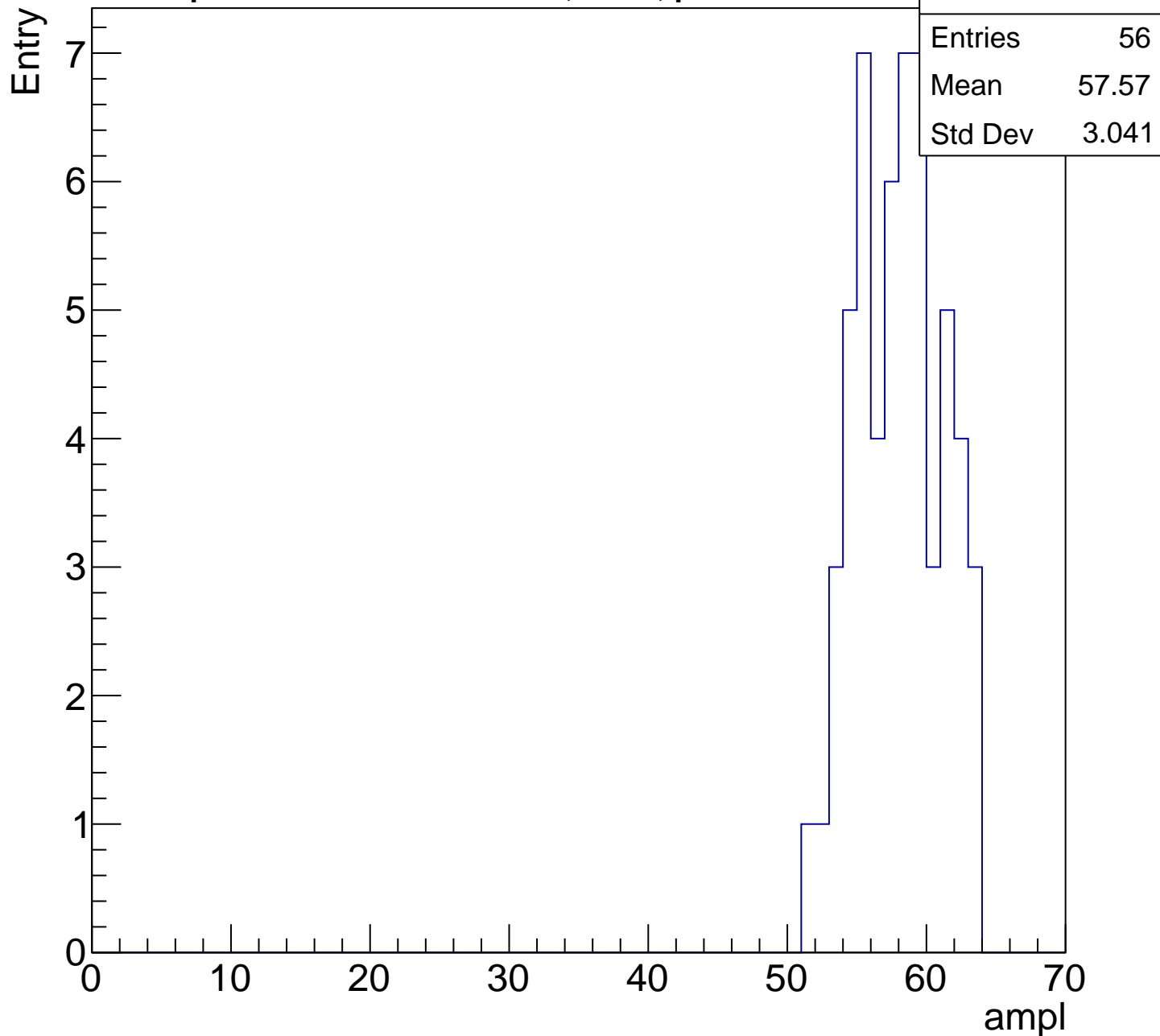
B0L001S, U7-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

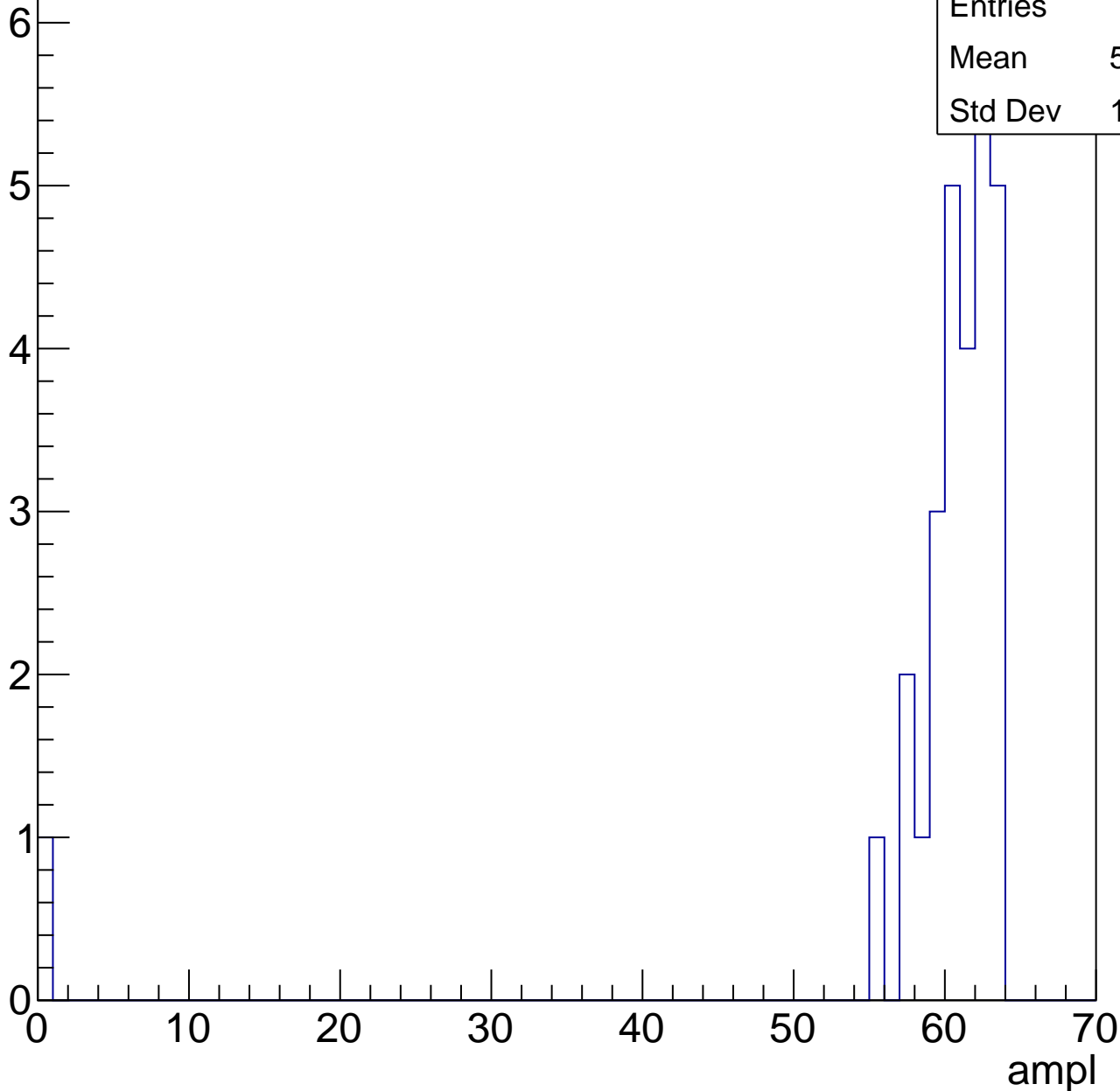


B0L001S, U7-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.39 |
| Std Dev | 11.42 |



B0L001S, U7-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch54, adc0

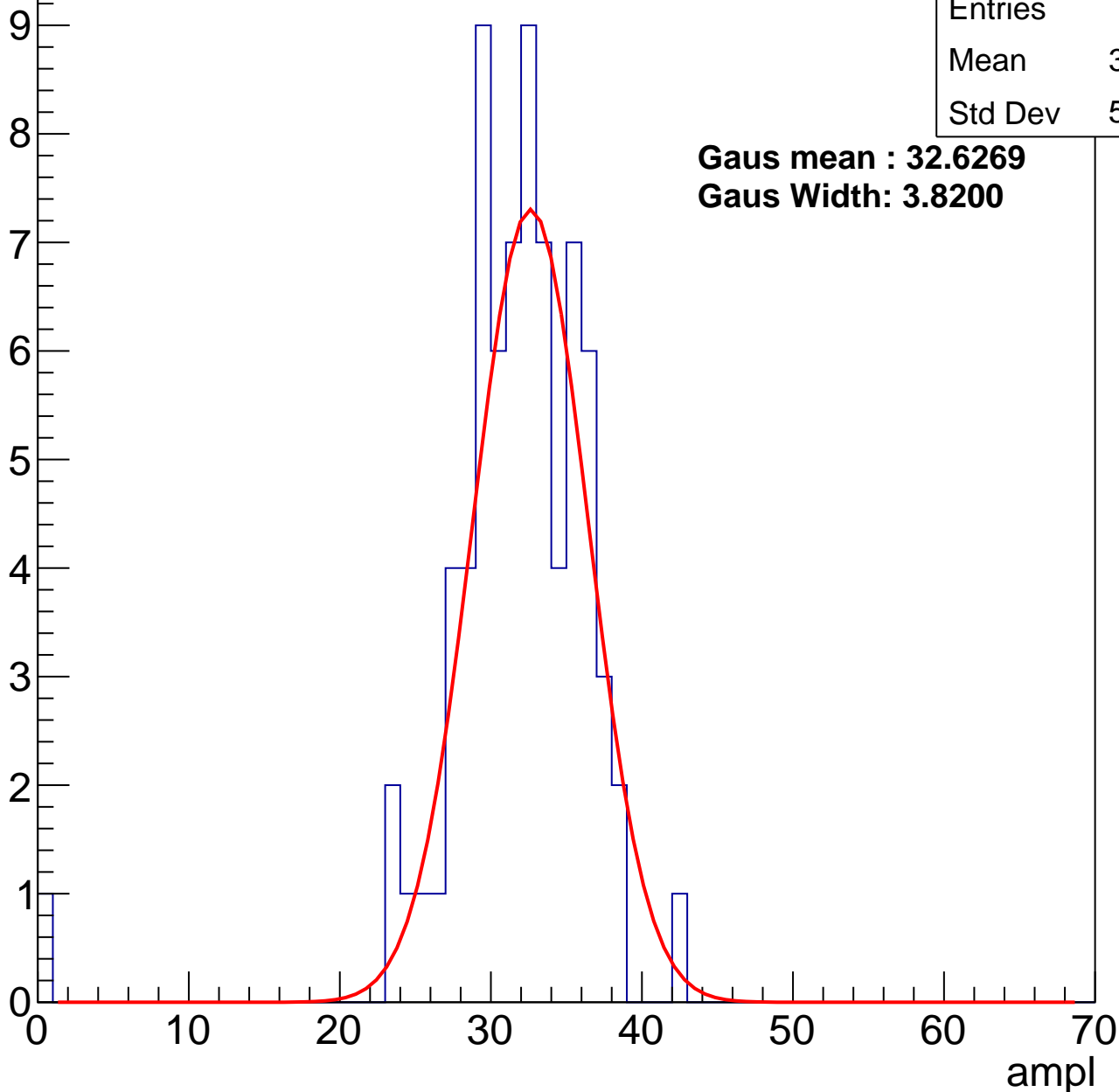
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 31.25 |
| Std Dev | 5.162 |

Gaus mean : 32.6269

Gaus Width: 3.8200



B0L001S, U7-ch54, adc1

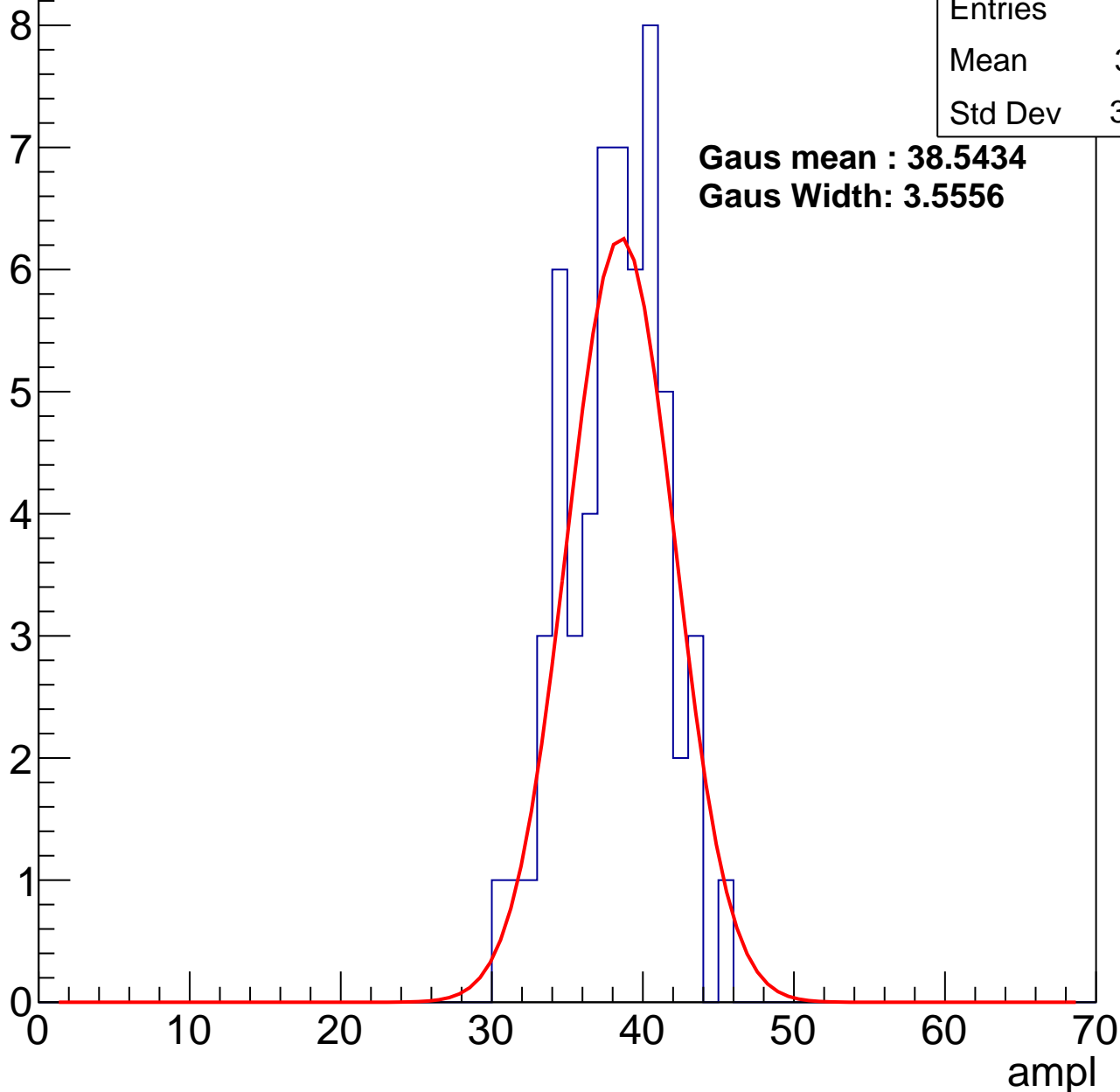
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 37.71 |
| Std Dev | 3.227 |

Gaus mean : 38.5434

Gaus Width: 3.5556



B0L001S, U7-ch54, adc2

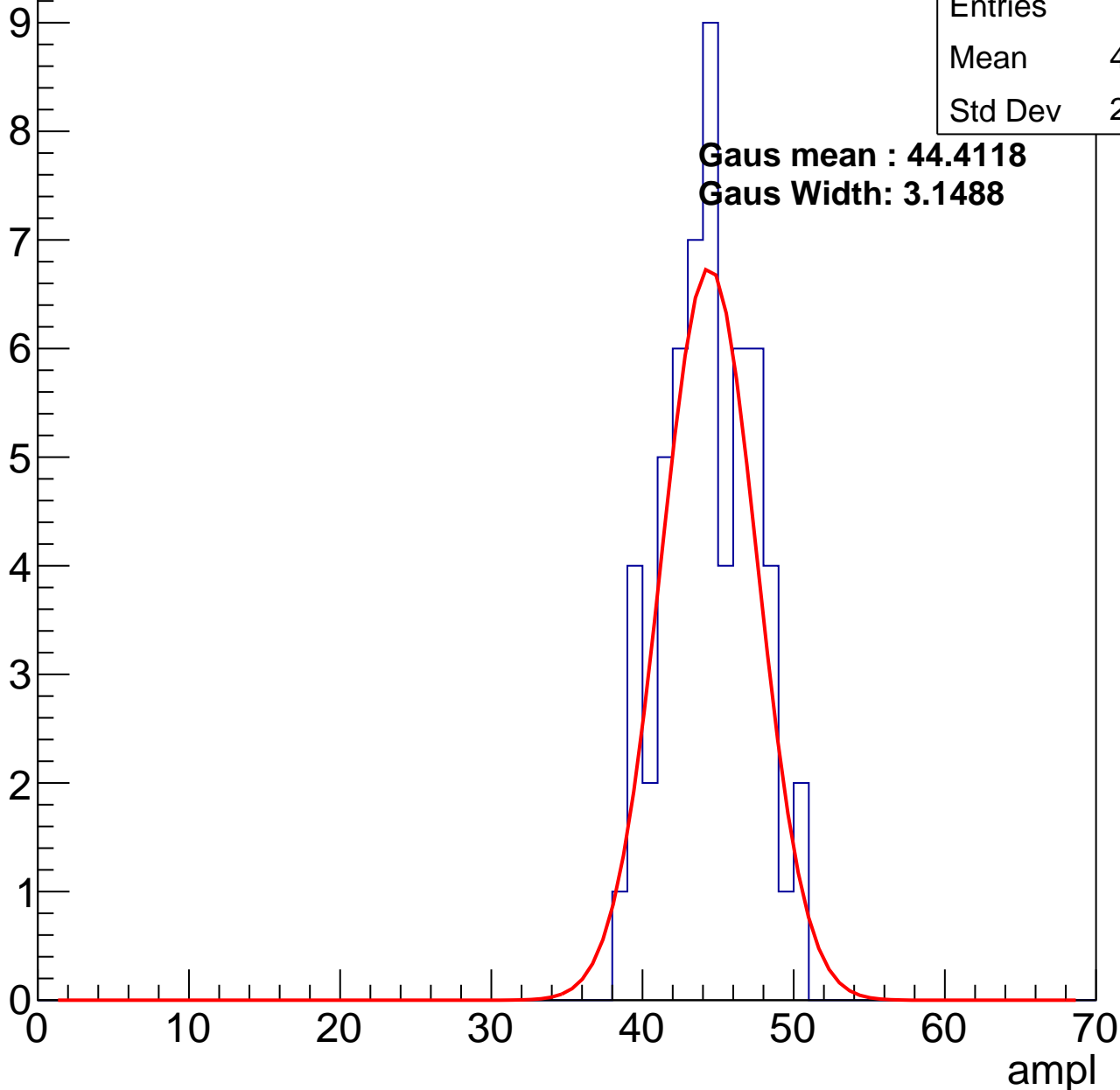
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 43.98 |
| Std Dev | 2.923 |

Gaus mean : 44.4118

Gaus Width: 3.1488

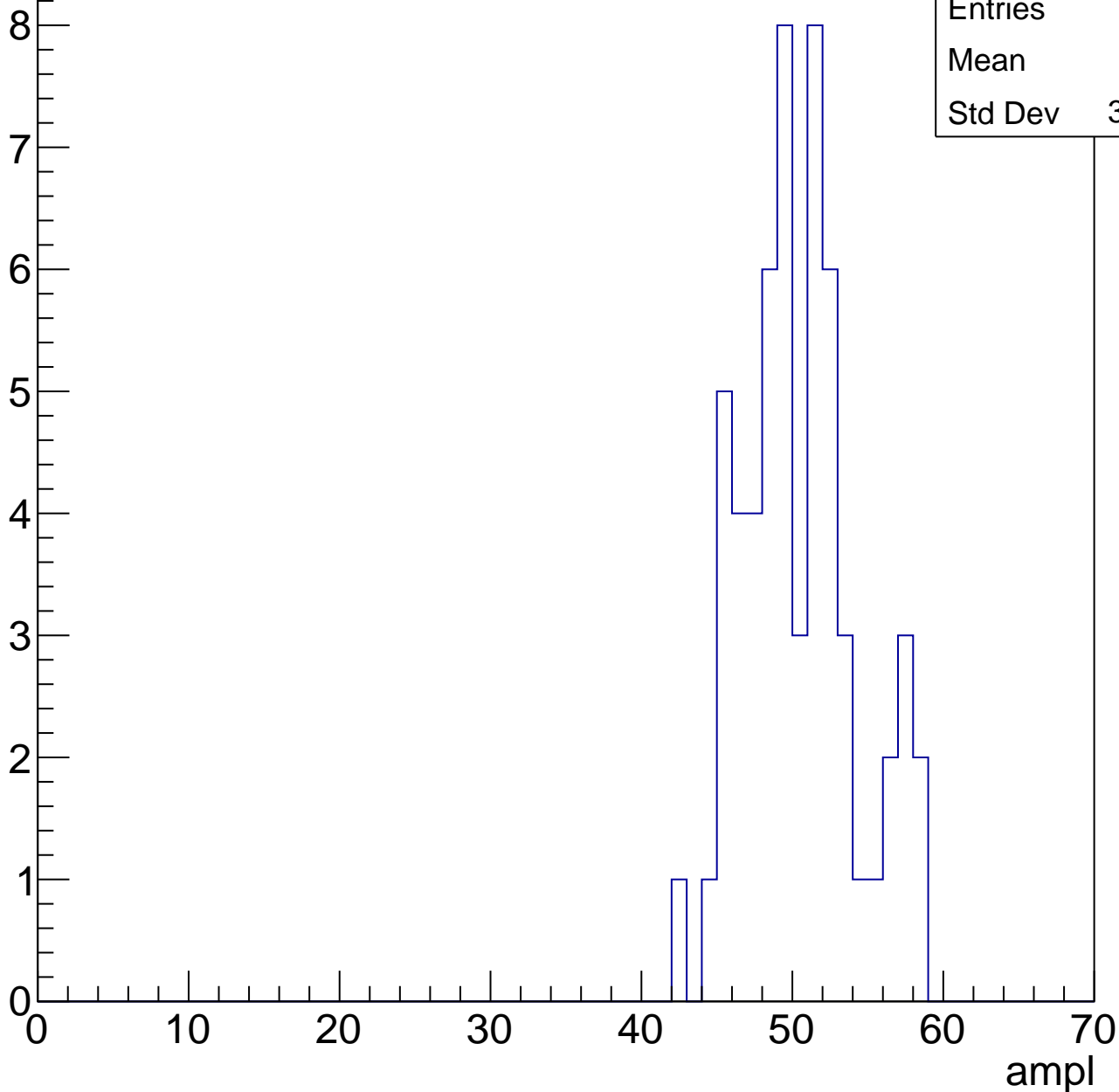


B0L001S, U7-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 50 |
| Std Dev | 3.723 |

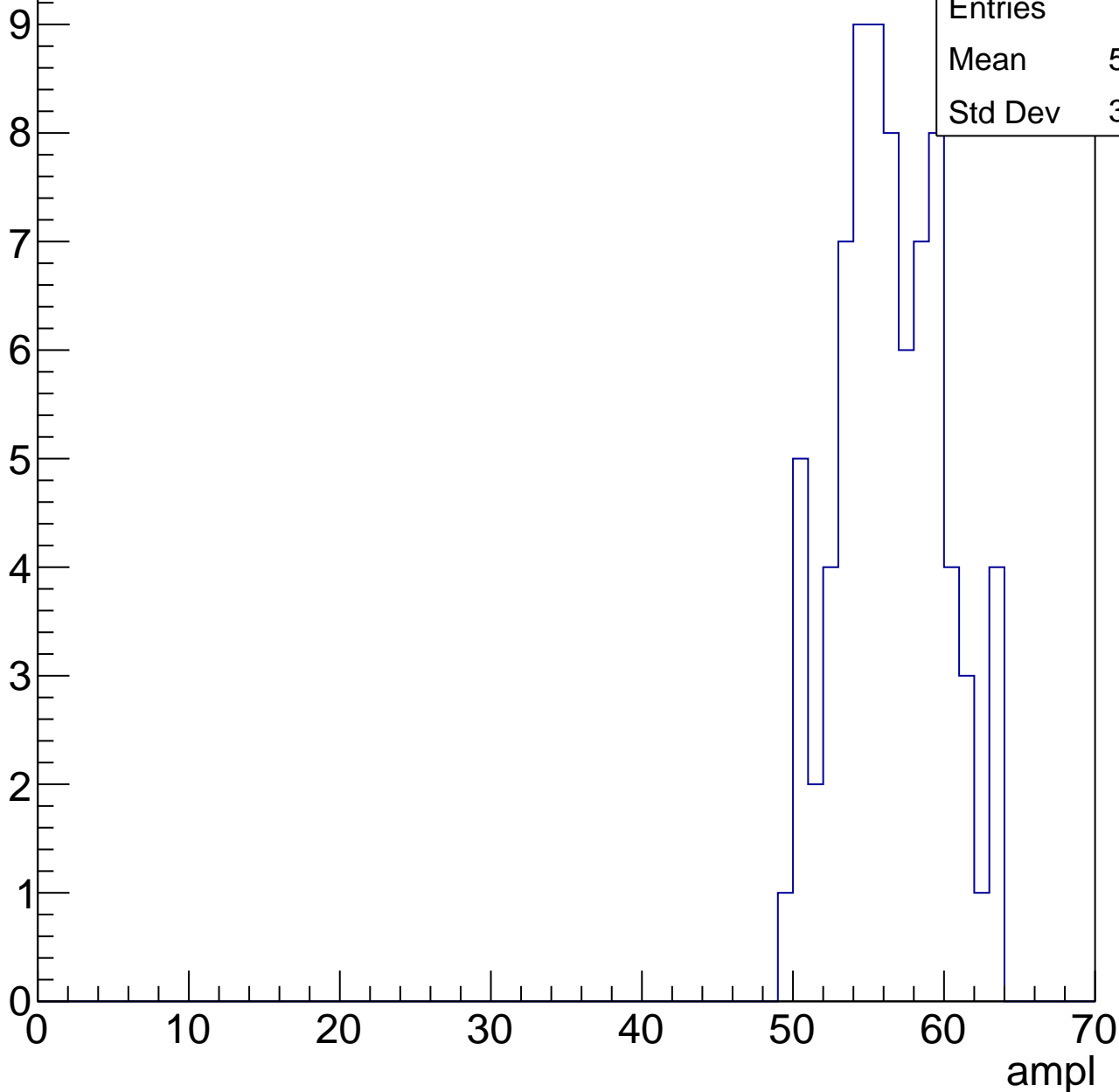


B0L001S, U7-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 55.97 |
| Std Dev | 3.449 |

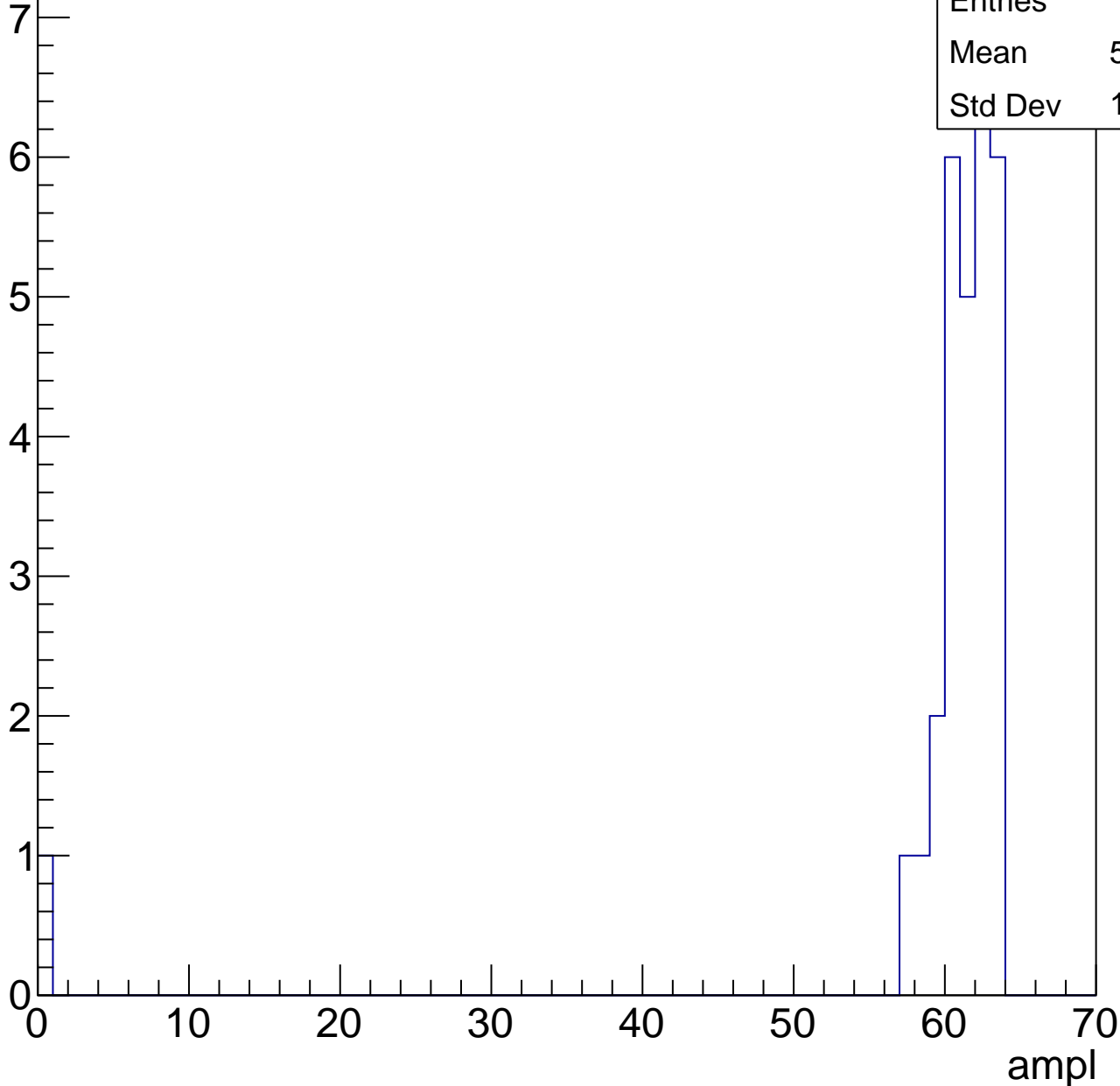


B0L001S, U7-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

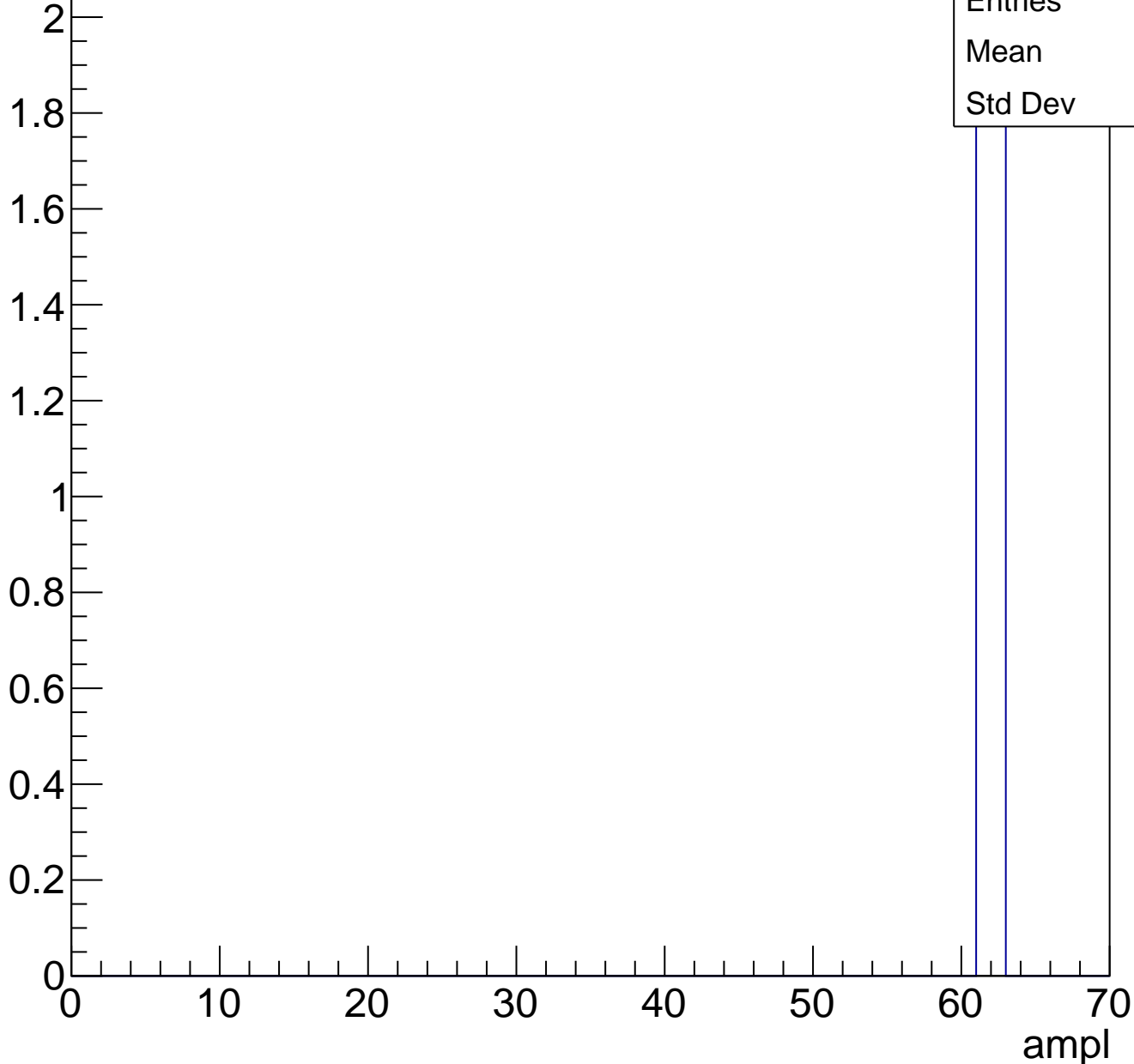
| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.97 |
| Std Dev | 11.25 |



B0L001S, U7-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch55, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 83 |
| Mean | 28.98 |
| Std Dev | 5.862 |

Gaus mean : 30.0482

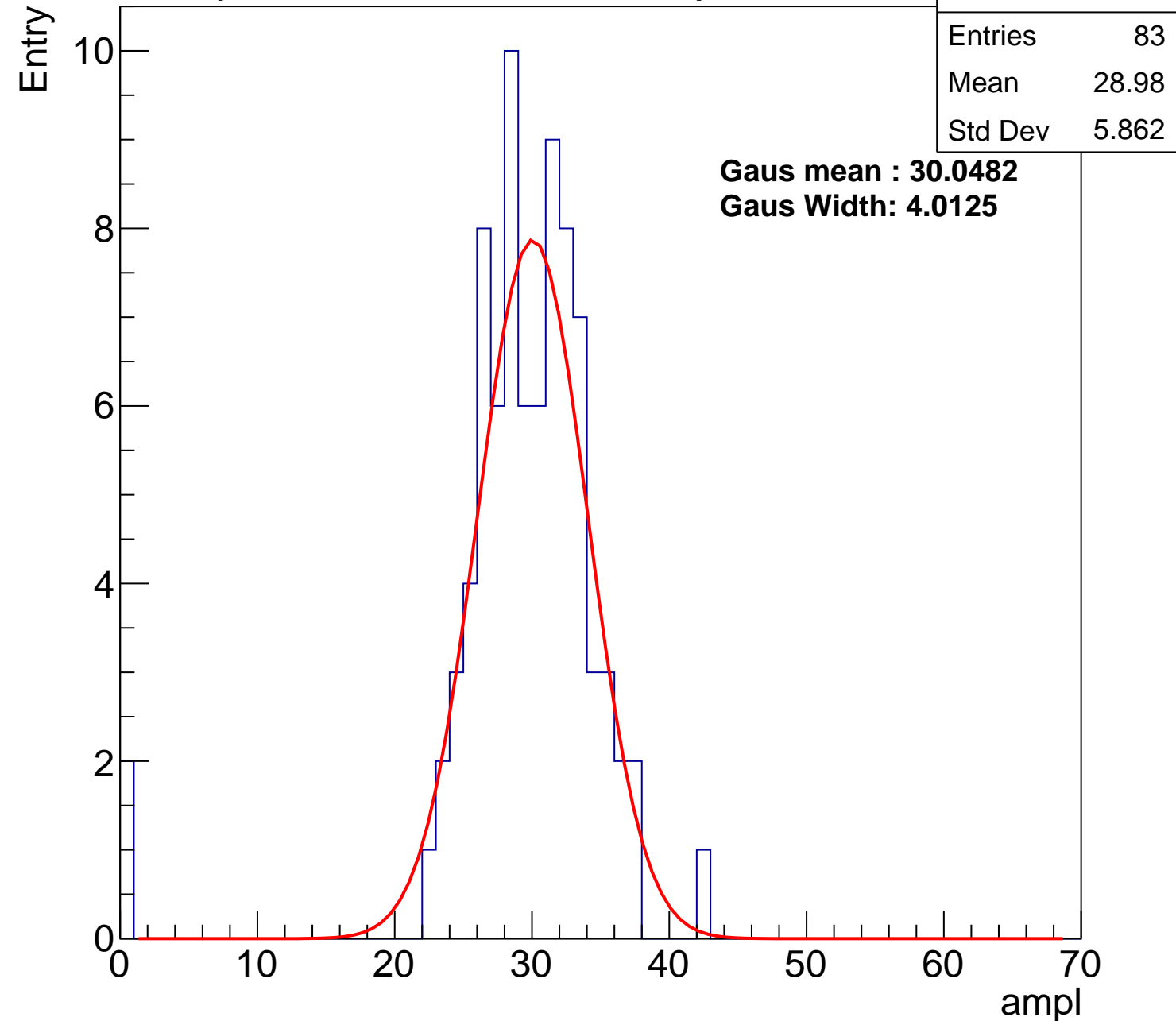
Gaus Width: 4.0125

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch55, adc1

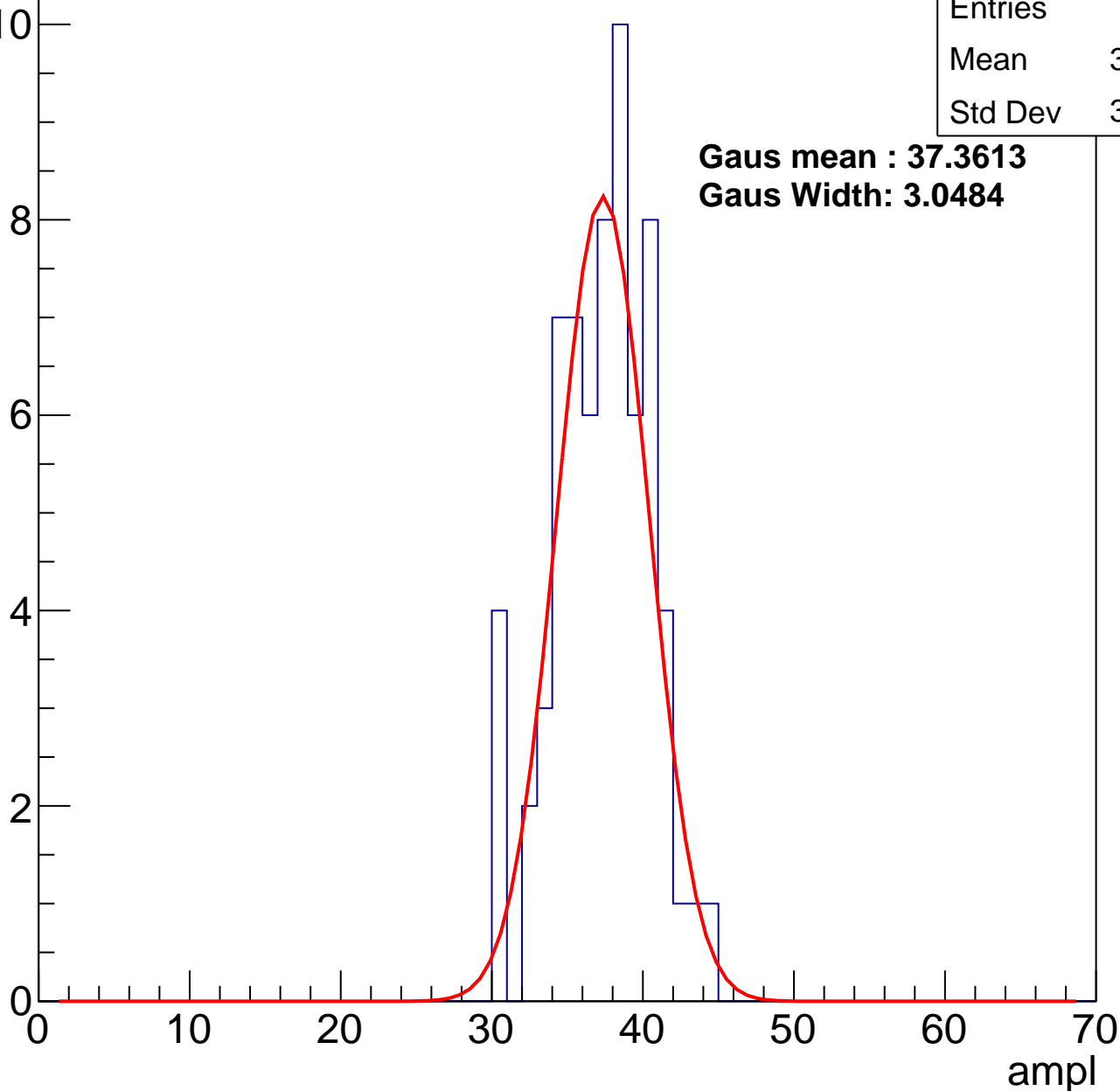
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.84 |
| Std Dev | 3.137 |

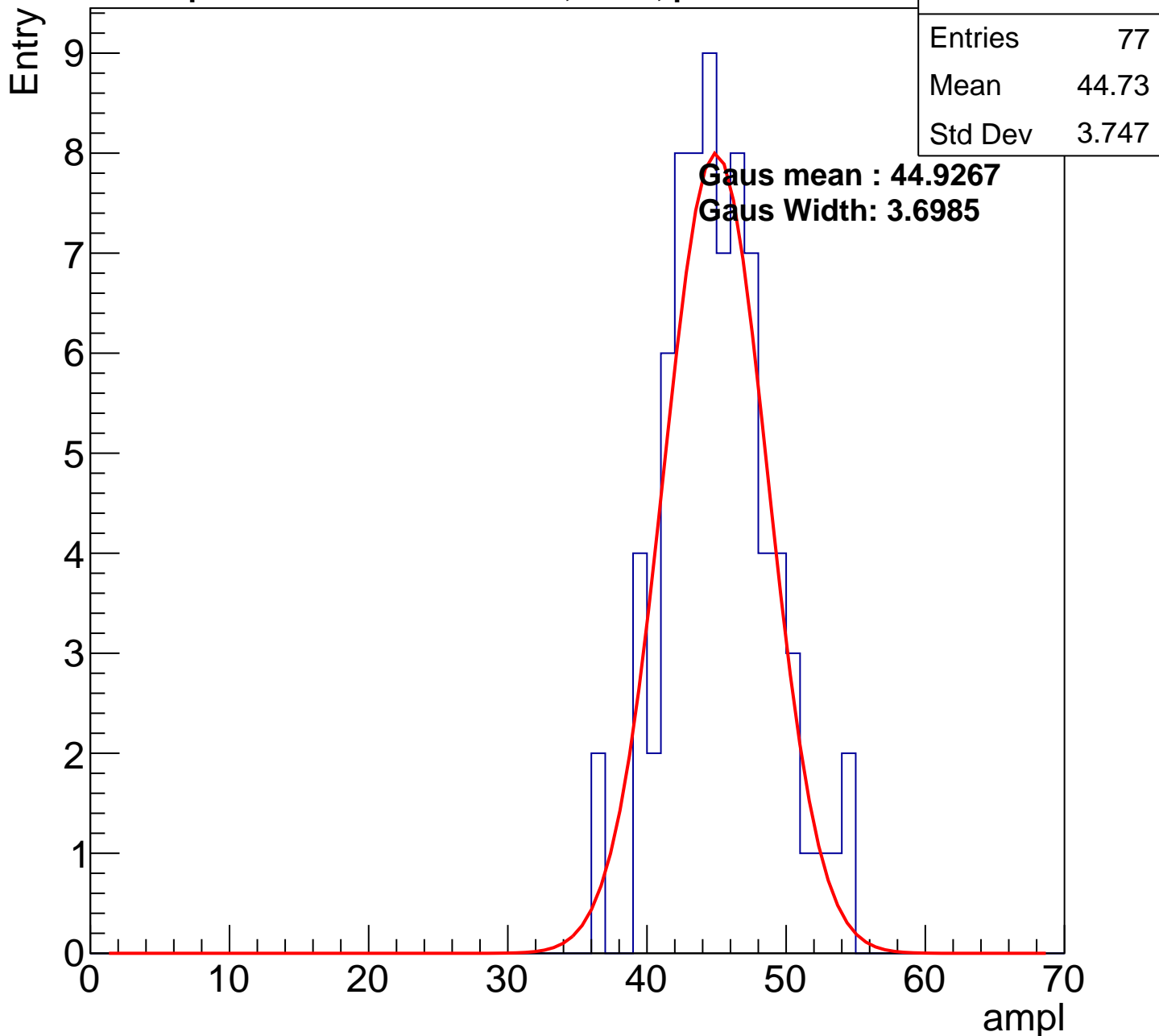
Gaus mean : 37.3613

Gaus Width: 3.0484



B0L001S, U7-ch55, adc2

calib_packv5_042523_0143.root, FC#9, port A1

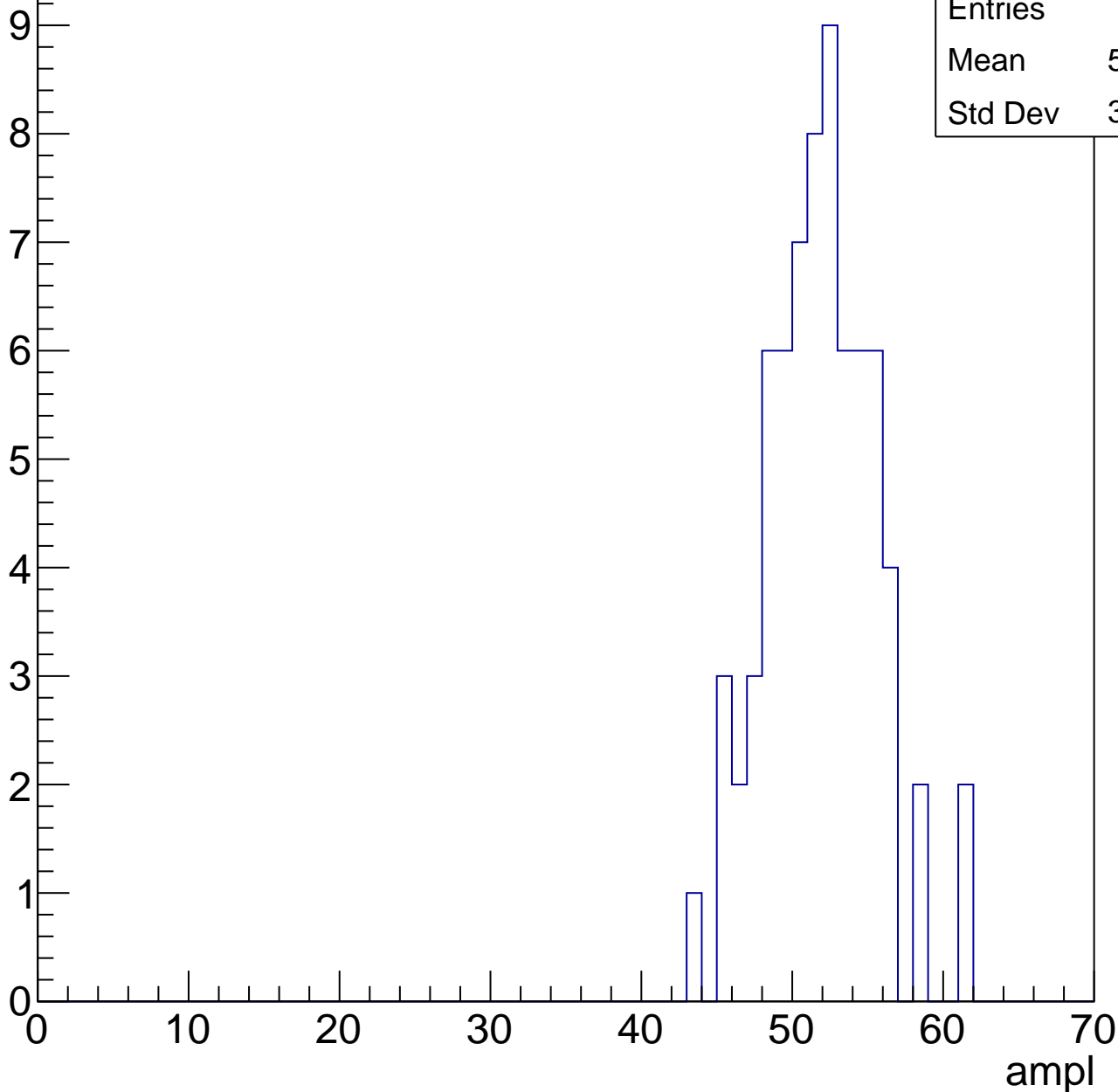


B0L001S, U7-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 51.45 |
| Std Dev | 3.603 |

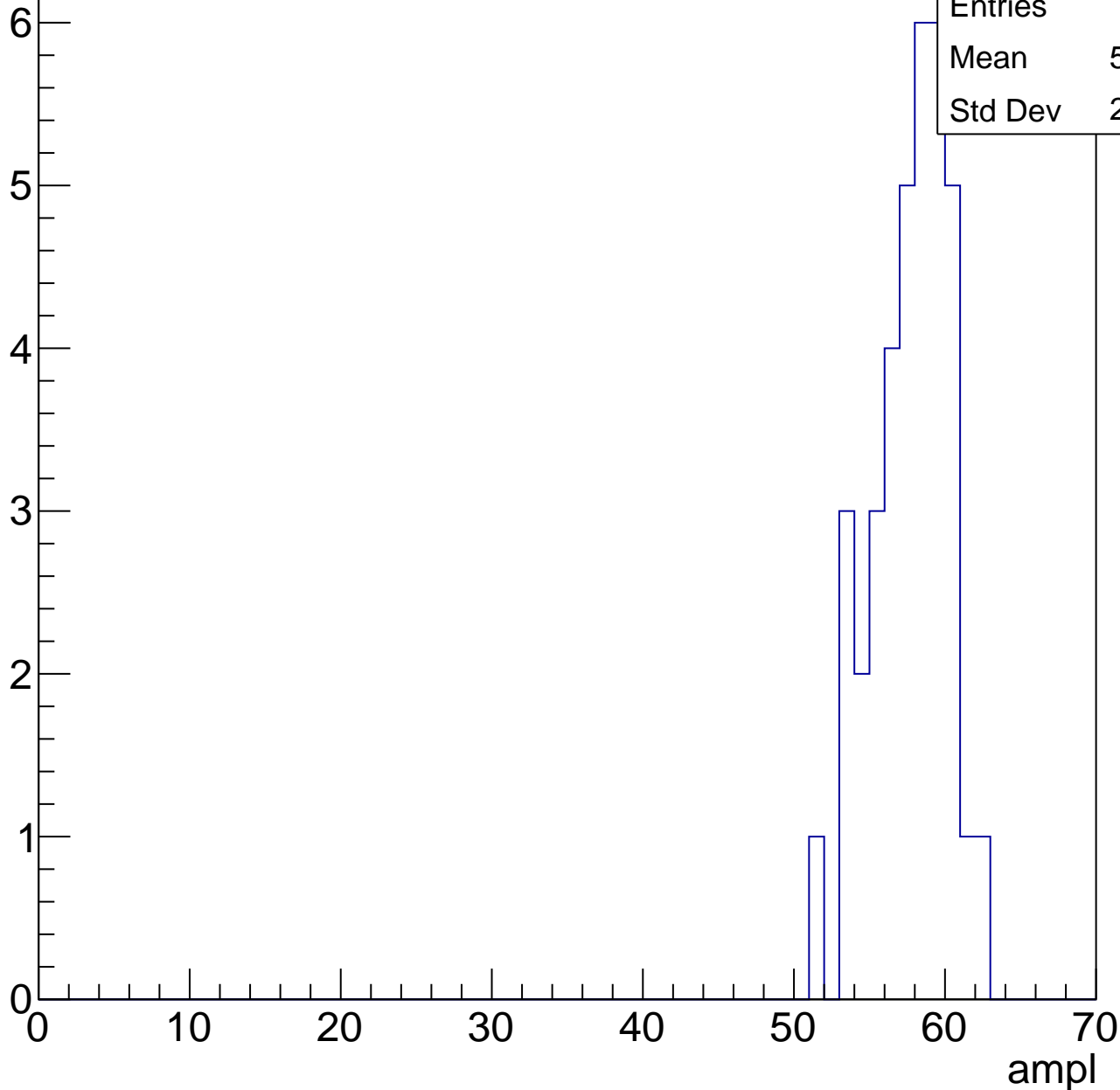


B0L001S, U7-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 57.22 |
| Std Dev | 2.506 |

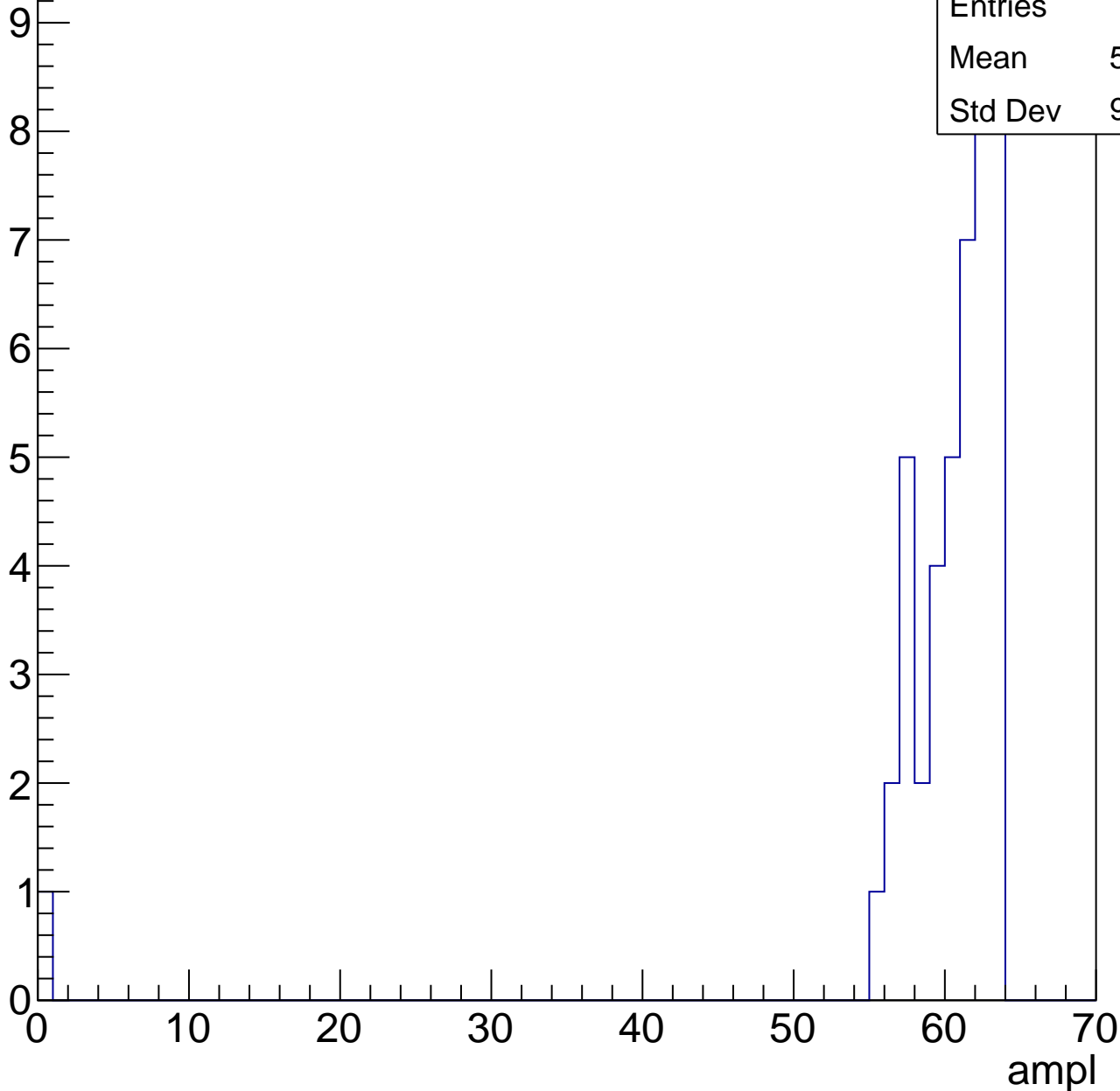


B0L001S, U7-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.02 |
| Std Dev | 9.183 |



B0L001S, U7-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch56, adc0

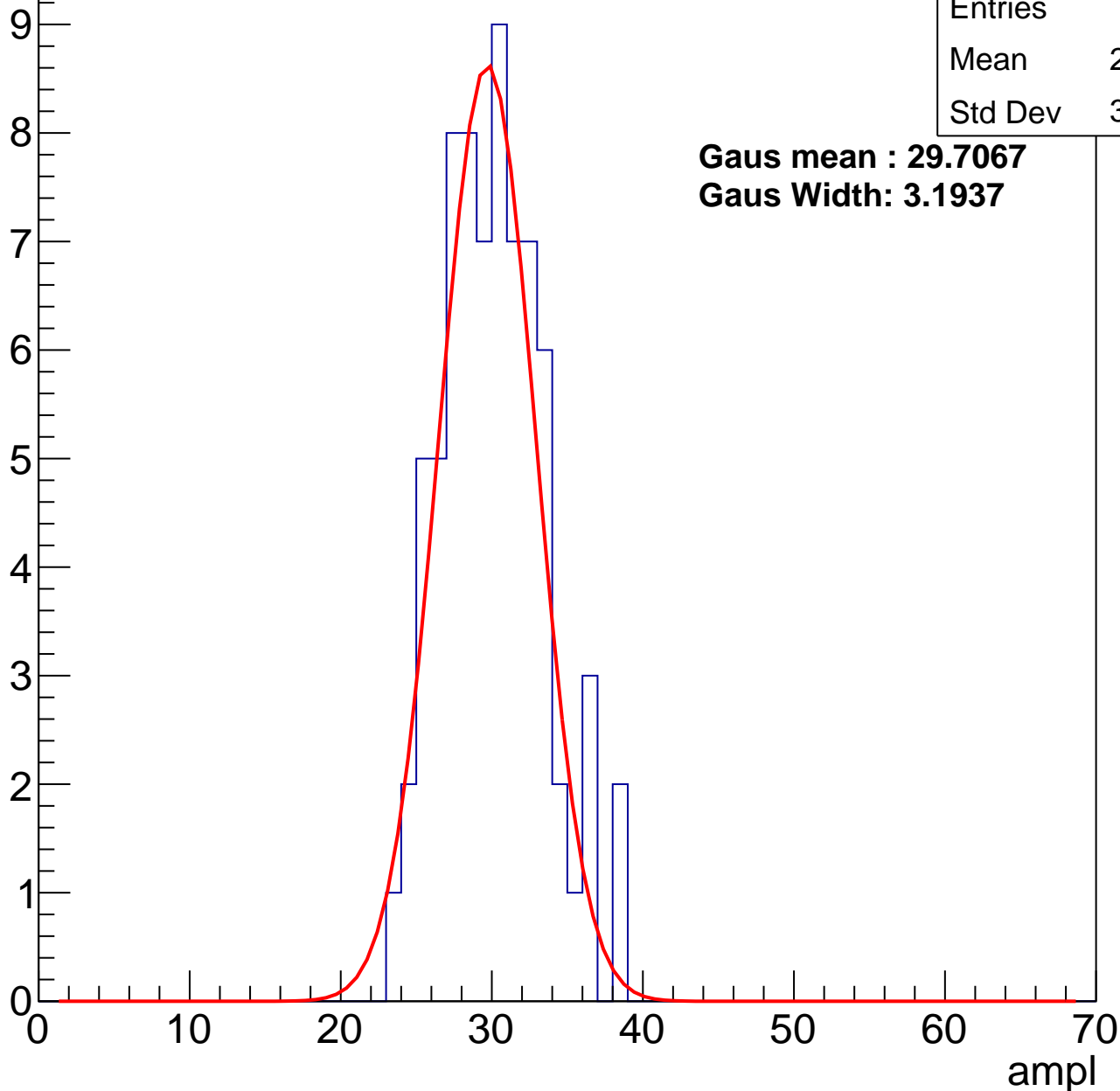
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.66 |
| Std Dev | 3.332 |

Gaus mean : 29.7067

Gaus Width: 3.1937



B0L001S, U7-ch56, adc1

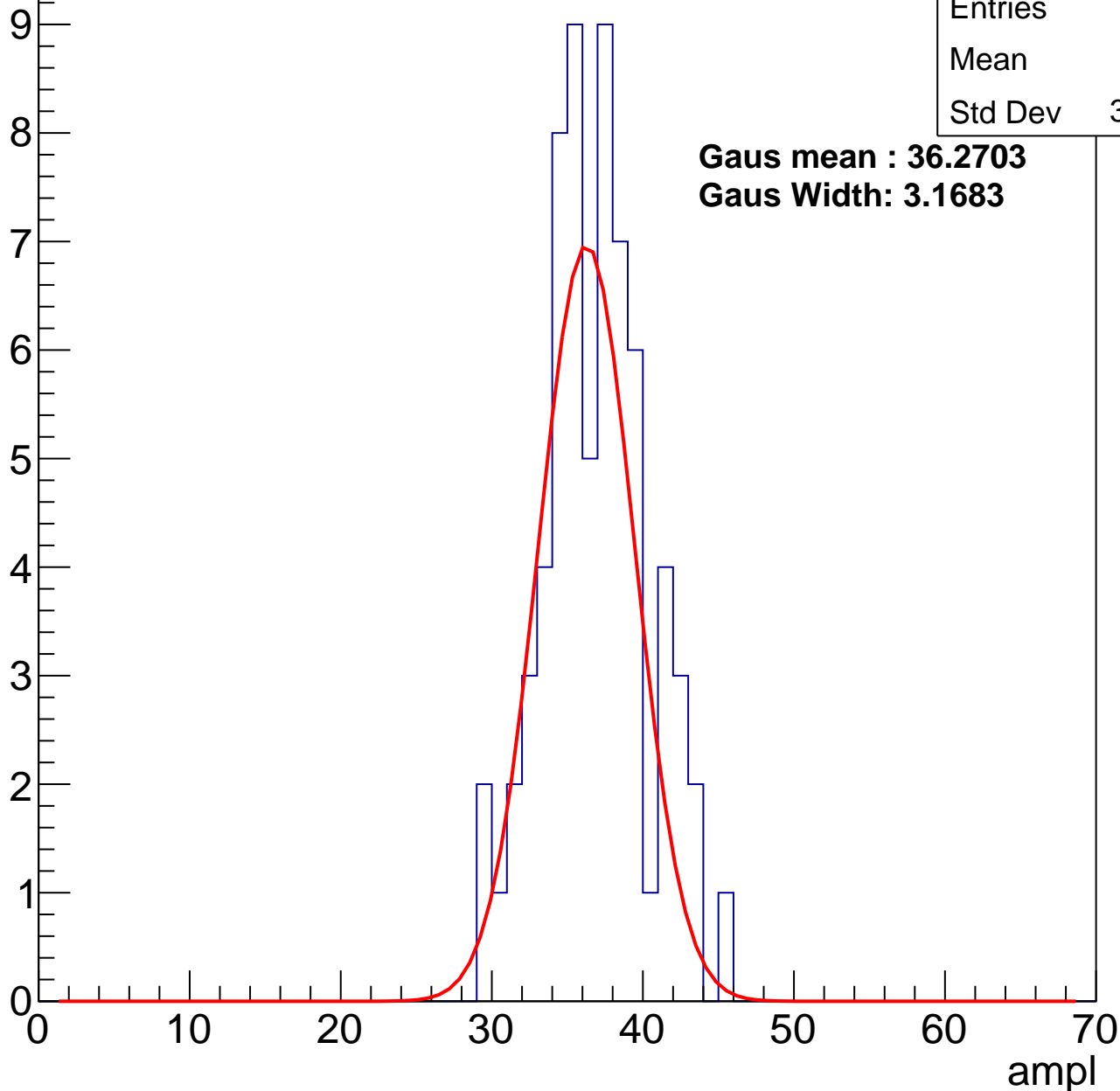
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.4 |
| Std Dev | 3.438 |

Gaus mean : 36.2703

Gaus Width: 3.1683



B0L001S, U7-ch56, adc2

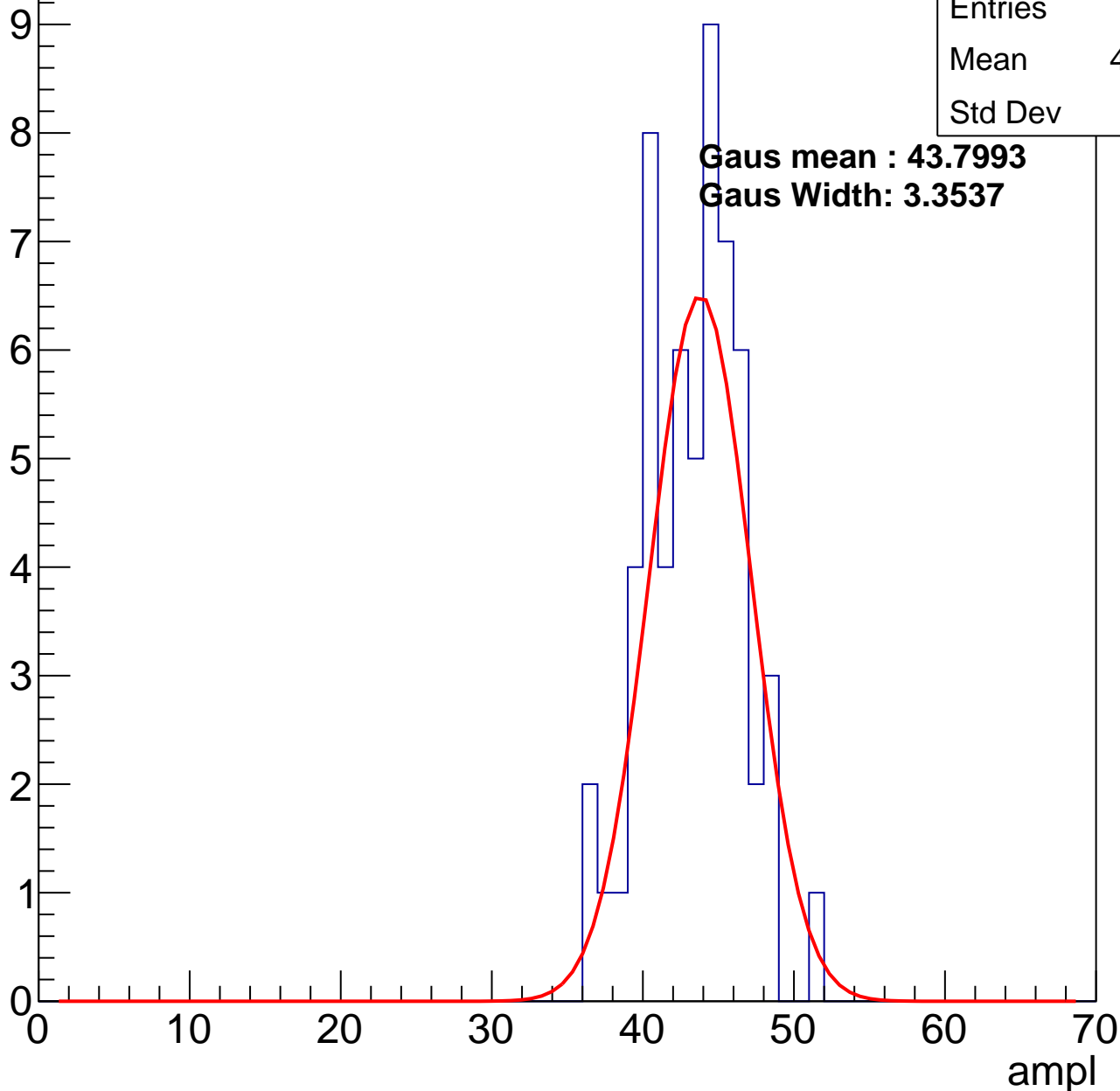
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.88 |
| Std Dev | 3.13 |

Gaus mean : 43.7993

Gaus Width: 3.3537

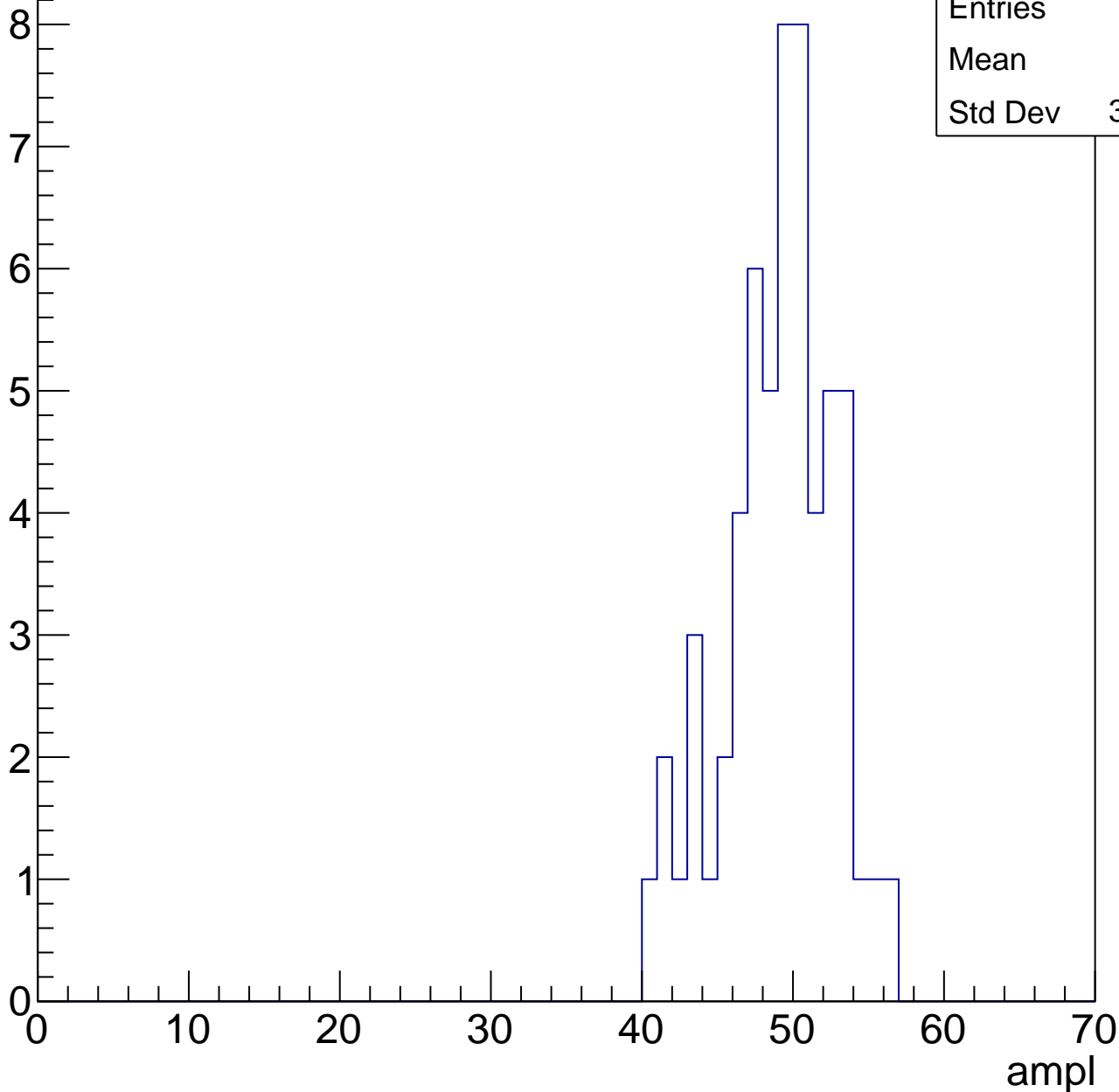


B0L001S, U7-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

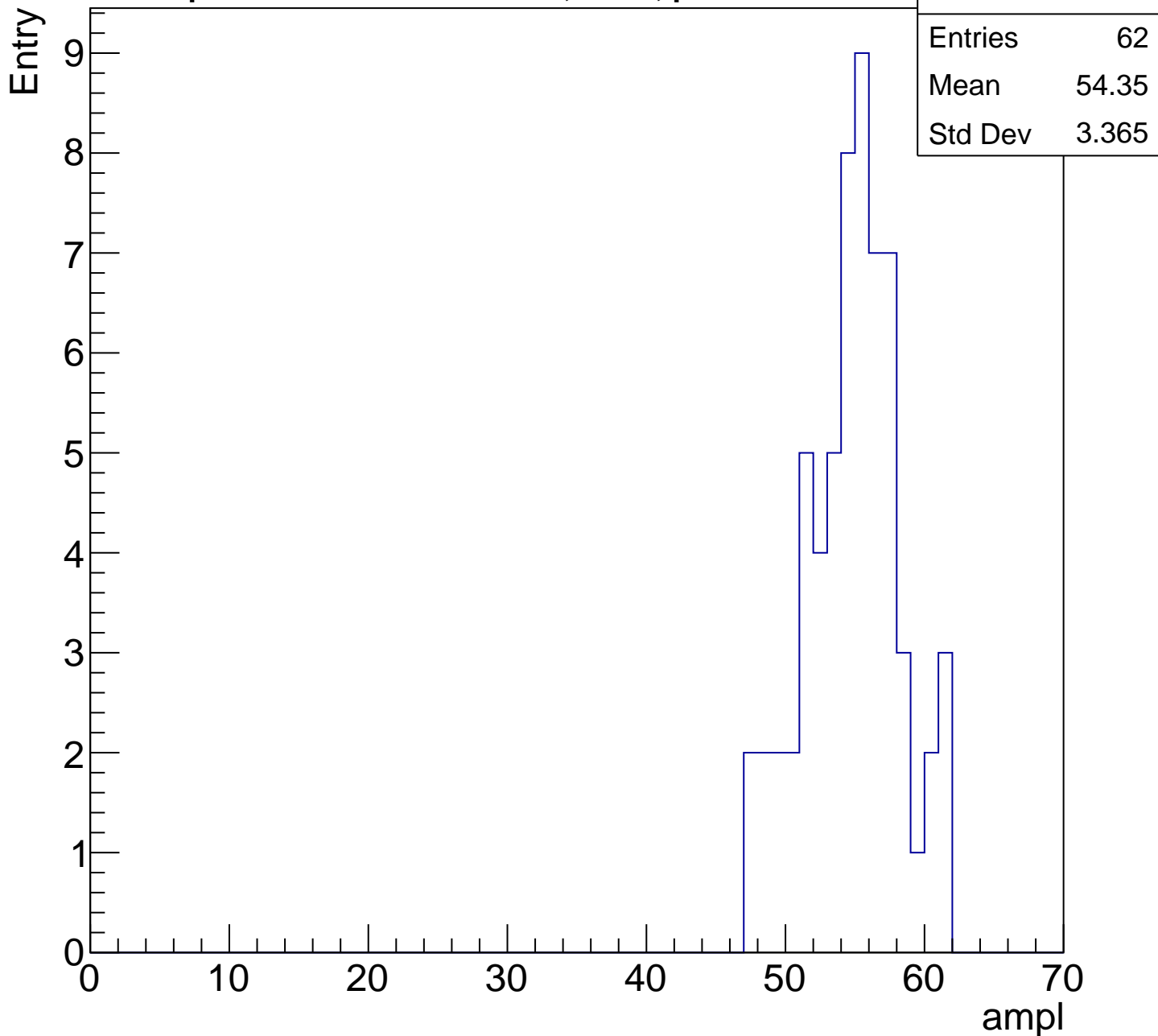
Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 48.6 |
| Std Dev | 3.567 |



B0L001S, U7-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

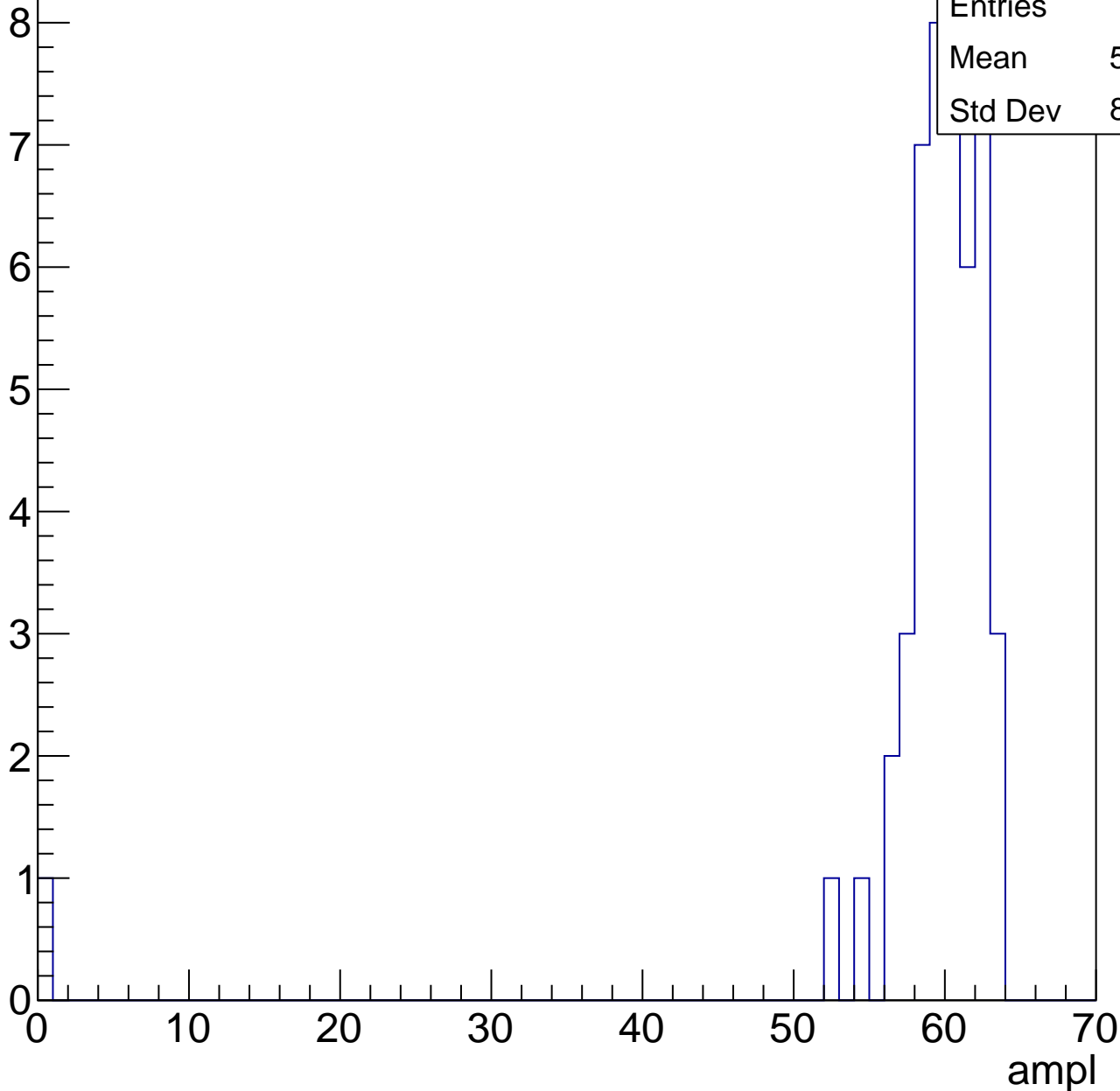


B0L001S, U7-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.29 |
| Std Dev | 8.803 |

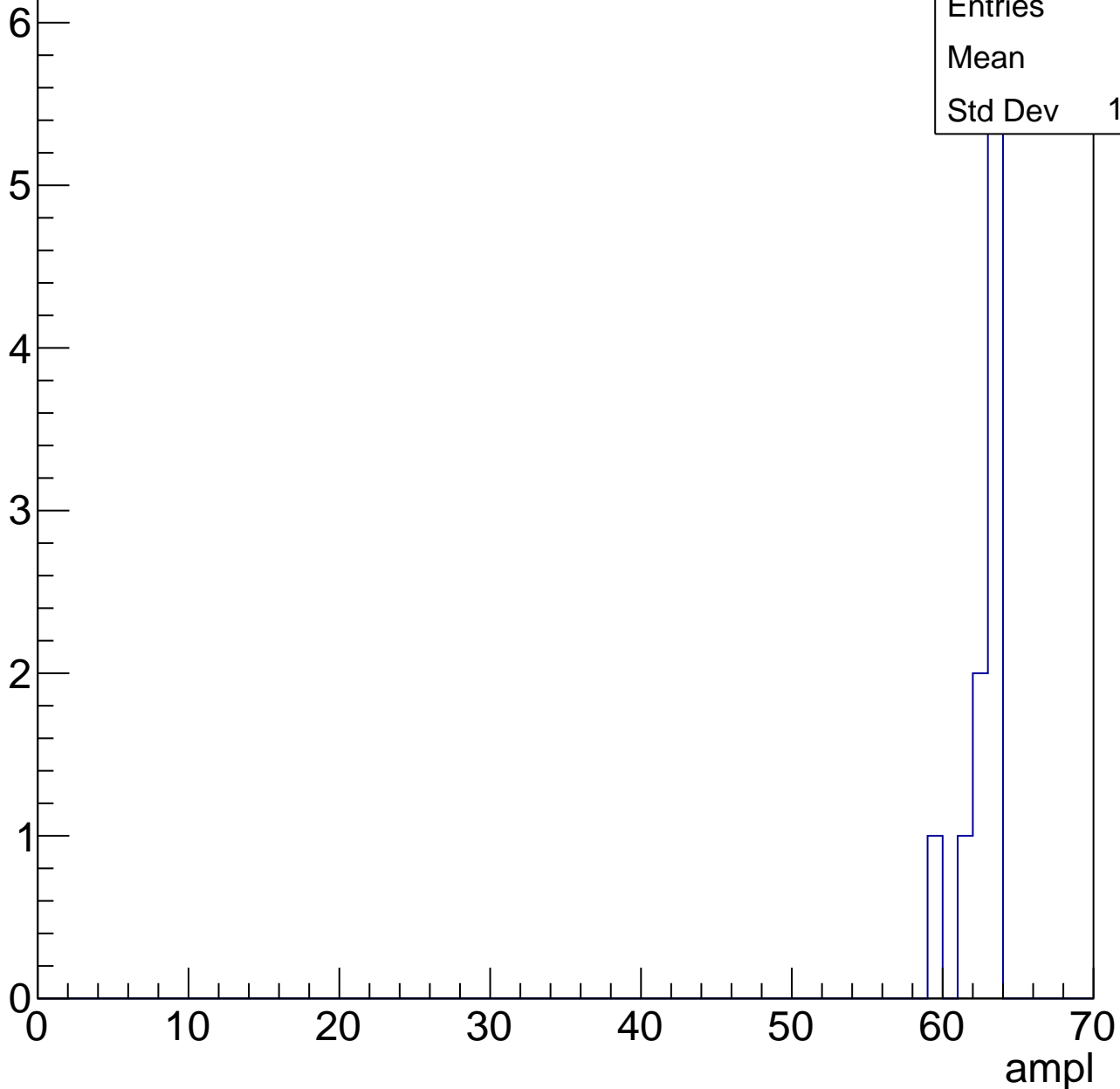


B0L001S, U7-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 10 |
| Mean | 62.2 |
| Std Dev | 1.249 |



B0L001S, U7-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch57, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 68 |
| Mean | 31.91 |
| Std Dev | 6.485 |

Gaus mean : 34.0769

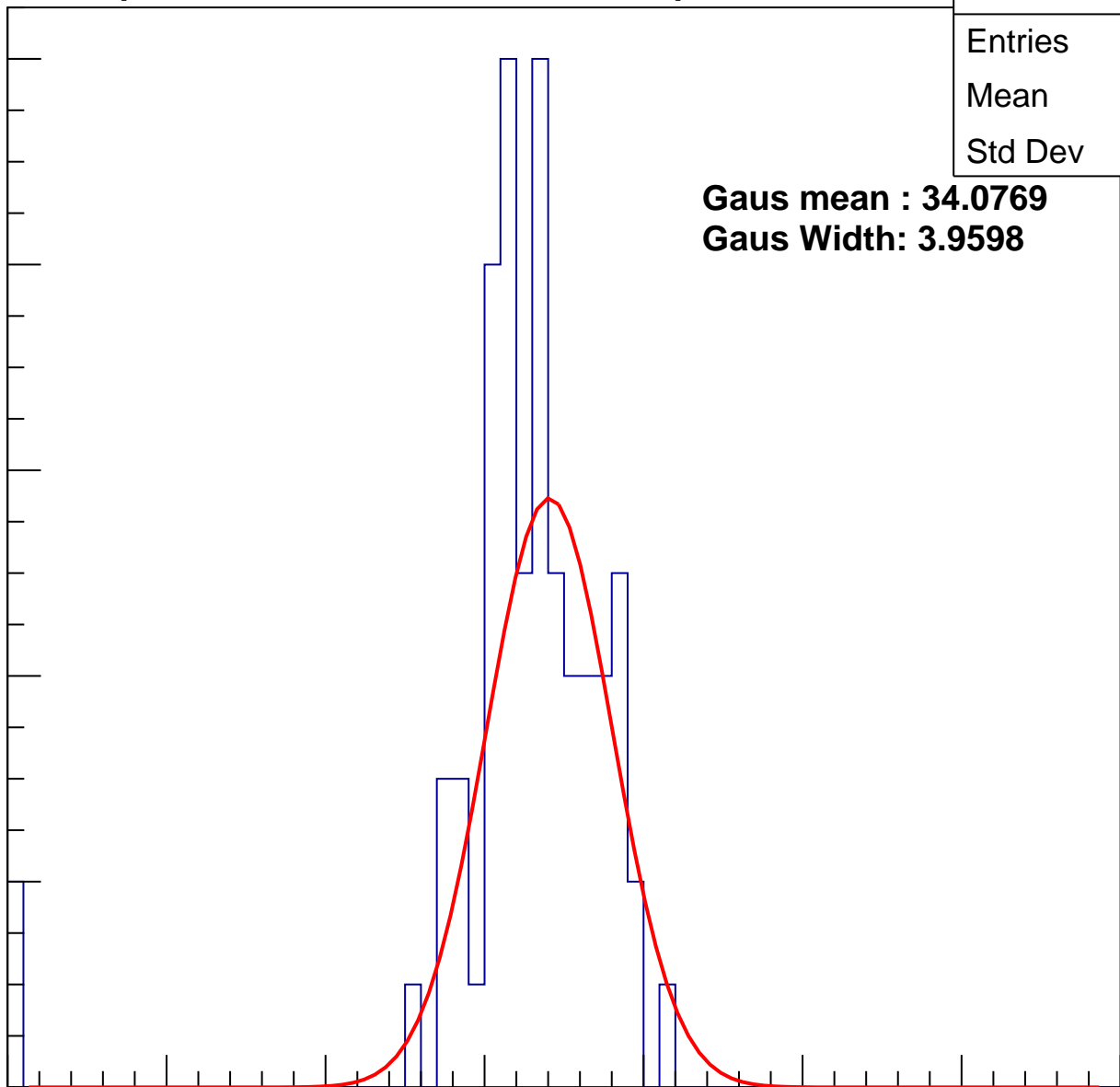
Gaus Width: 3.9598

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch57, adc1

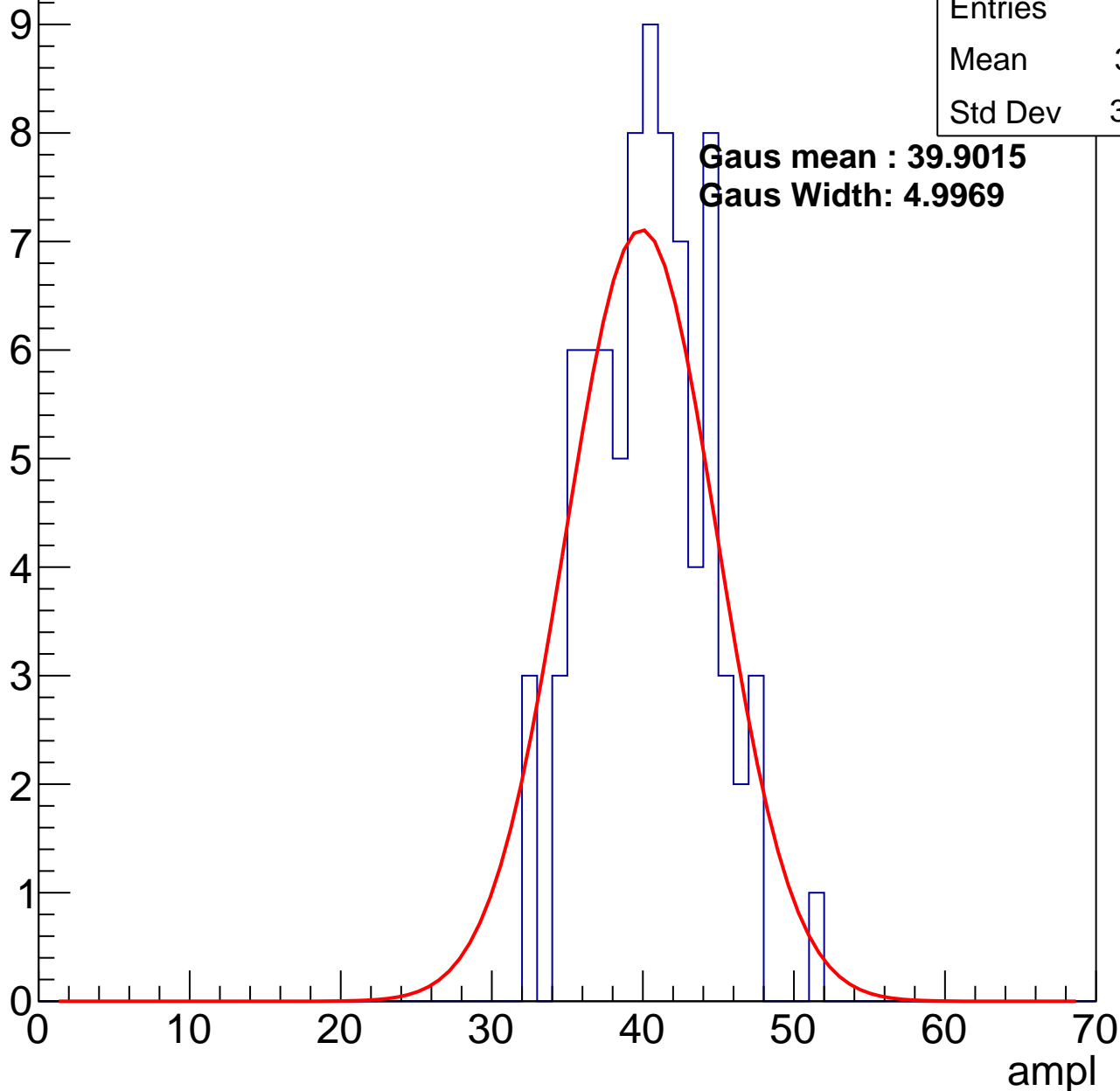
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 39.91 |
| Std Dev | 3.892 |

Gaus mean : 39.9015

Gaus Width: 4.9969

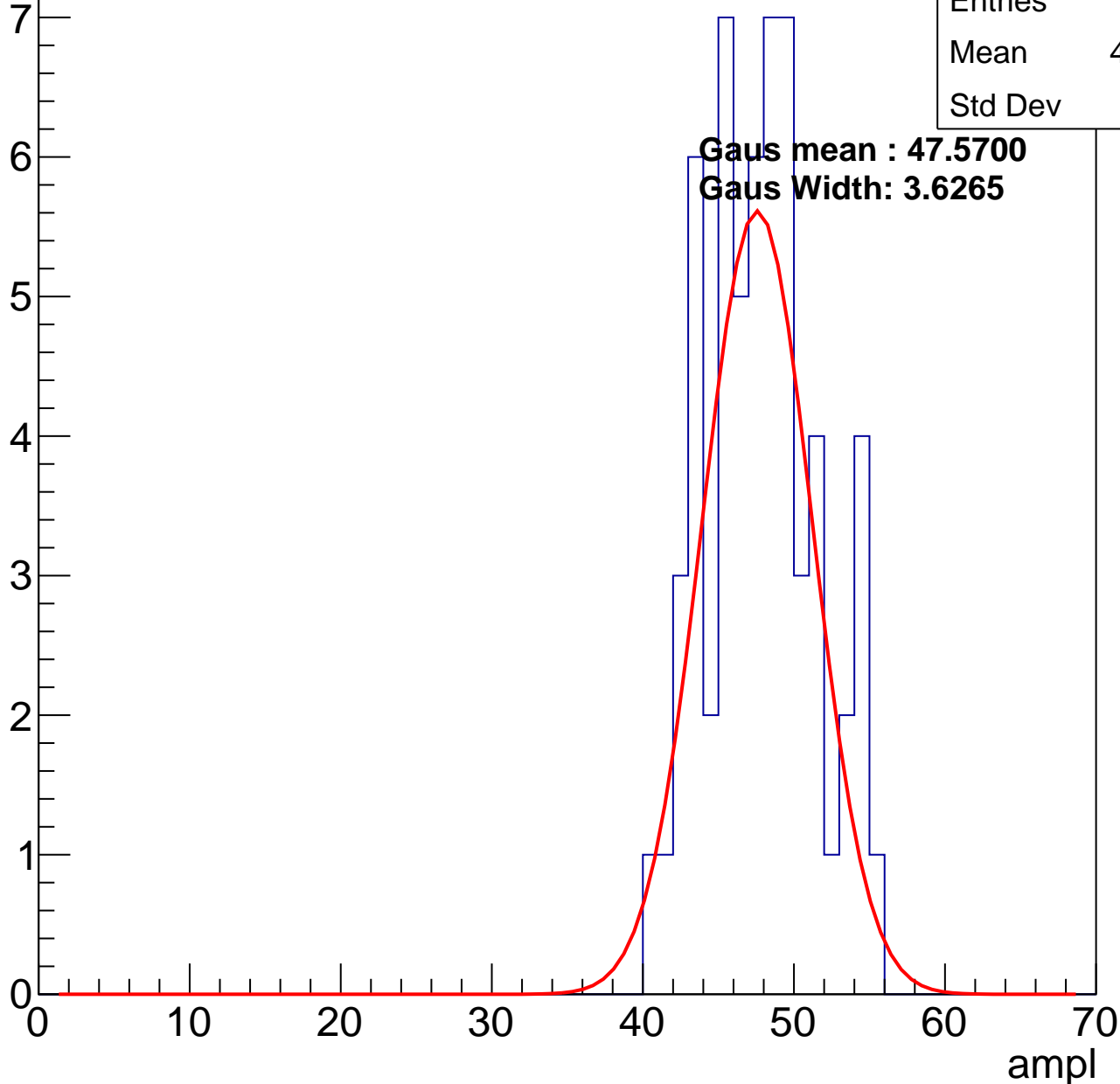


B0L001S, U7-ch57, adc2

calib_packv5_042523_0143.root, FC#9, port A1

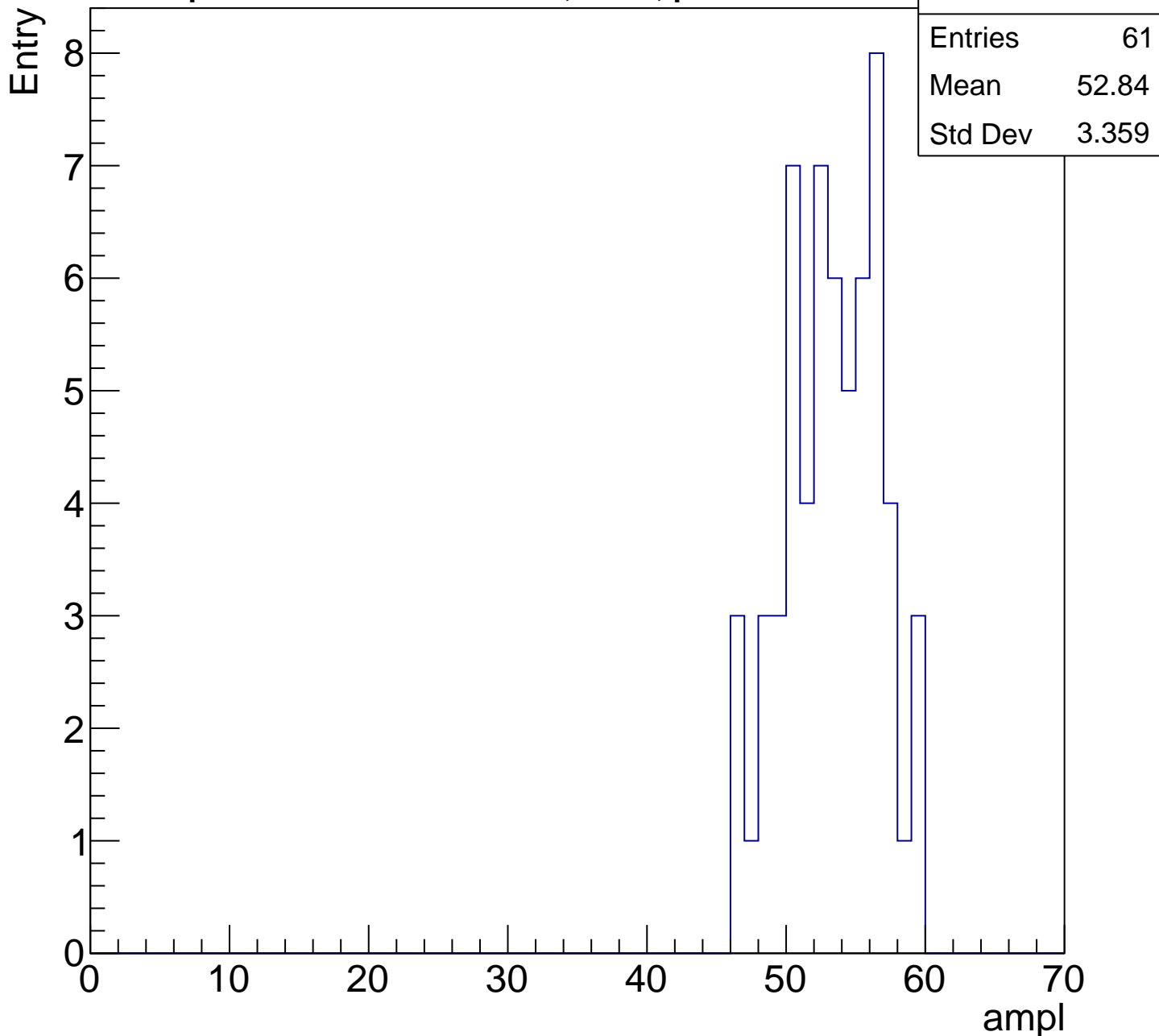
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.37 |
| Std Dev | 3.61 |



B0L001S, U7-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

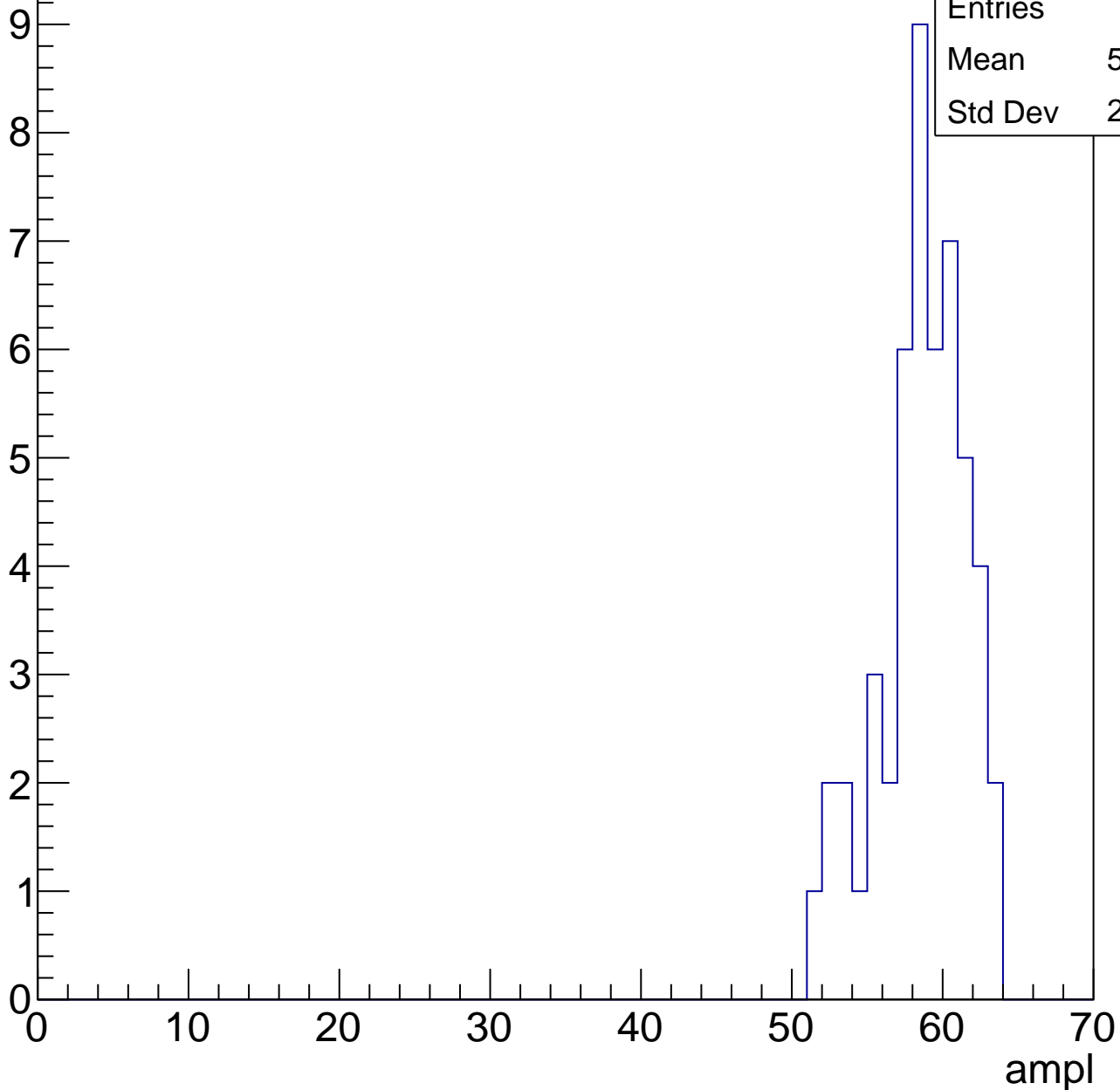


B0L001S, U7-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.18 |
| Std Dev | 2.896 |

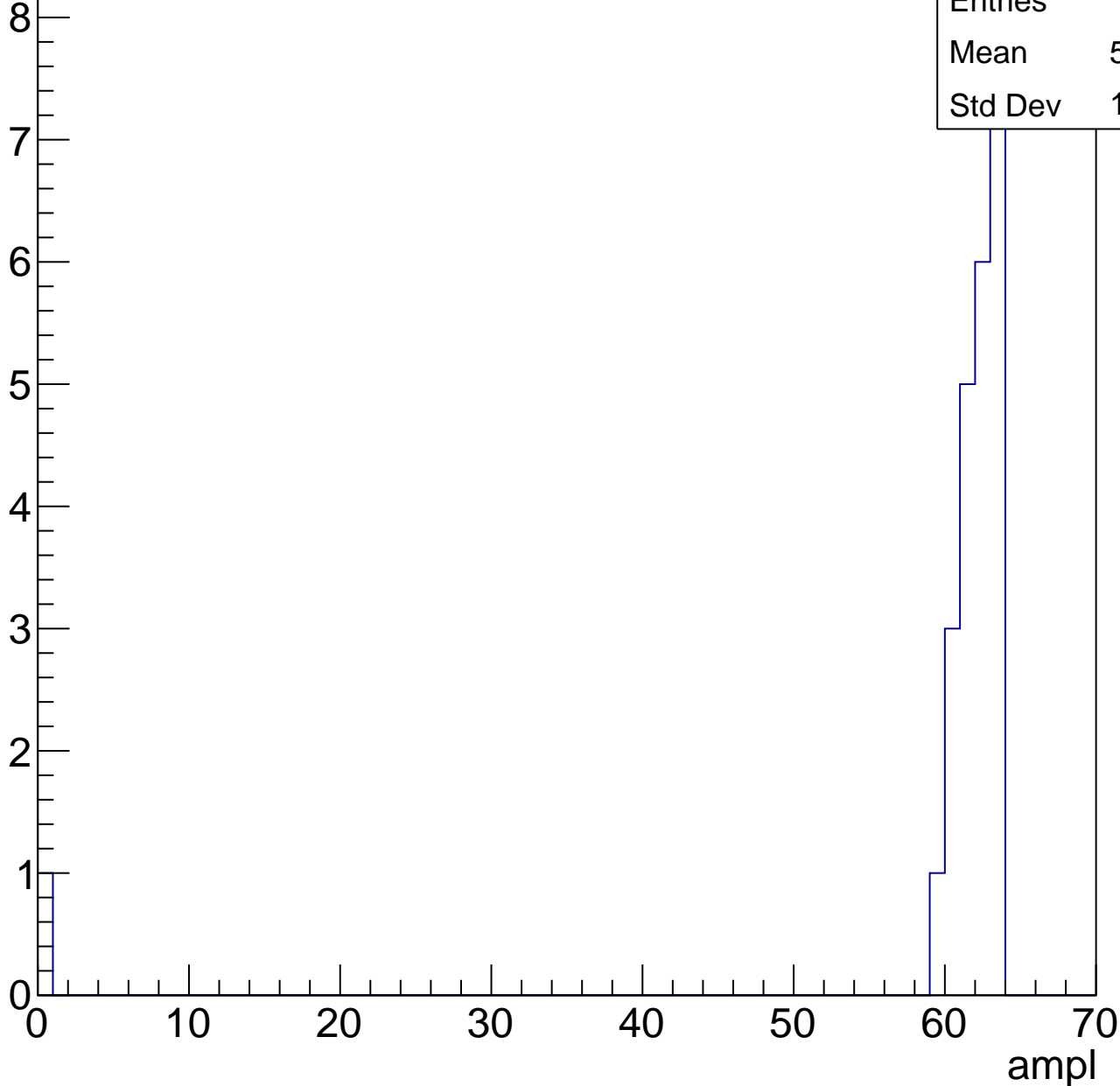


B0L001S, U7-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 59.17 |
| Std Dev | 12.39 |



B0L001S, U7-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch58, adc0

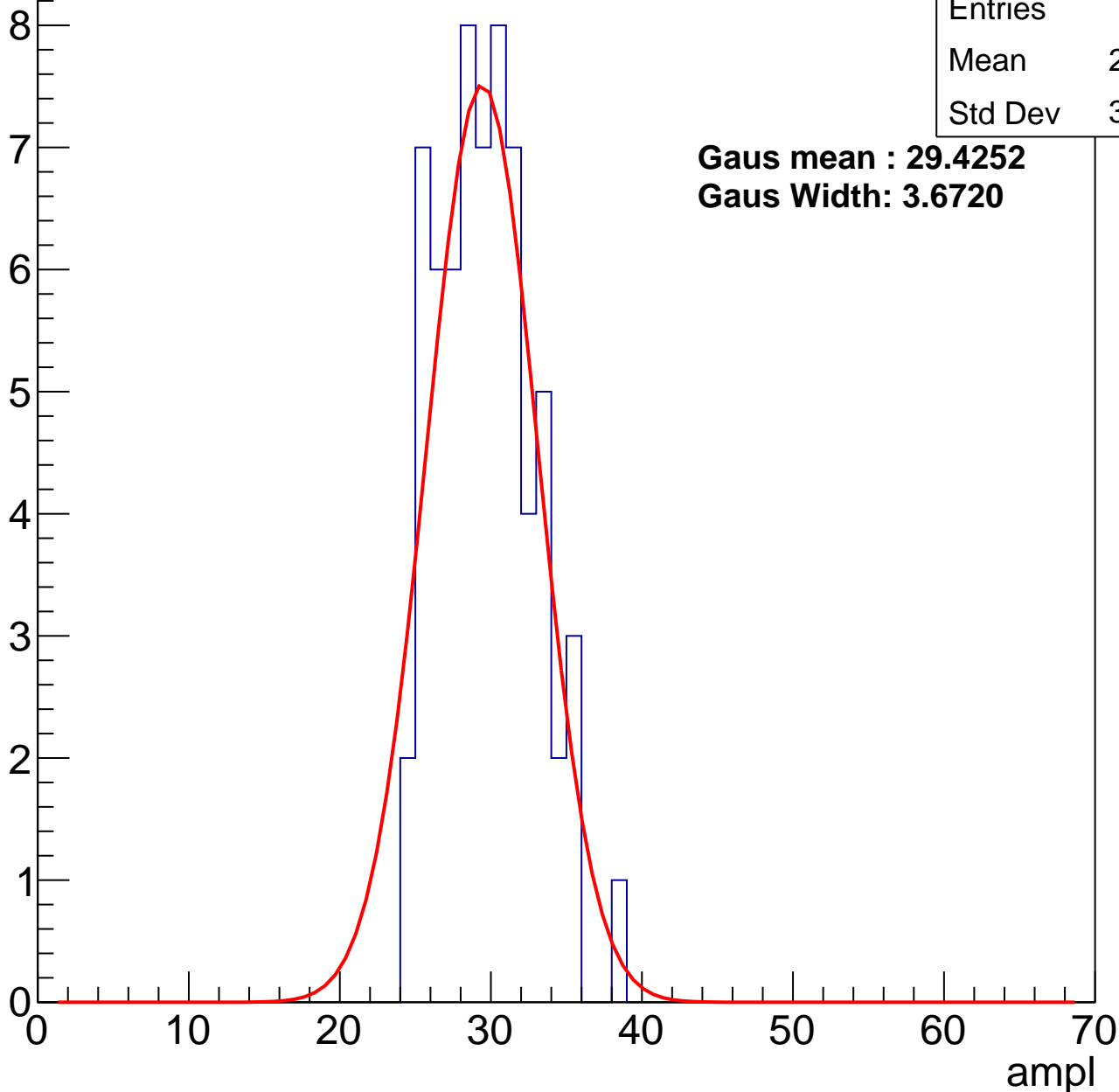
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 29.23 |
| Std Dev | 3.103 |

Gaus mean : 29.4252

Gaus Width: 3.6720



B0L001S, U7-ch58, adc1

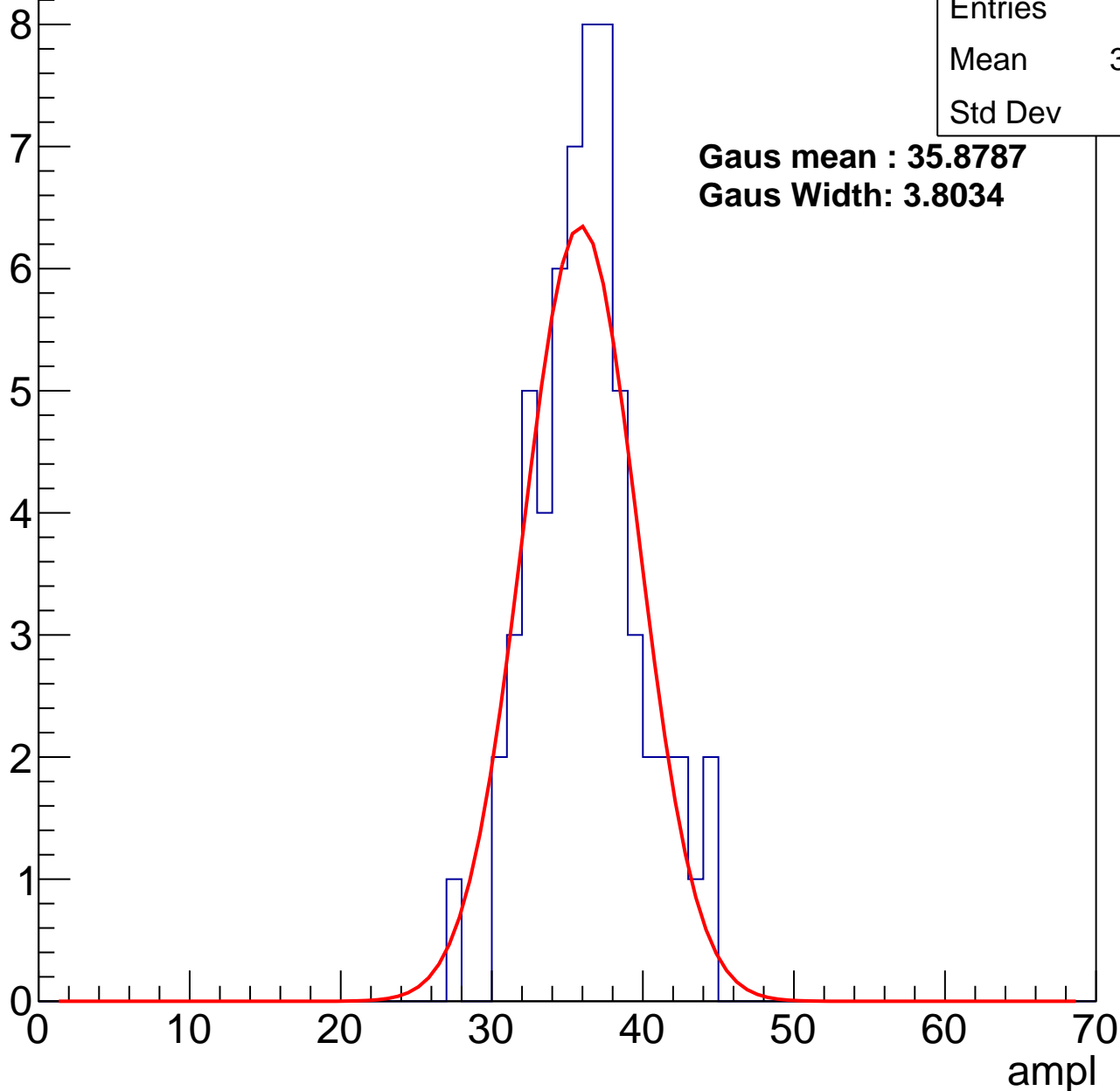
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.89 |
| Std Dev | 3.53 |

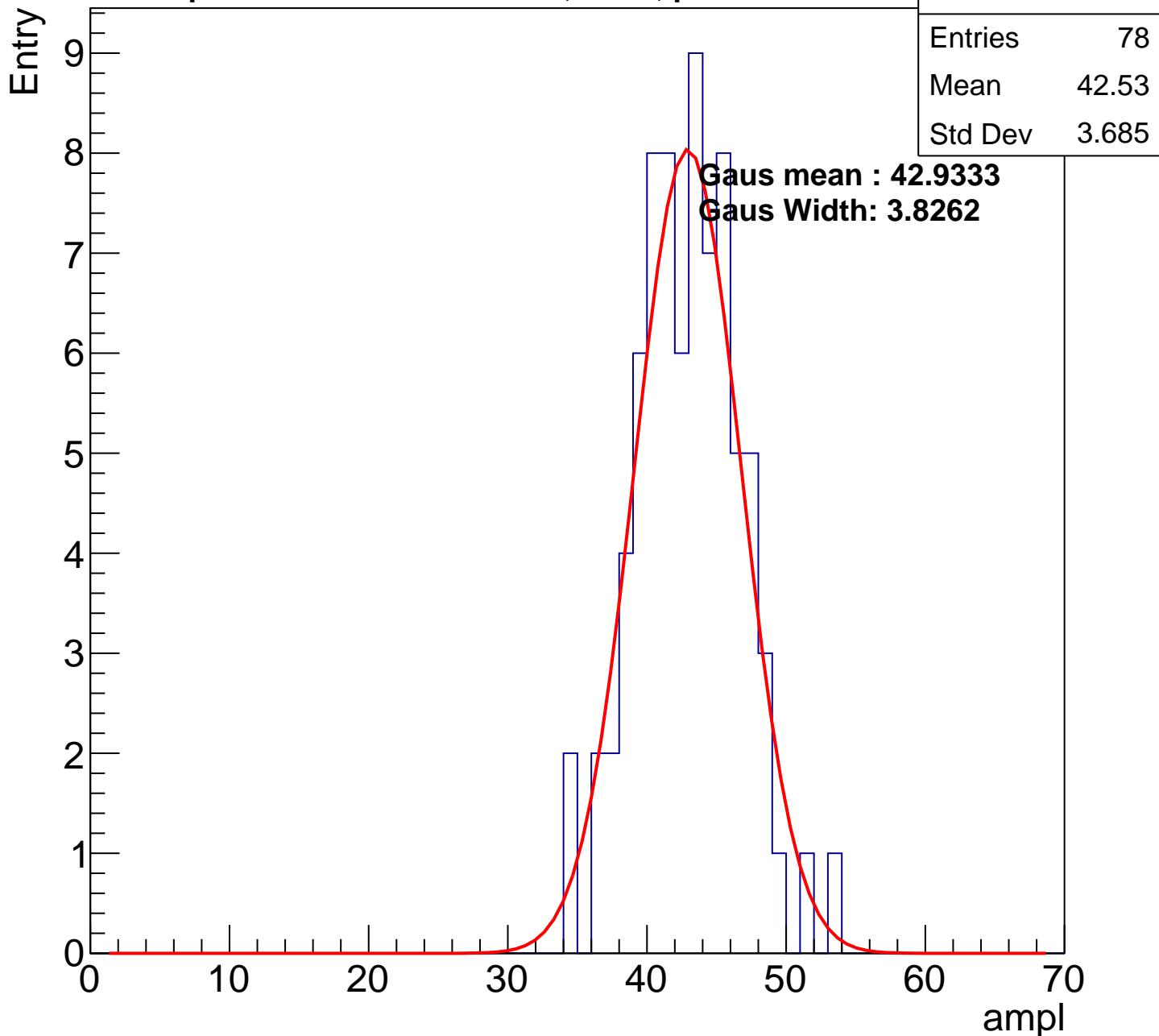
Gaus mean : 35.8787

Gaus Width: 3.8034



B0L001S, U7-ch58, adc2

calib_packv5_042523_0143.root, FC#9, port A1

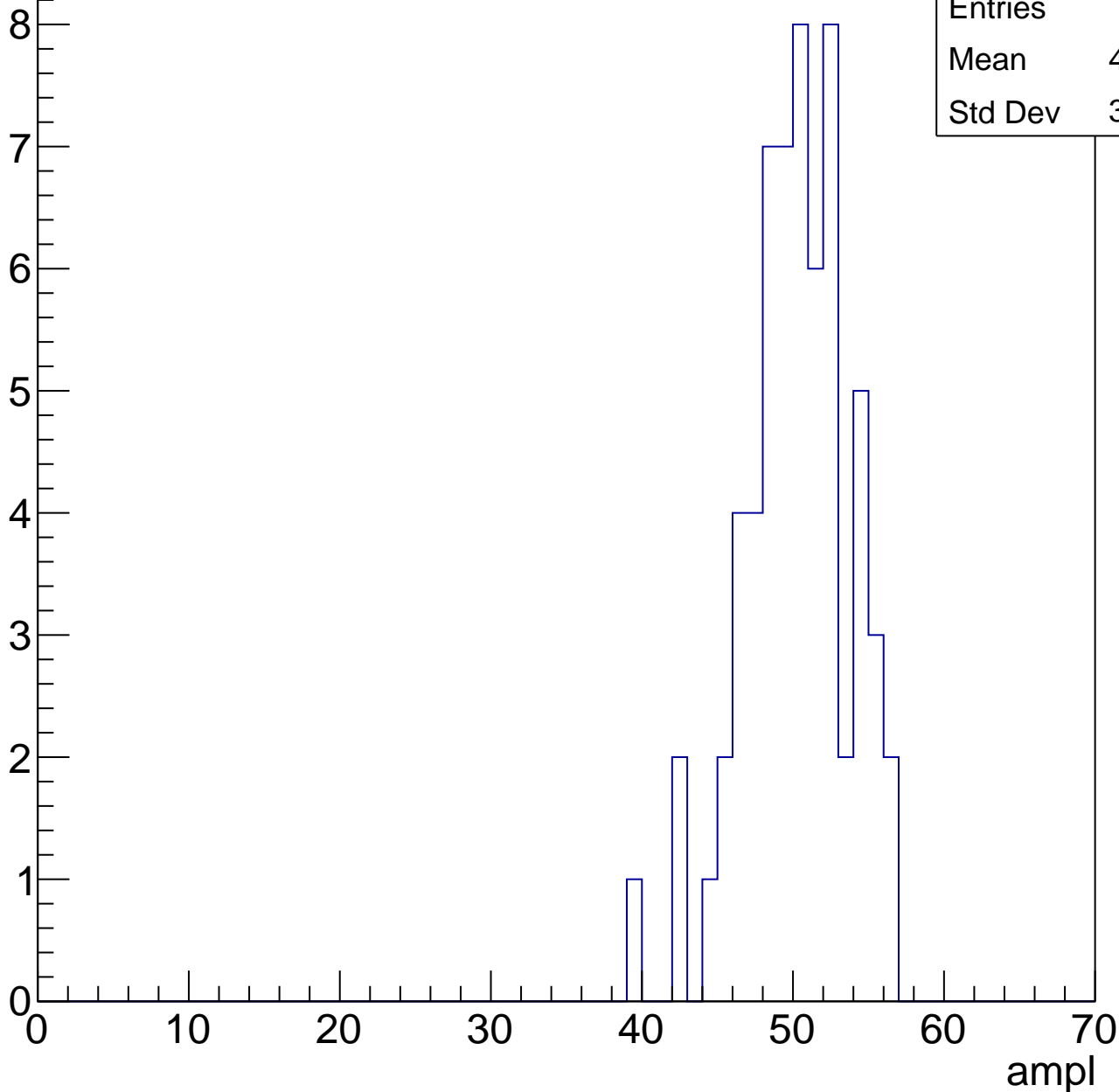


B0L001S, U7-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

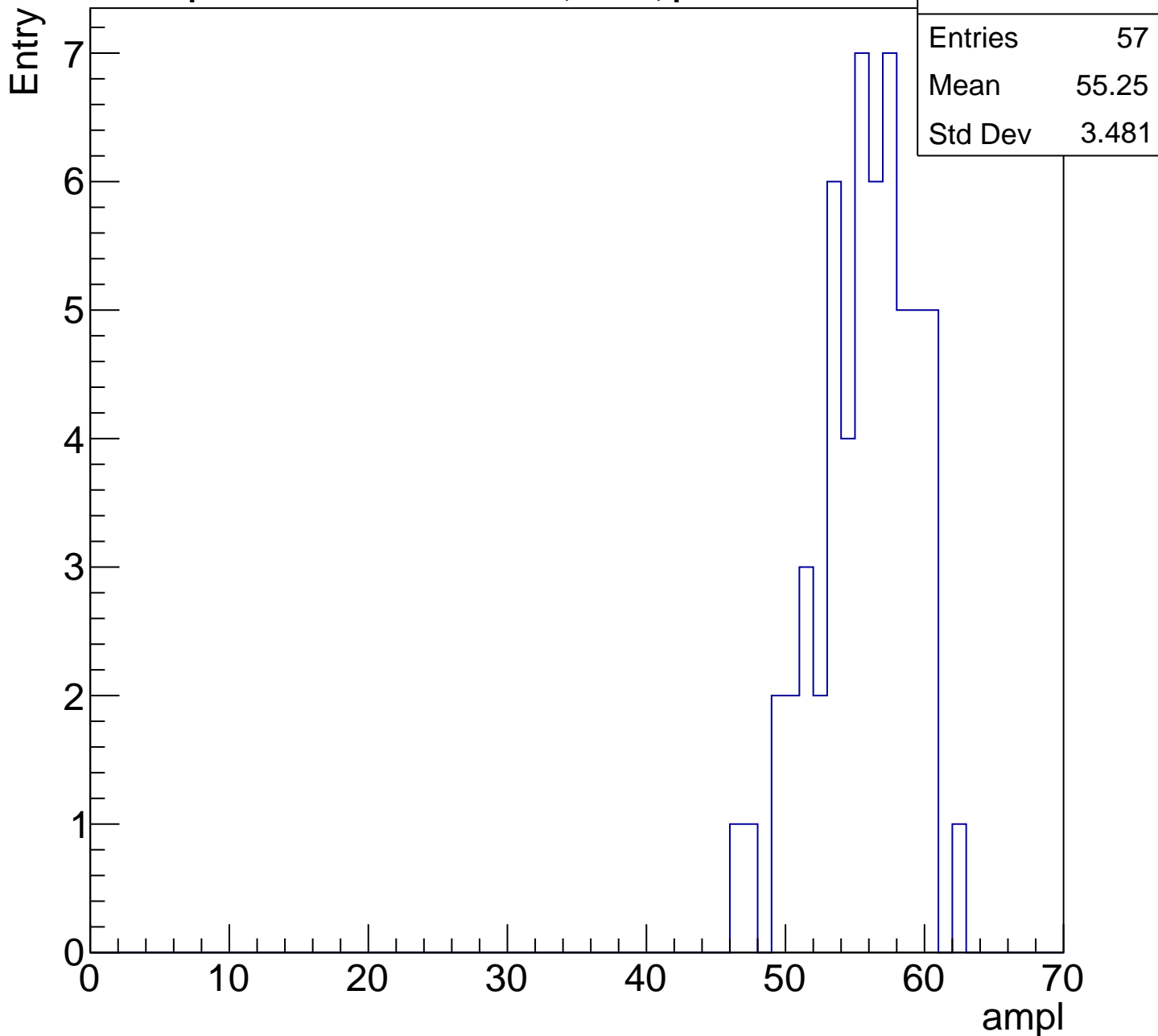
Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.73 |
| Std Dev | 3.474 |



B0L001S, U7-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

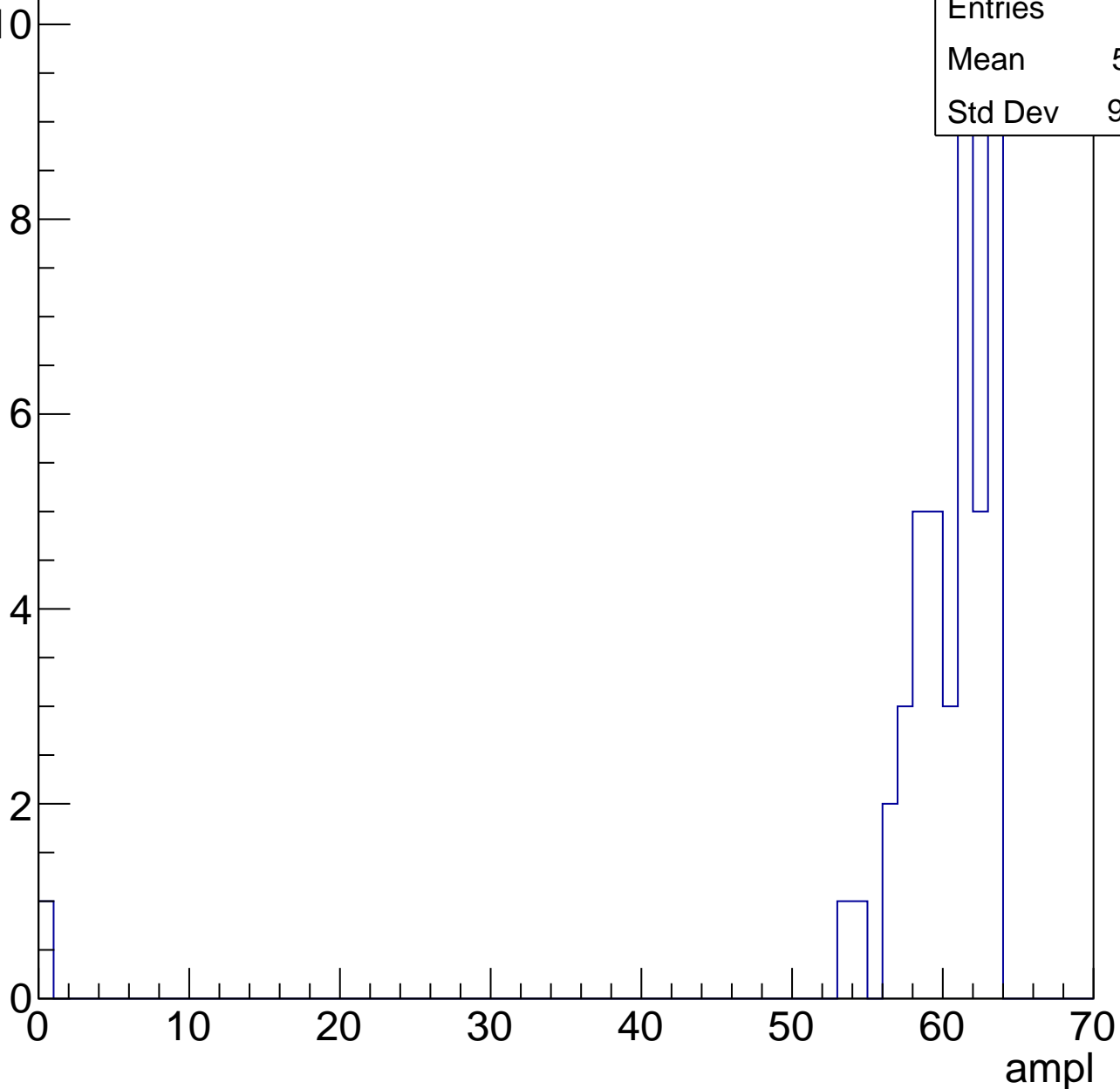


B0L001S, U7-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

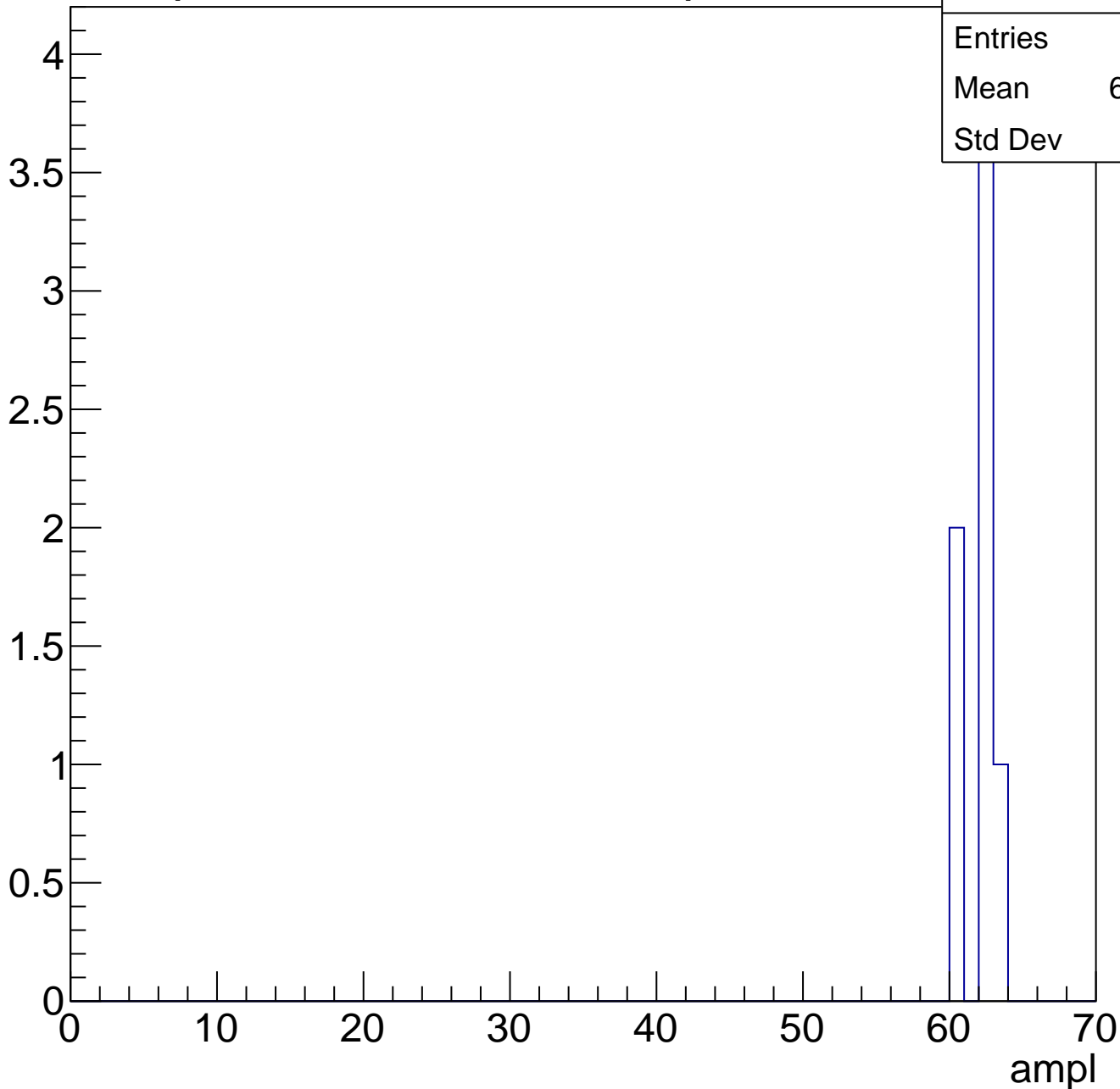
| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.71 |
| Std Dev | 9.196 |



B0L001S, U7-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch59, adc0

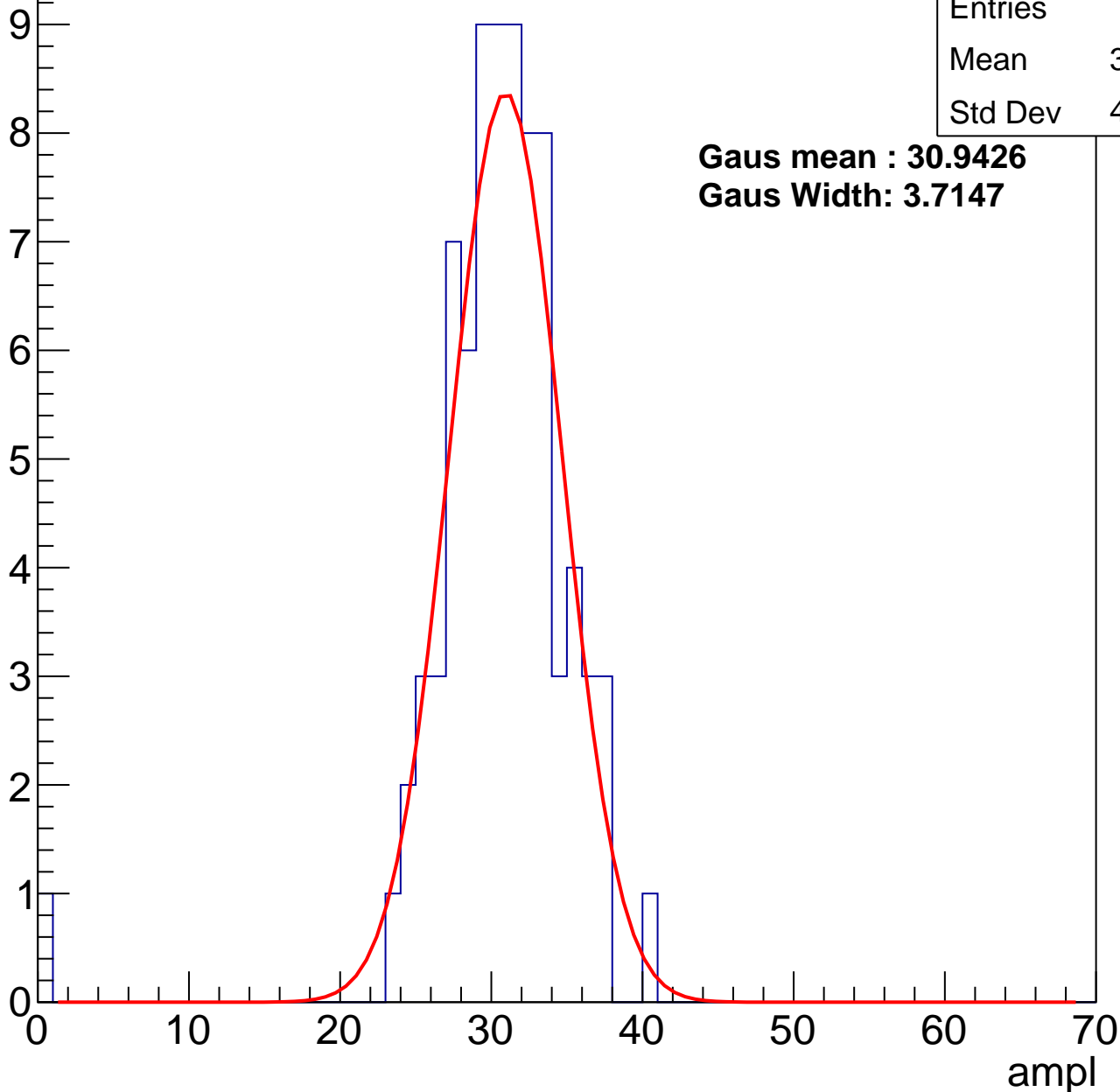
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 30.15 |
| Std Dev | 4.814 |

Gaus mean : 30.9426

Gaus Width: 3.7147



B0L001S, U7-ch59, adc1

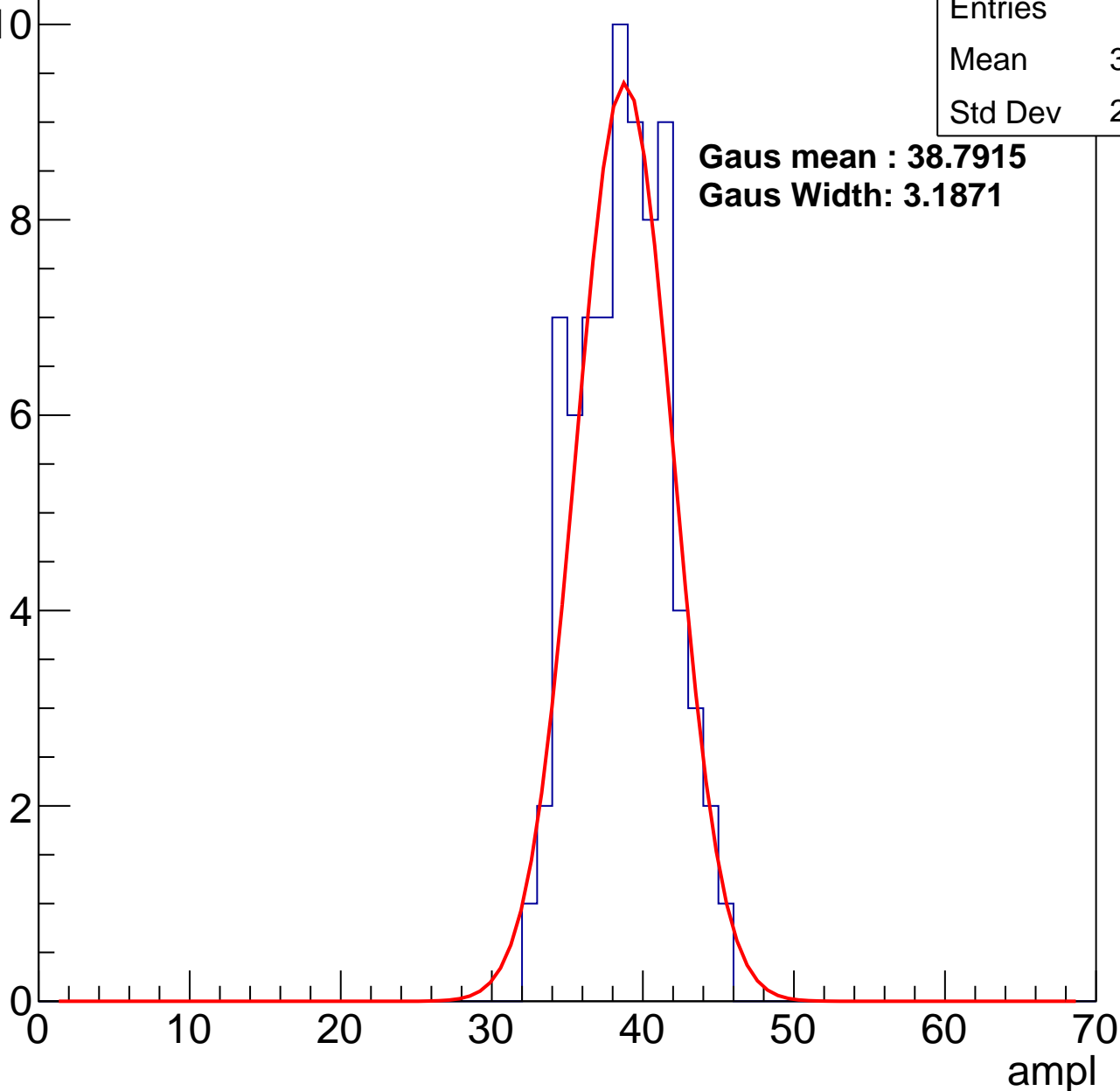
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 38.25 |
| Std Dev | 2.956 |

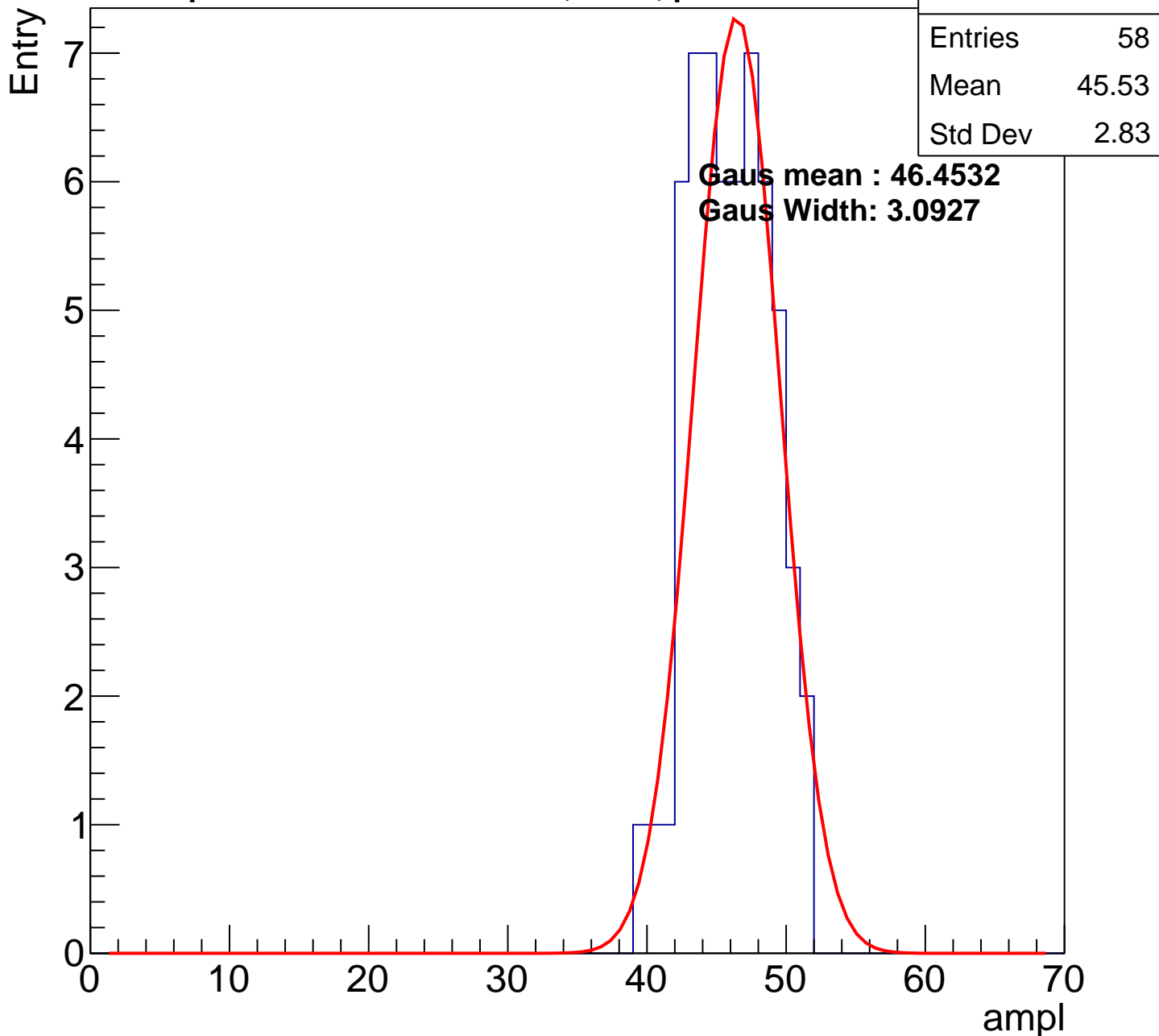
Gaus mean : 38.7915

Gaus Width: 3.1871



B0L001S, U7-ch59, adc2

calib_packv5_042523_0143.root, FC#9, port A1

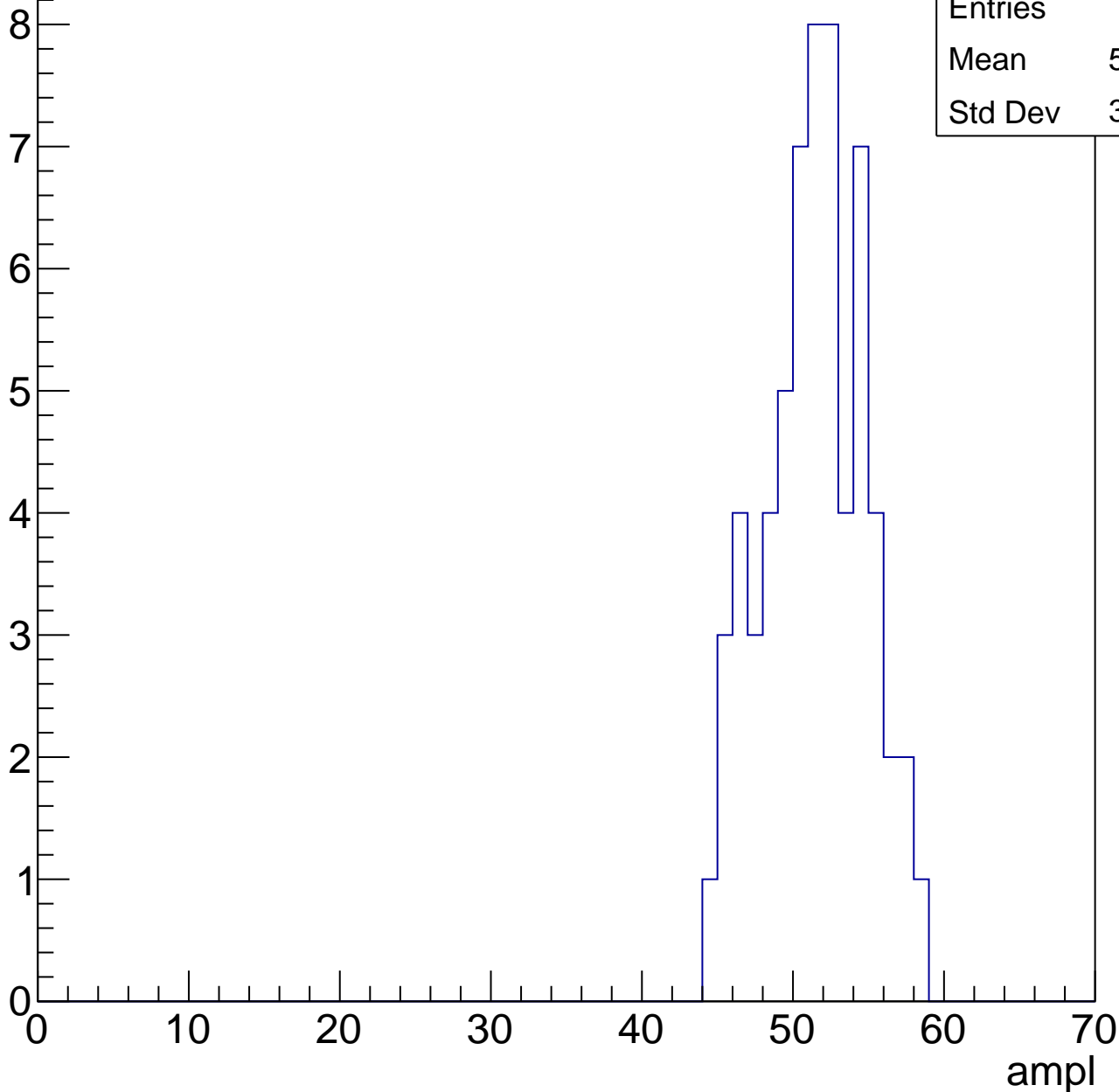


B0L001S, U7-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

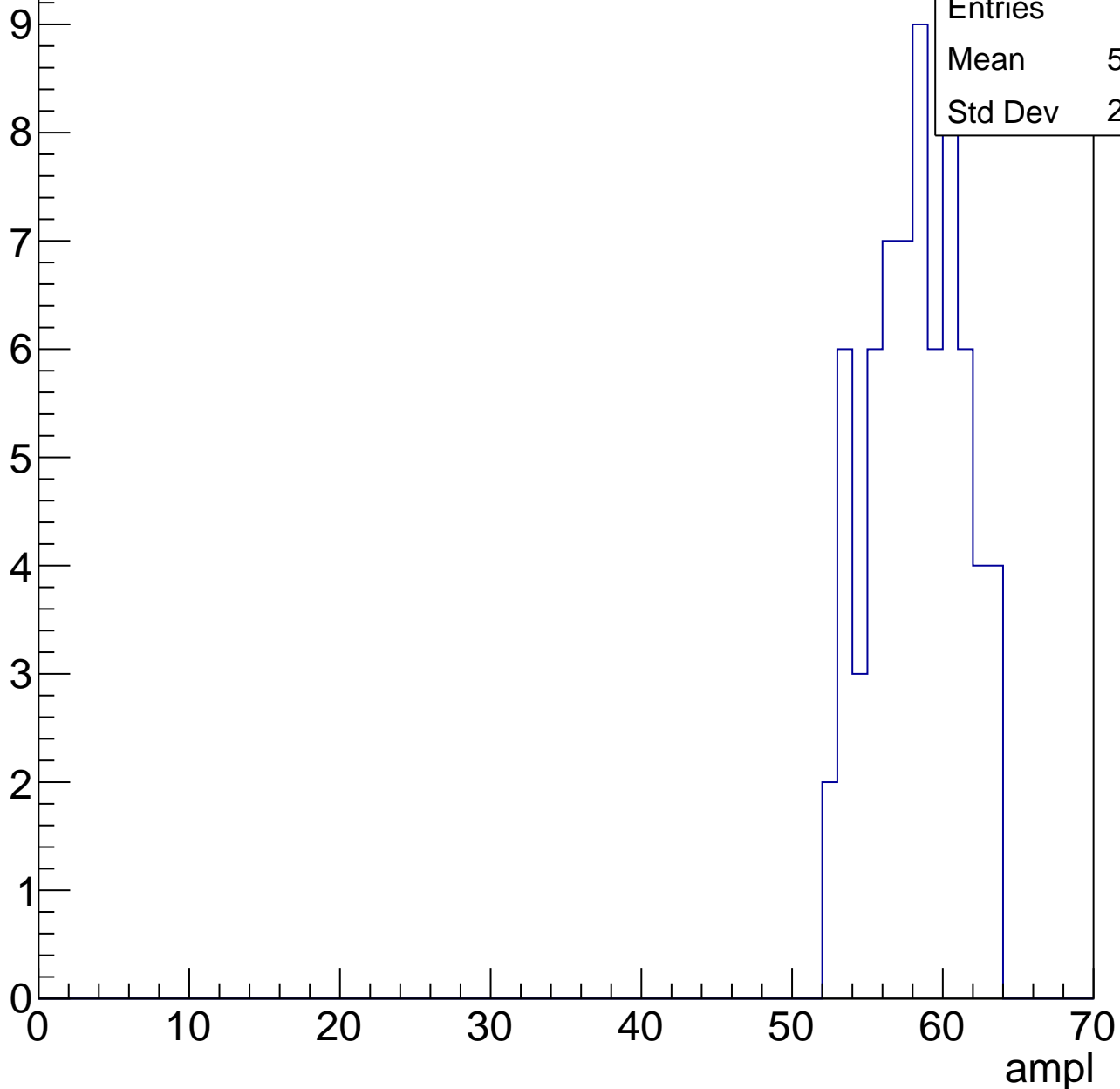
| | |
|---------|-------|
| Entries | 63 |
| Mean | 50.94 |
| Std Dev | 3.309 |



B0L001S, U7-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

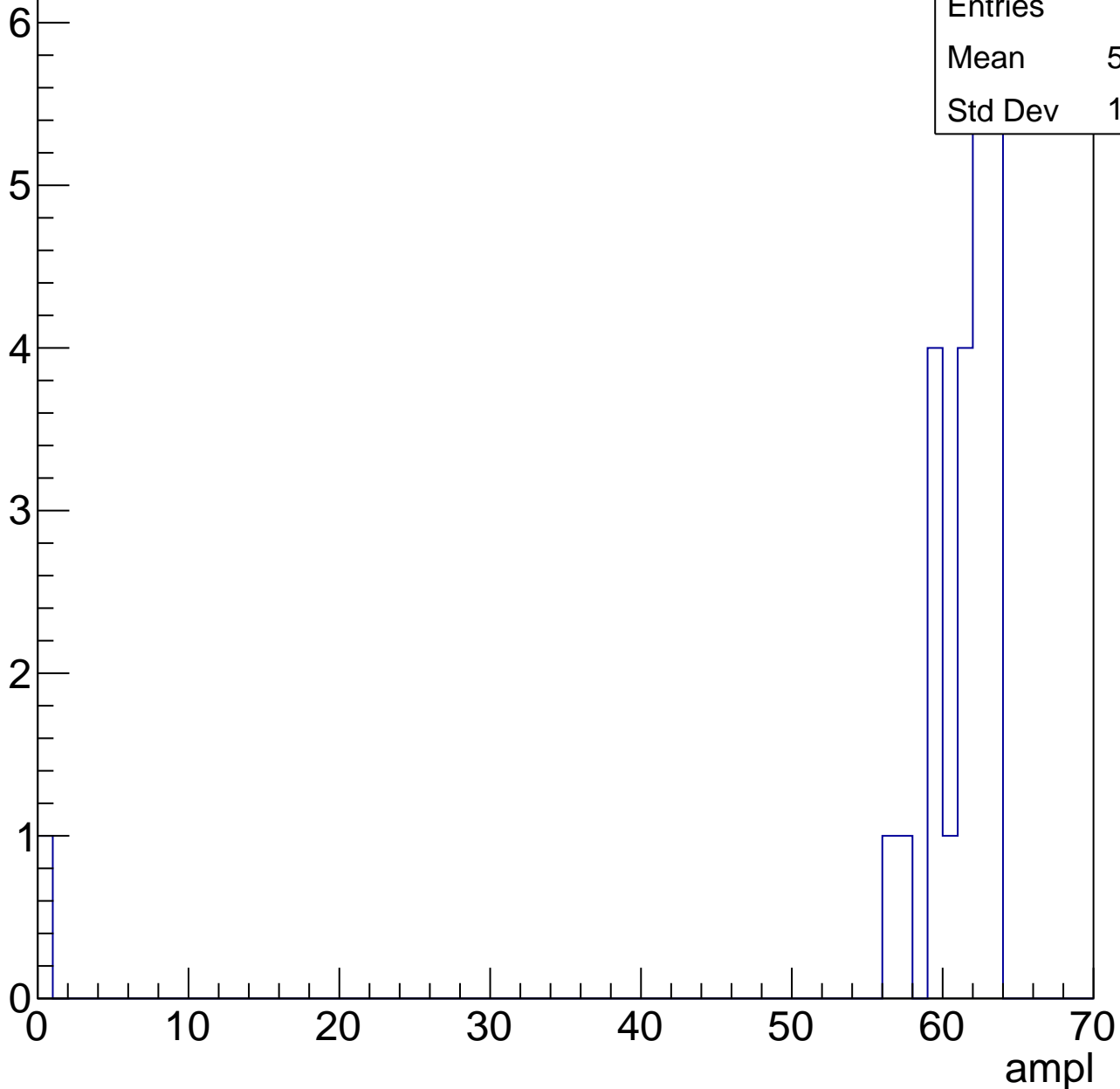


B0L001S, U7-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.46 |
| Std Dev | 12.34 |



B0L001S, U7-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch60, adc0

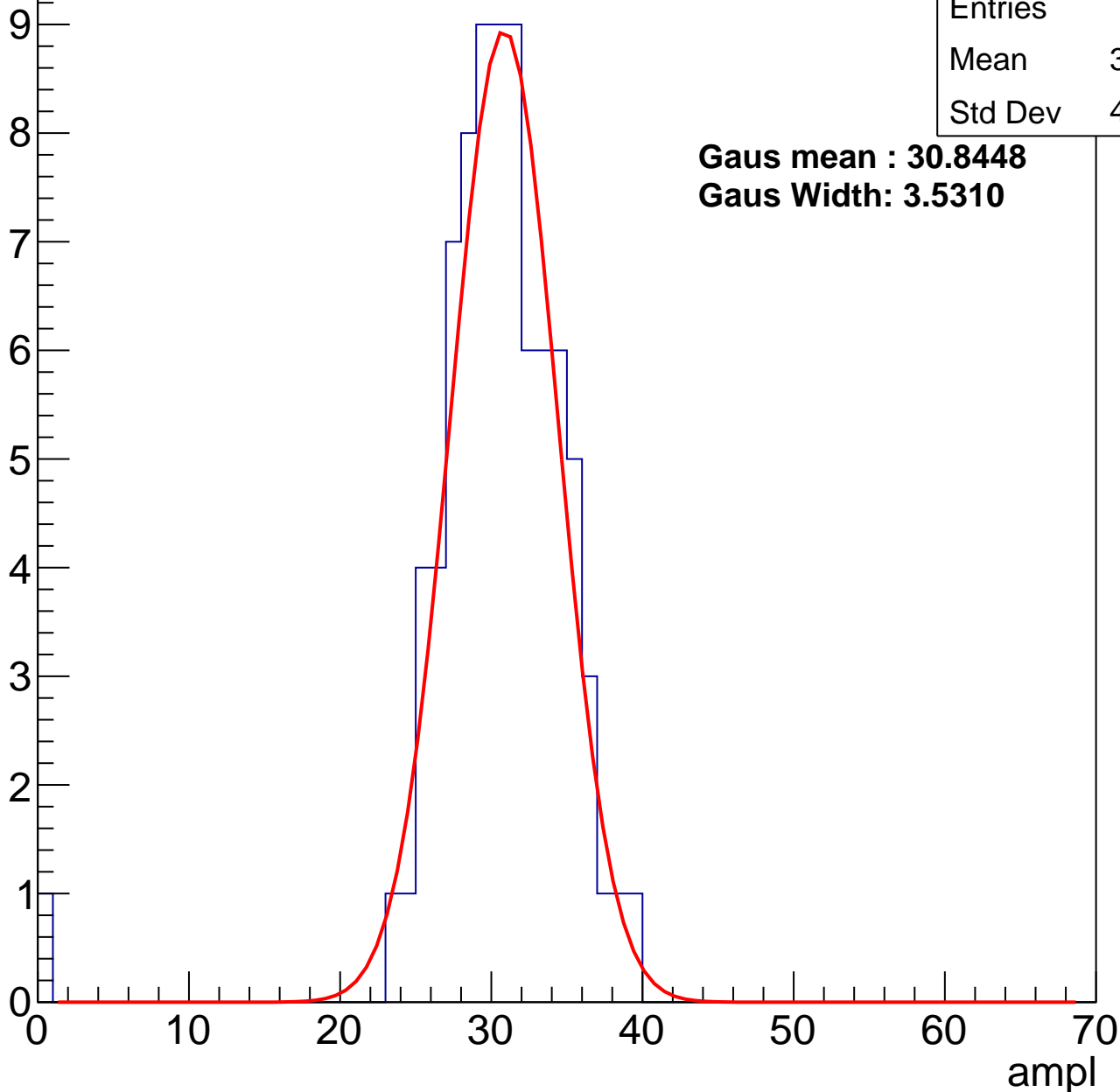
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 30.06 |
| Std Dev | 4.766 |

Gaus mean : 30.8448

Gaus Width: 3.5310



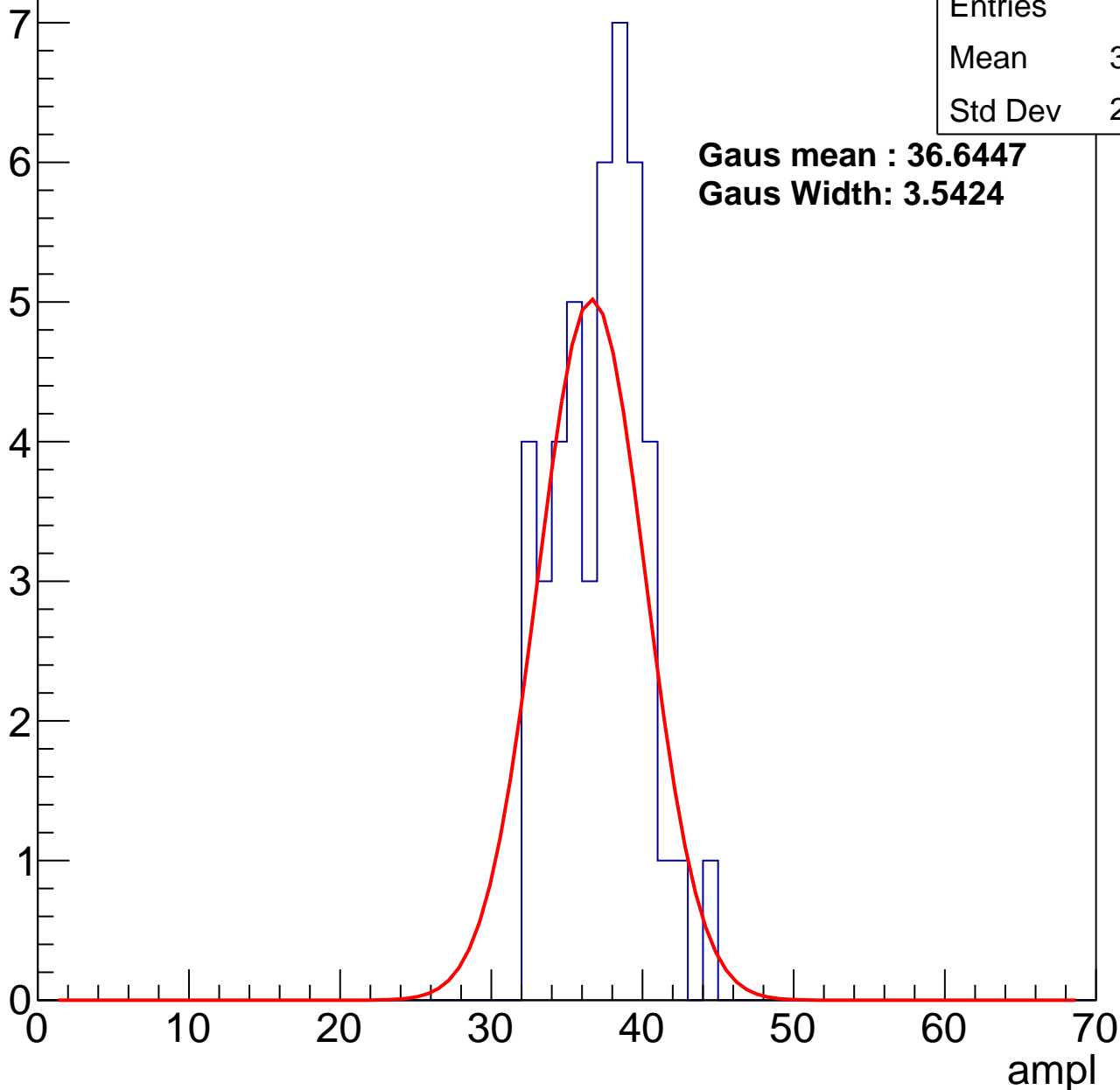
B0L001S, U7-ch60, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 36.78 |
| Std Dev | 2.835 |

Gaus mean : 36.6447
Gaus Width: 3.5424



B0L001S, U7-ch60, adc2

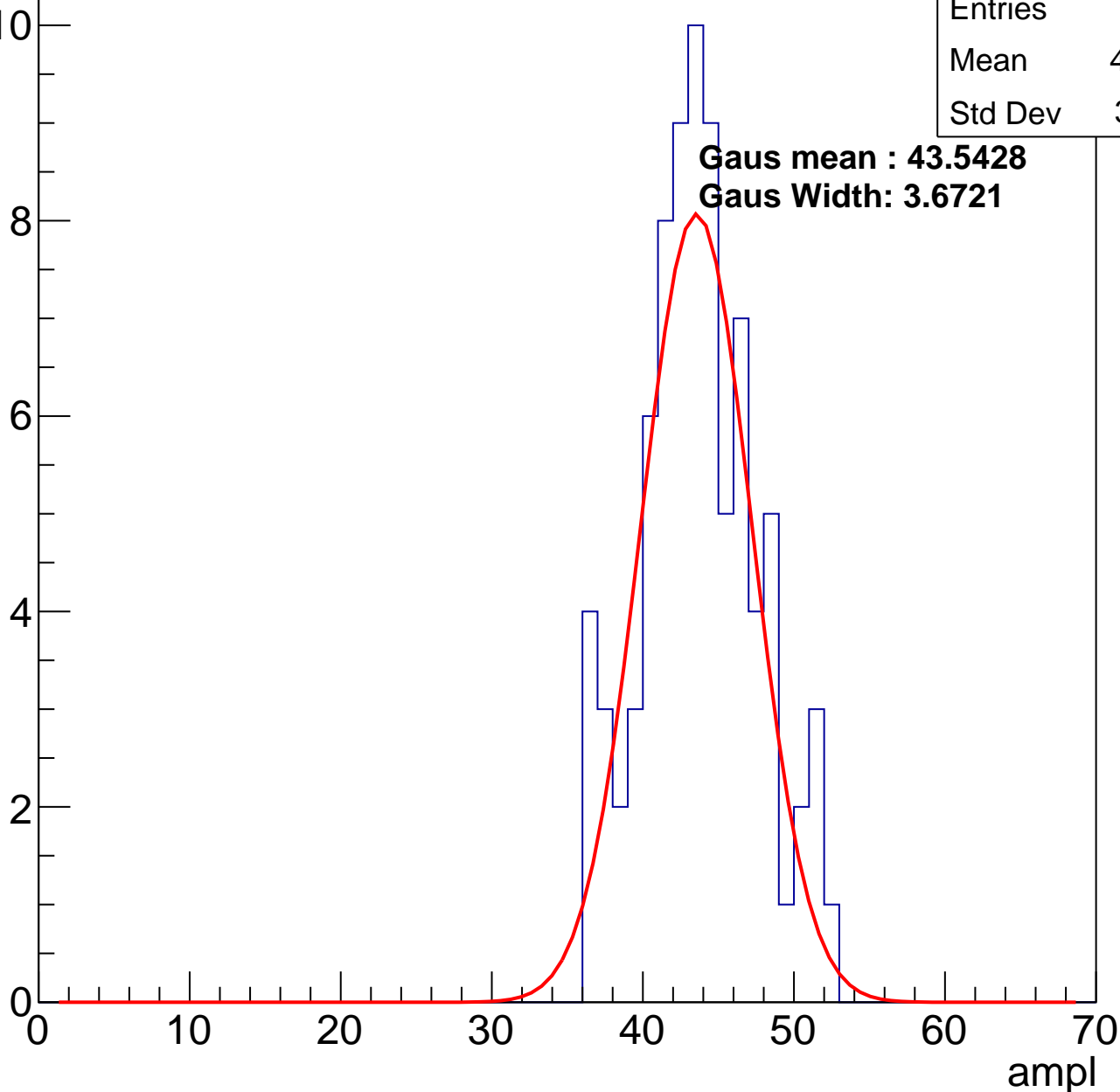
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 43.28 |
| Std Dev | 3.791 |

Gaus mean : 43.5428

Gaus Width: 3.6721

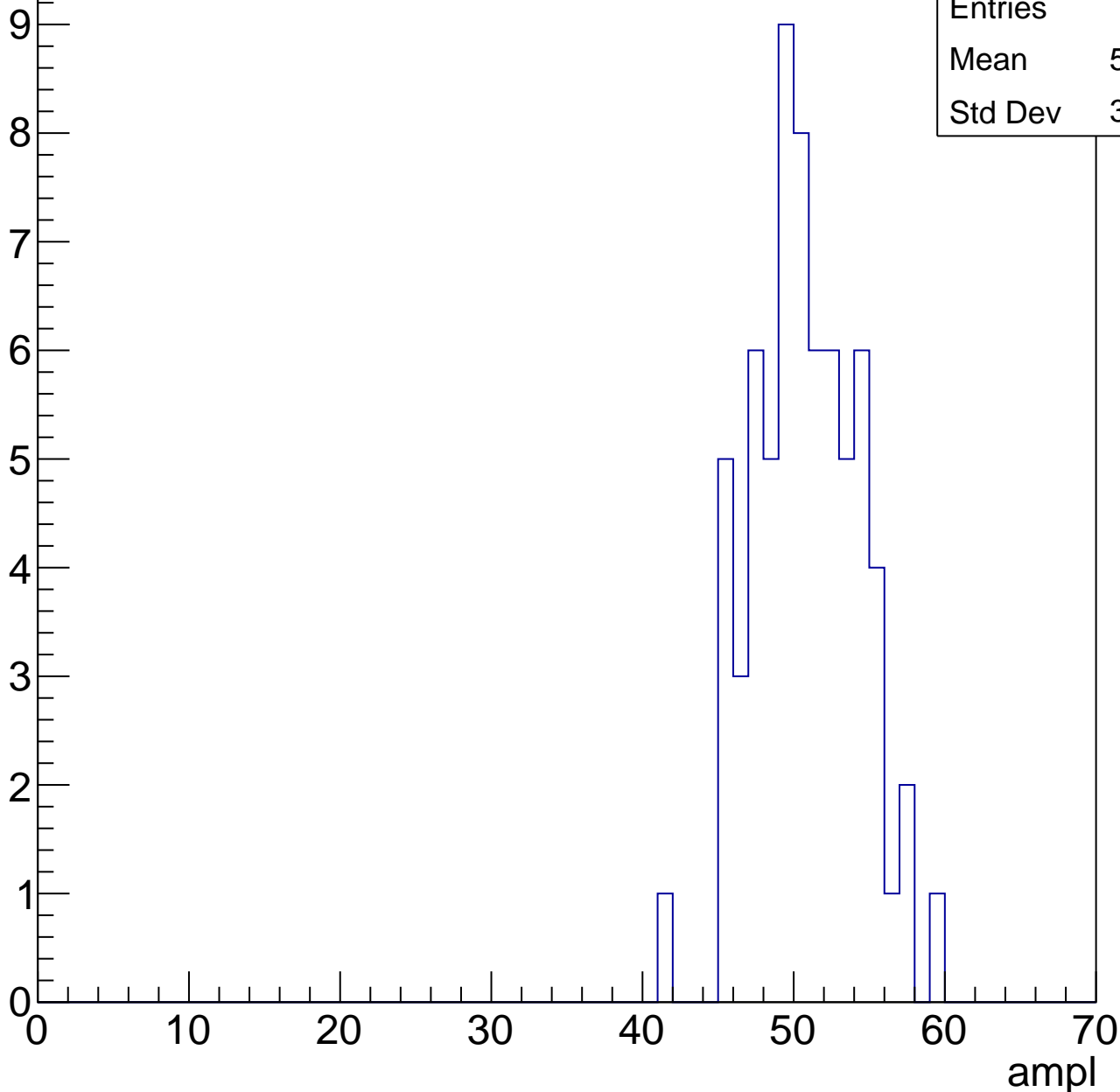


B0L001S, U7-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

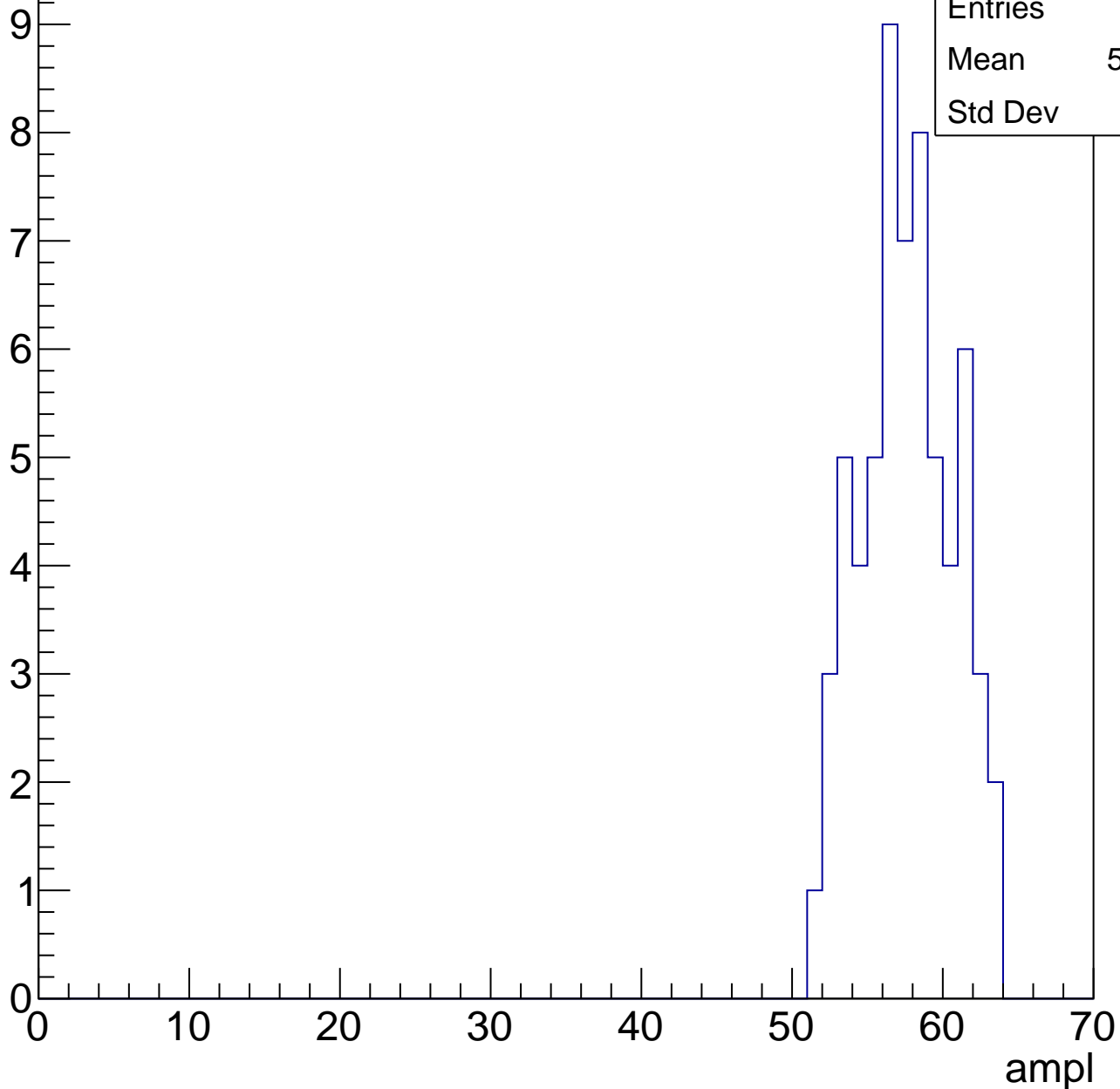
| | |
|---------|-------|
| Entries | 68 |
| Mean | 50.34 |
| Std Dev | 3.462 |



B0L001S, U7-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



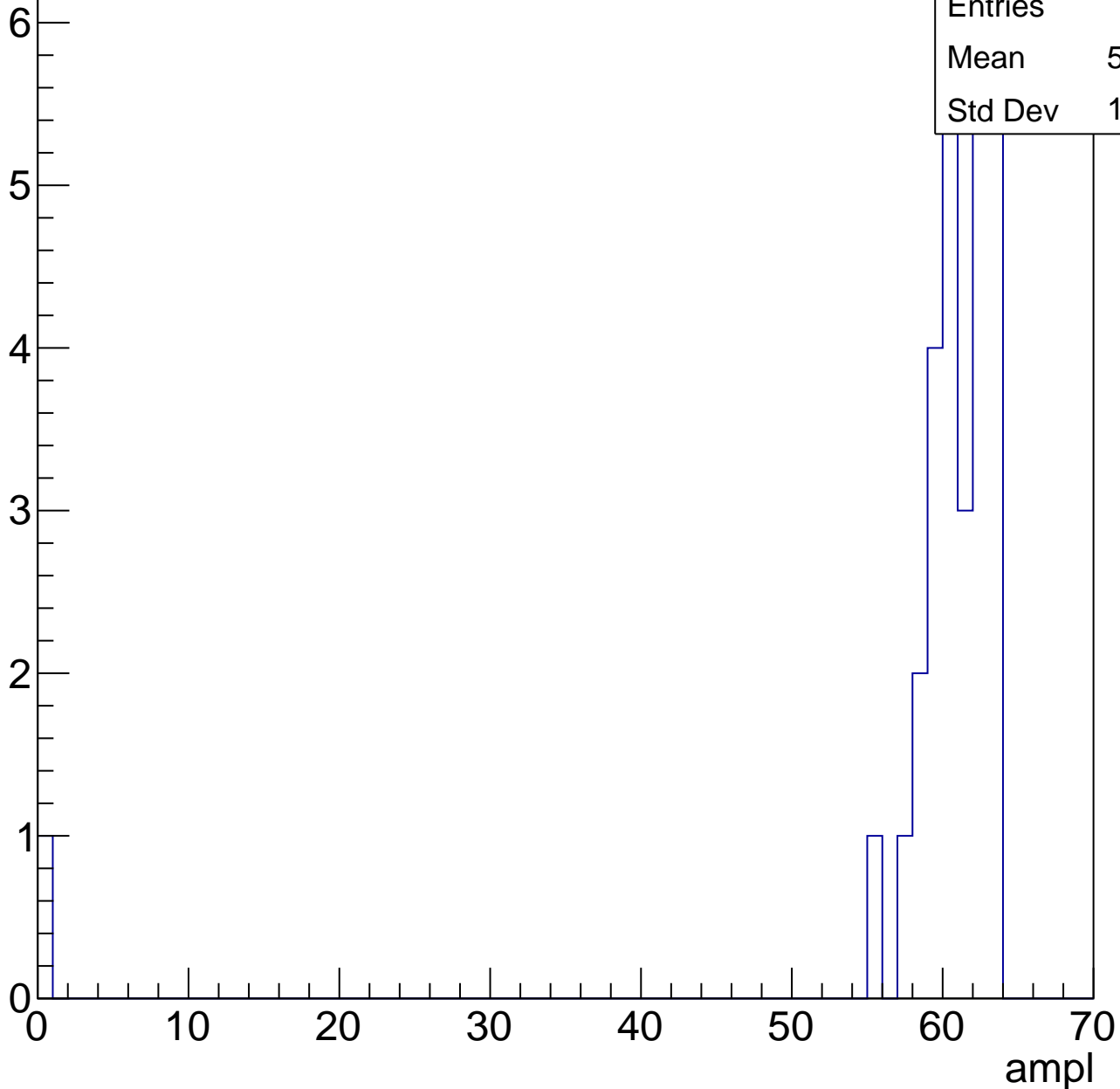
| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.15 |
| Std Dev | 3.01 |

B0L001S, U7-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

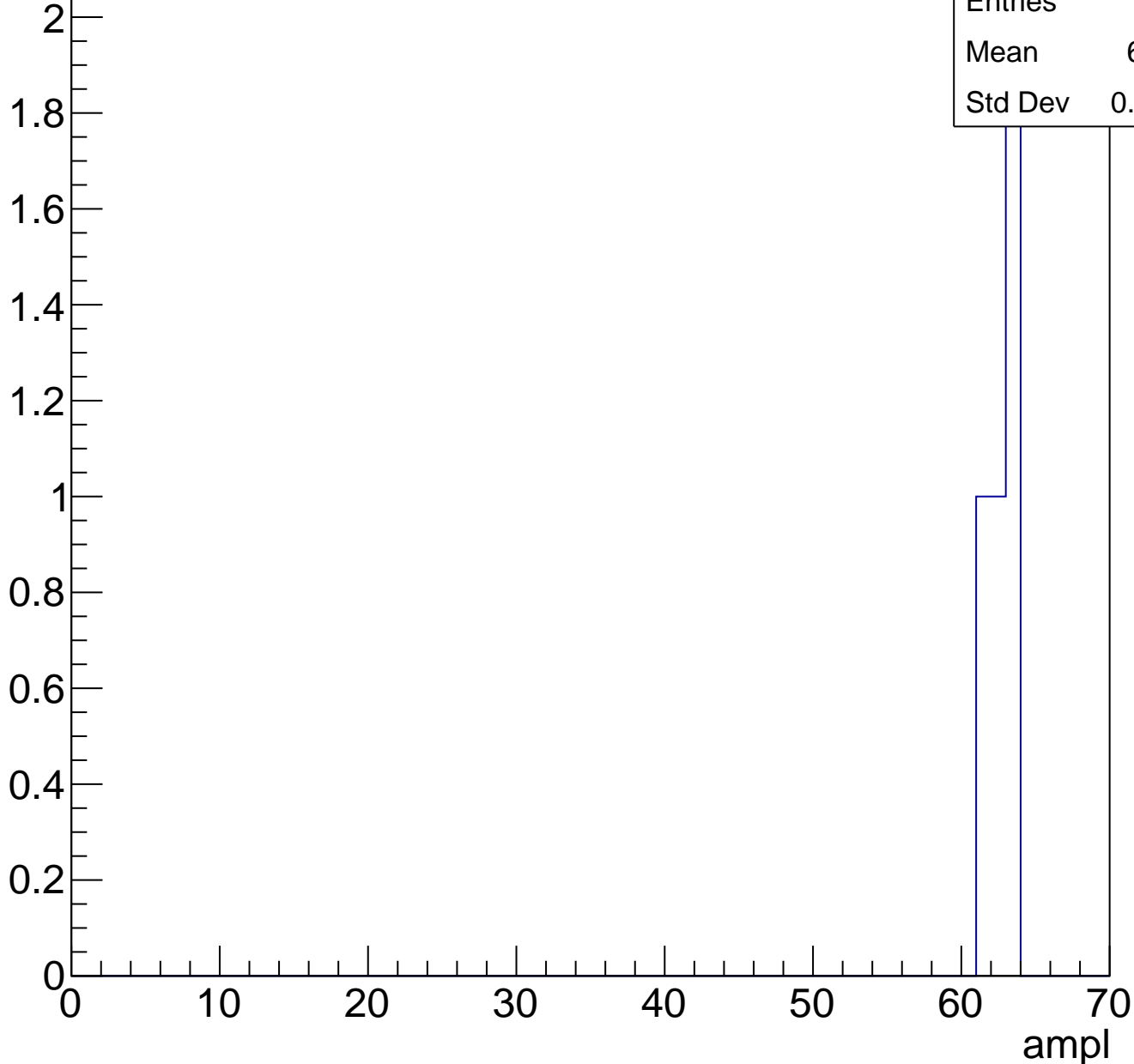
| | |
|---------|-------|
| Entries | 30 |
| Mean | 58.57 |
| Std Dev | 11.05 |



B0L001S, U7-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch61, adc0

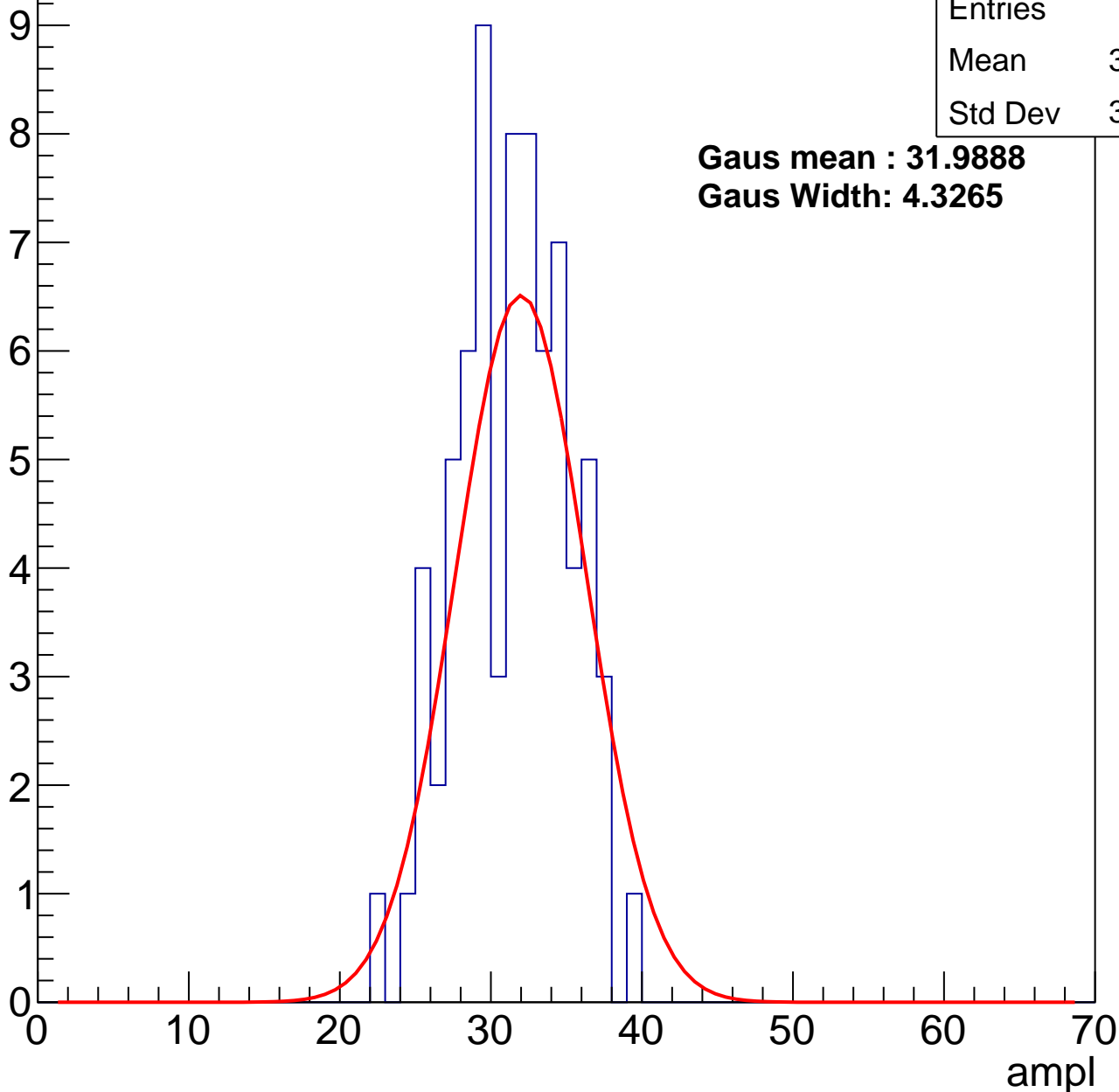
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 30.99 |
| Std Dev | 3.613 |

Gaus mean : 31.9888

Gaus Width: 4.3265



B0L001S, U7-ch61, adc1

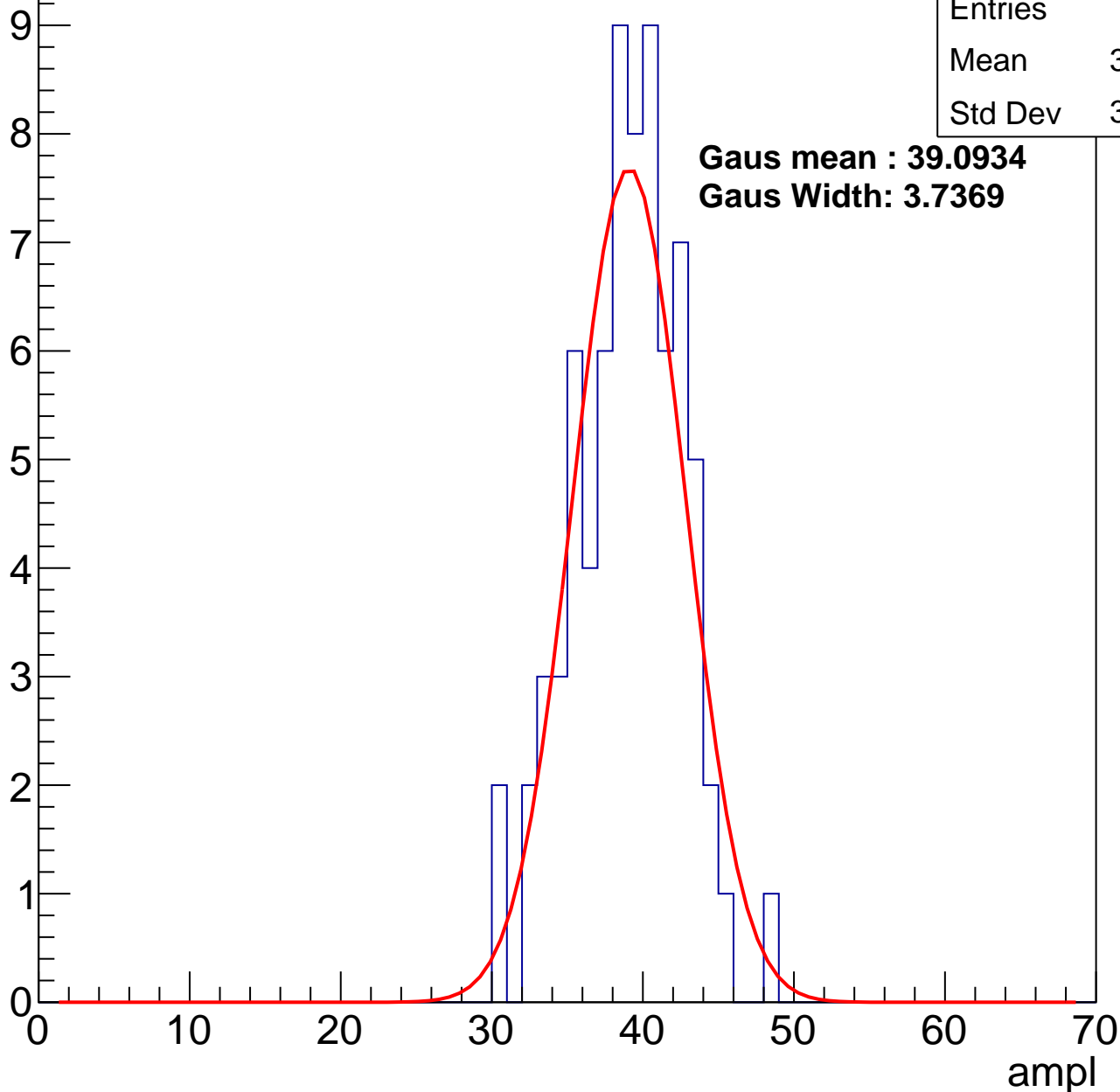
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 38.53 |
| Std Dev | 3.554 |

Gaus mean : 39.0934

Gaus Width: 3.7369



B0L001S, U7-ch61, adc2

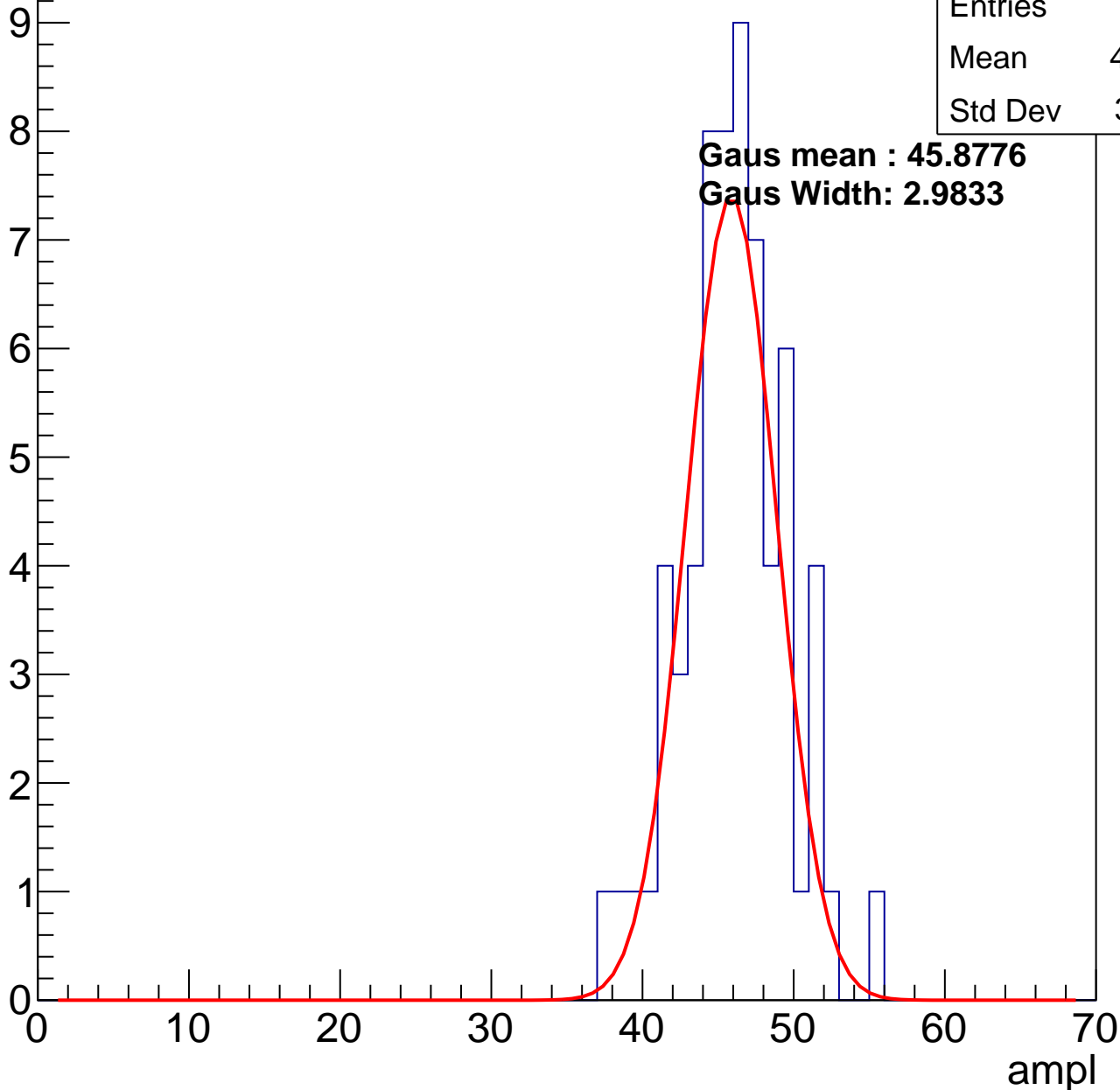
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.59 |
| Std Dev | 3.431 |

Gaus mean : 45.8776

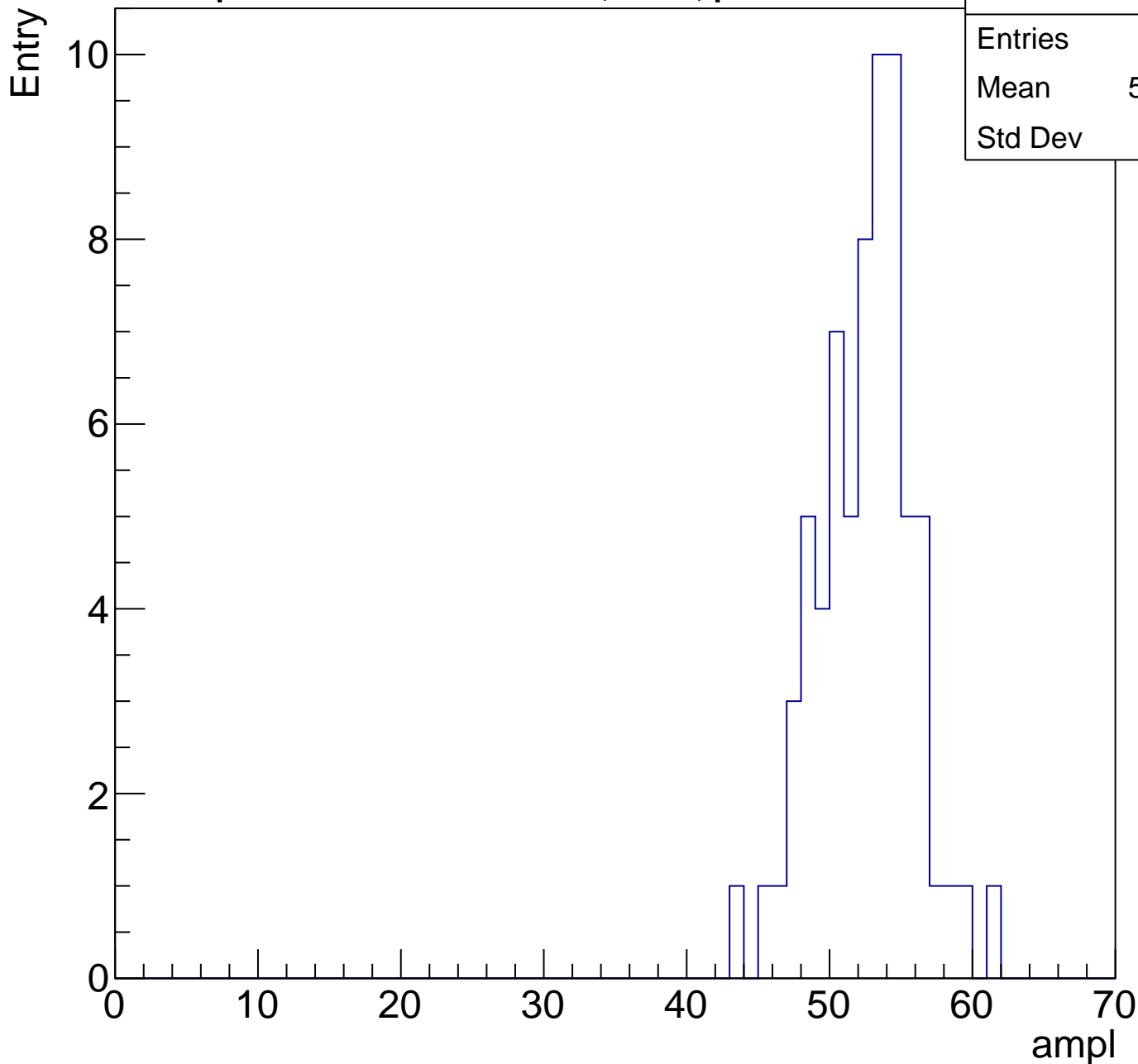
Gaus Width: 2.9833



B0L001S, U7-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

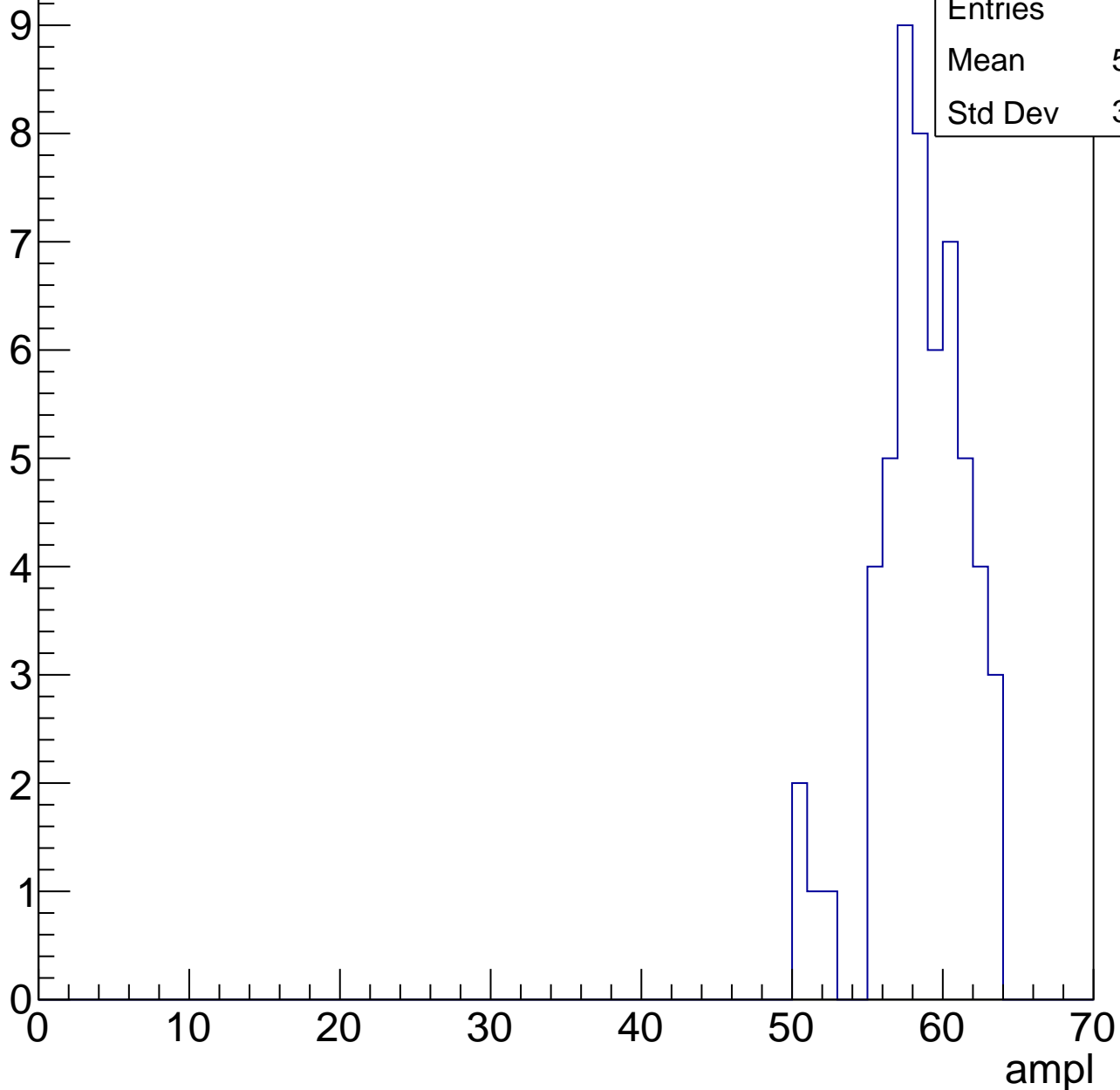
| | |
|---------|-------|
| Entries | 69 |
| Mean | 52.06 |
| Std Dev | 3.31 |



B0L001S, U7-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

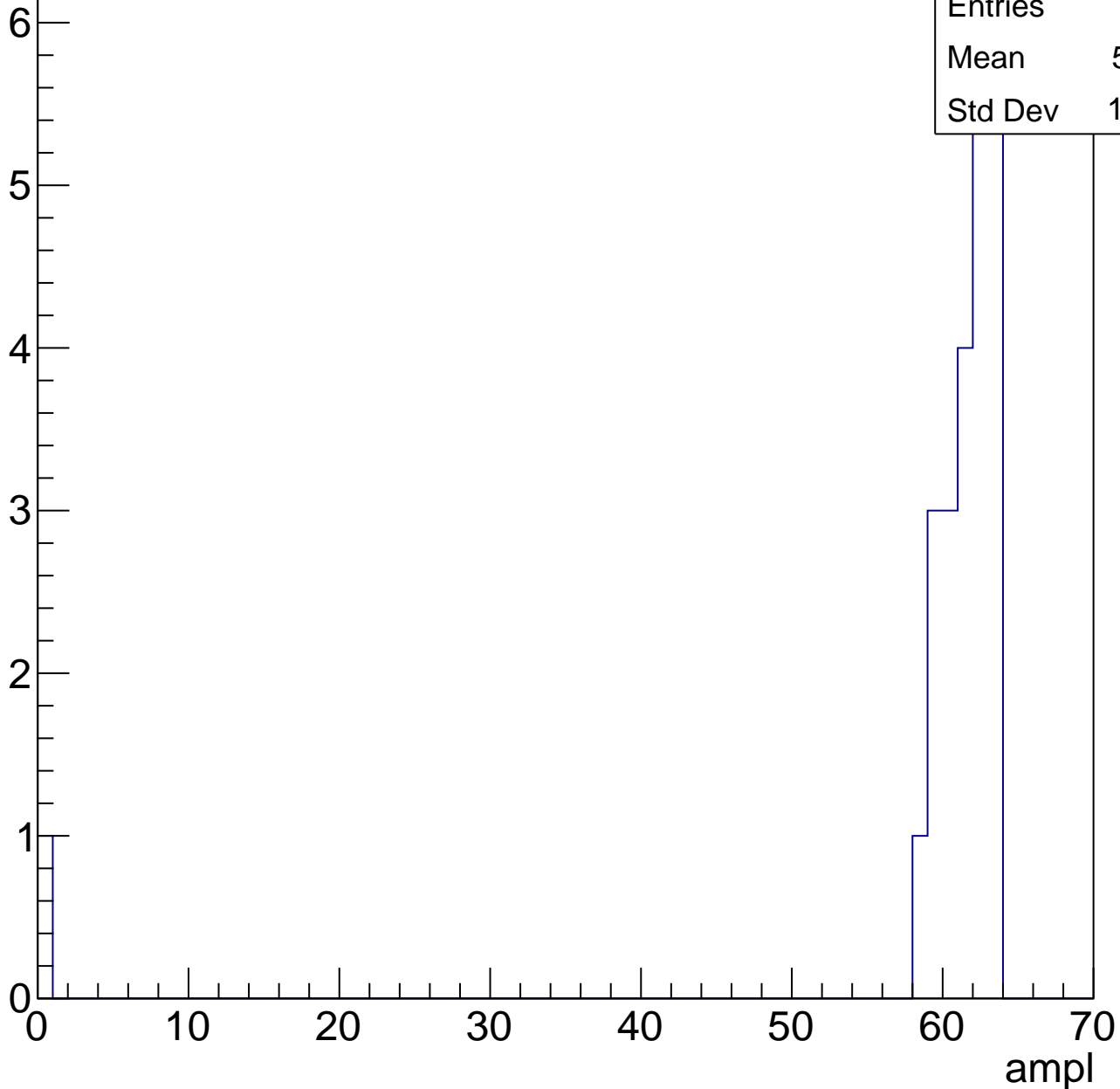


B0L001S, U7-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.71 |
| Std Dev | 12.33 |



B0L001S, U7-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

ampl

B0L001S, U7-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch62, adc0

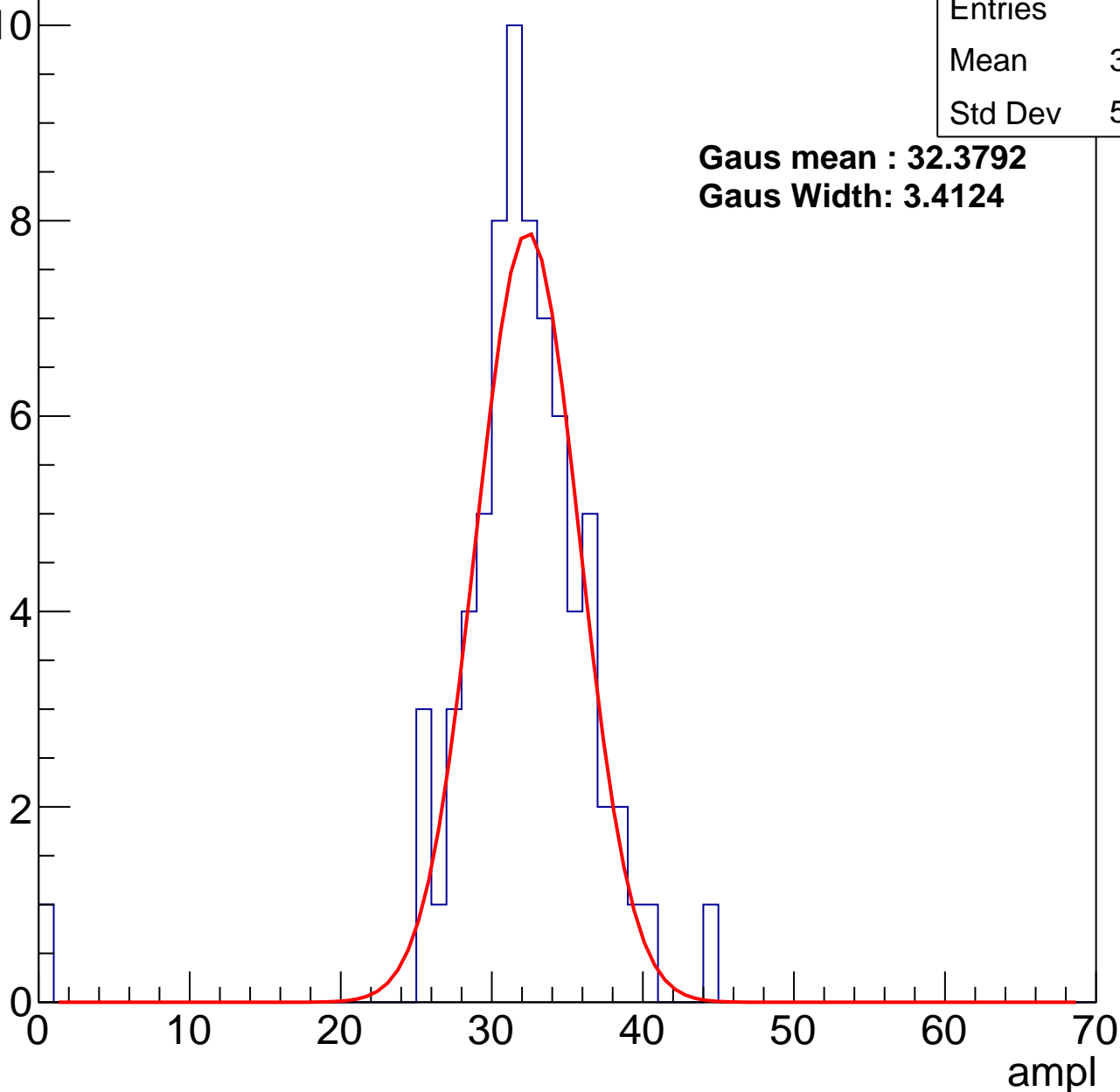
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 31.57 |
| Std Dev | 5.198 |

Gaus mean : 32.3792

Gaus Width: 3.4124



B0L001S, U7-ch62, adc1

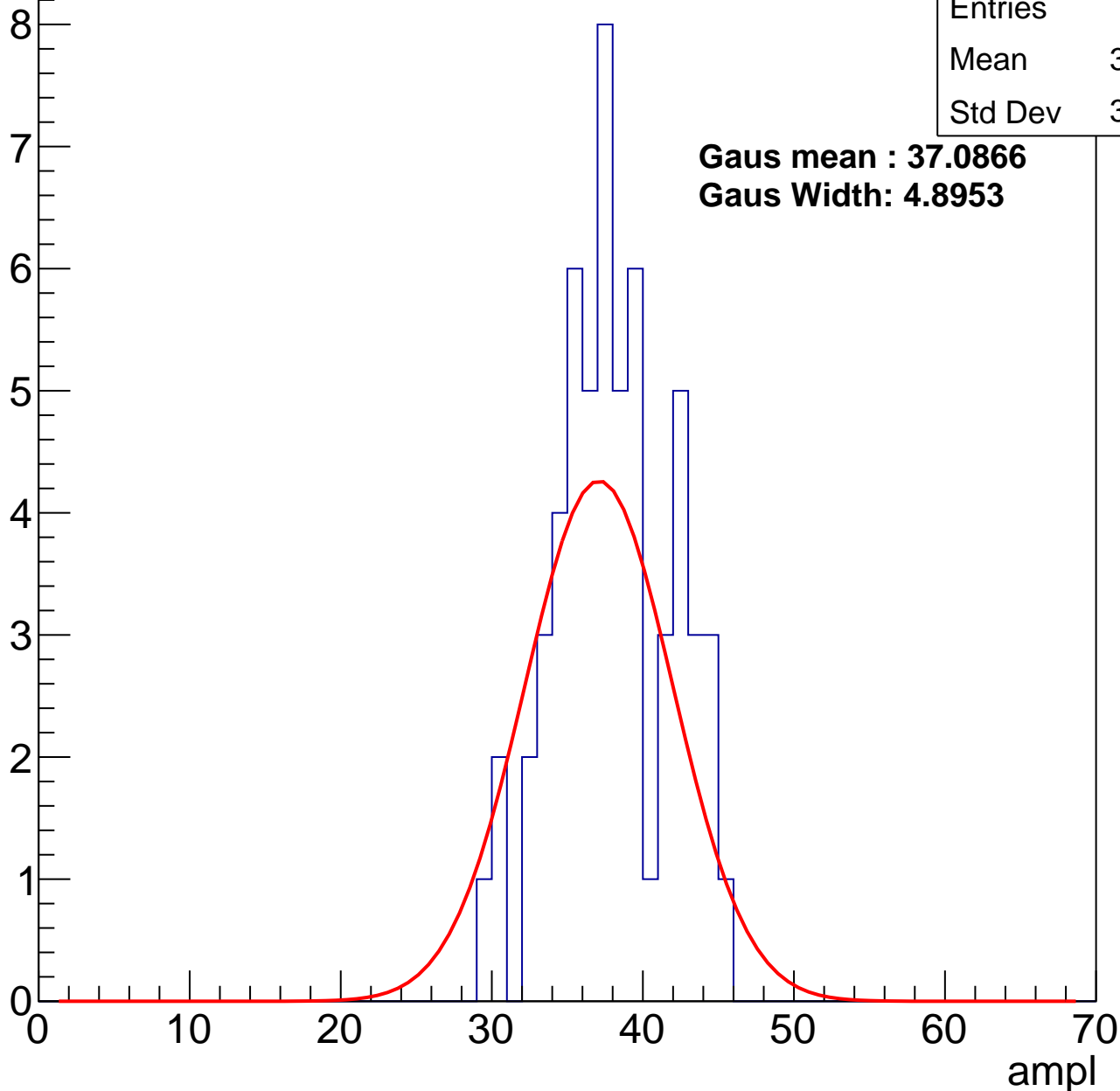
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 37.53 |
| Std Dev | 3.802 |

Gaus mean : 37.0866

Gaus Width: 4.8953



B0L001S, U7-ch62, adc2

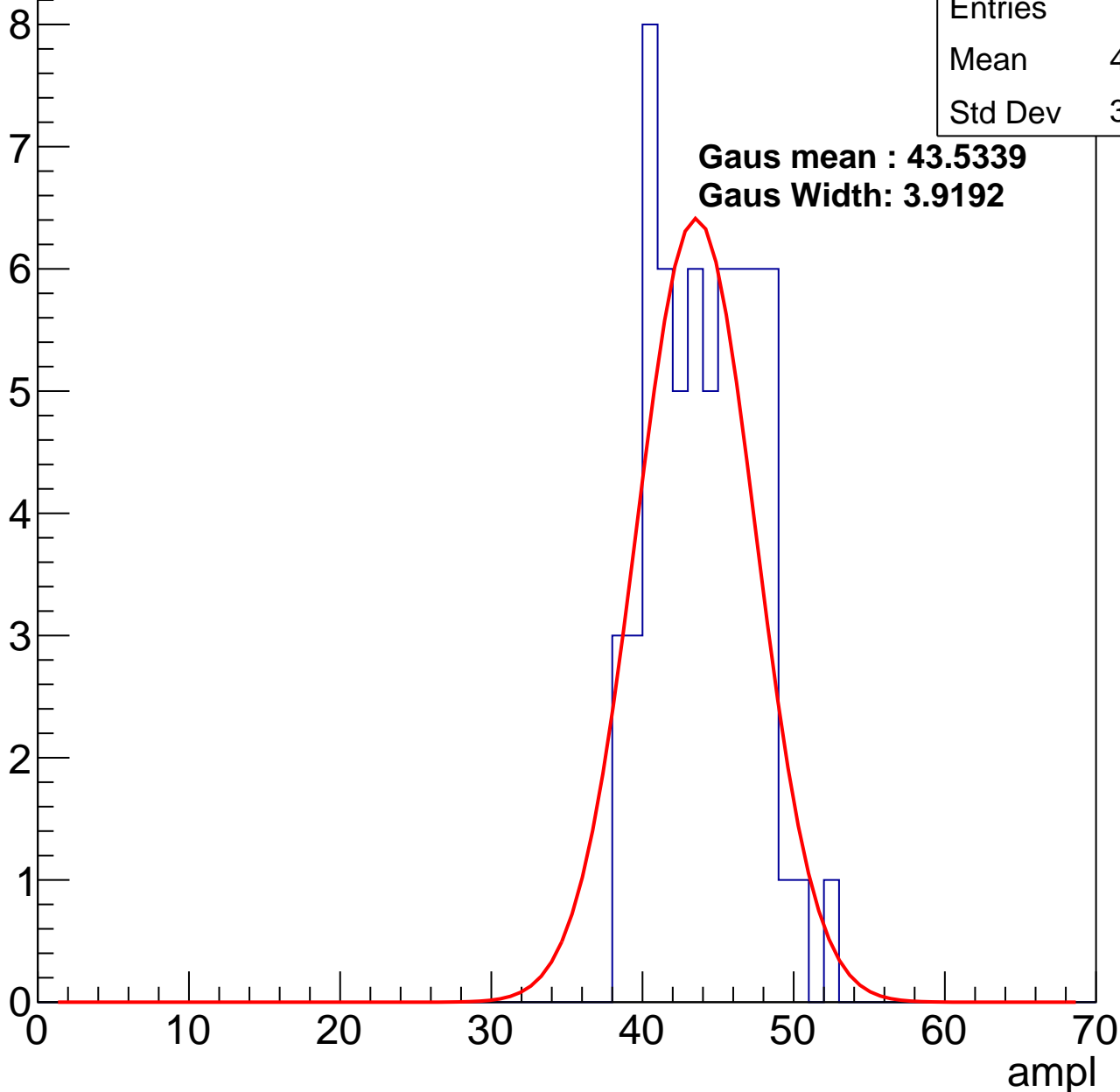
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.68 |
| Std Dev | 3.309 |

Gaus mean : 43.5339

Gaus Width: 3.9192

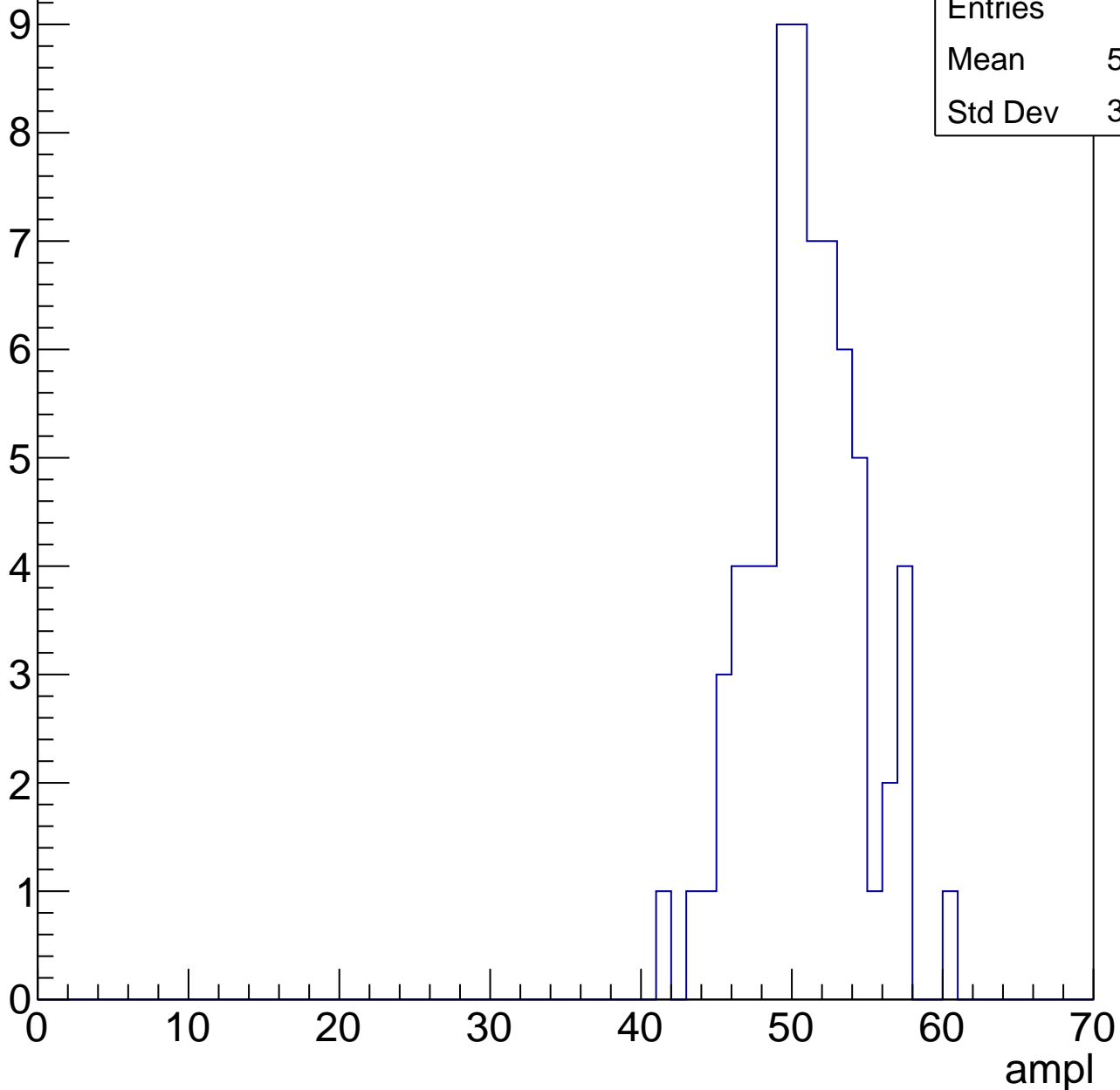


B0L001S, U7-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.46 |
| Std Dev | 3.638 |

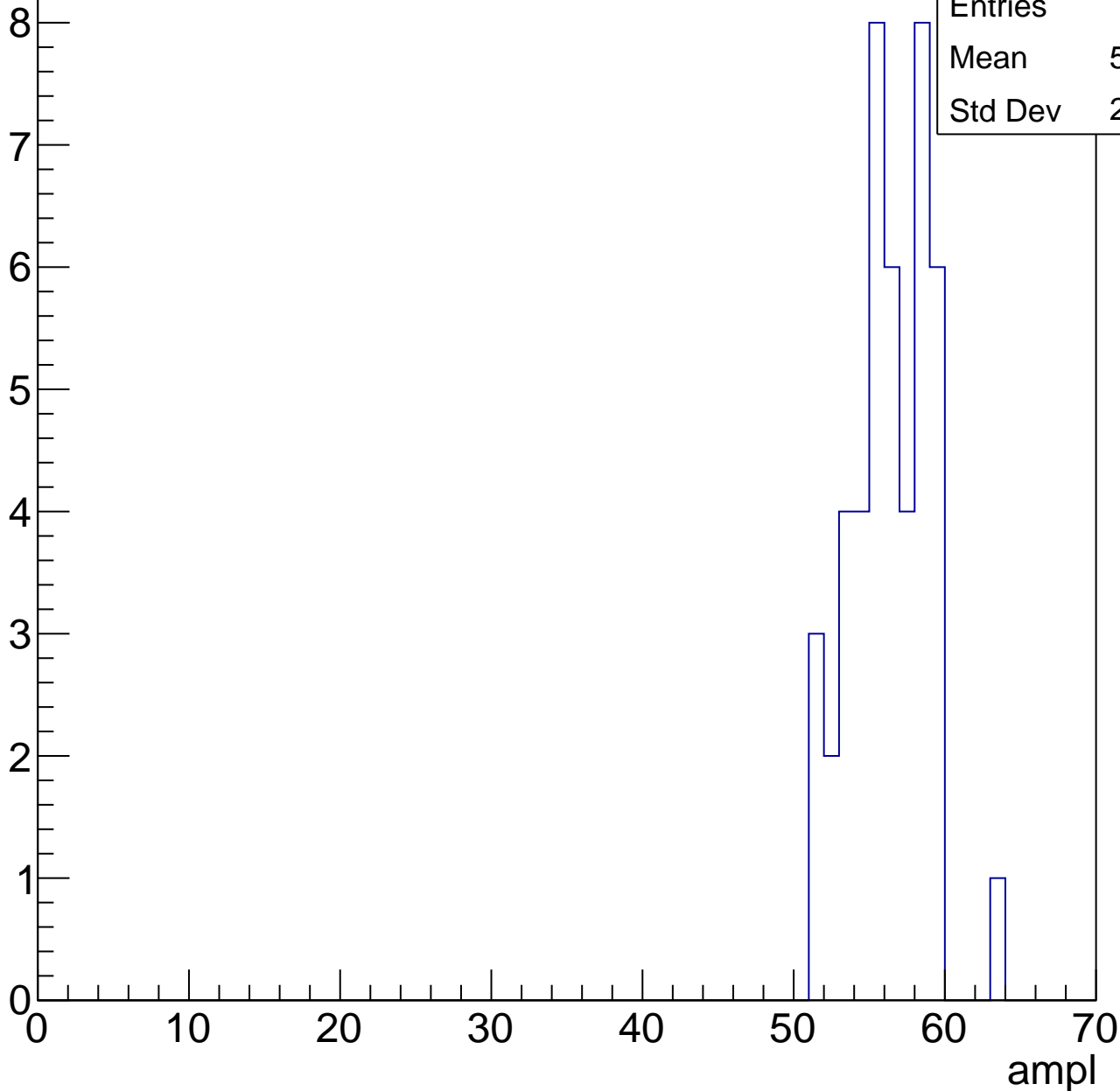


B0L001S, U7-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 55.87 |
| Std Dev | 2.576 |

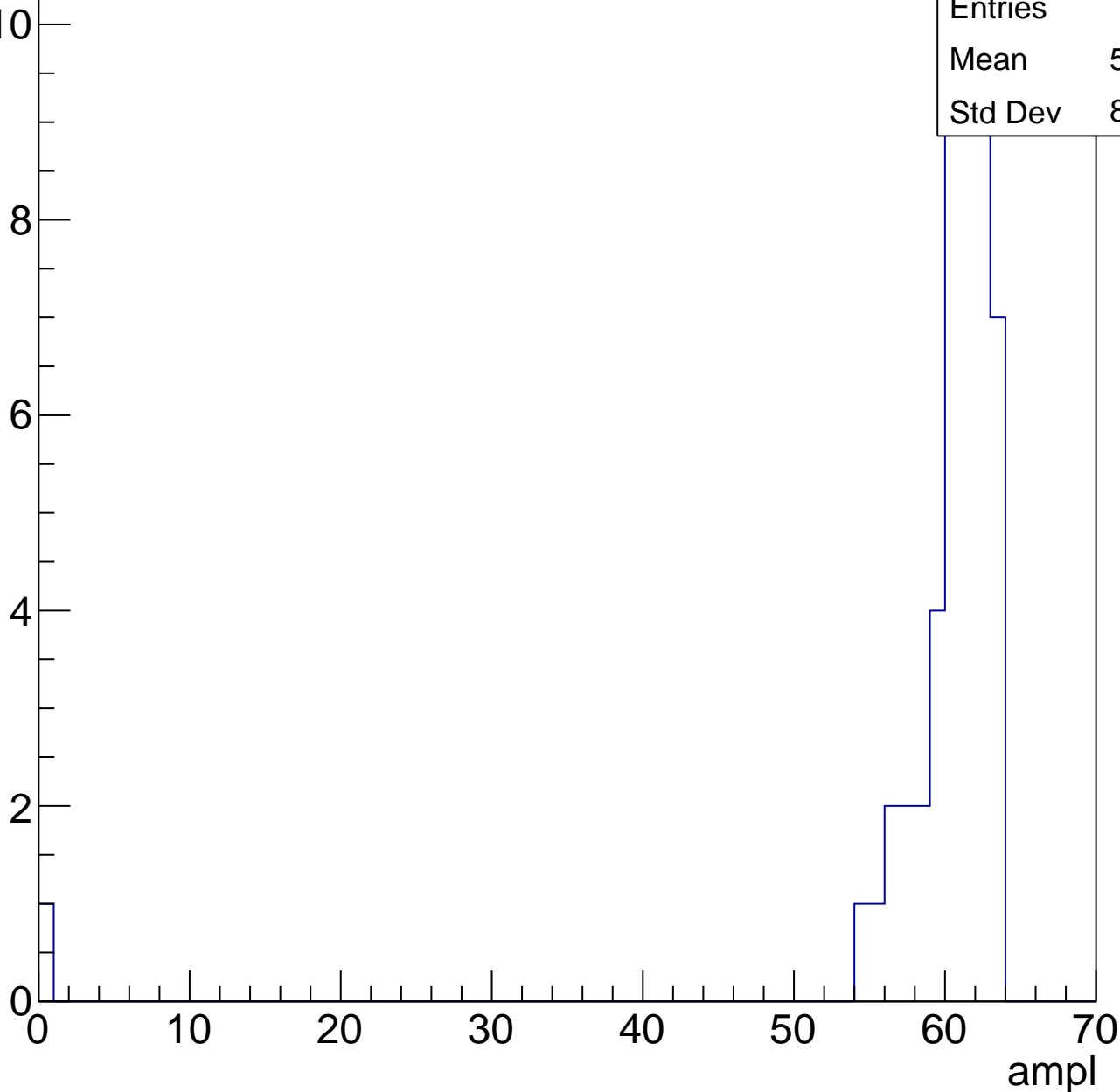


B0L001S, U7-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 59.14 |
| Std Dev | 8.806 |



B0L001S, U7-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

B0L001S, U7-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch63, adc0

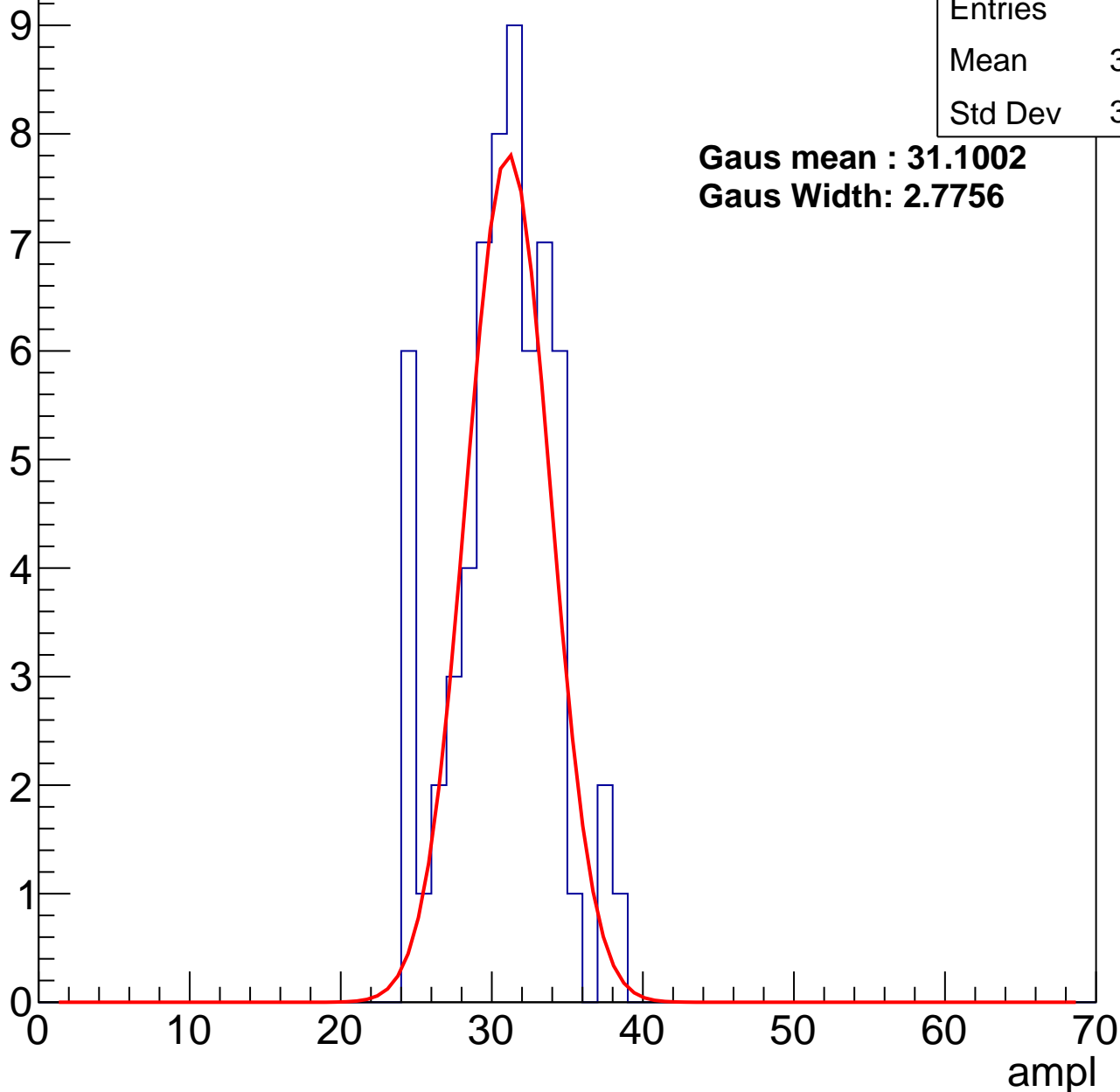
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 30.32 |
| Std Dev | 3.323 |

Gaus mean : 31.1002

Gaus Width: 2.7756



B0L001S, U7-ch63, adc1

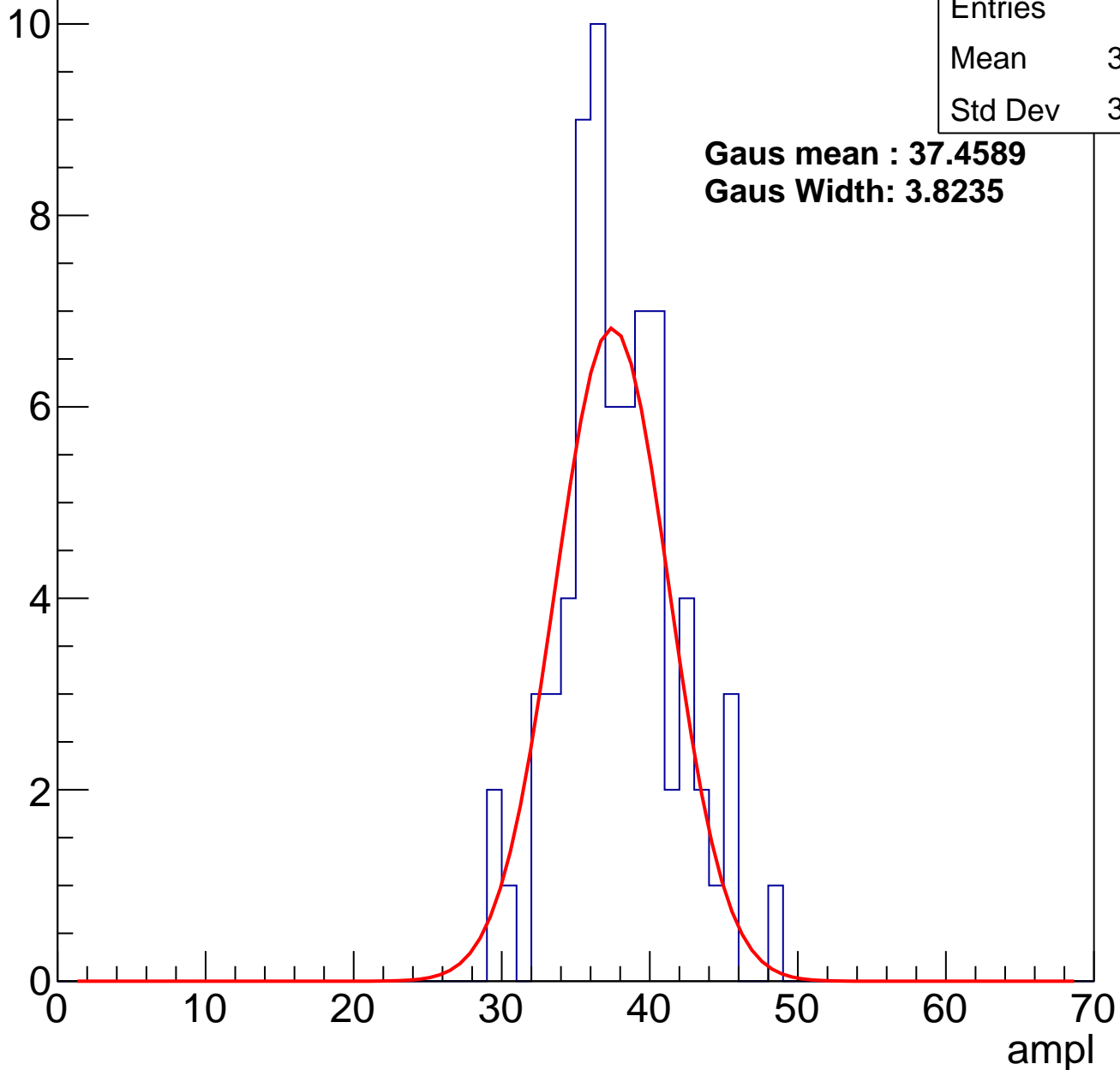
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.46 |
| Std Dev | 3.808 |

Gaus mean : 37.4589

Gaus Width: 3.8235

Entry



B0L001S, U7-ch63, adc2

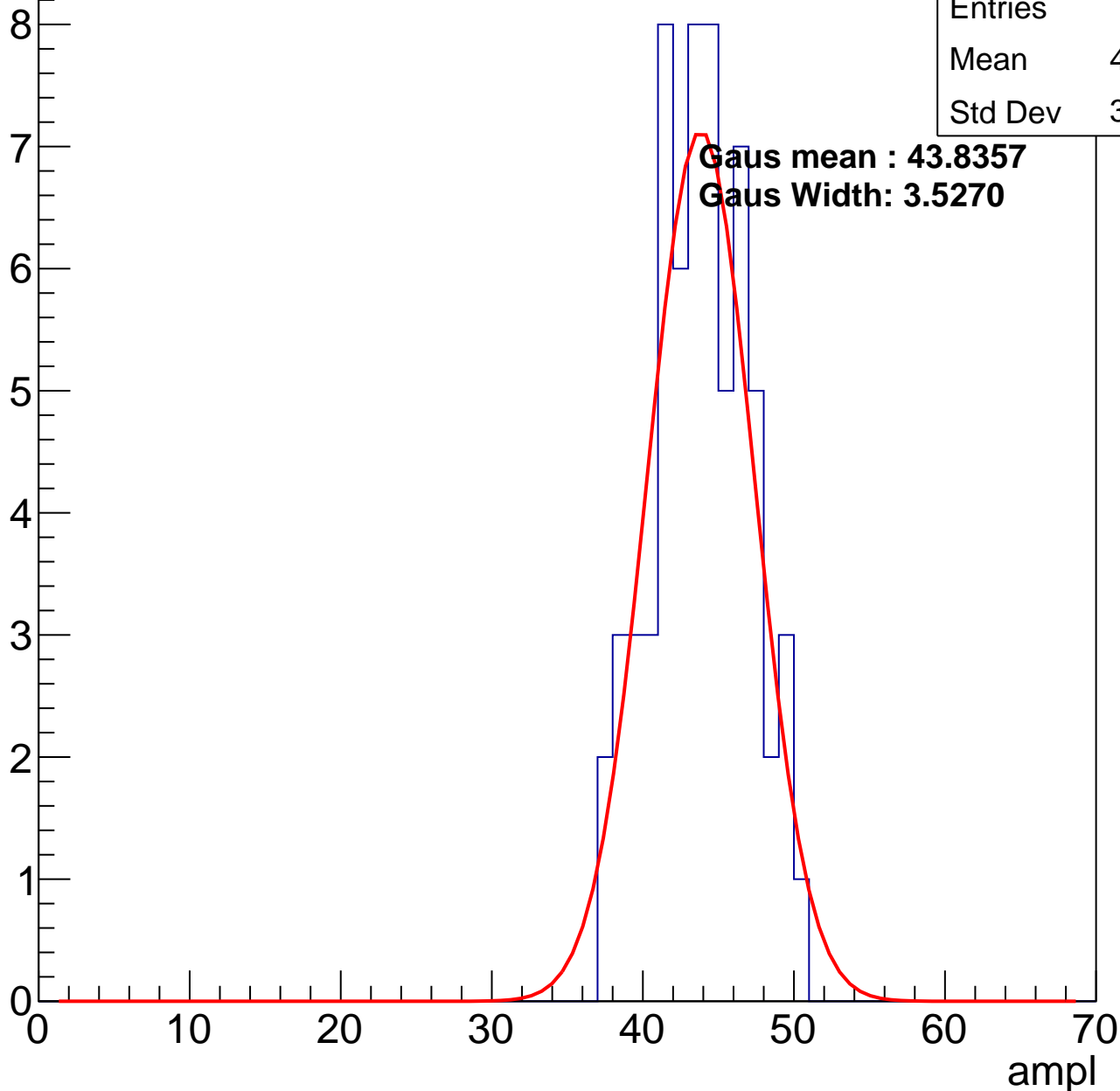
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.38 |
| Std Dev | 3.135 |

Gaus mean : 43.8357

Gaus Width: 3.5270

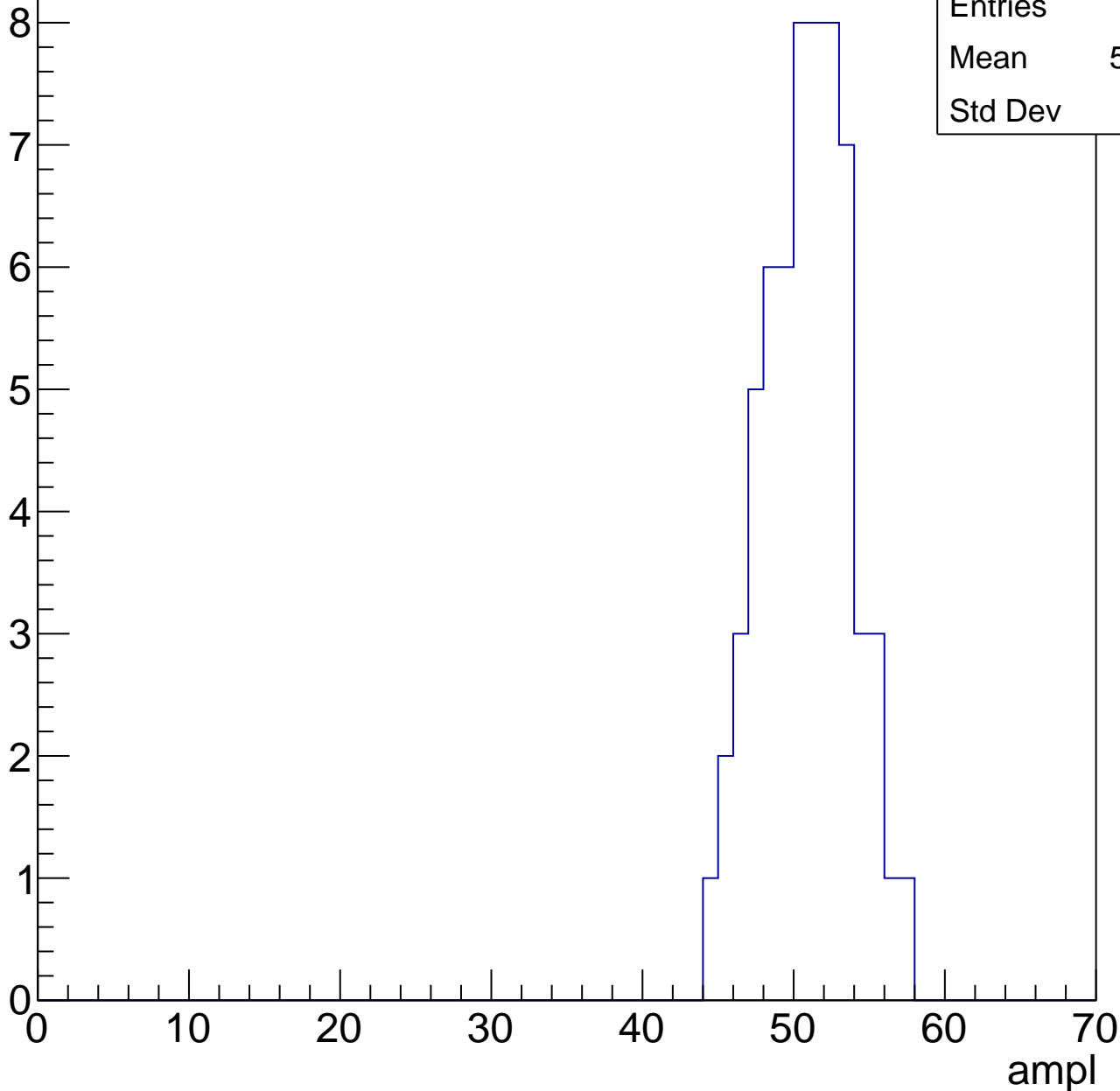


B0L001S, U7-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

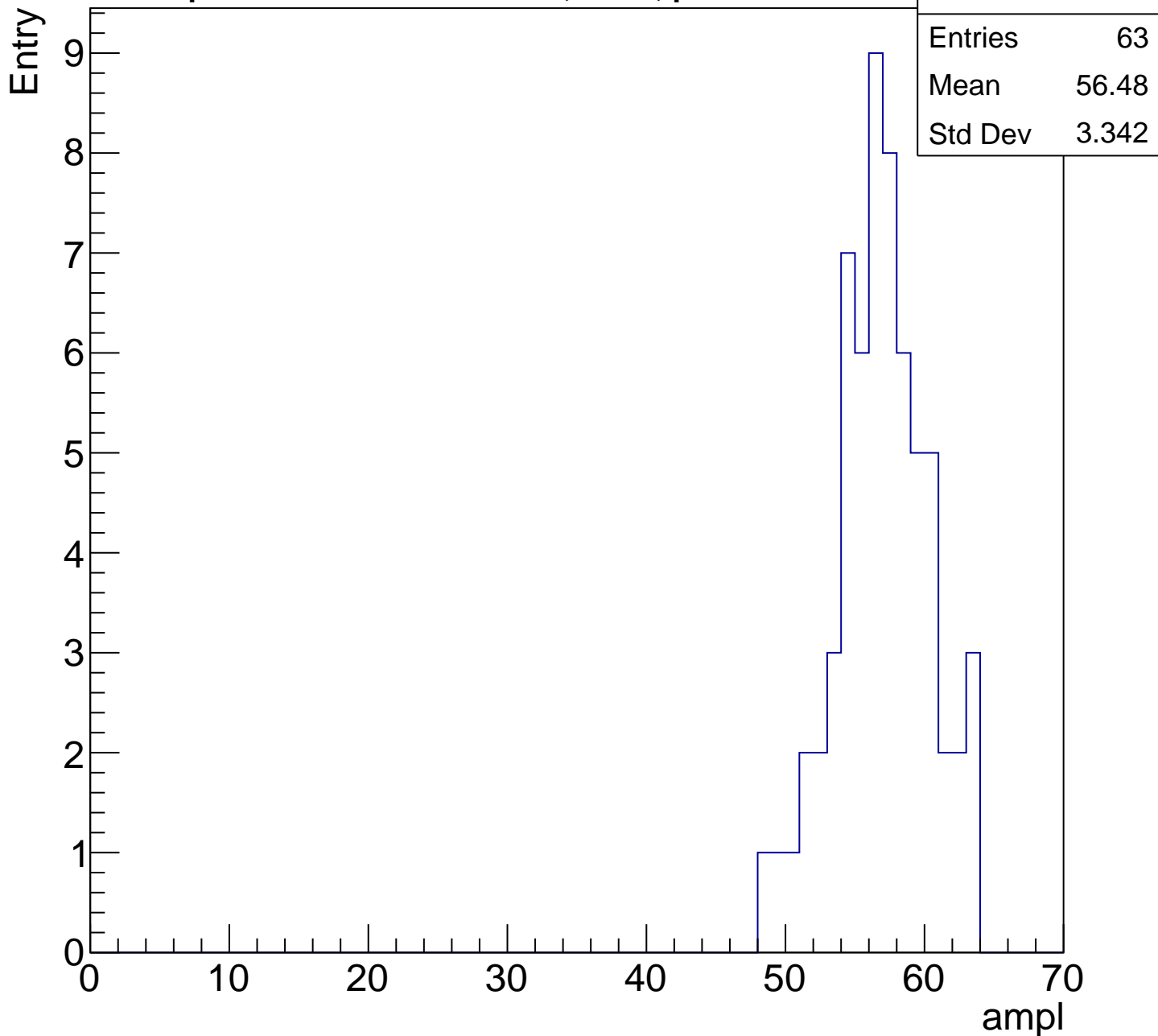
Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 50.39 |
| Std Dev | 2.87 |



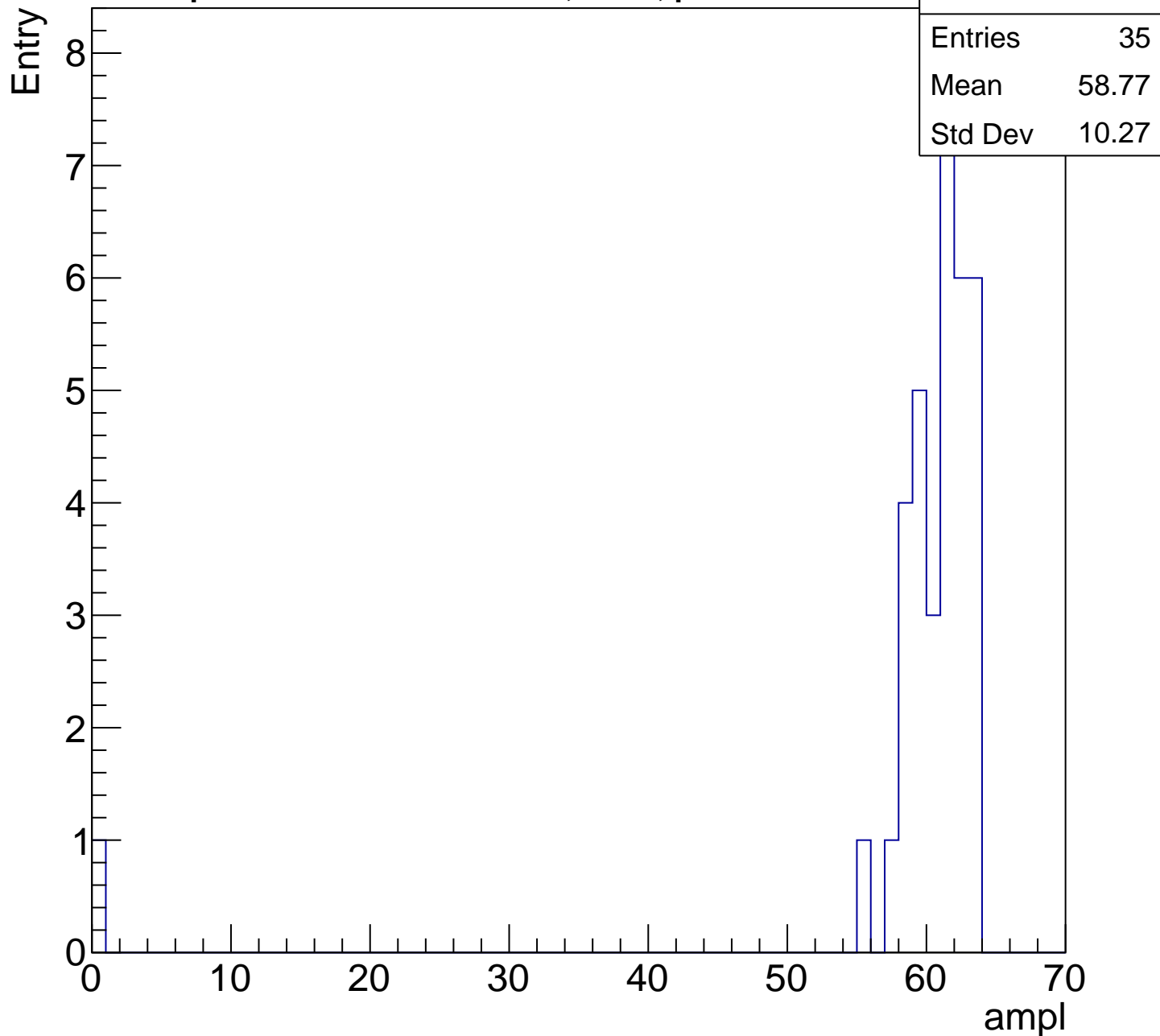
B0L001S, U7-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

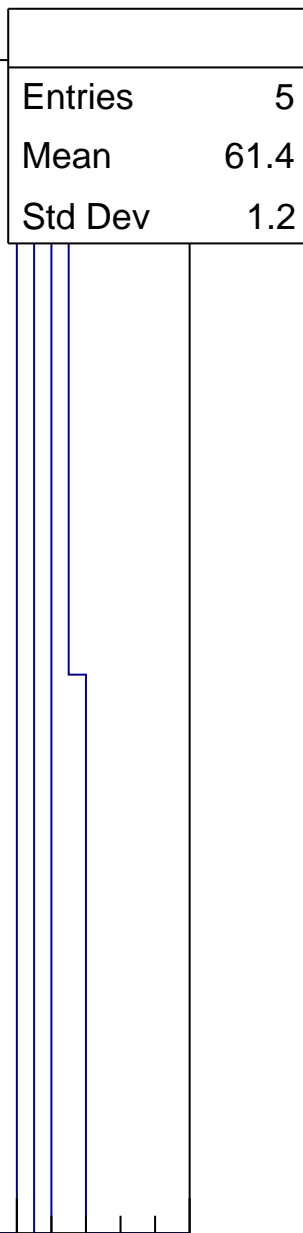
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 5 |
| Mean | 61.4 |
| Std Dev | 1.2 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch64, adc0

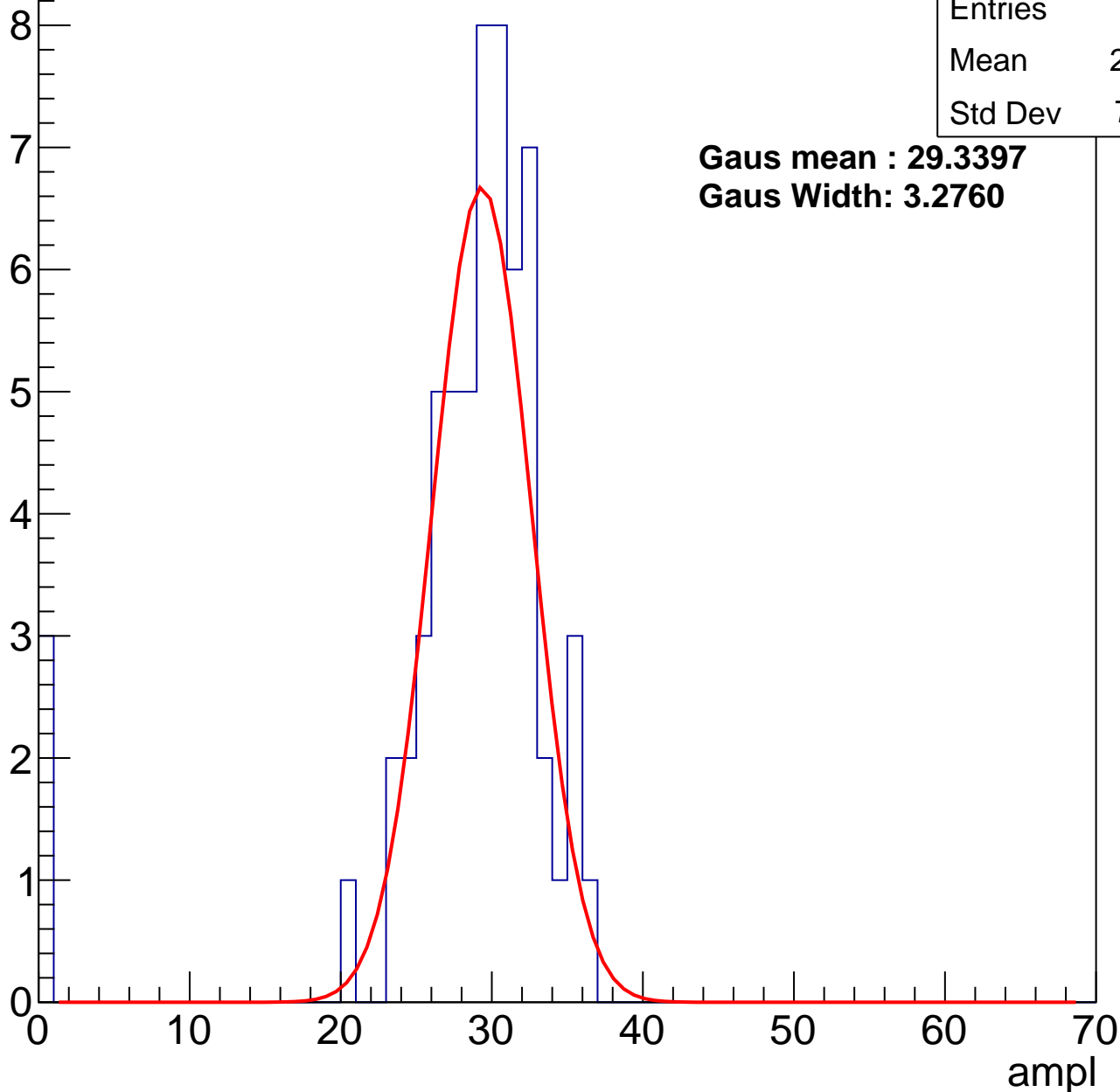
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 27.69 |
| Std Dev | 7.011 |

Gaus mean : 29.3397

Gaus Width: 3.2760



B0L001S, U7-ch64, adc1

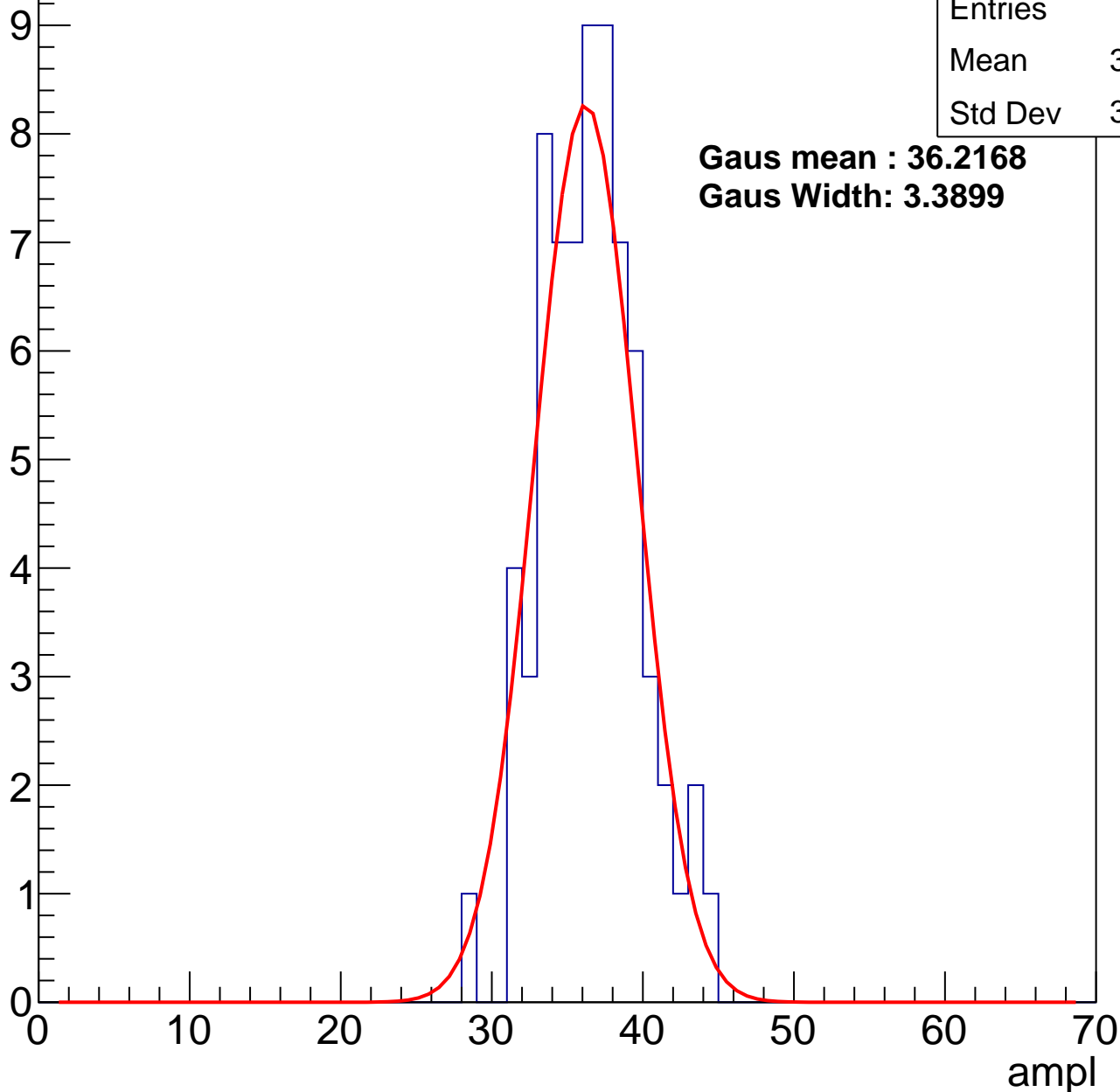
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 36.09 |
| Std Dev | 3.175 |

Gaus mean : 36.2168

Gaus Width: 3.3899



B0L001S, U7-ch64, adc2

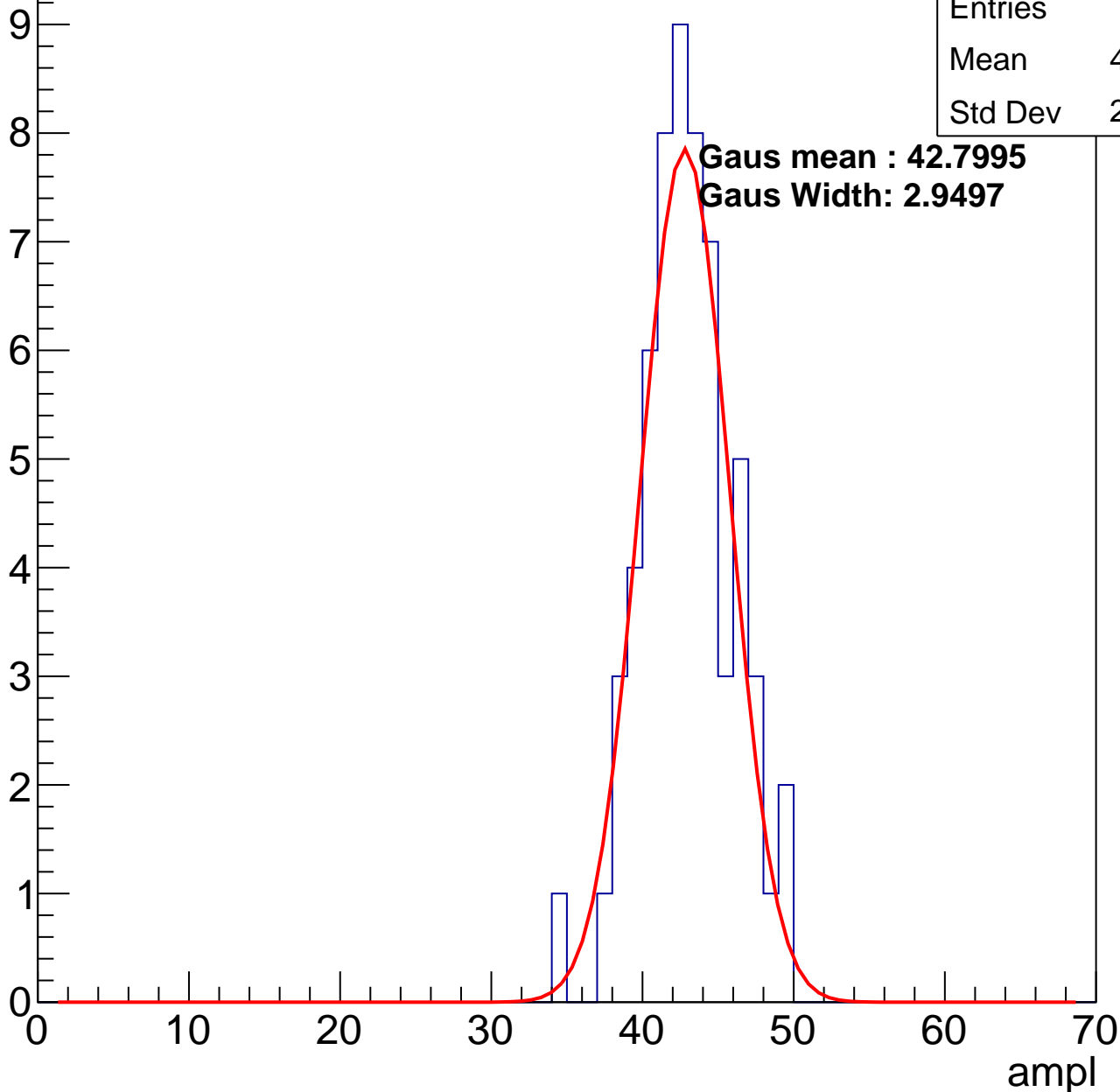
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.48 |
| Std Dev | 2.984 |

Gaus mean : 42.7995

Gaus Width: 2.9497

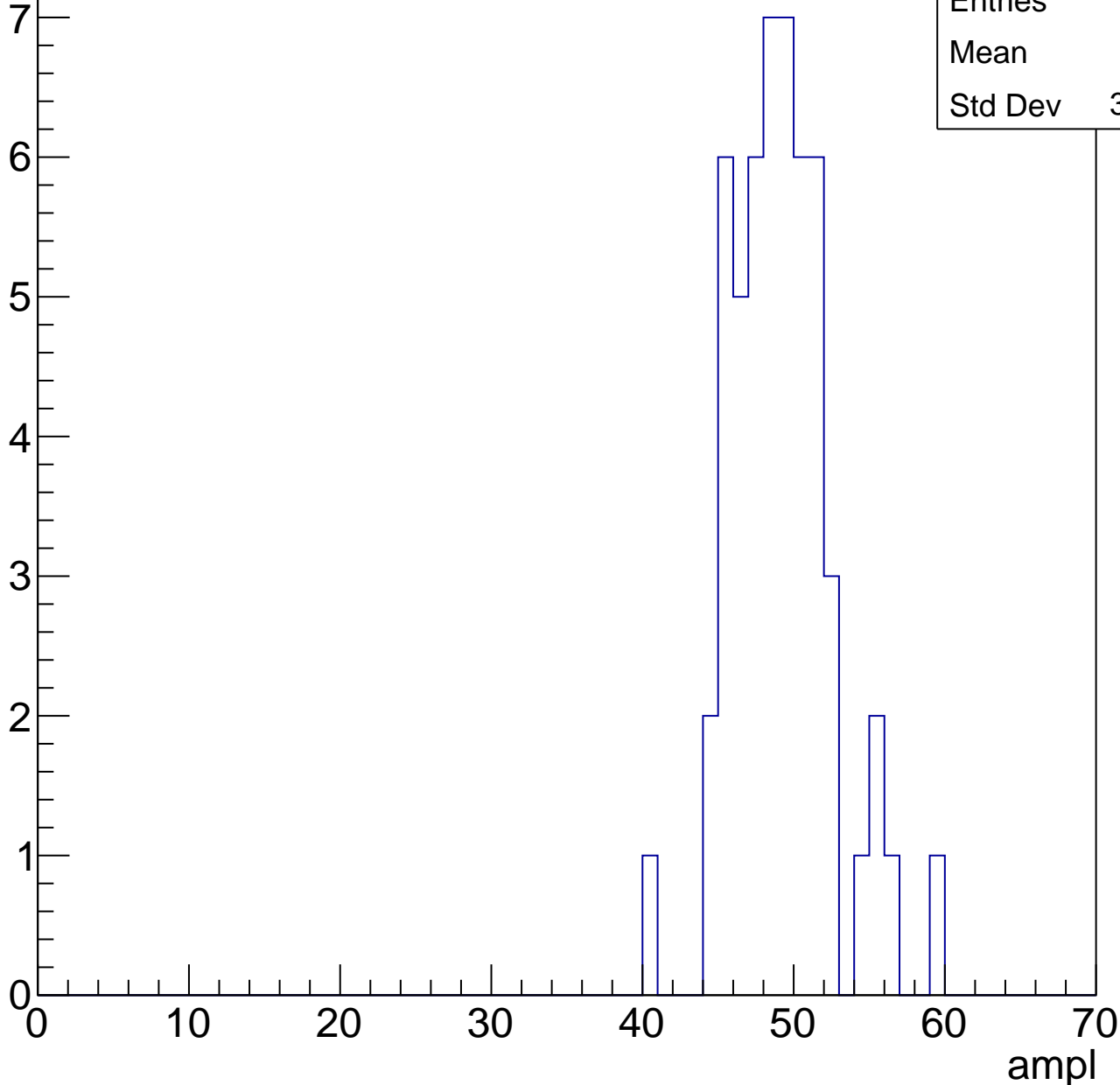


B0L001S, U7-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 48.7 |
| Std Dev | 3.337 |

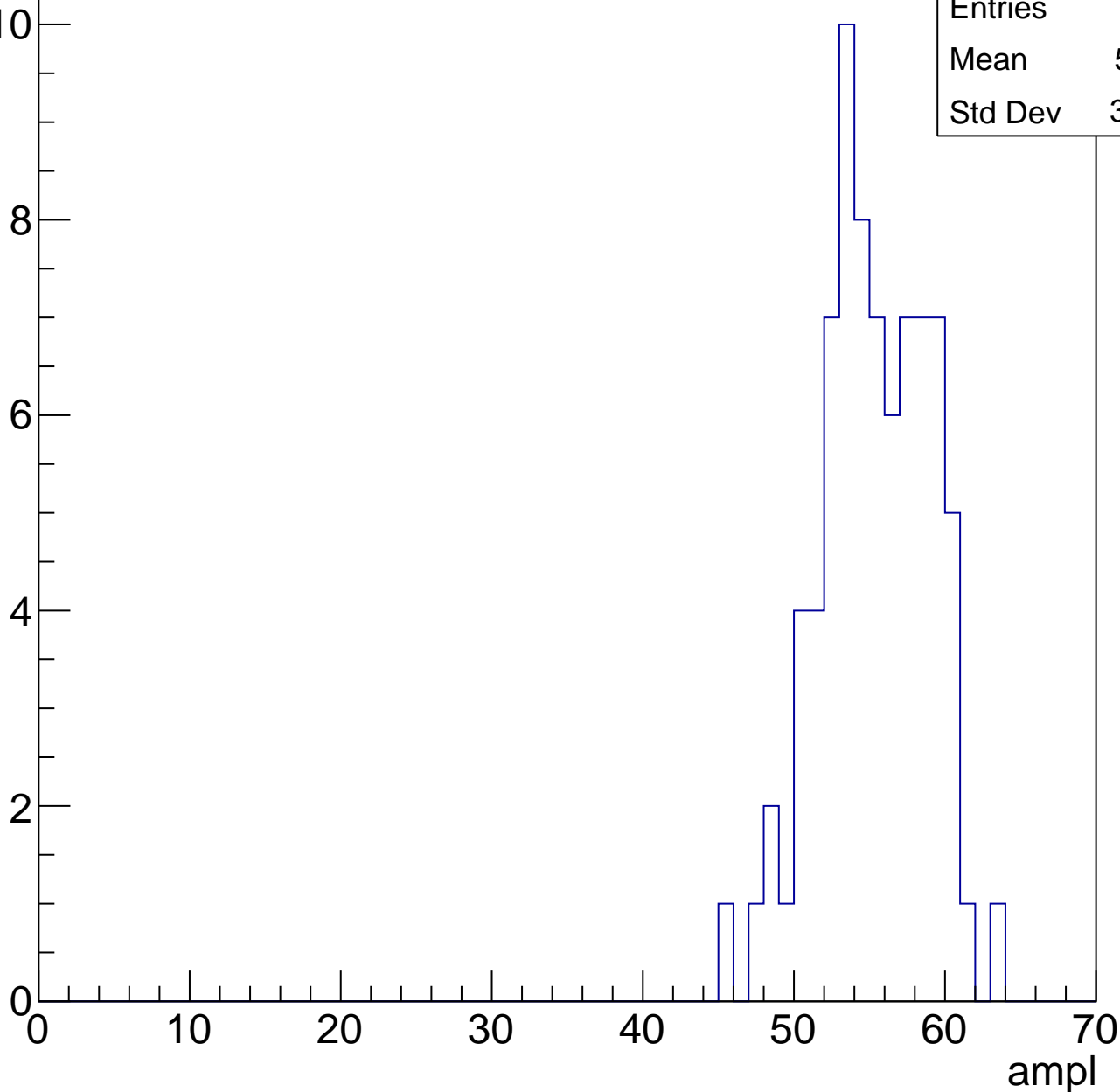


B0L001S, U7-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

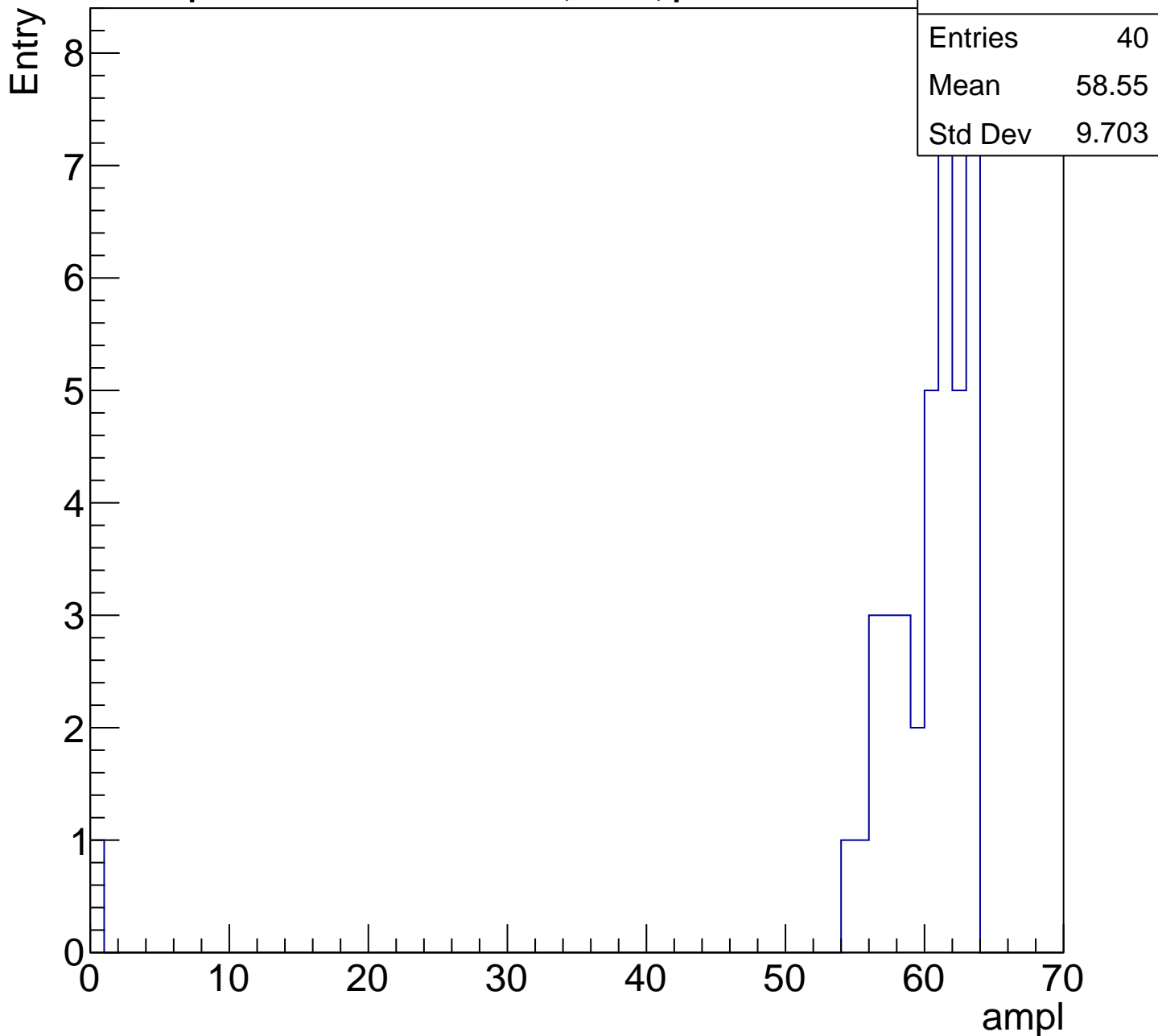
Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 54.81 |
| Std Dev | 3.565 |



B0L001S, U7-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

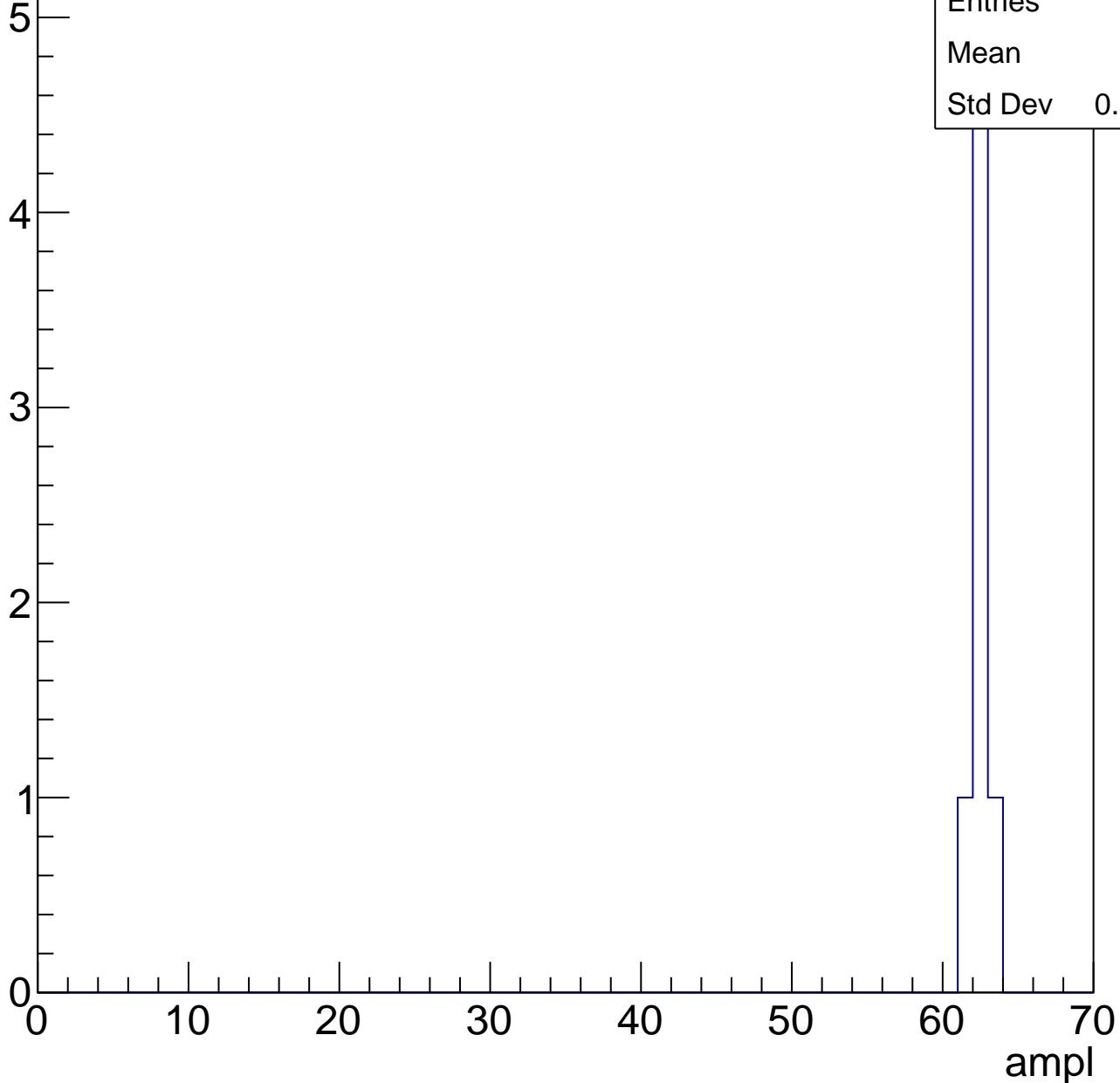


B0L001S, U7-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|--------|
| Entries | 7 |
| Mean | 62 |
| Std Dev | 0.5345 |



B0L001S, U7-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch65, adc0

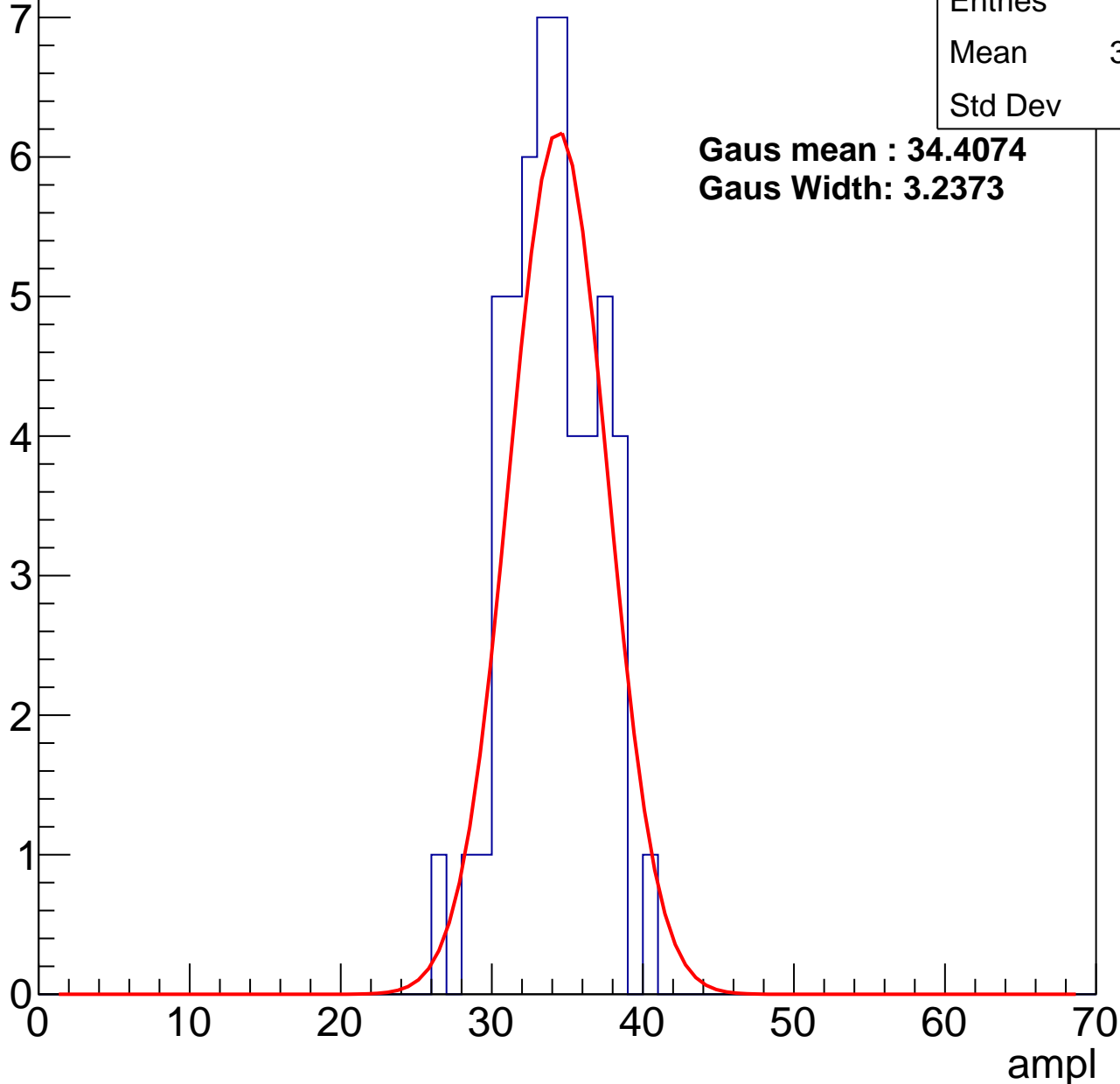
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 33.53 |
| Std Dev | 2.92 |

Gaus mean : 34.4074

Gaus Width: 3.2373



B0L001S, U7-ch65, adc1

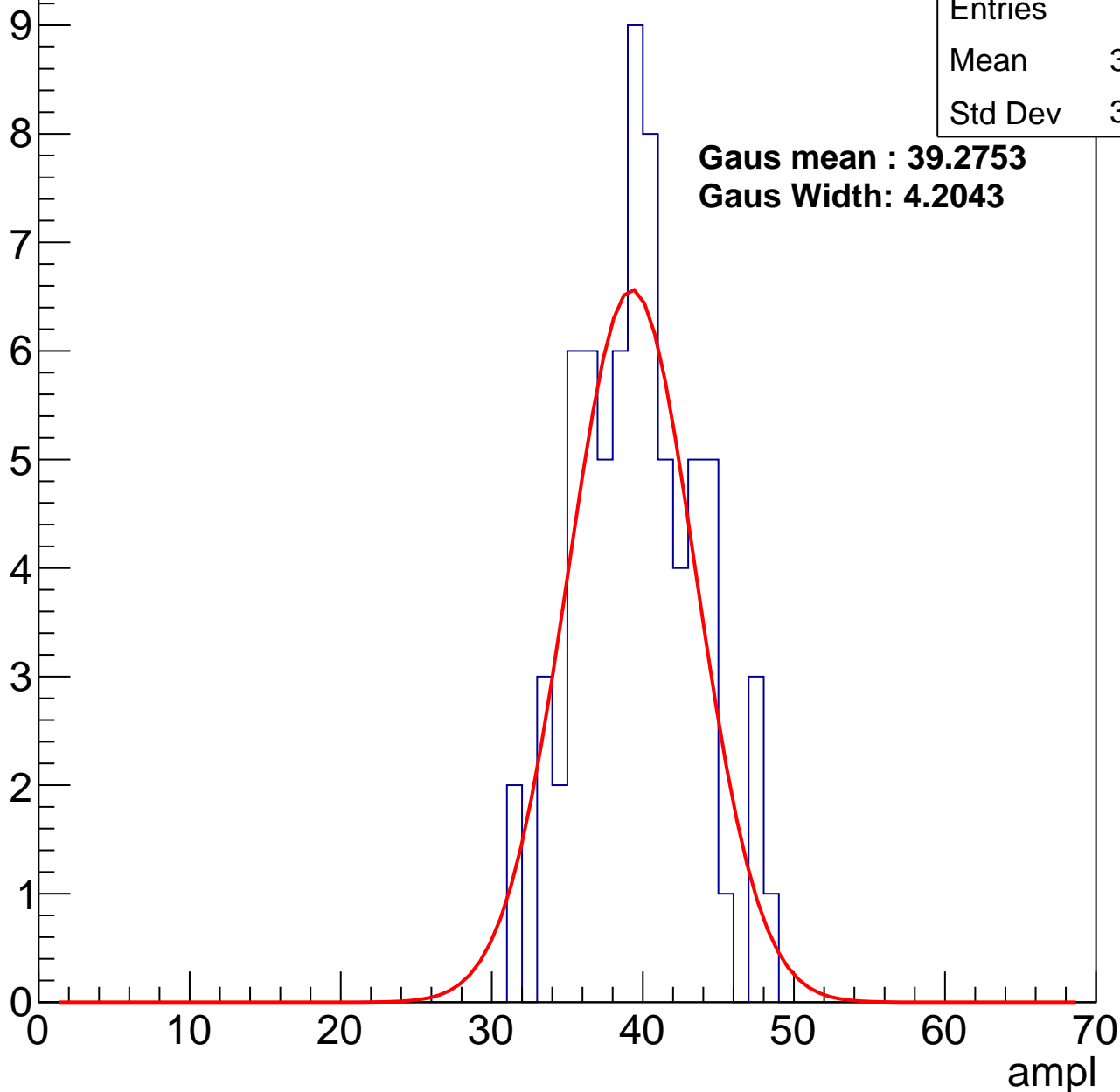
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 39.17 |
| Std Dev | 3.824 |

Gaus mean : 39.2753

Gaus Width: 4.2043



B0L001S, U7-ch65, adc2

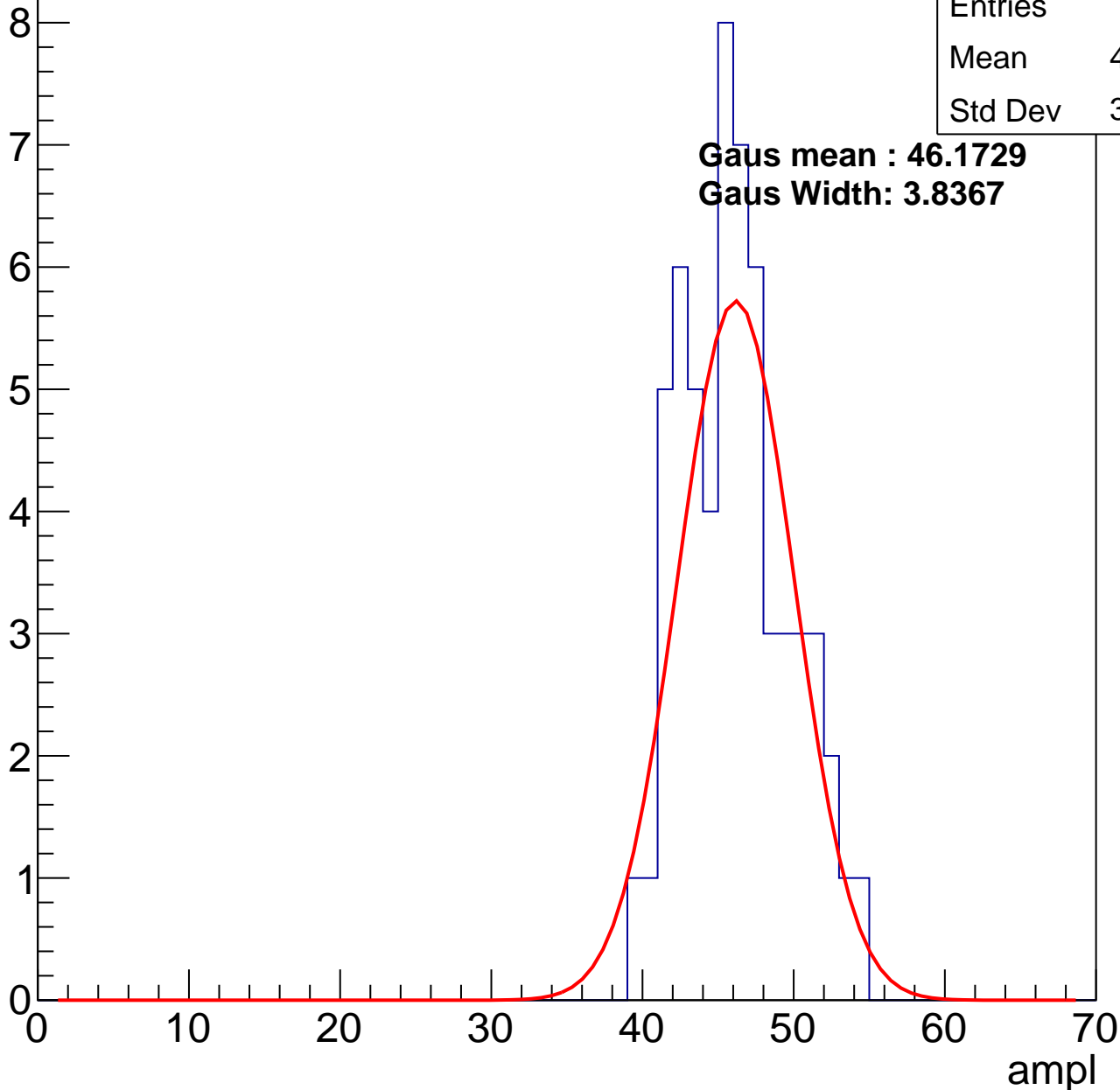
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 45.69 |
| Std Dev | 3.499 |

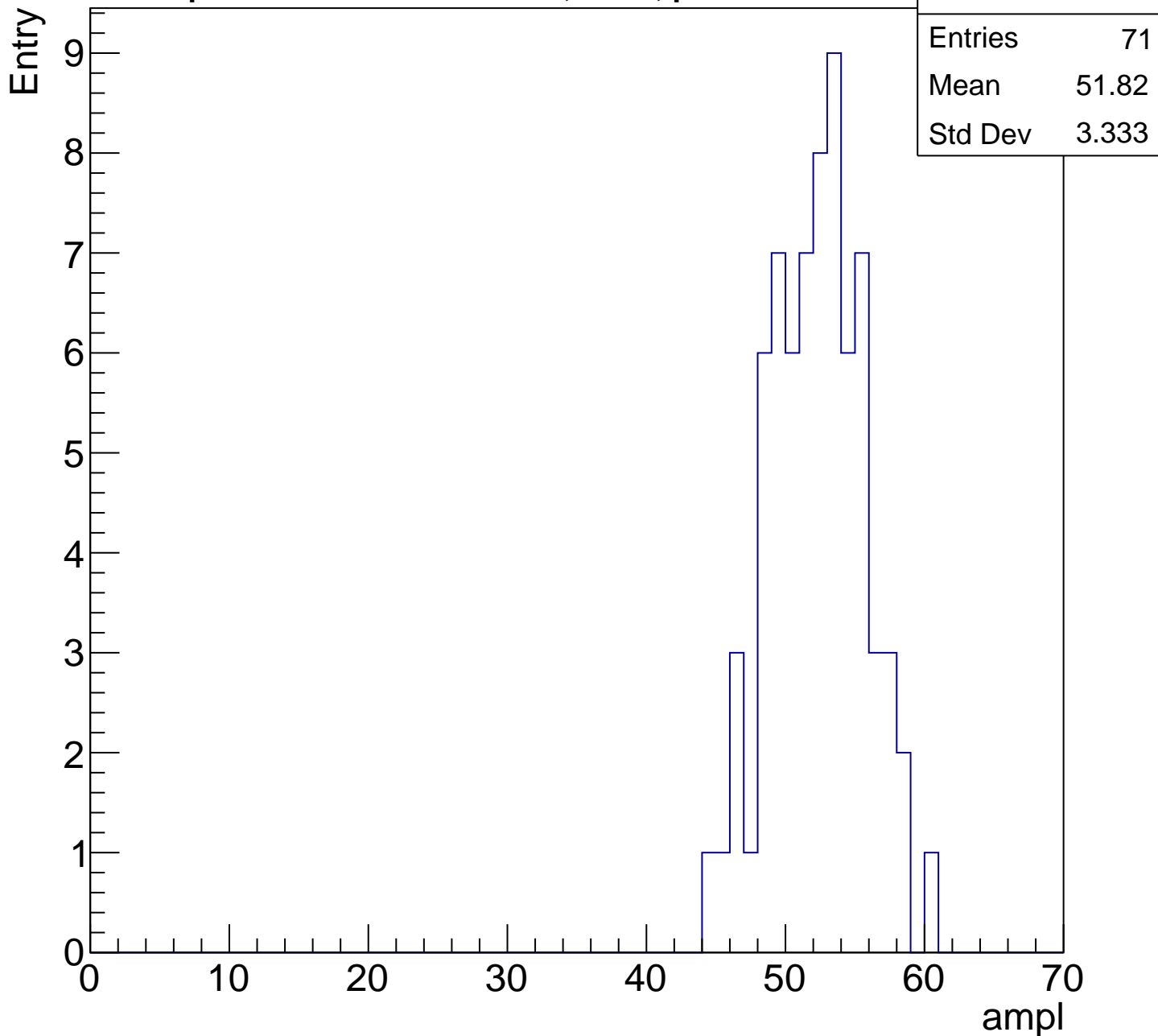
Gaus mean : 46.1729

Gaus Width: 3.8367



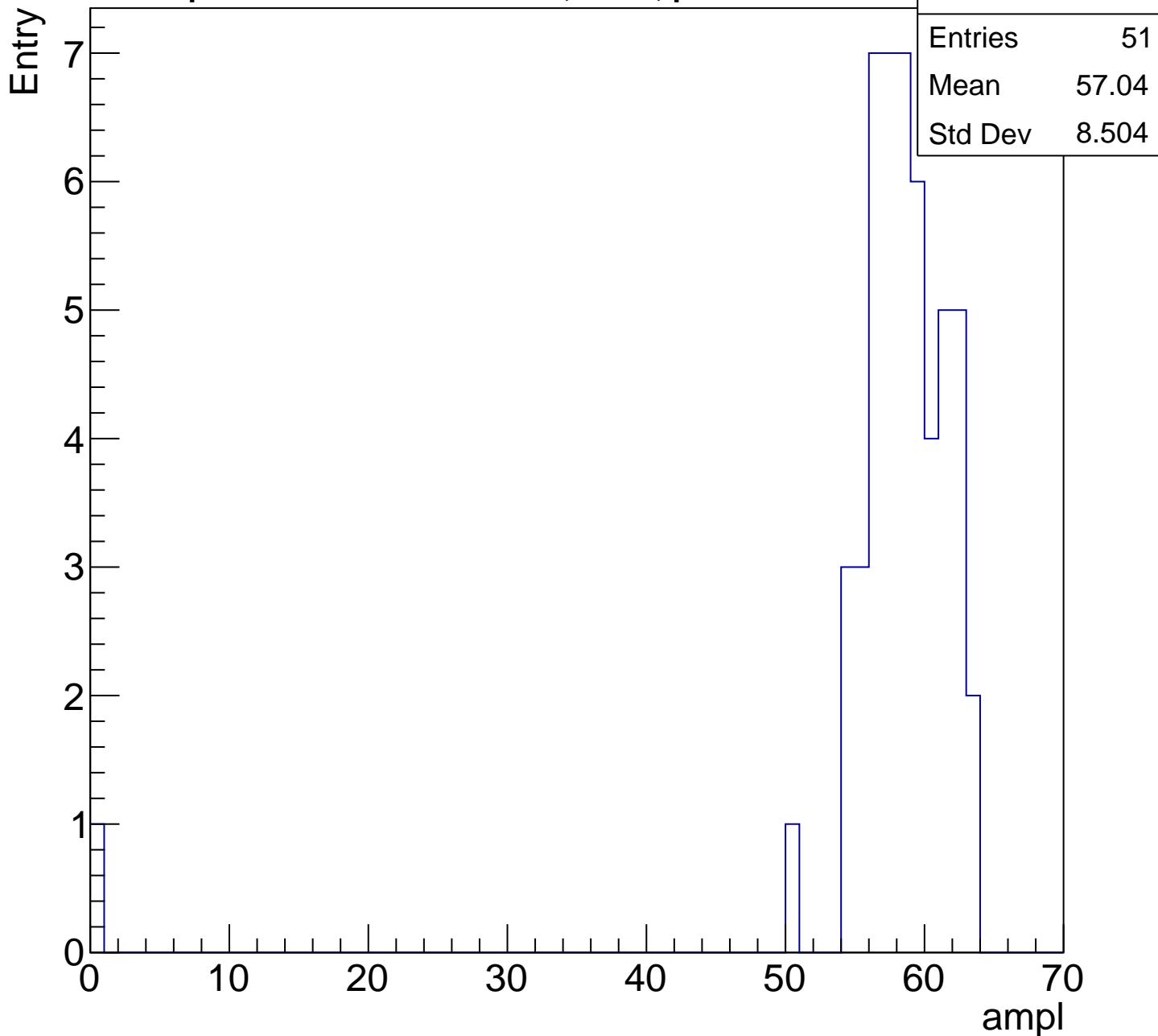
B0L001S, U7-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

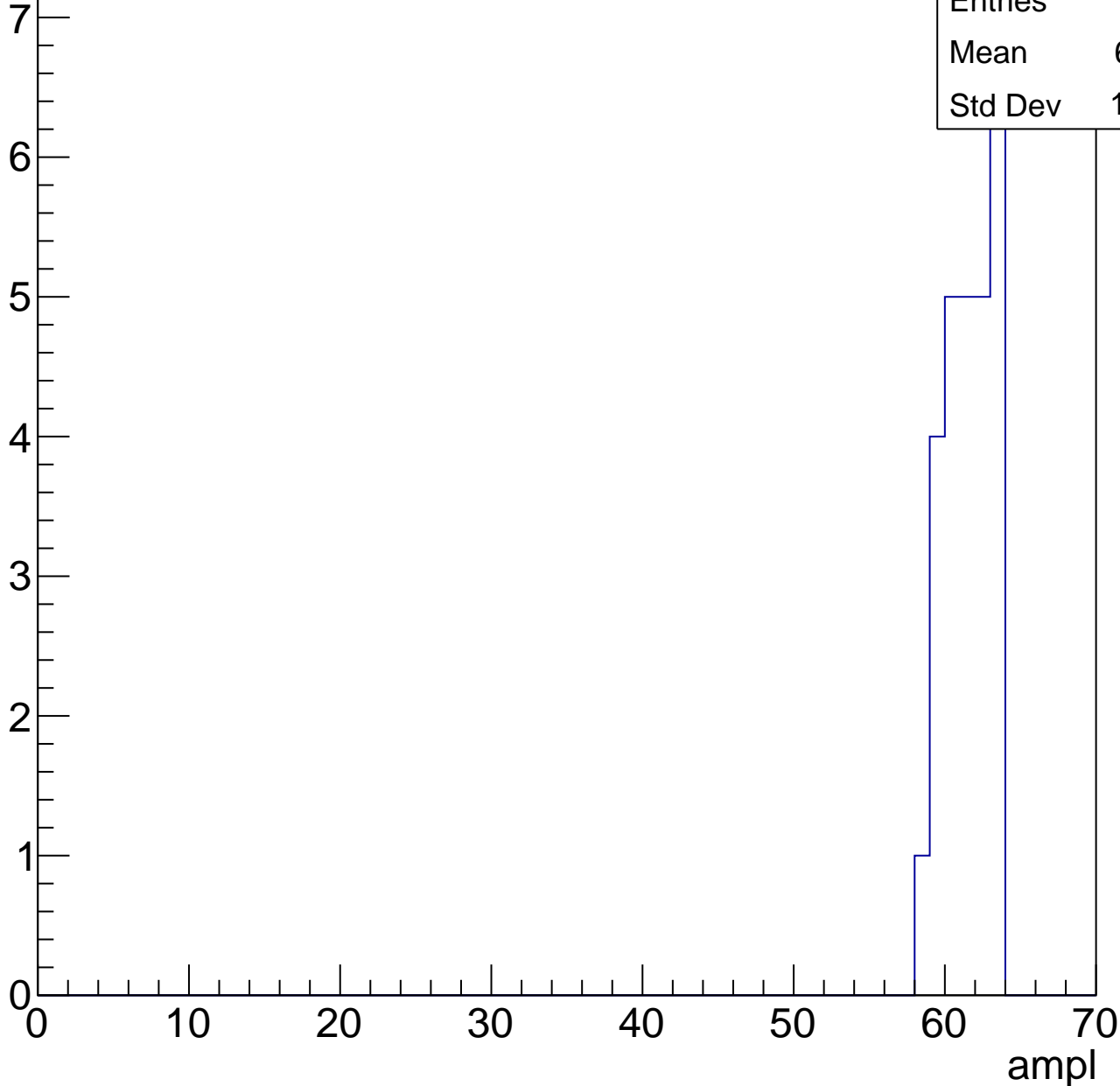


B0L001S, U7-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

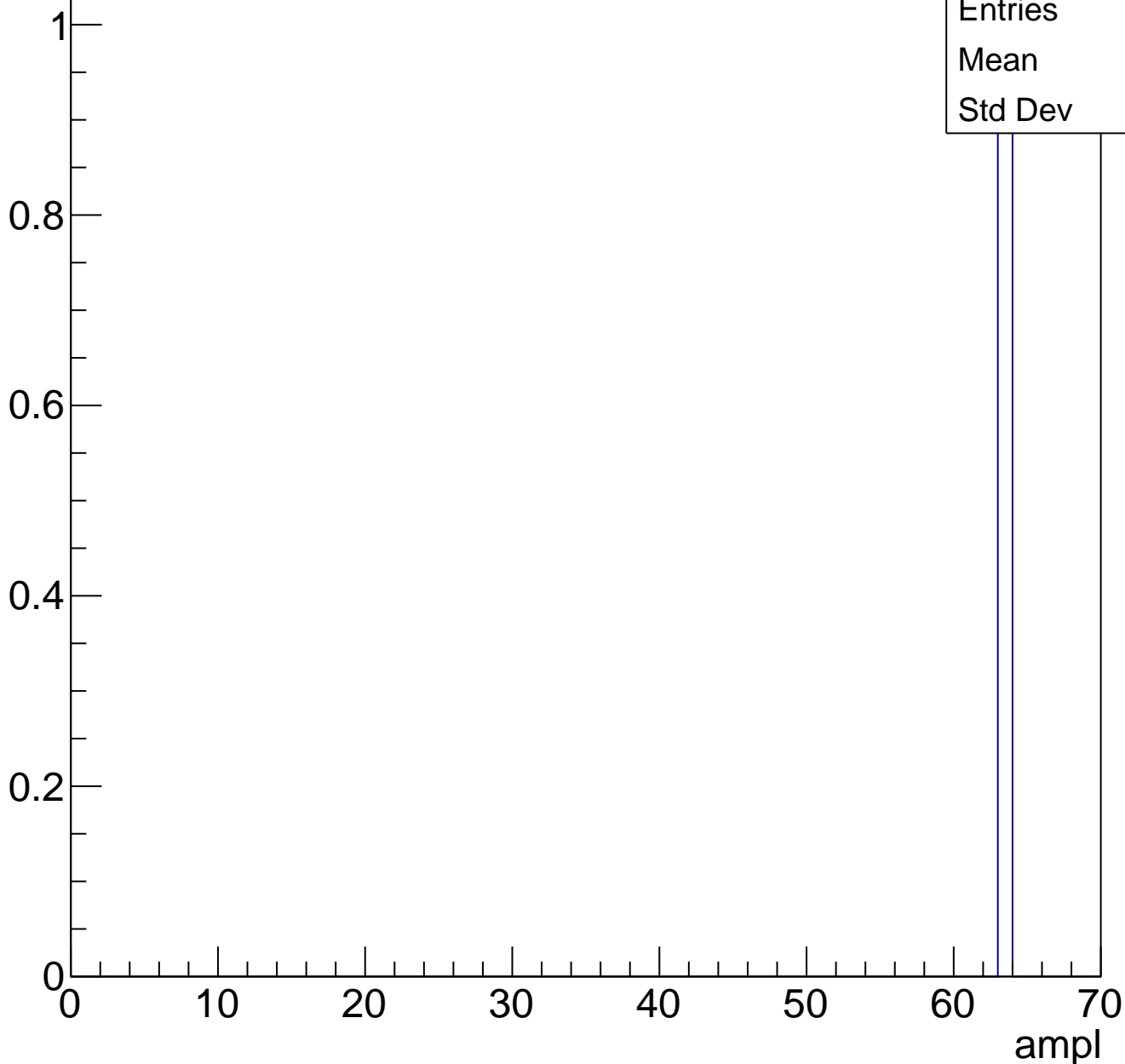
| | |
|---------|-------|
| Entries | 27 |
| Mean | 61.11 |
| Std Dev | 1.523 |



B0L001S, U7-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch66, adc0

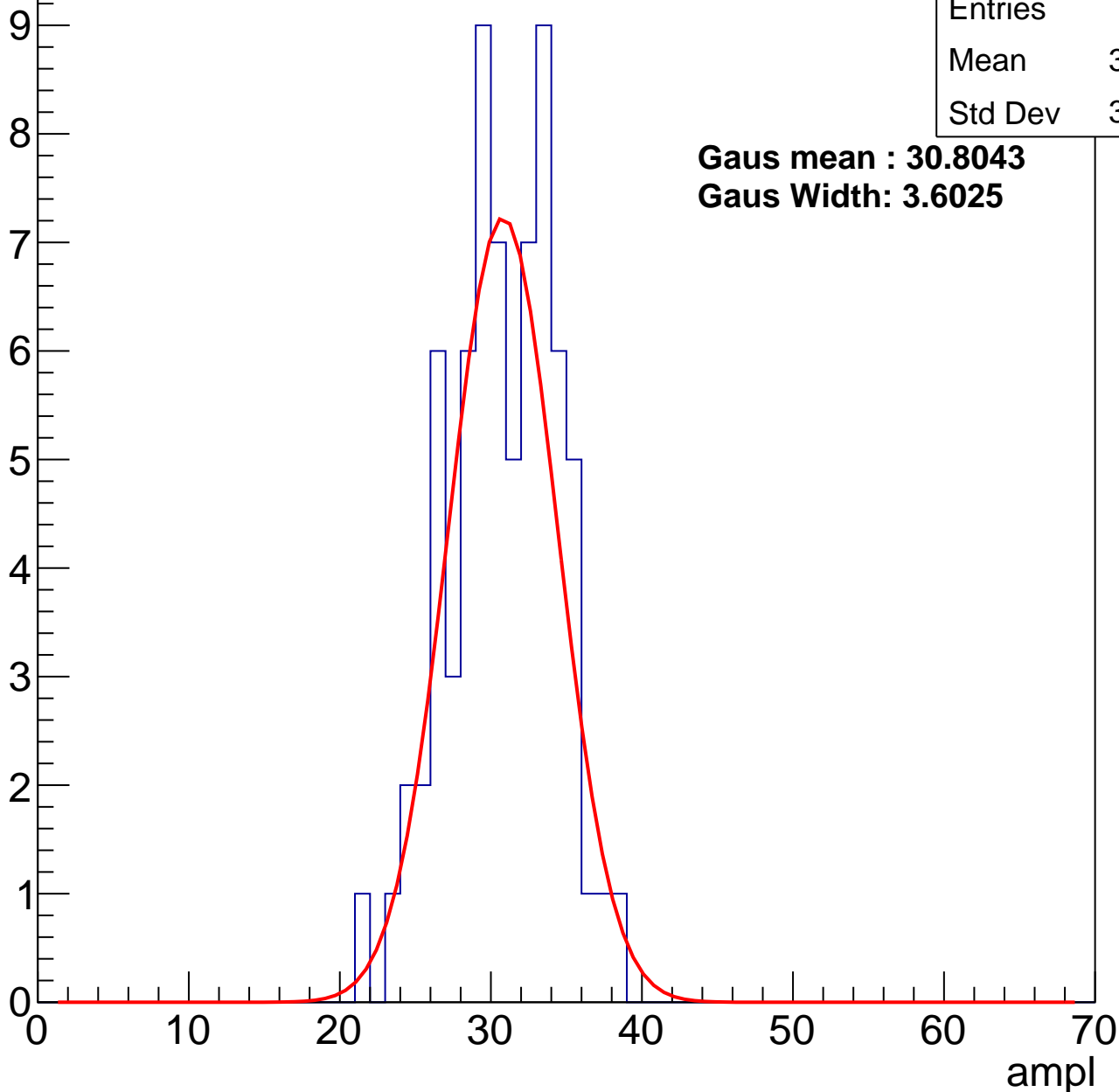
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 30.33 |
| Std Dev | 3.504 |

Gaus mean : 30.8043

Gaus Width: 3.6025



B0L001S, U7-ch66, adc1

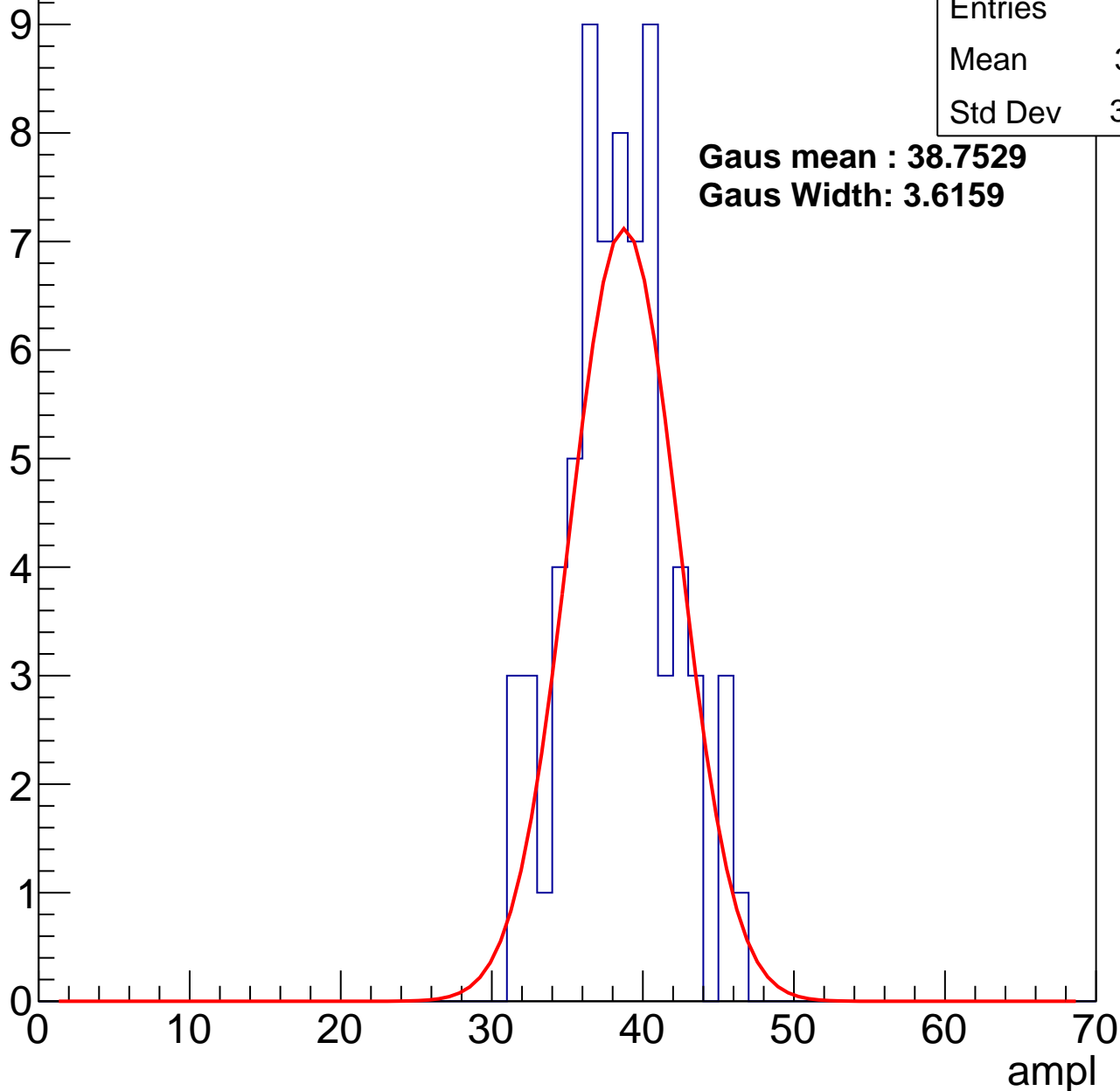
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.91 |
| Std Dev | 3.488 |

Gaus mean : 38.7529

Gaus Width: 3.6159

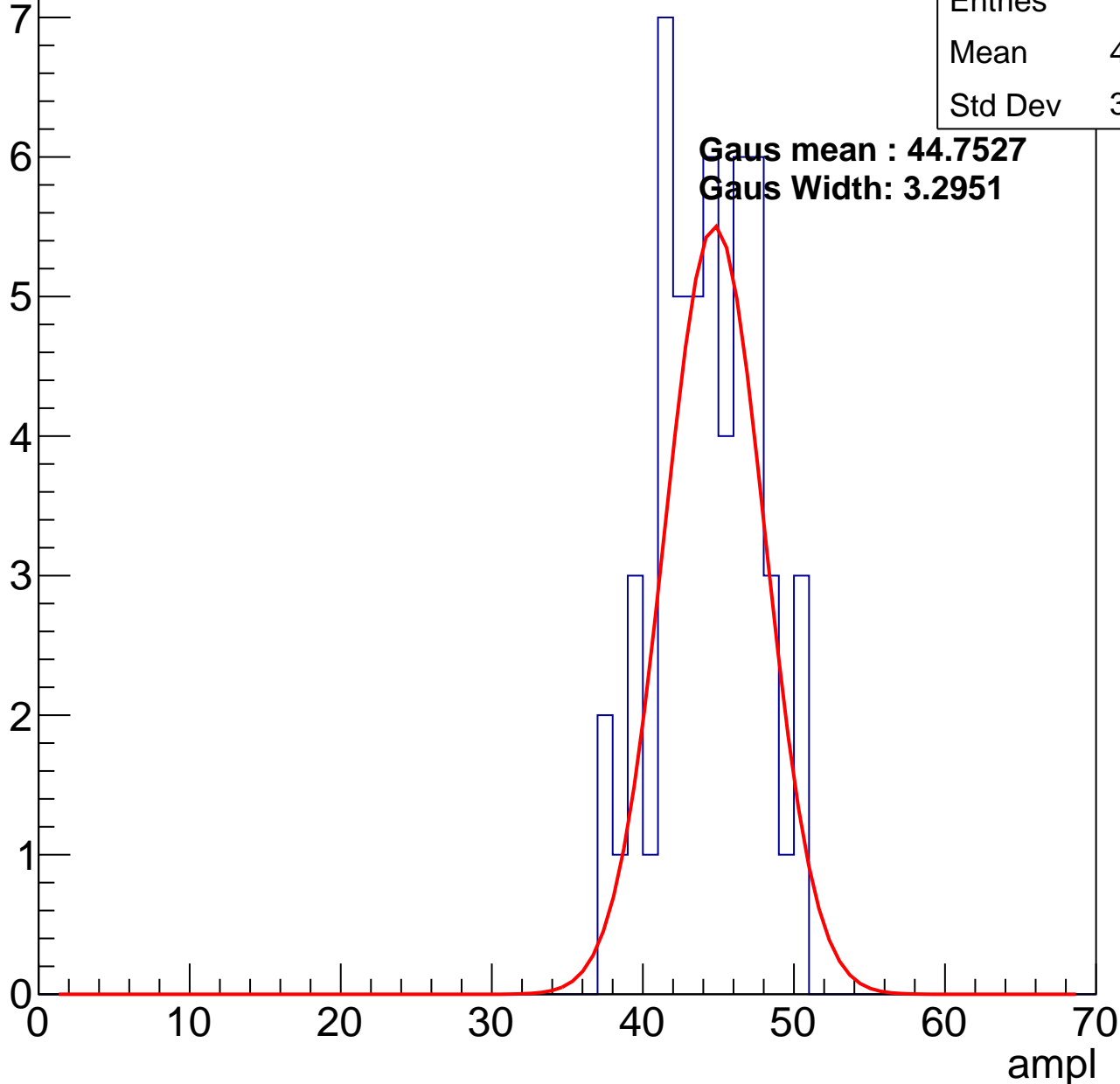


B0L001S, U7-ch66, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 43.89 |
| Std Dev | 3.295 |

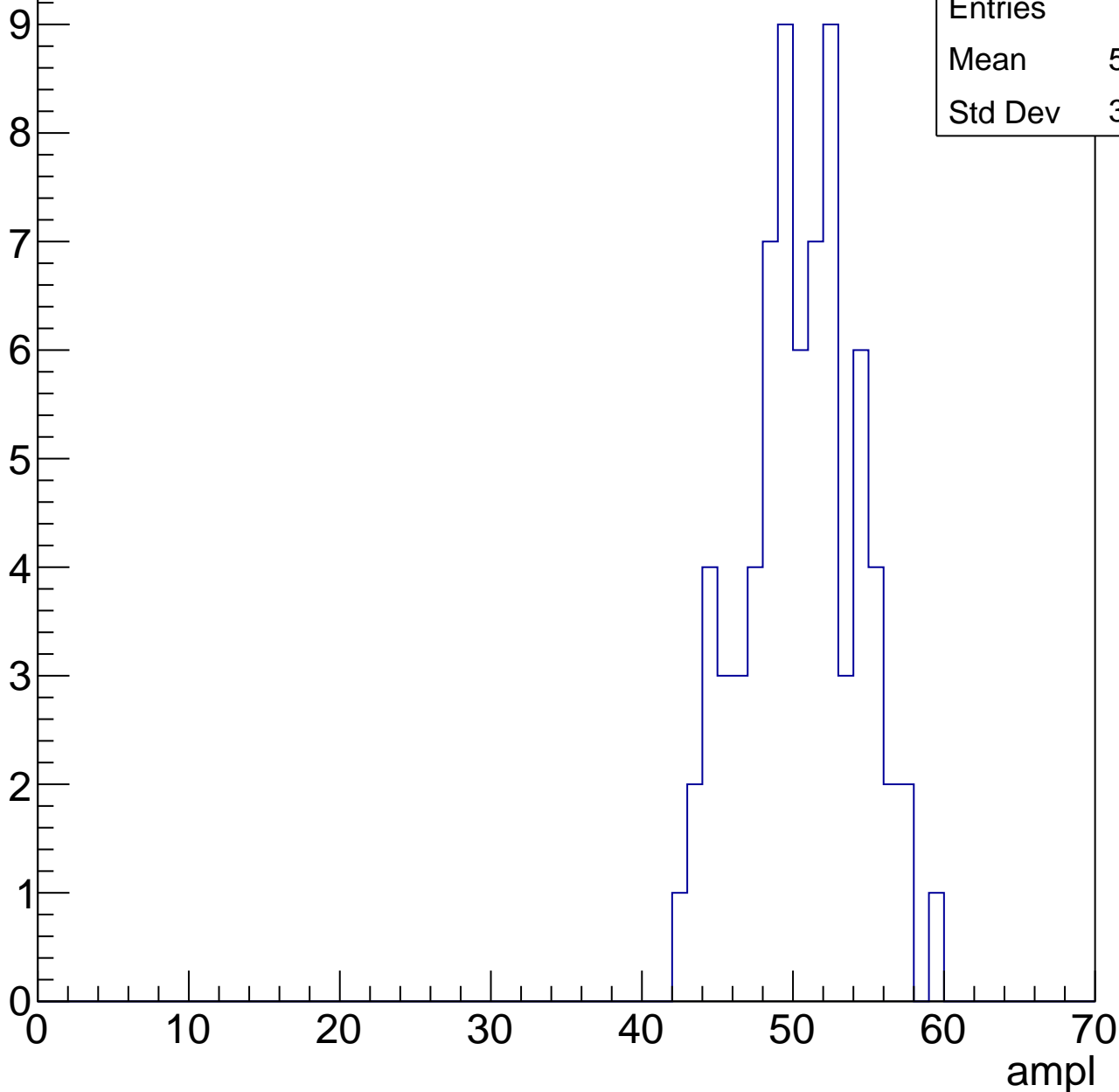


B0L001S, U7-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 50.07 |
| Std Dev | 3.736 |

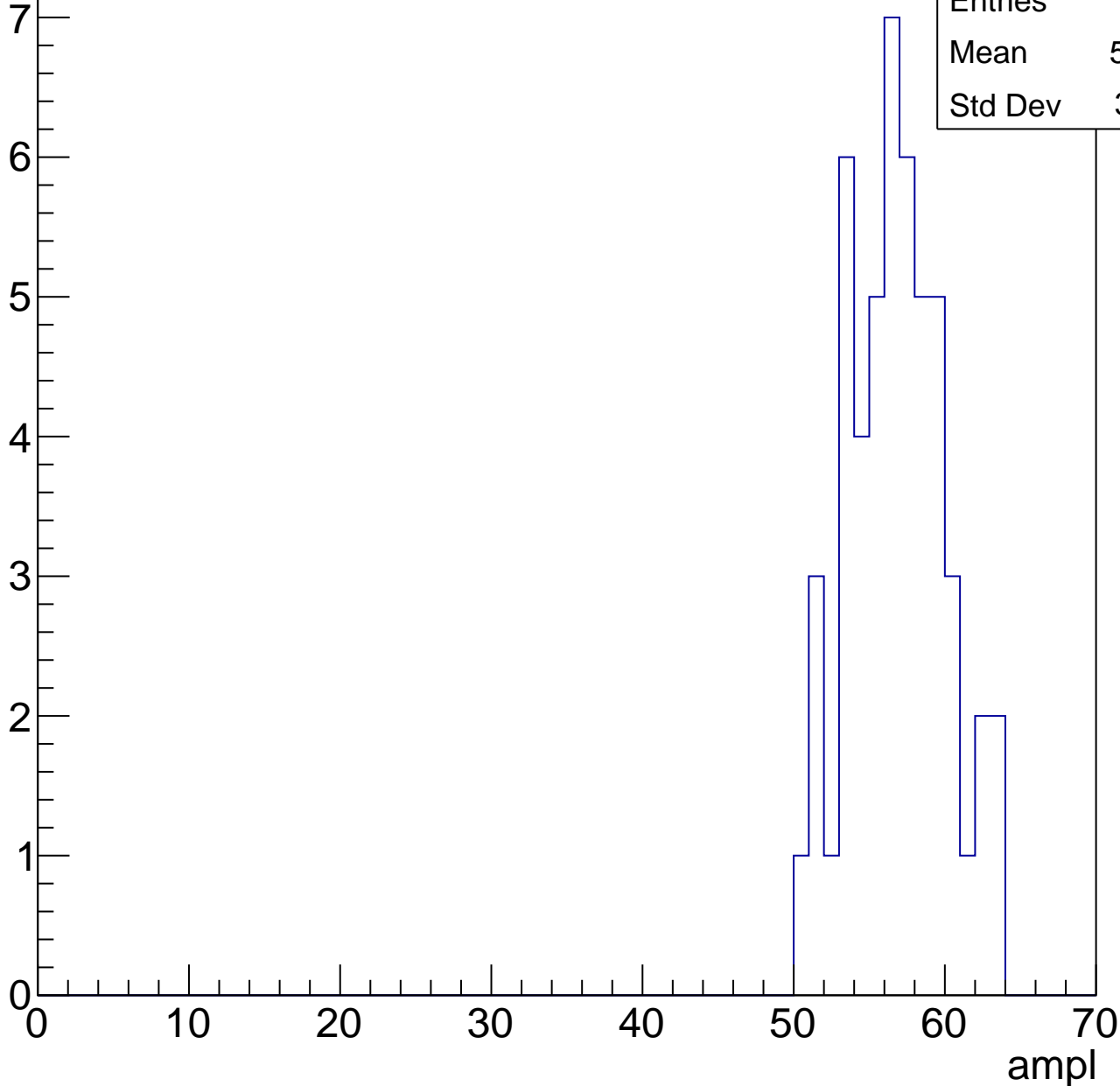


B0L001S, U7-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

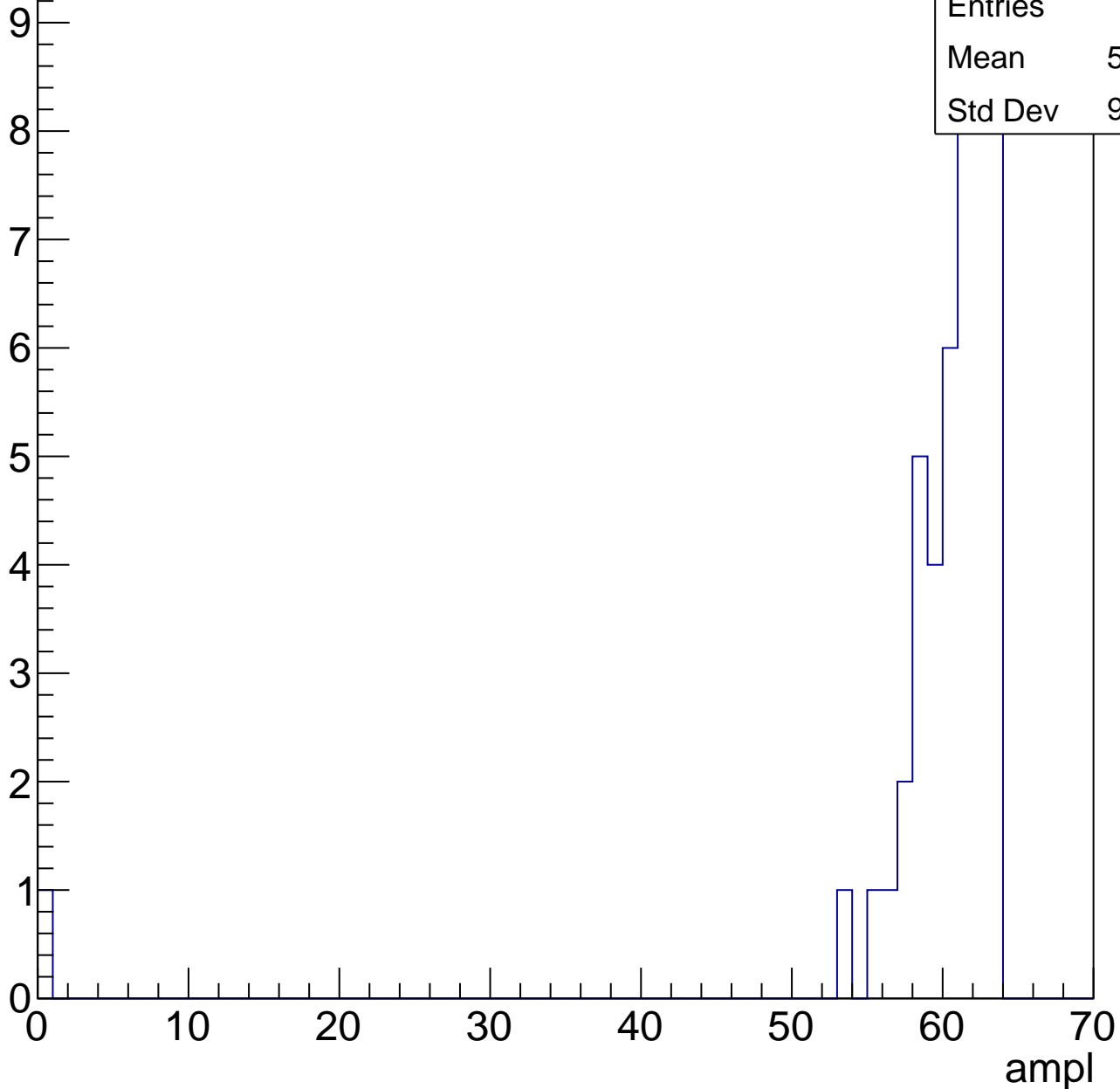
| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.35 |
| Std Dev | 3.161 |



B0L001S, U7-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch67, adc0

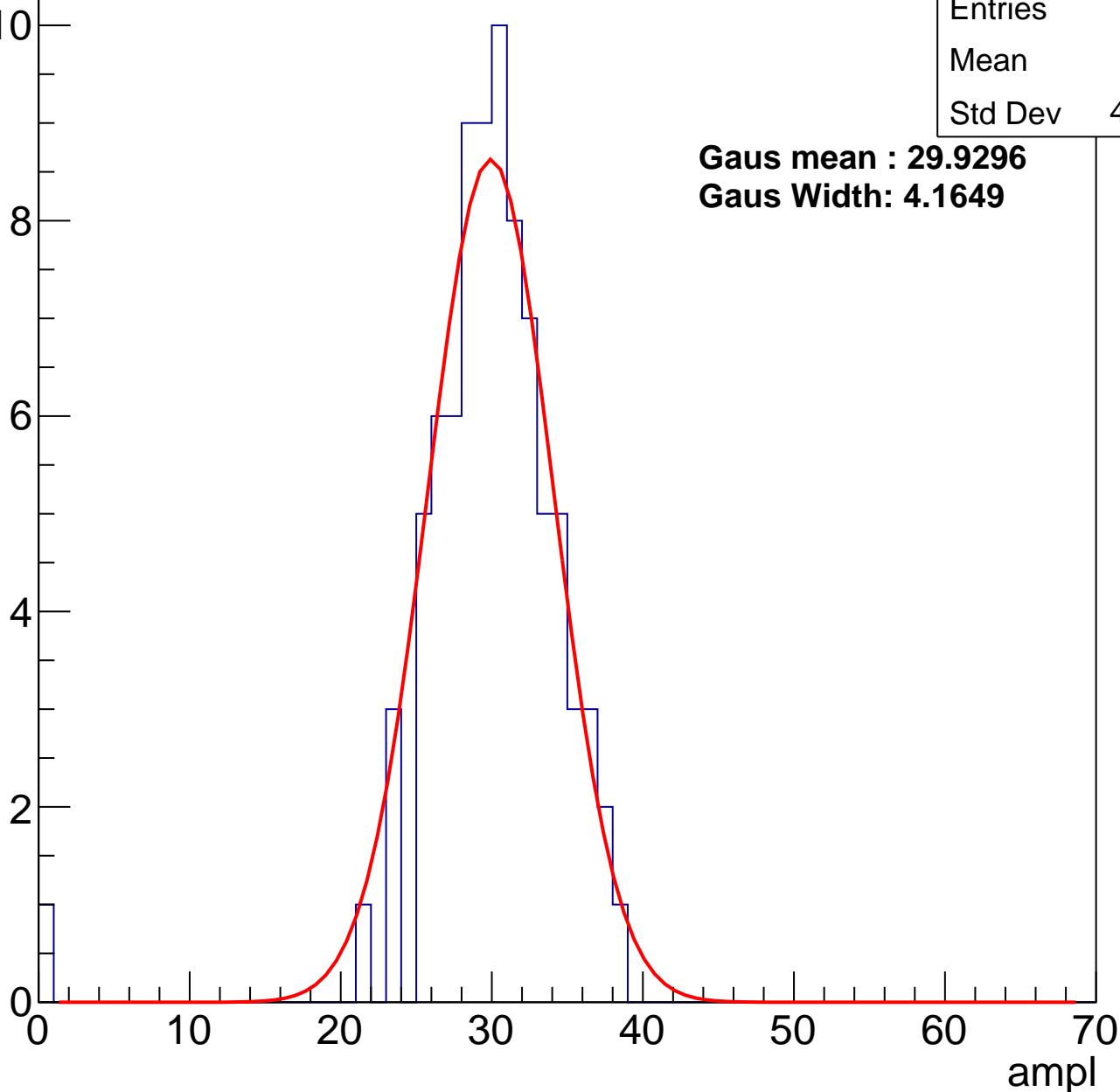
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 29.5 |
| Std Dev | 4.797 |

Gaus mean : 29.9296

Gaus Width: 4.1649



B0L001S, U7-ch67, adc1

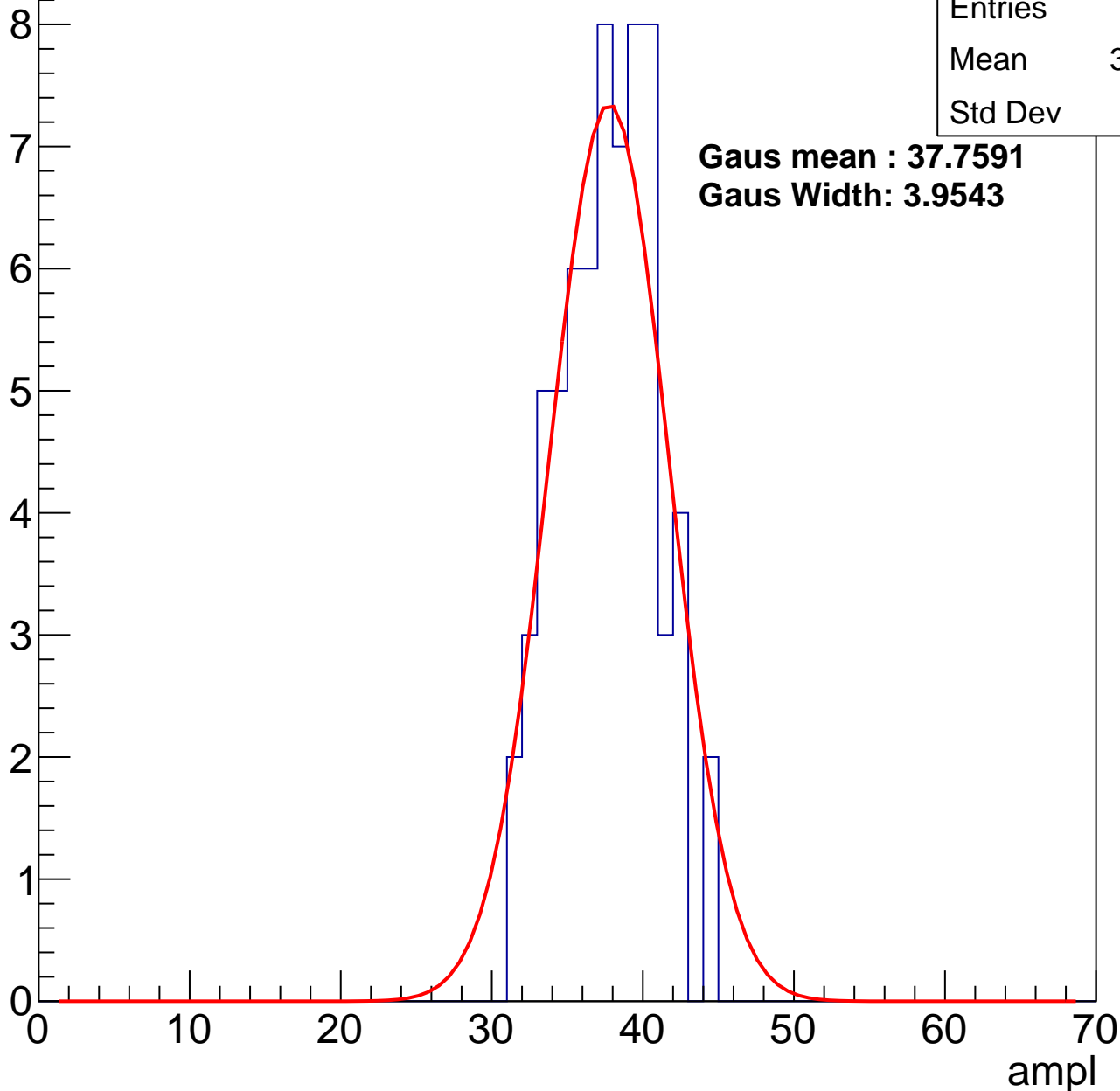
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.19 |
| Std Dev | 3.13 |

Gaus mean : 37.7591

Gaus Width: 3.9543



B0L001S, U7-ch67, adc2

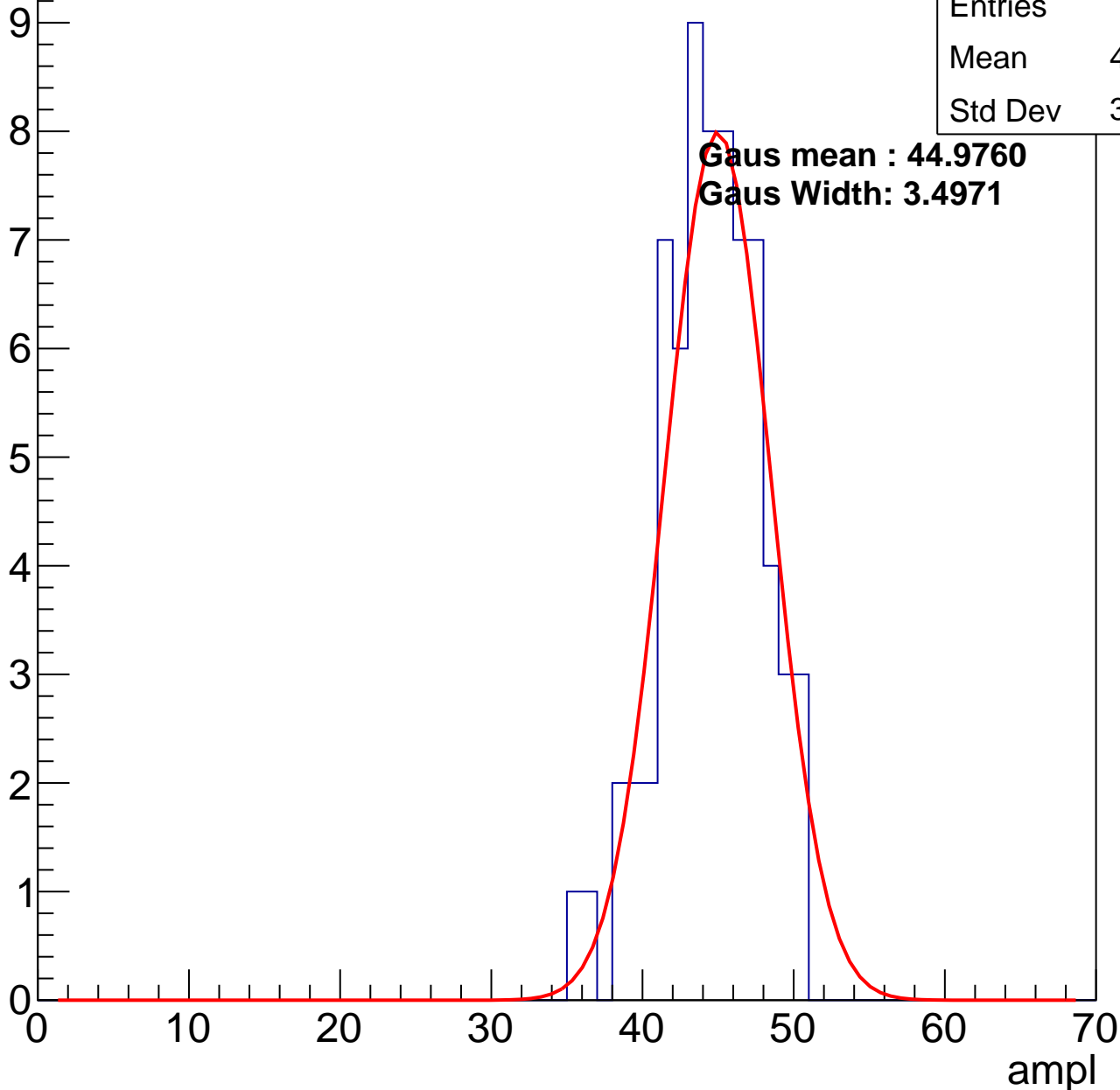
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 44.04 |
| Std Dev | 3.253 |

Gaus mean : 44.9760

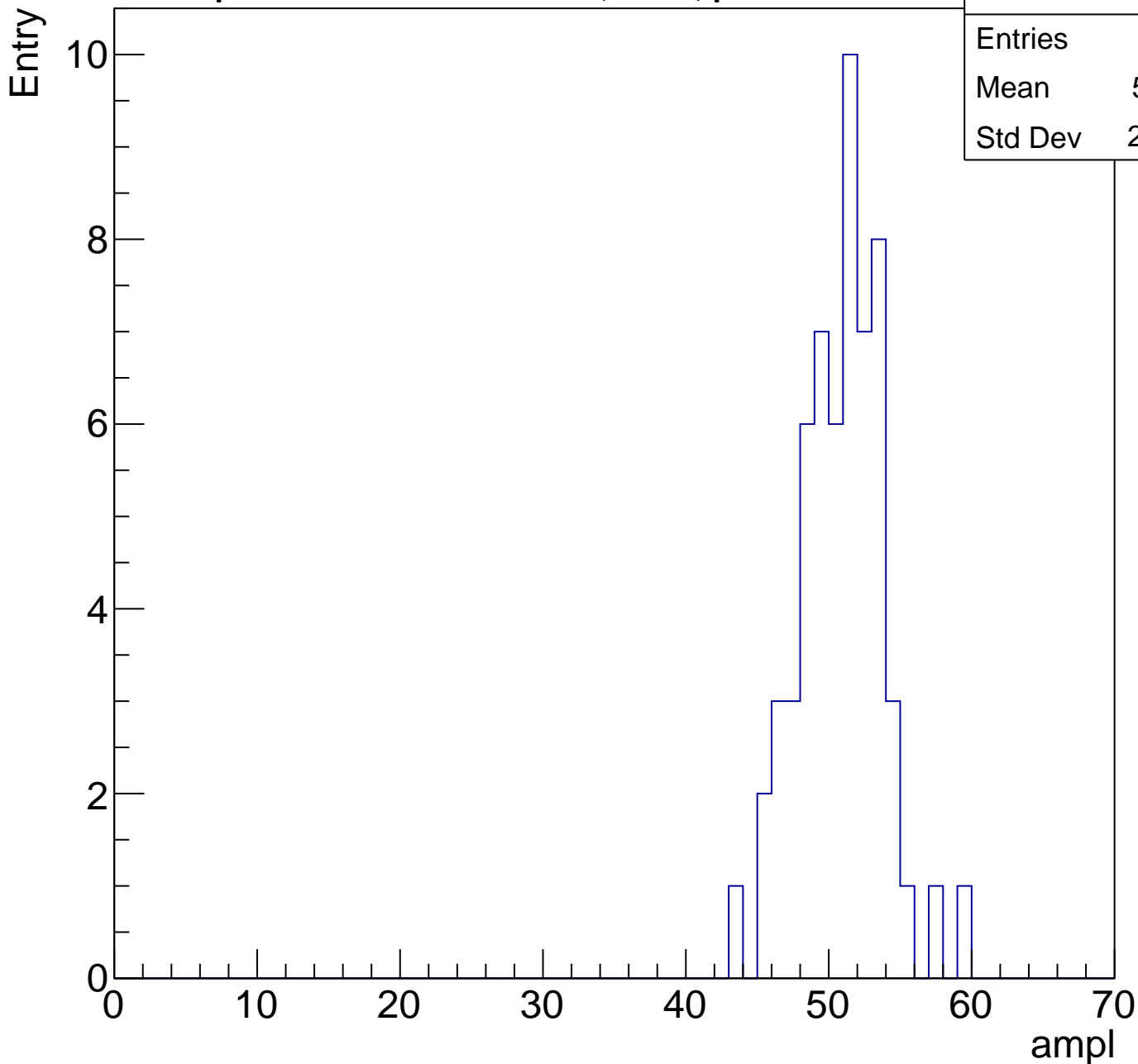
Gaus Width: 3.4971



B0L001S, U7-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 59 |
| Mean | 50.41 |
| Std Dev | 2.935 |



B0L001S, U7-ch67, adc4

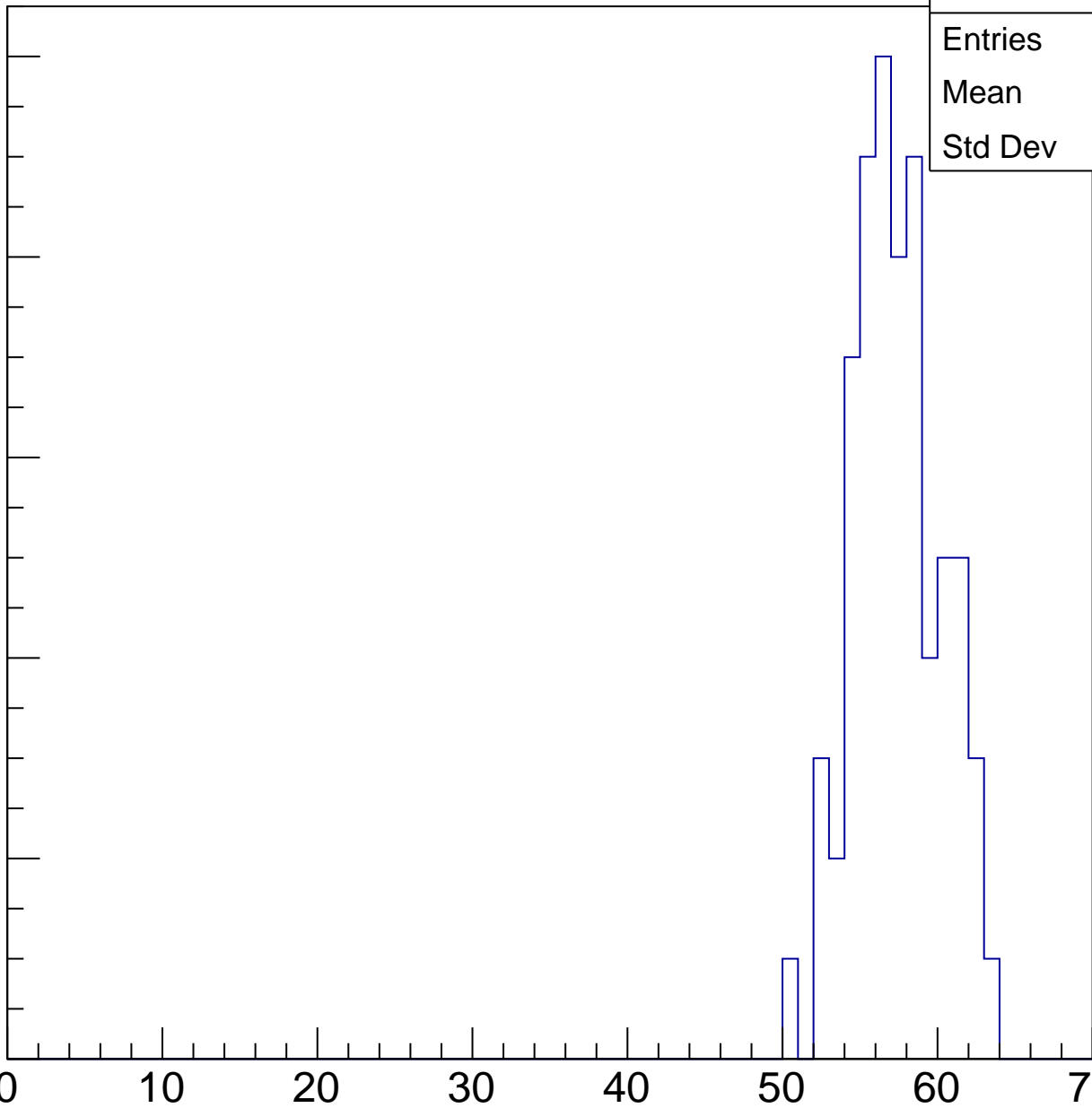
calib_packv5_042523_0143.root, FC#9, port A1

Entry

10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 67 |
| Mean | 56.91 |
| Std Dev | 2.8 |

ampl

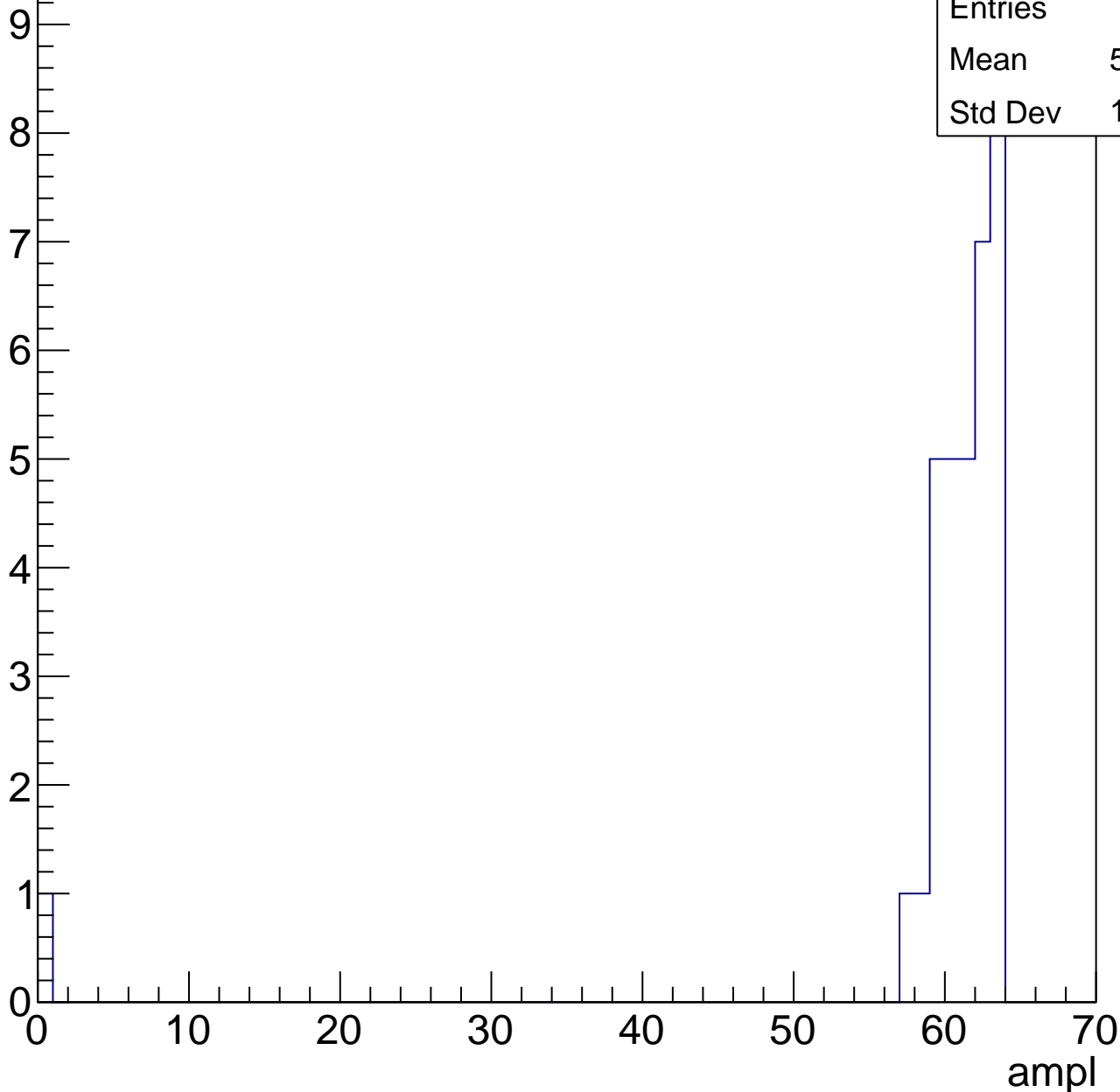


B0L001S, U7-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 59.29 |
| Std Dev | 10.45 |



B0L001S, U7-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch68, adc0

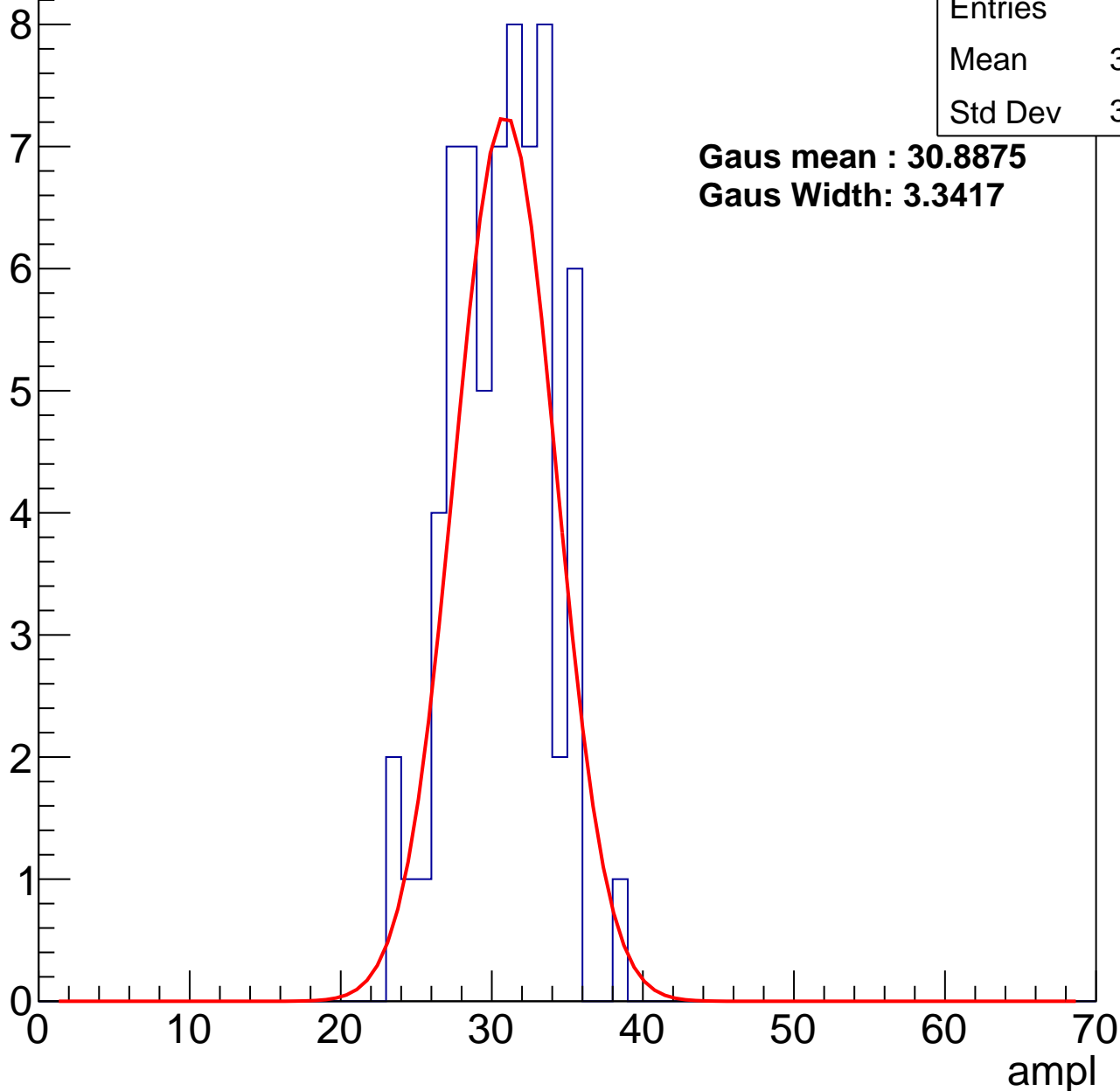
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 30.17 |
| Std Dev | 3.189 |

Gaus mean : 30.8875

Gaus Width: 3.3417



B0L001S, U7-ch68, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 37.42 |
| Std Dev | 3.277 |

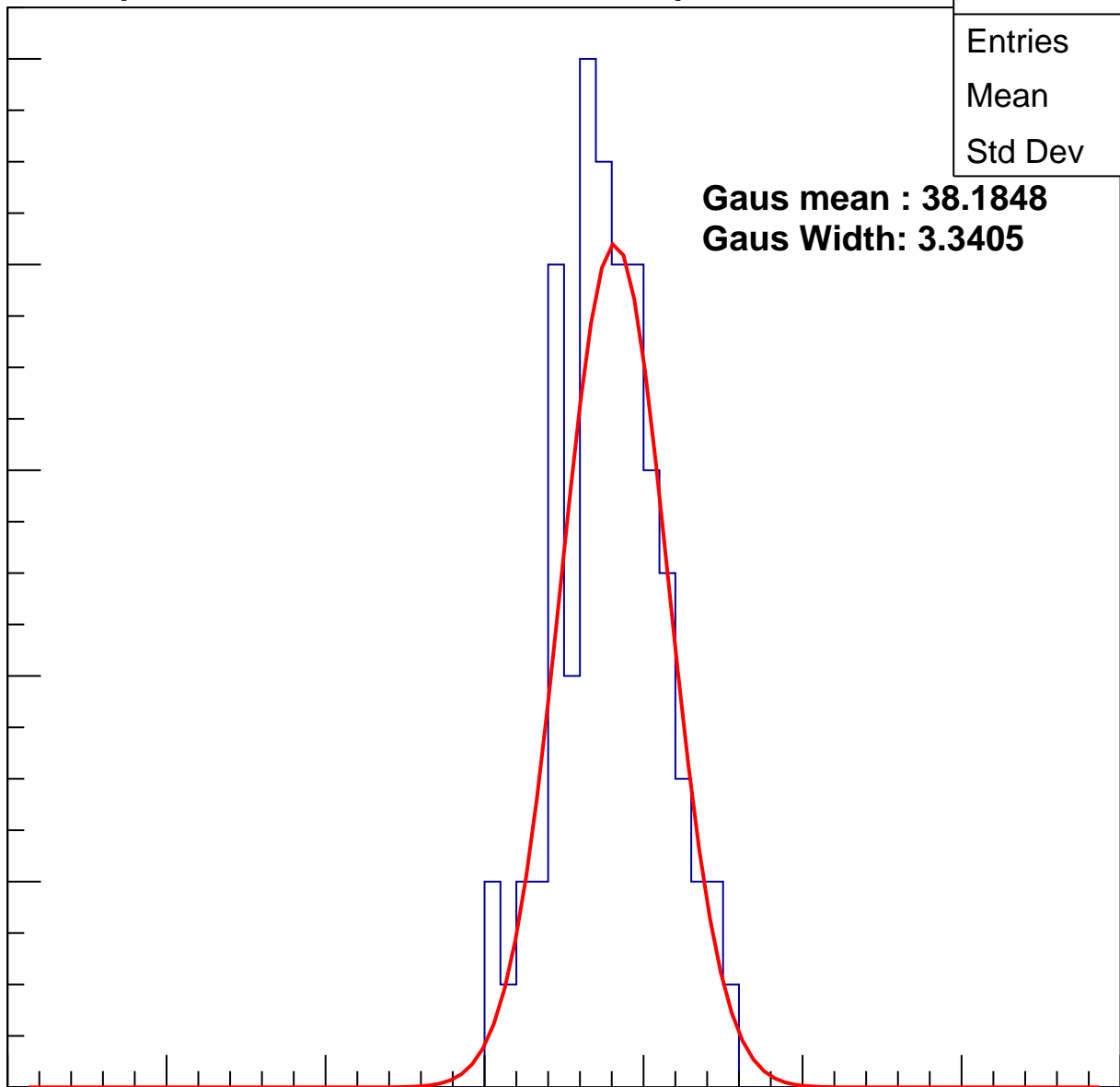
Gaus mean : 38.1848

Gaus Width: 3.3405

10
8
6
4
2
0

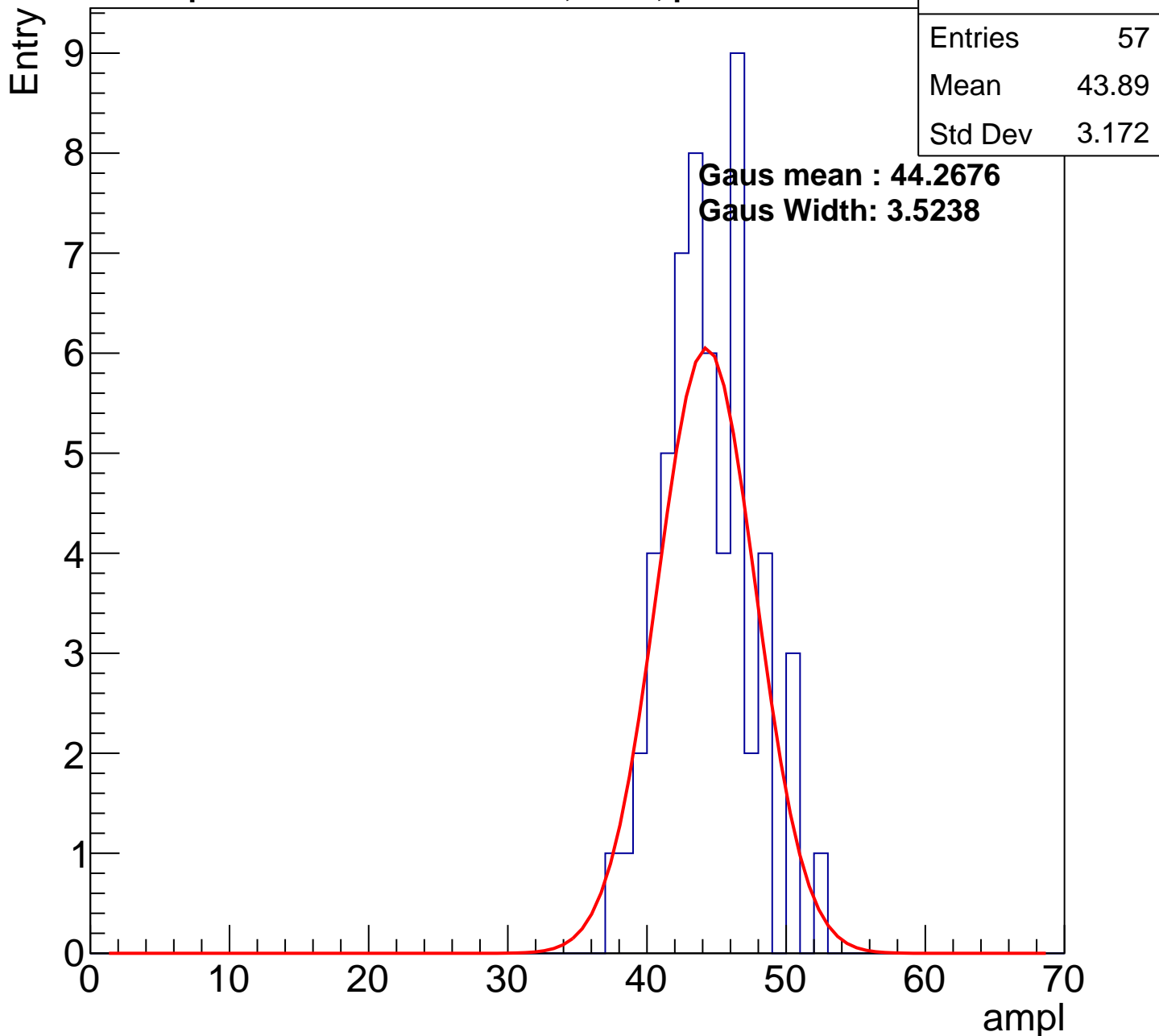
0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch68, adc2

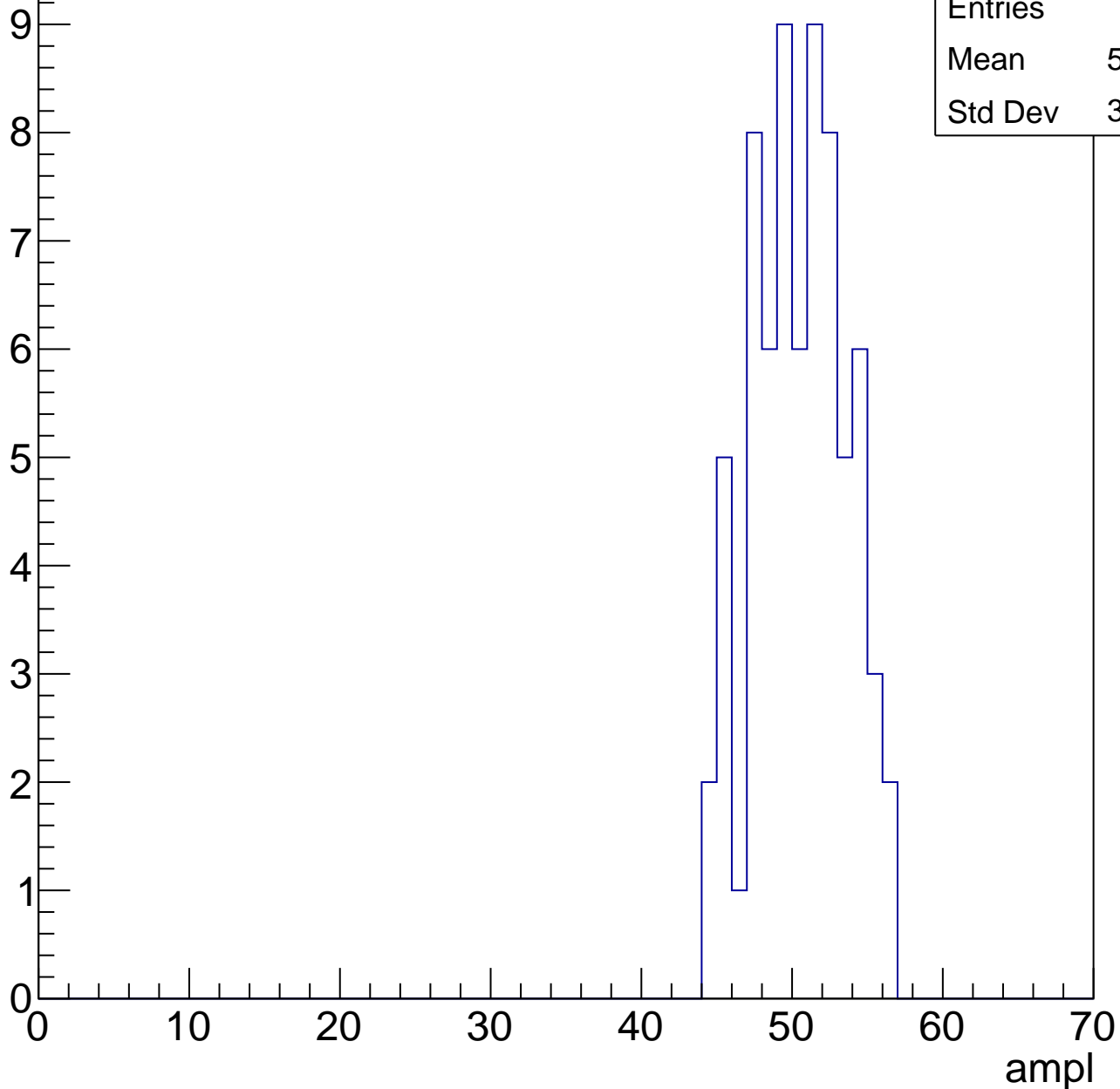
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch68, adc3

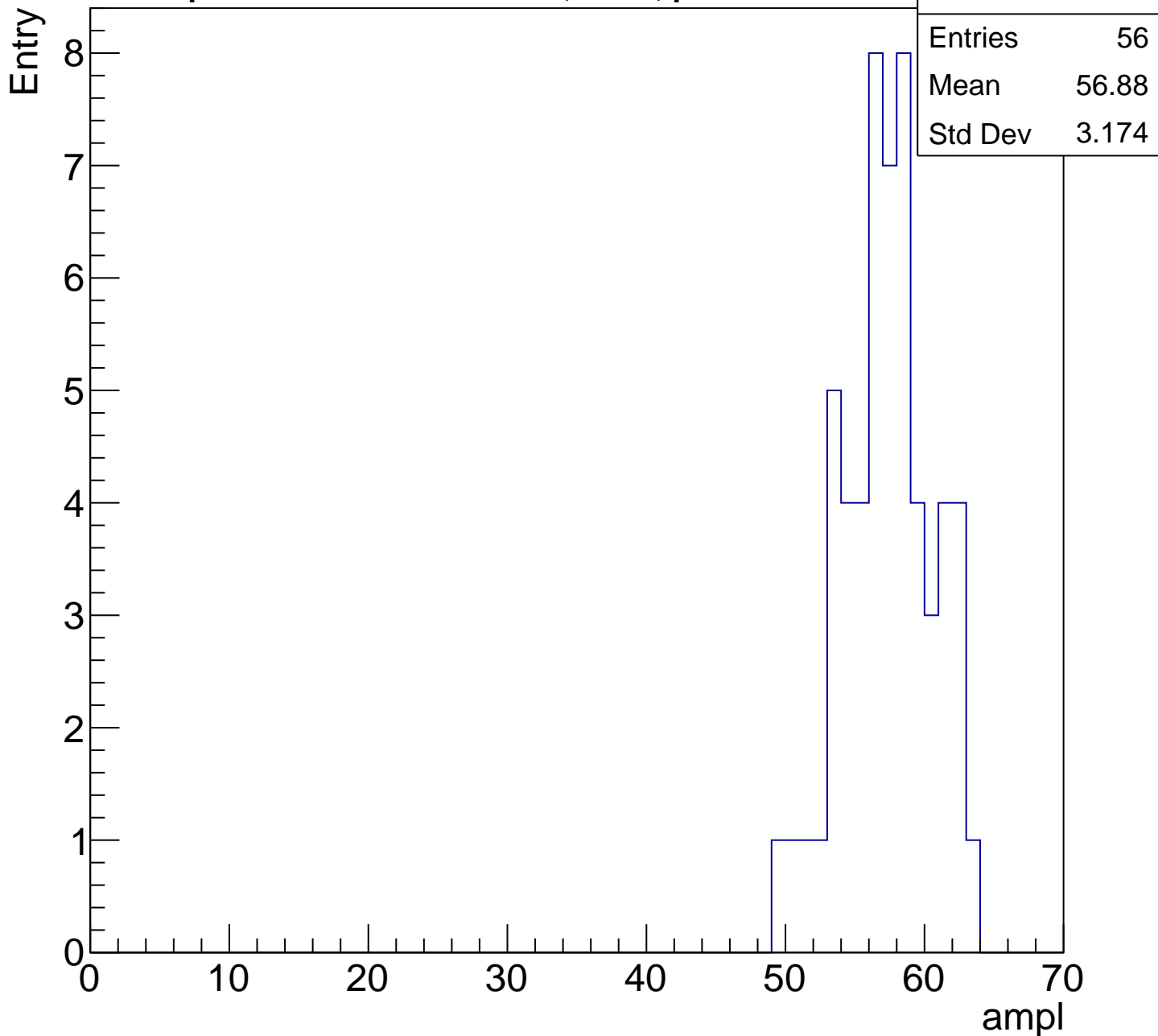
calib_packv5_042523_0143.root, FC#9, port A1

Entry



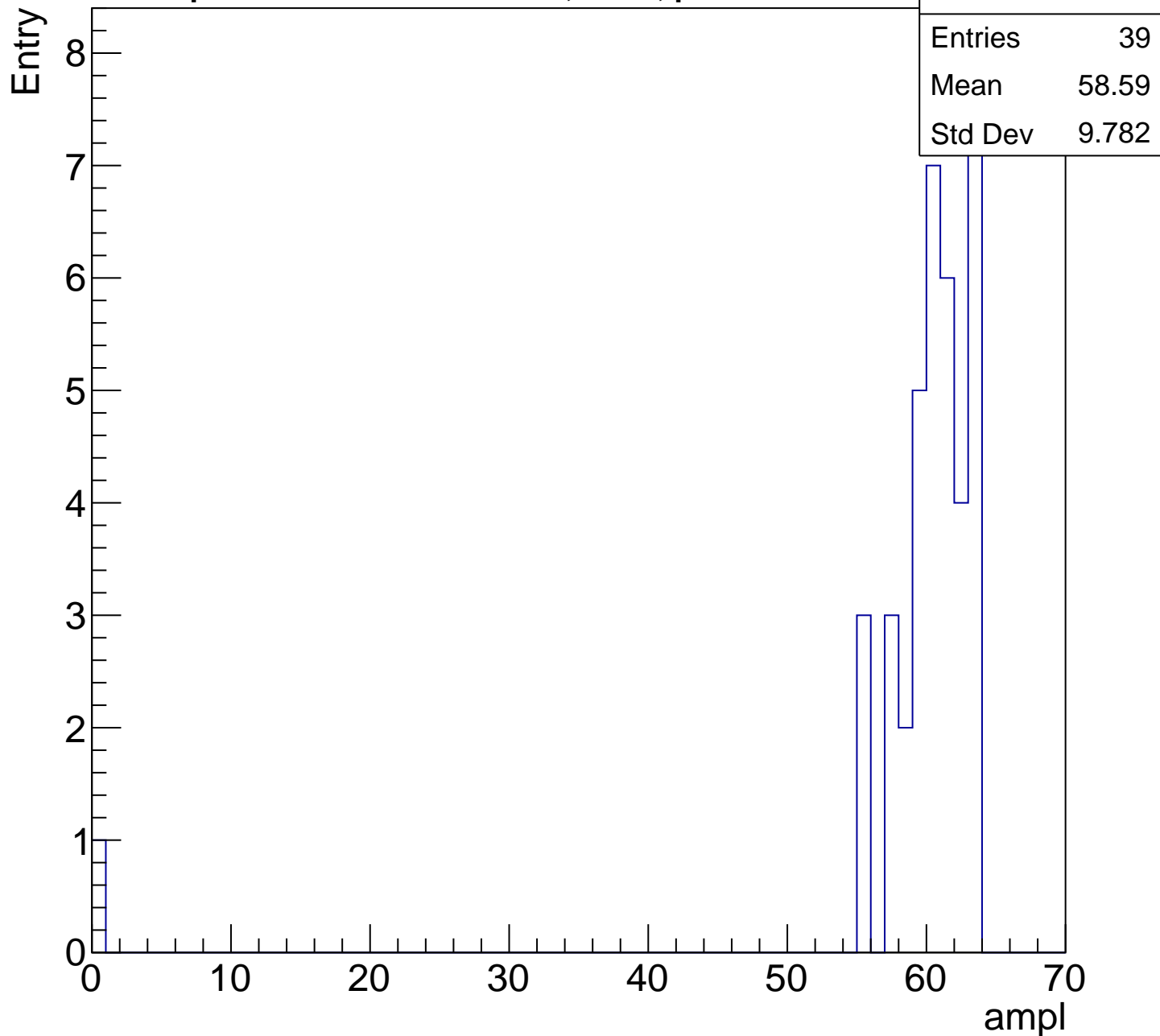
B0L001S, U7-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

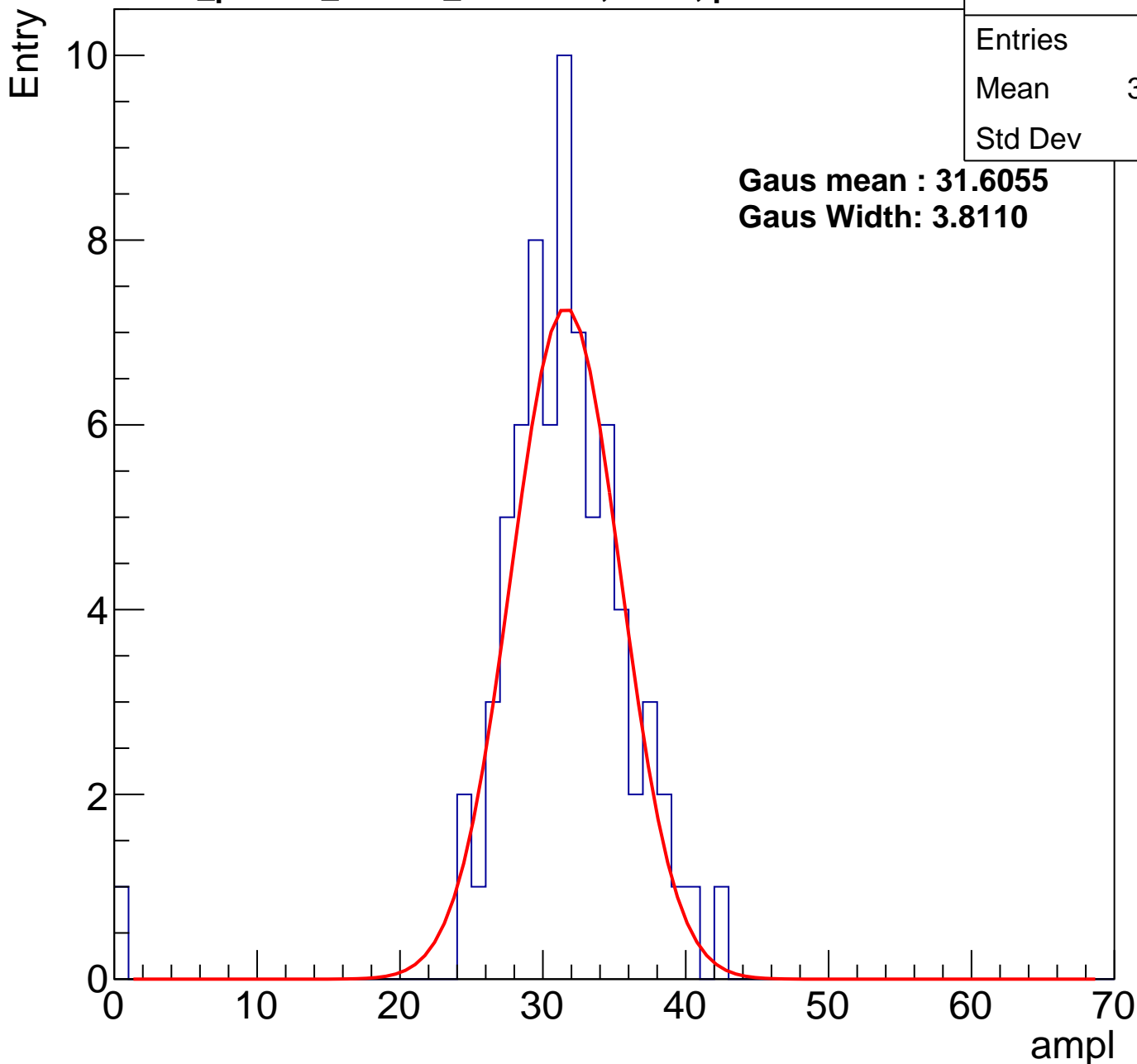
B0L001S, U7-ch69, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 30.93 |
| Std Dev | 5.21 |

Gaus mean : 31.6055

Gaus Width: 3.8110



B0L001S, U7-ch69, adc1

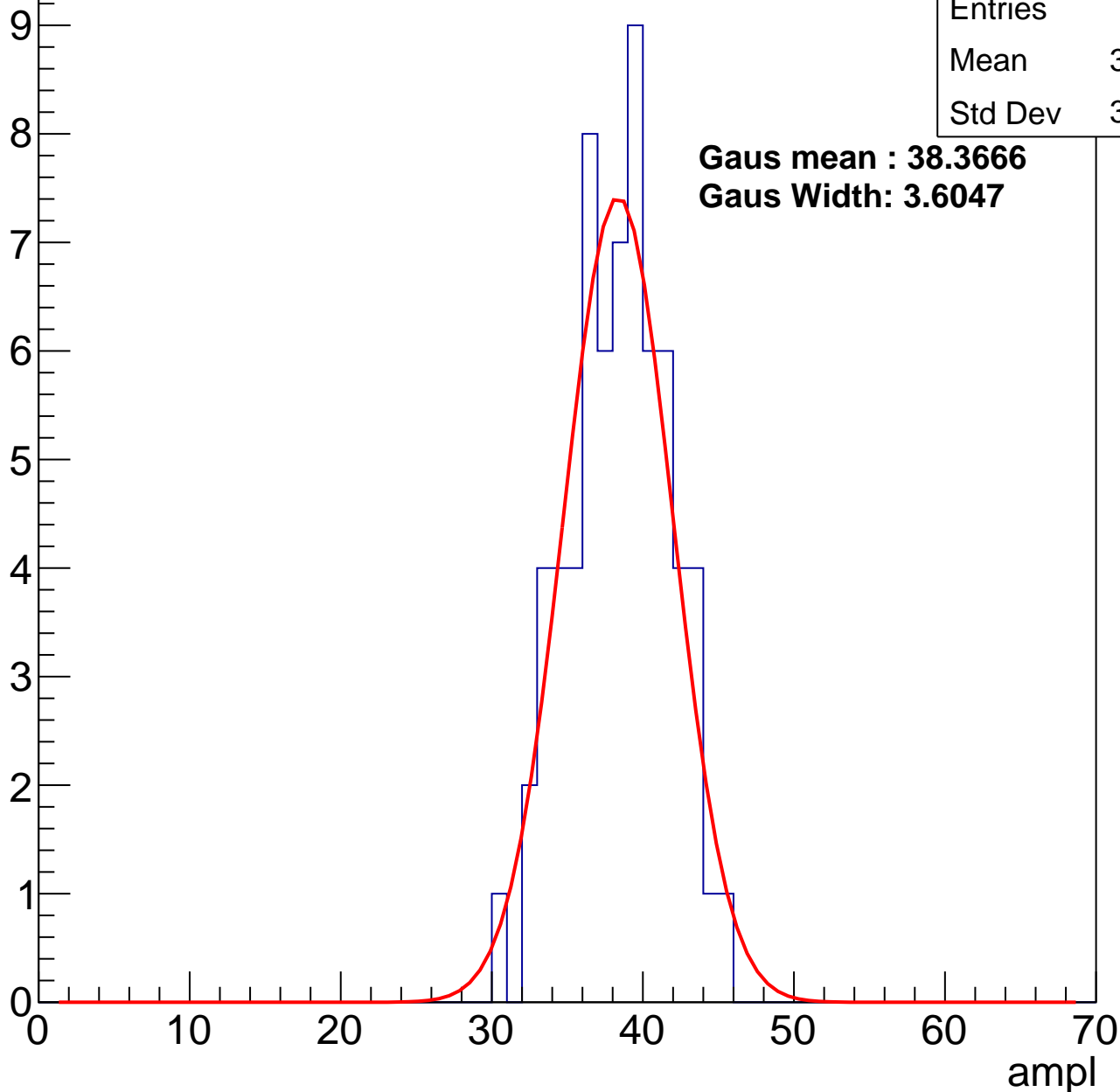
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 37.97 |
| Std Dev | 3.255 |

Gaus mean : 38.3666

Gaus Width: 3.6047



B0L001S, U7-ch69, adc2

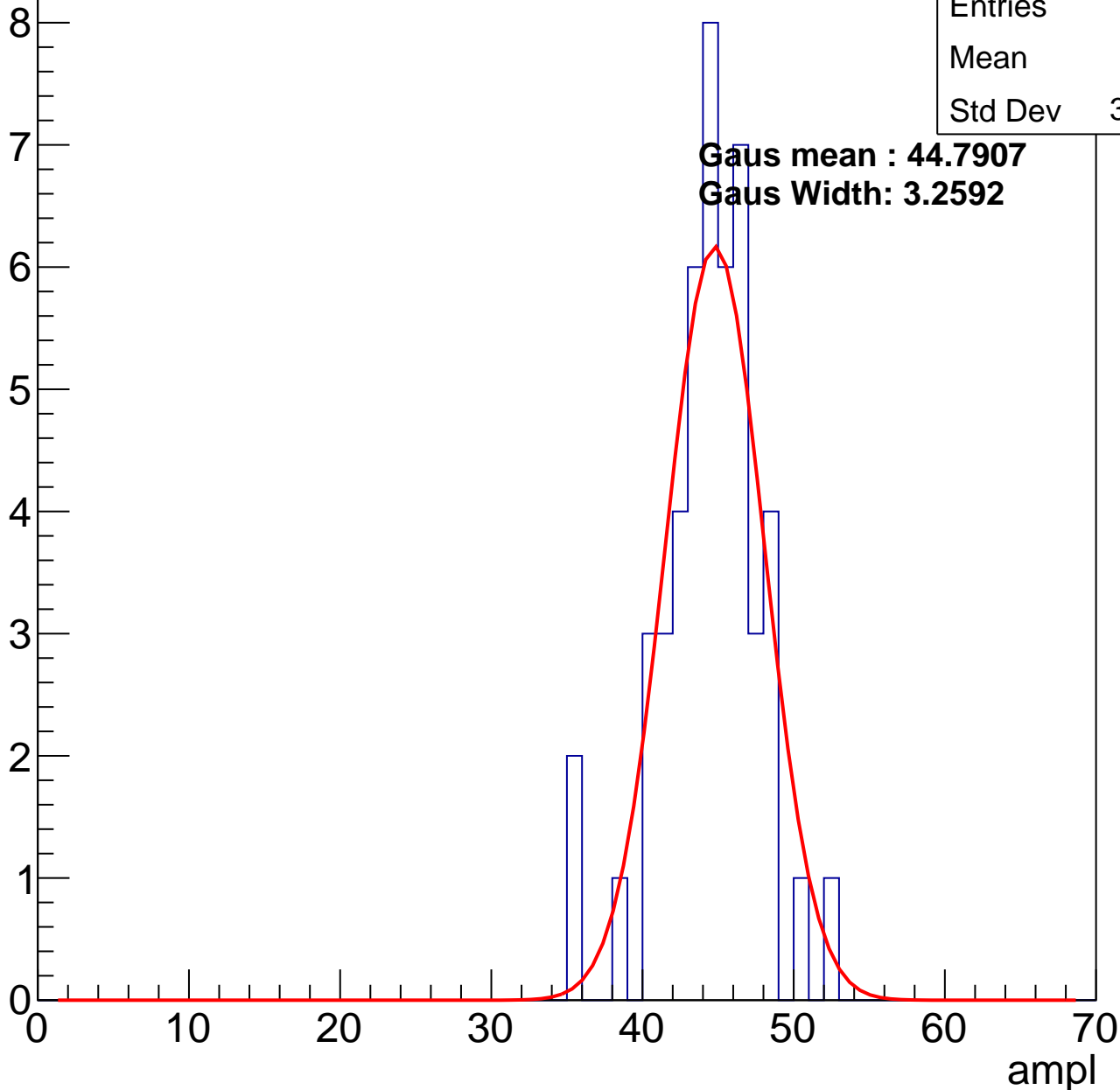
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 44 |
| Std Dev | 3.258 |

Gaus mean : 44.7907

Gaus Width: 3.2592



B0L001S, U7-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 51 |
| Std Dev | 3.919 |

Entry

10

8

6

4

2

0

0

10

20

30

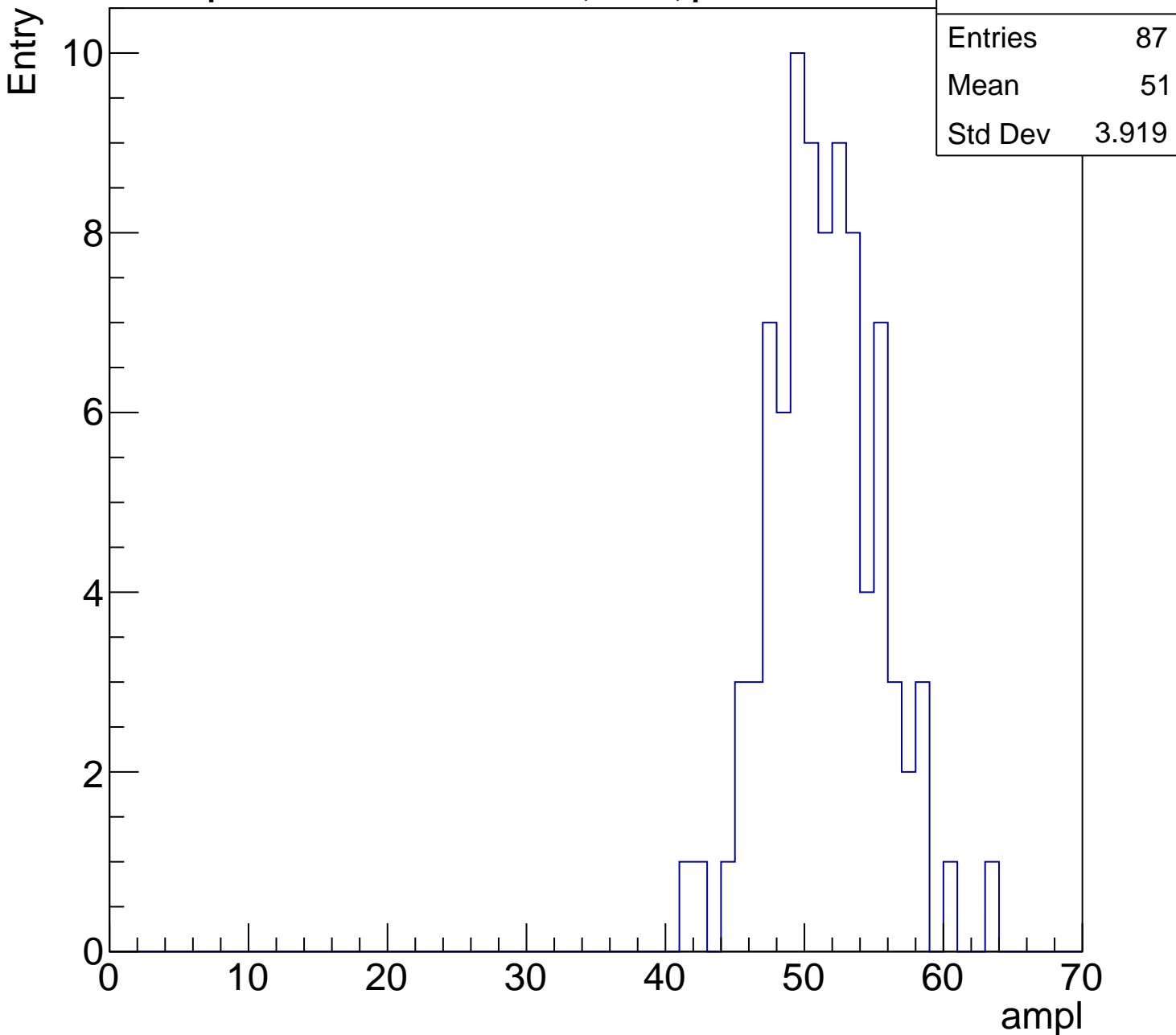
40

50

60

ampl

70

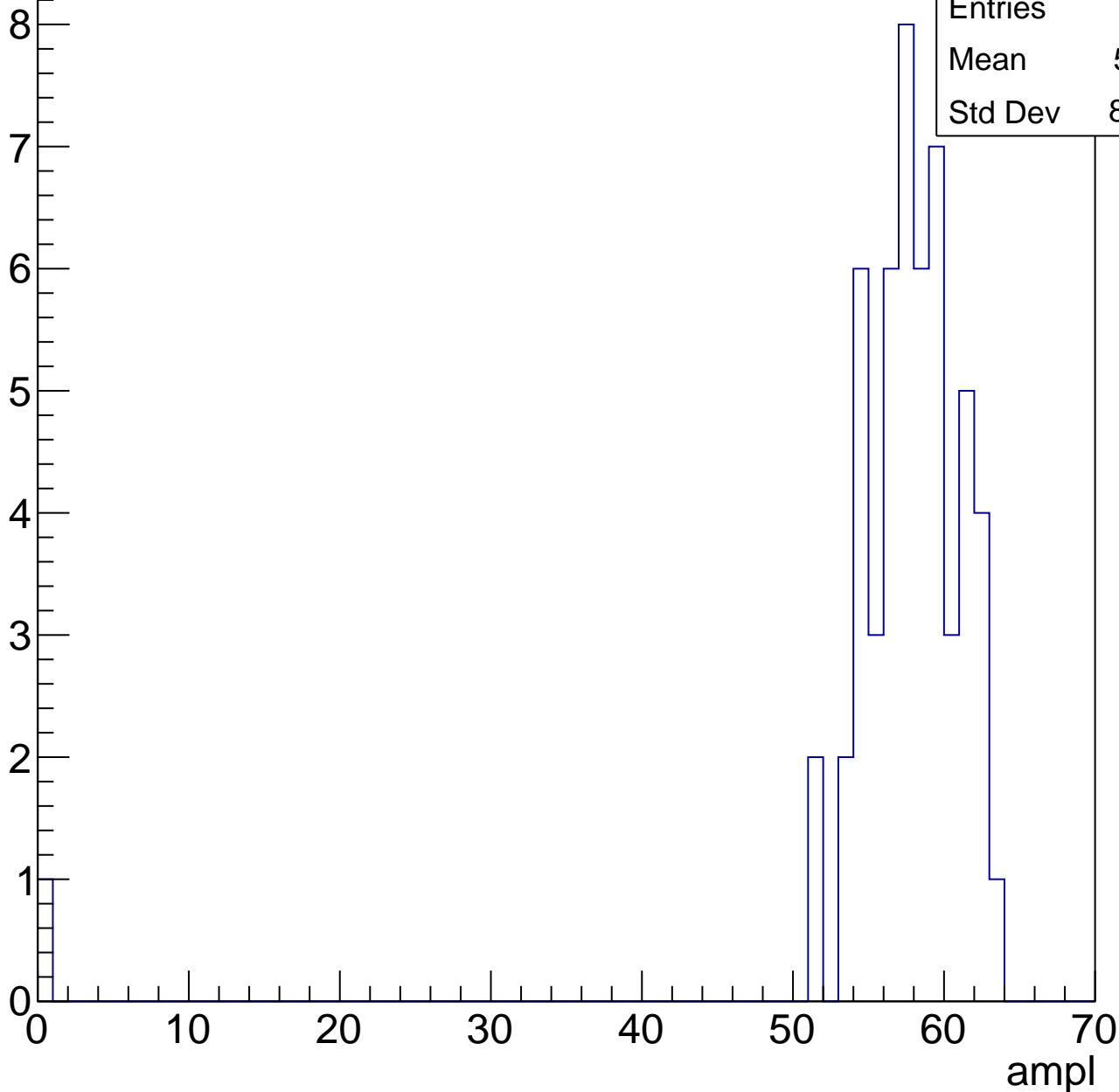


B0L001S, U7-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 56.41 |
| Std Dev | 8.259 |

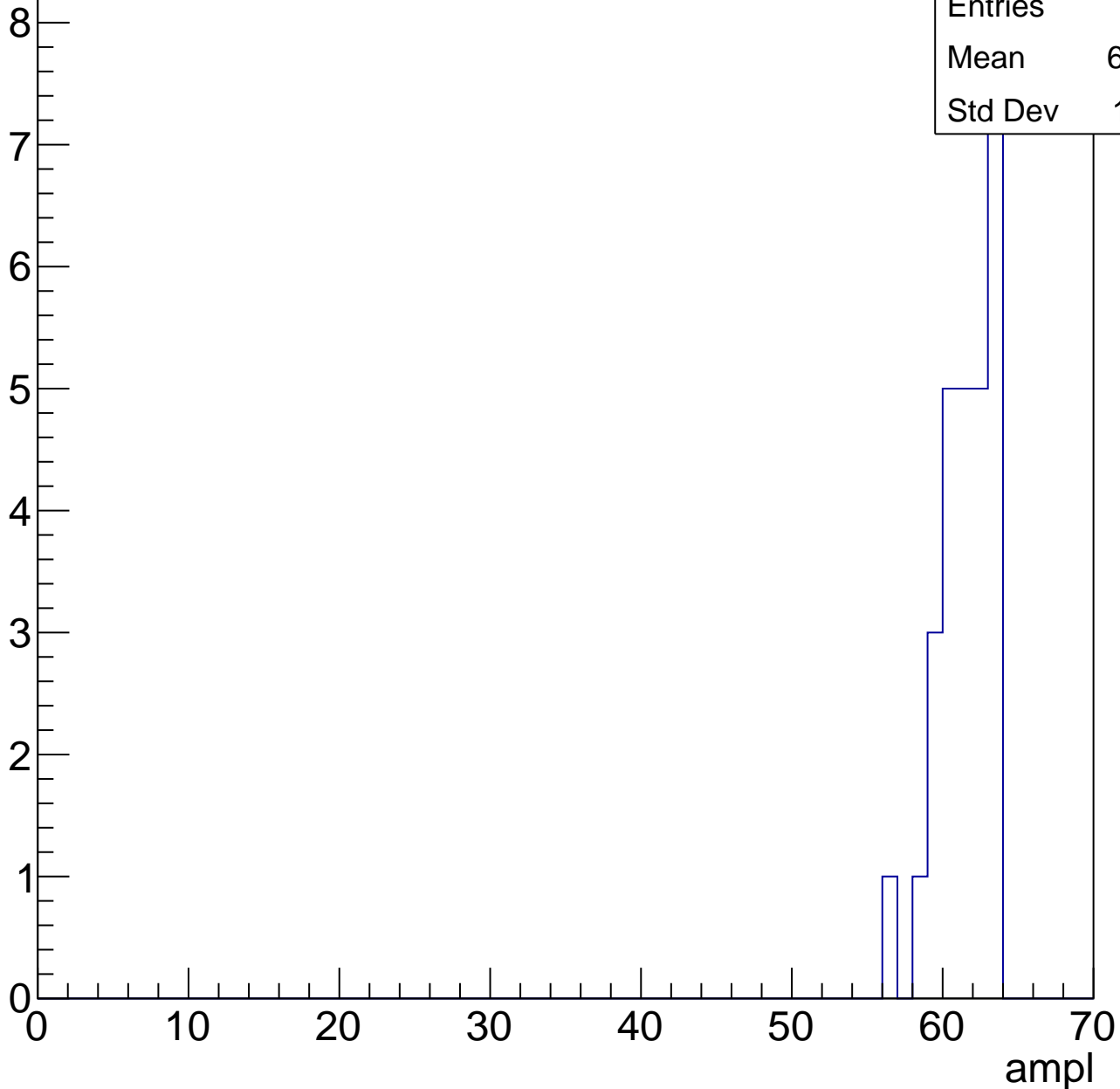


B0L001S, U7-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 61.07 |
| Std Dev | 1.771 |



B0L001S, U7-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch70, adc0

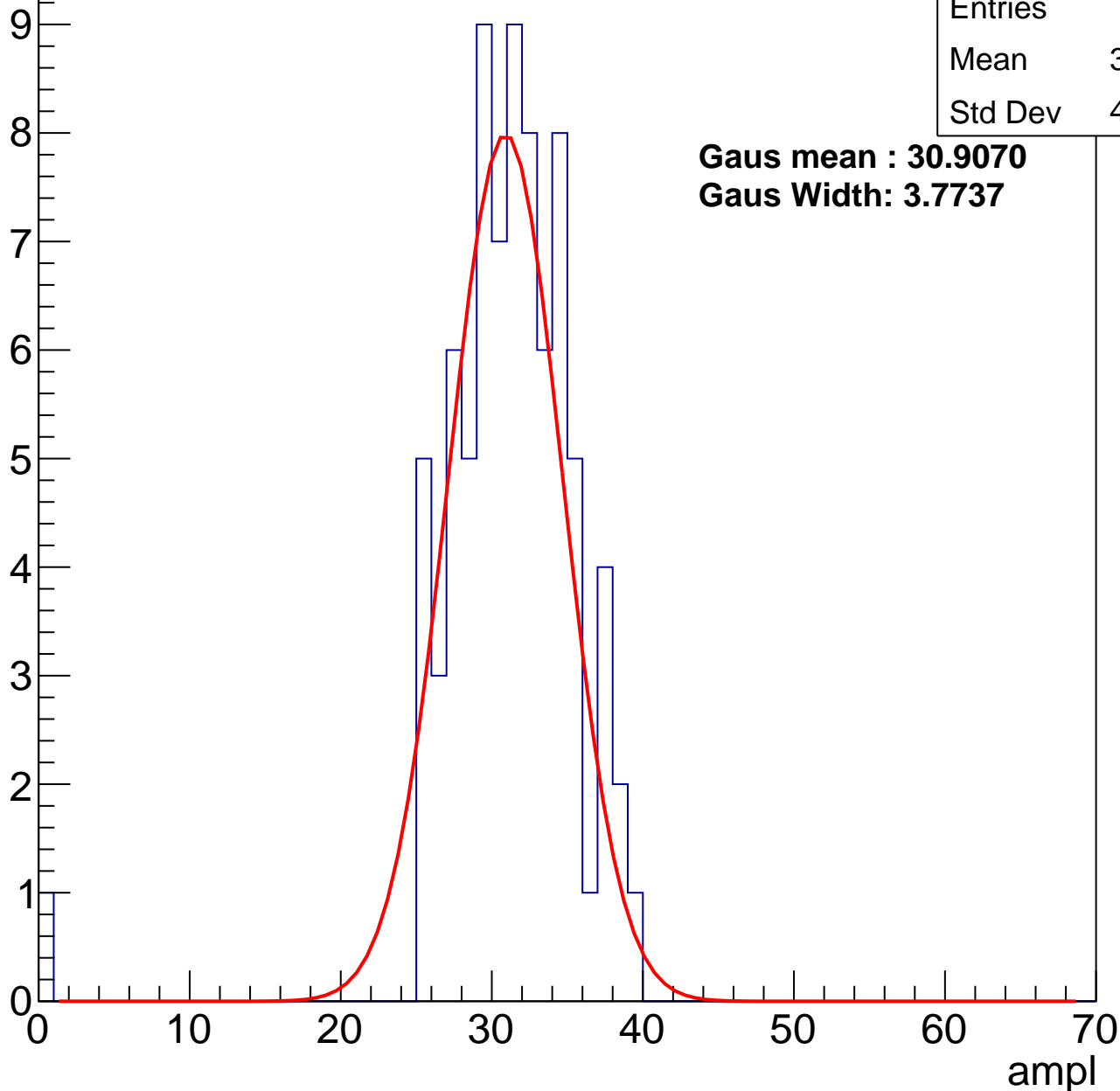
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 30.69 |
| Std Dev | 4.883 |

Gaus mean : 30.9070

Gaus Width: 3.7737



B0L001S, U7-ch70, adc1

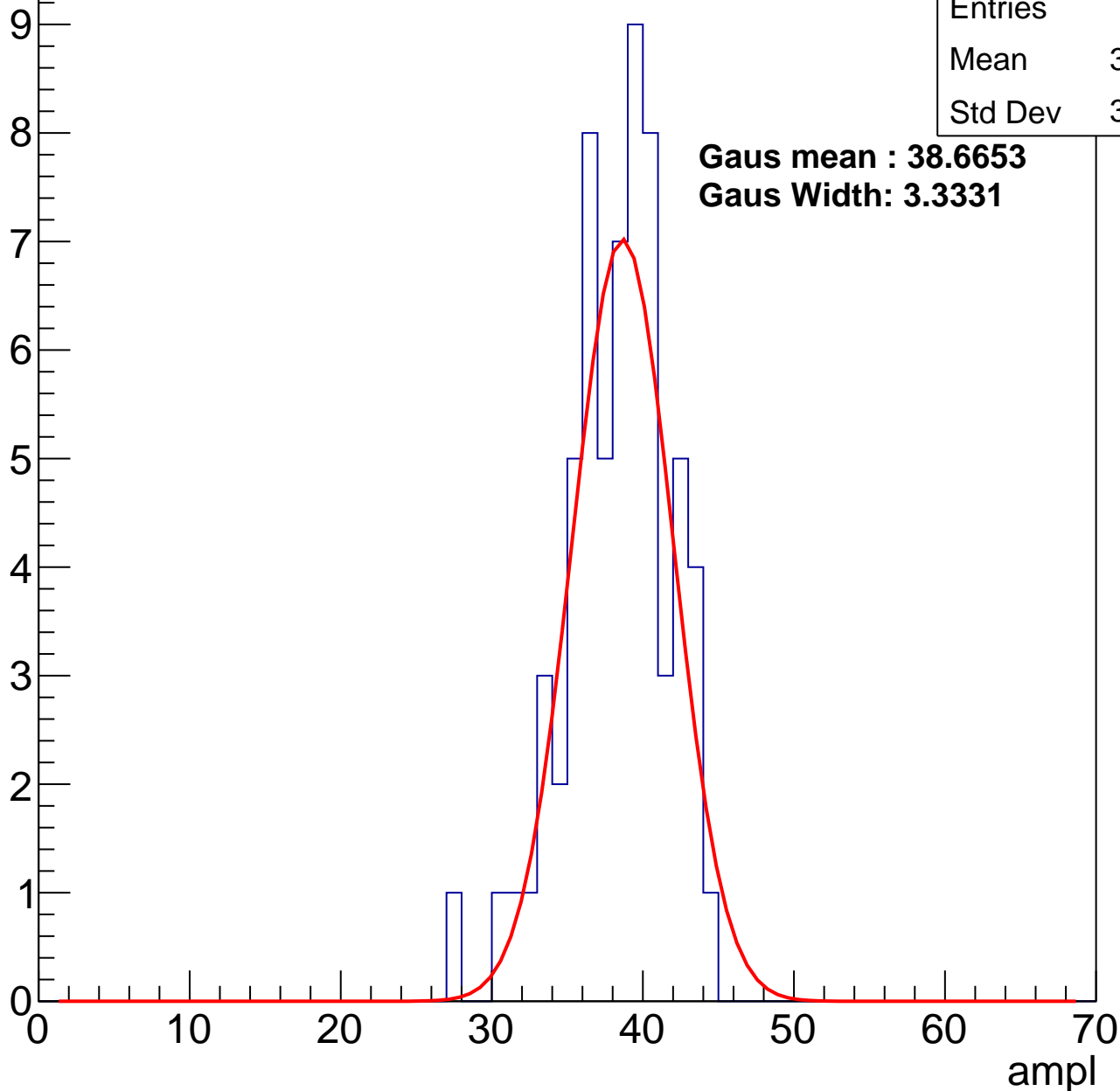
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37.83 |
| Std Dev | 3.403 |

Gaus mean : 38.6653

Gaus Width: 3.3331



B0L001S, U7-ch70, adc2

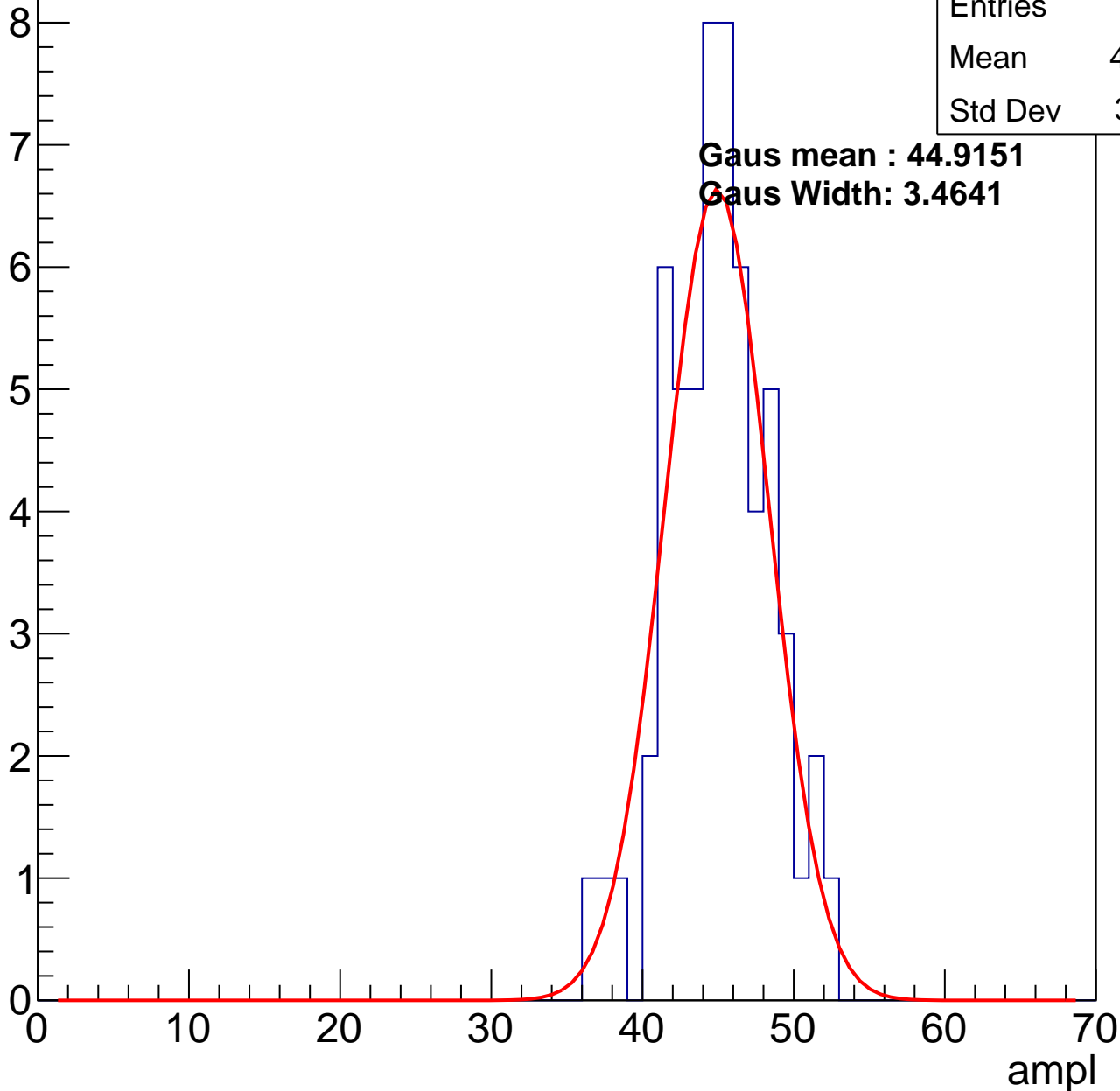
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 44.56 |
| Std Dev | 3.351 |

Gaus mean : 44.9151

Gaus Width: 3.4641

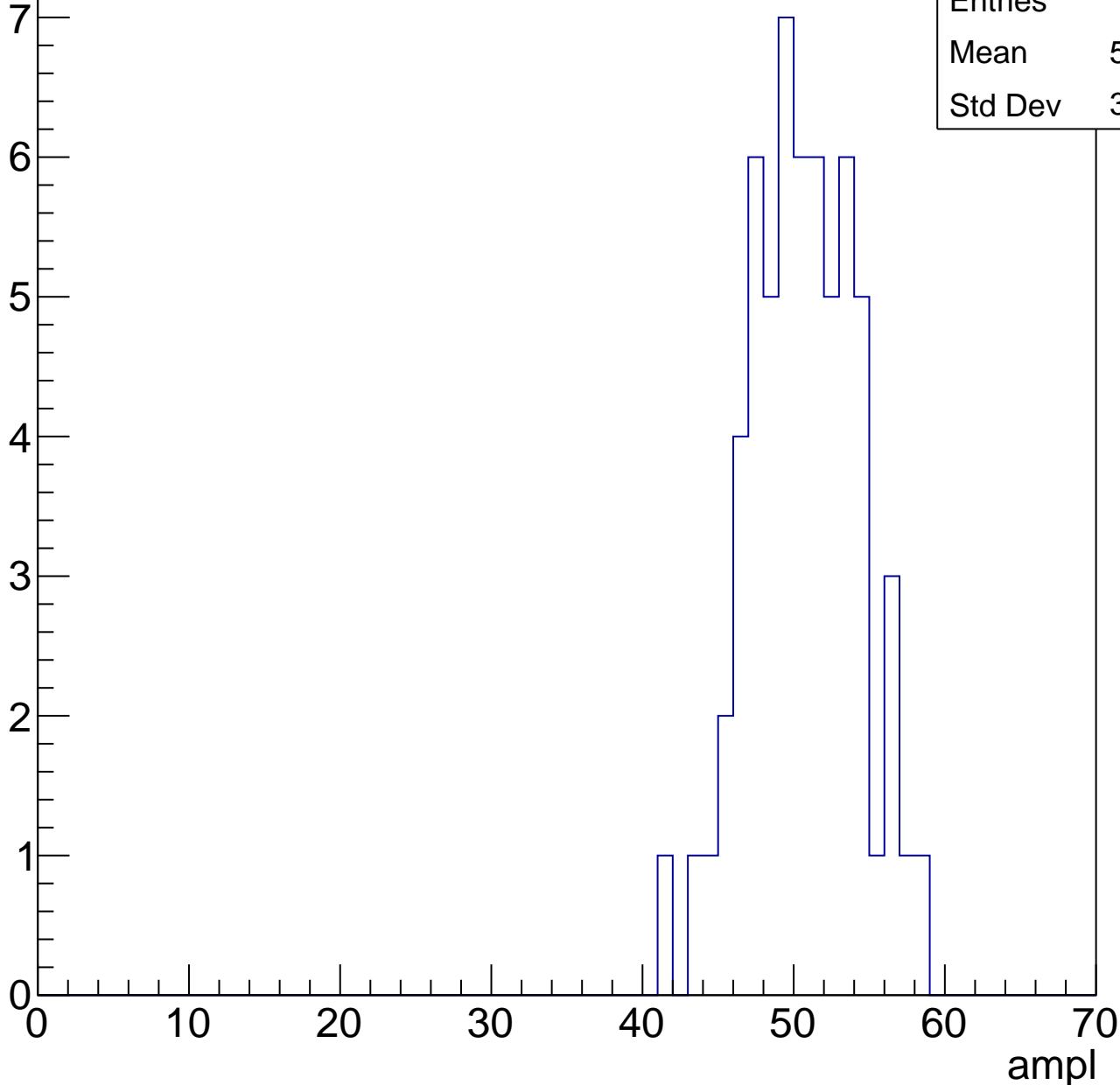


B0L001S, U7-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 50.15 |
| Std Dev | 3.543 |

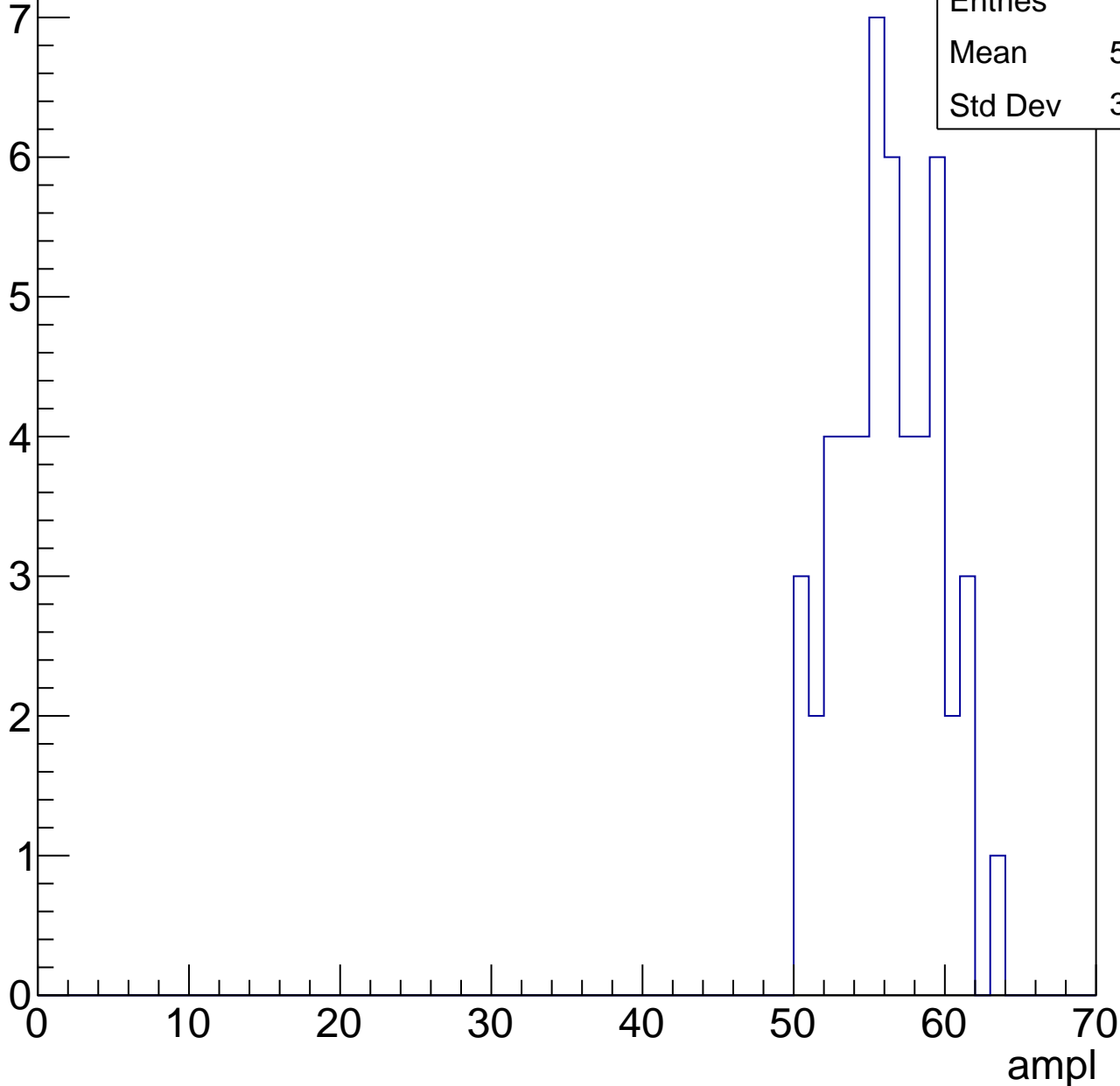


B0L001S, U7-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 55.78 |
| Std Dev | 3.189 |

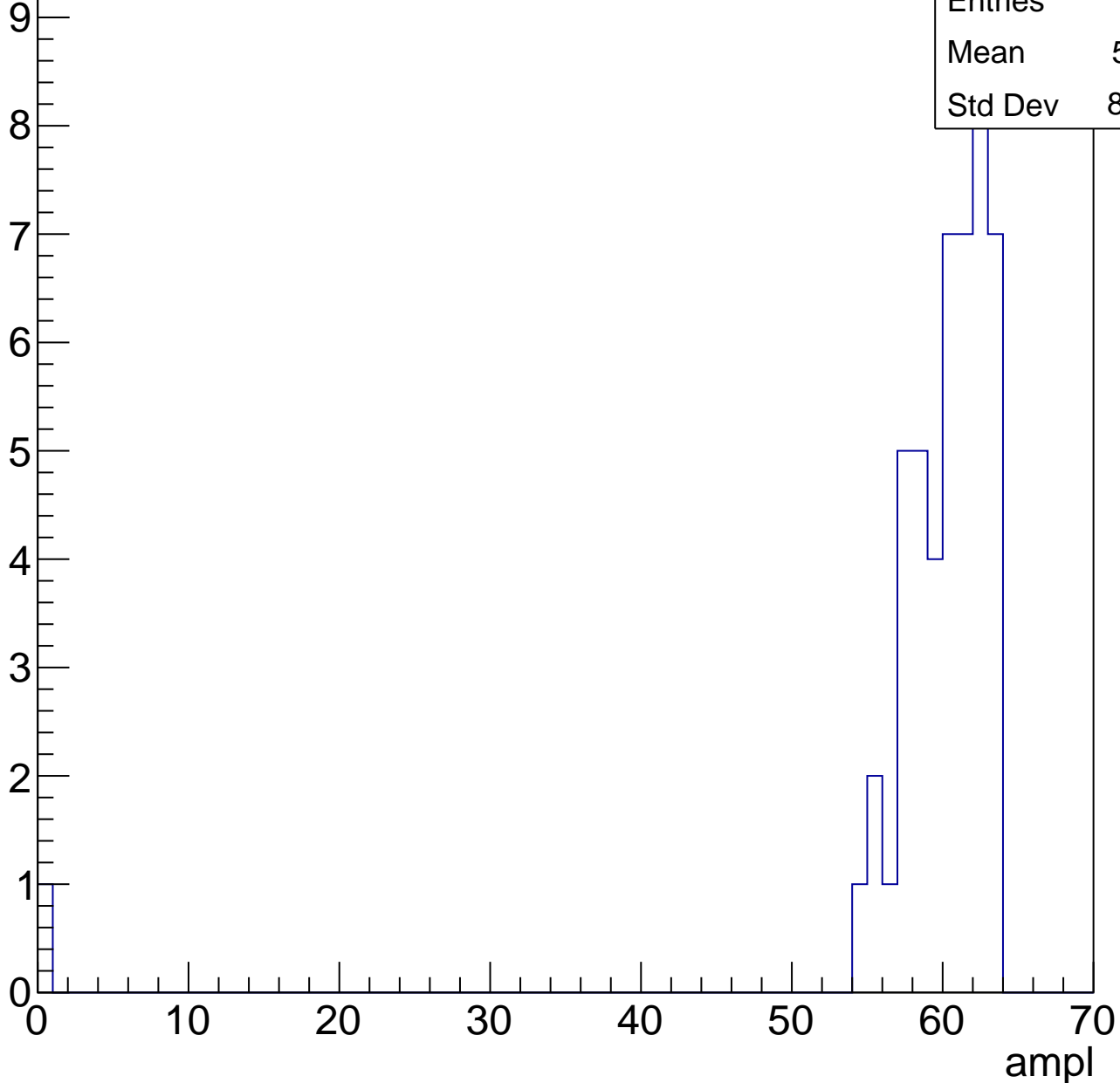


B0L001S, U7-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

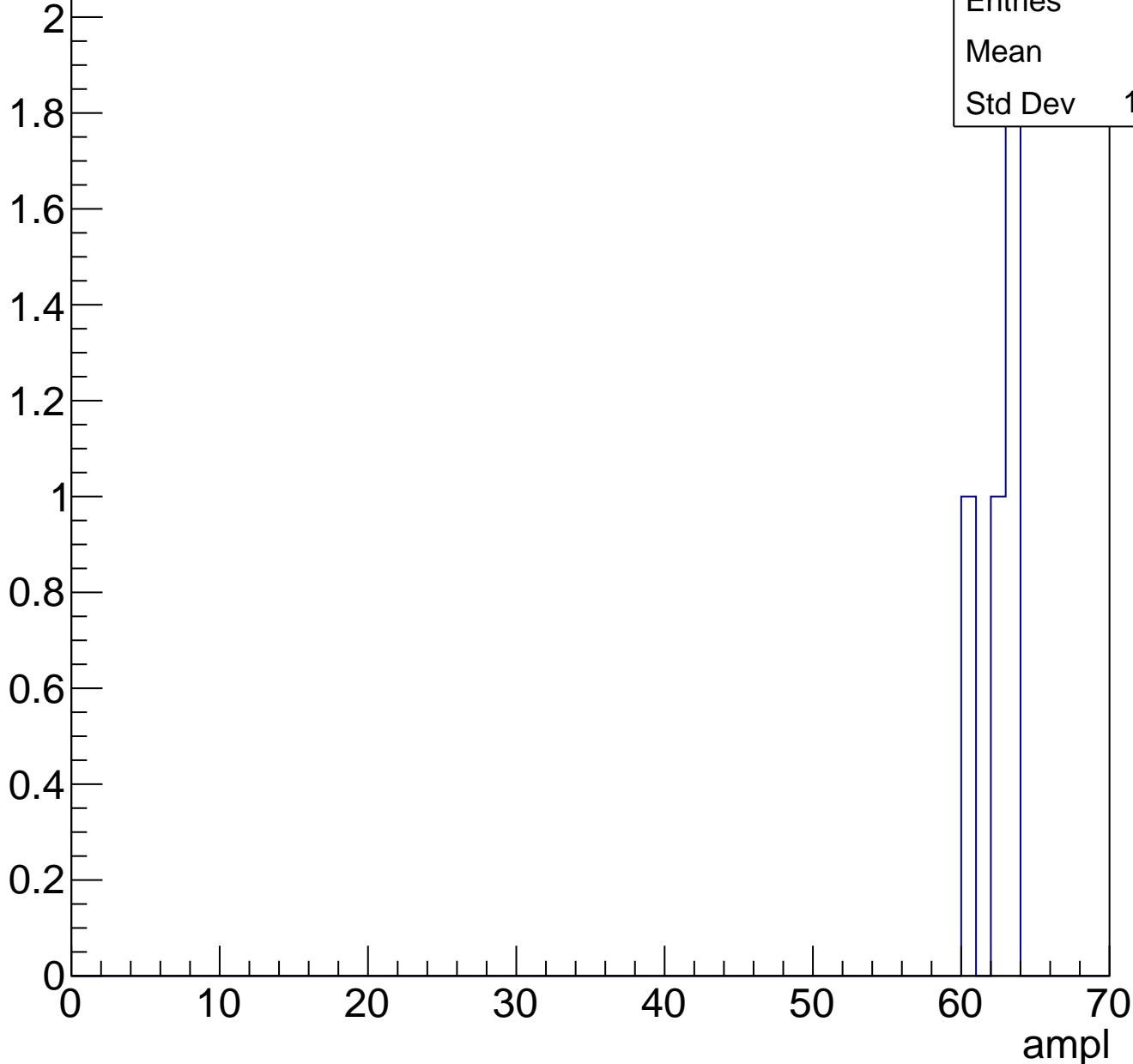
| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.71 |
| Std Dev | 8.802 |



B0L001S, U7-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch71, adc0

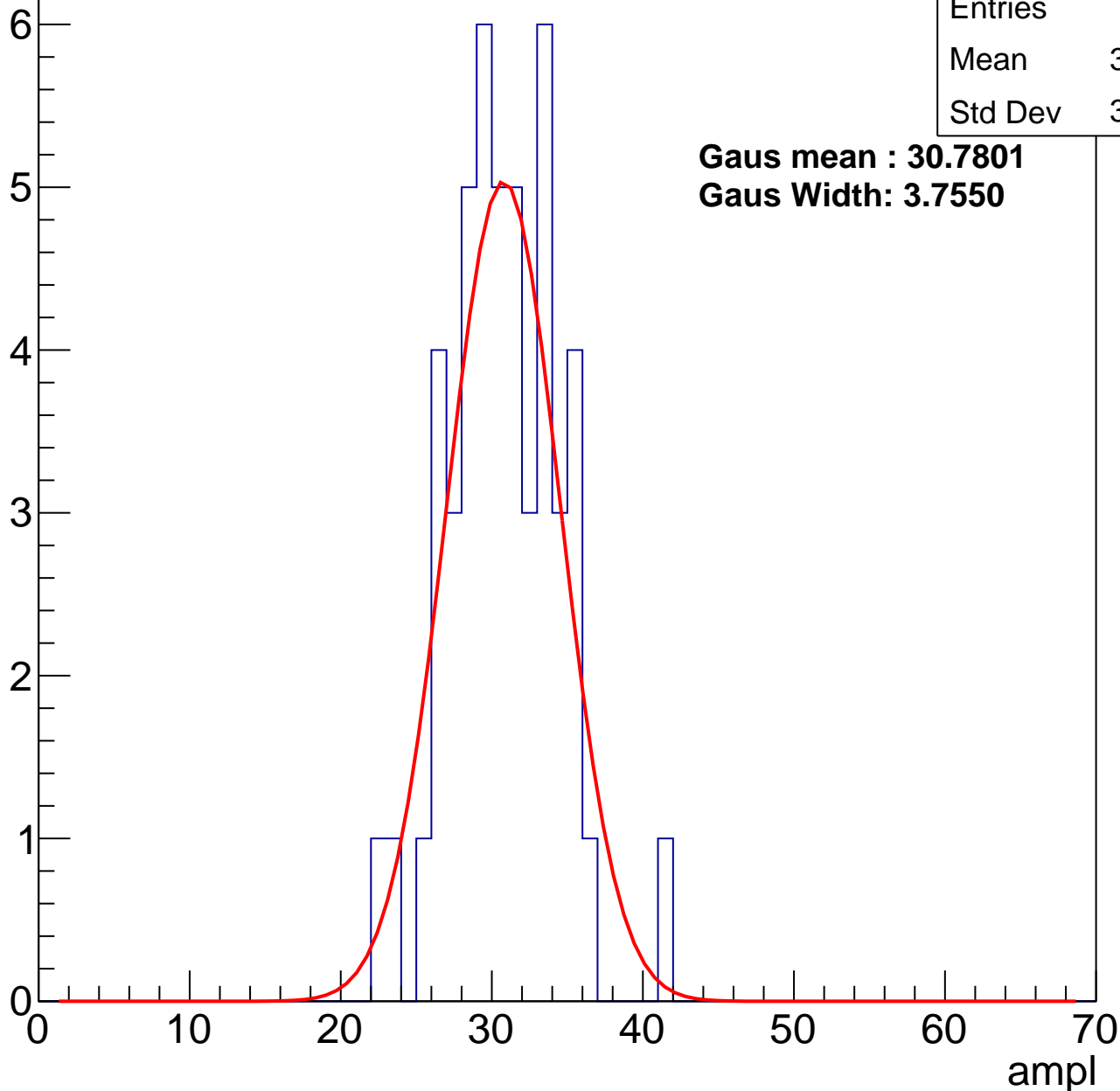
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 30.35 |
| Std Dev | 3.572 |

Gaus mean : 30.7801

Gaus Width: 3.7550



B0L001S, U7-ch71, adc1

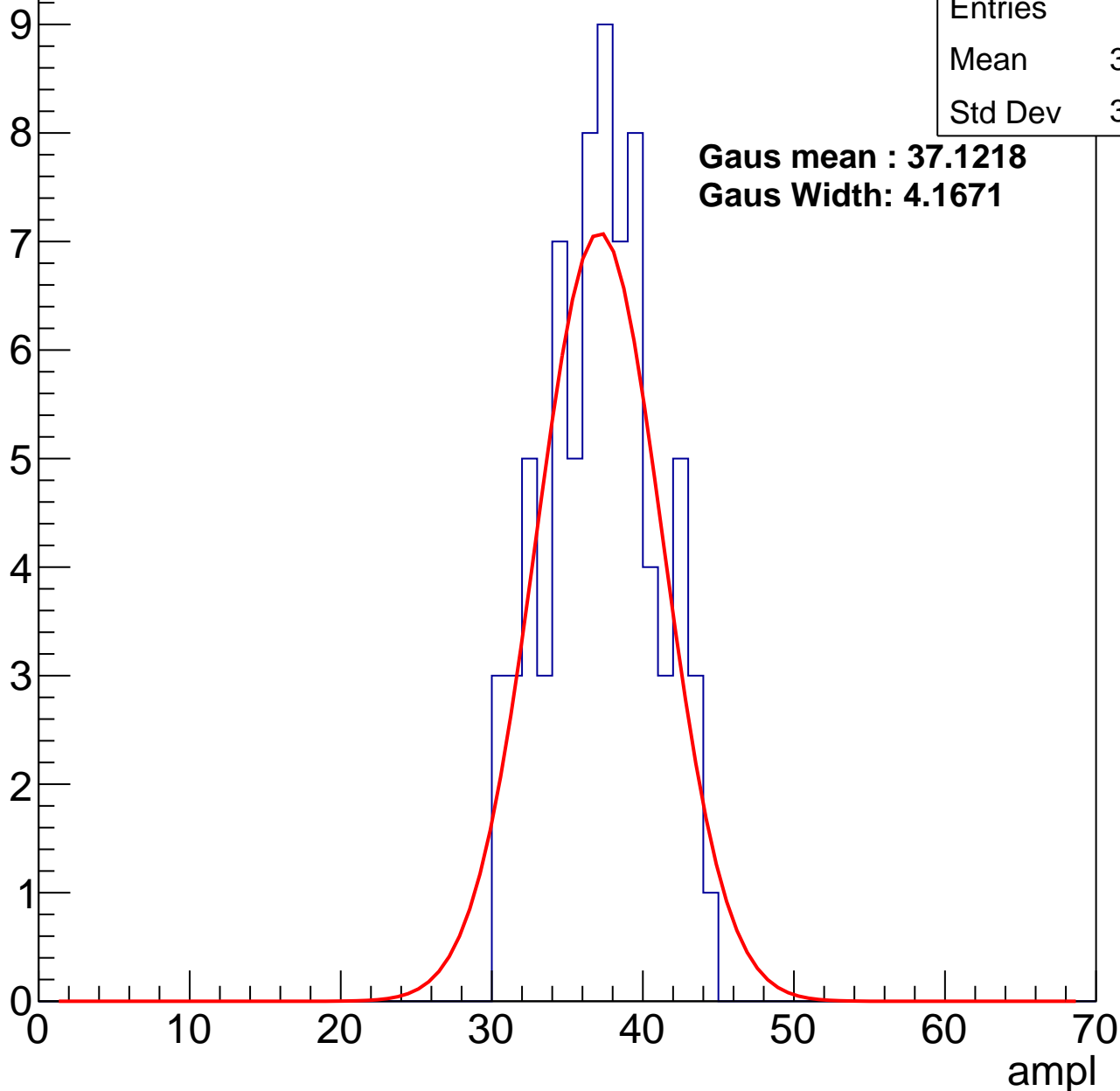
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.76 |
| Std Dev | 3.525 |

Gaus mean : 37.1218

Gaus Width: 4.1671



B0L001S, U7-ch71, adc2

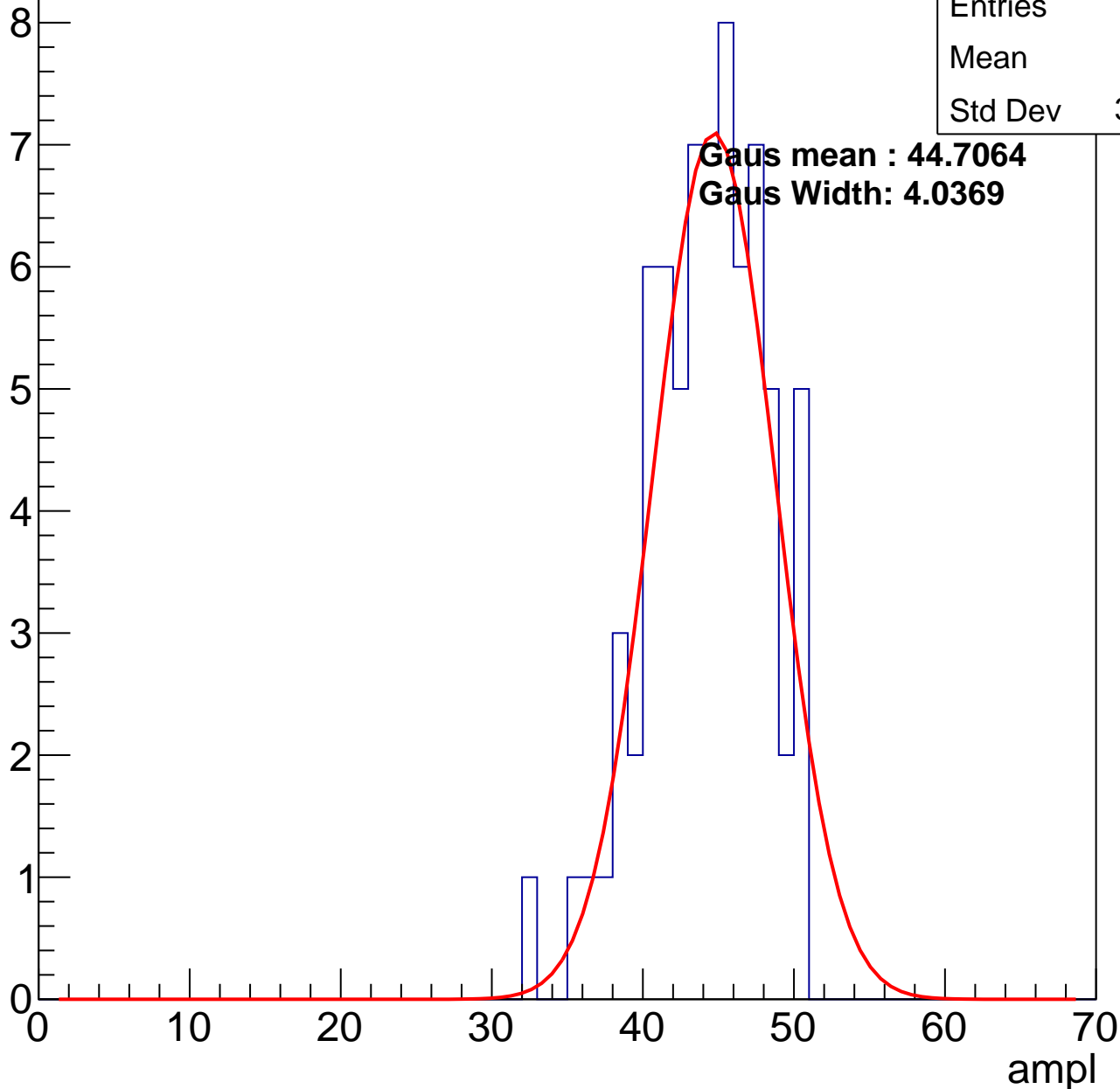
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.7 |
| Std Dev | 3.831 |

Gaus mean : 44.7064

Gaus Width: 4.0369



B0L001S, U7-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 67 |
| Mean | 50.97 |
| Std Dev | 3.403 |

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

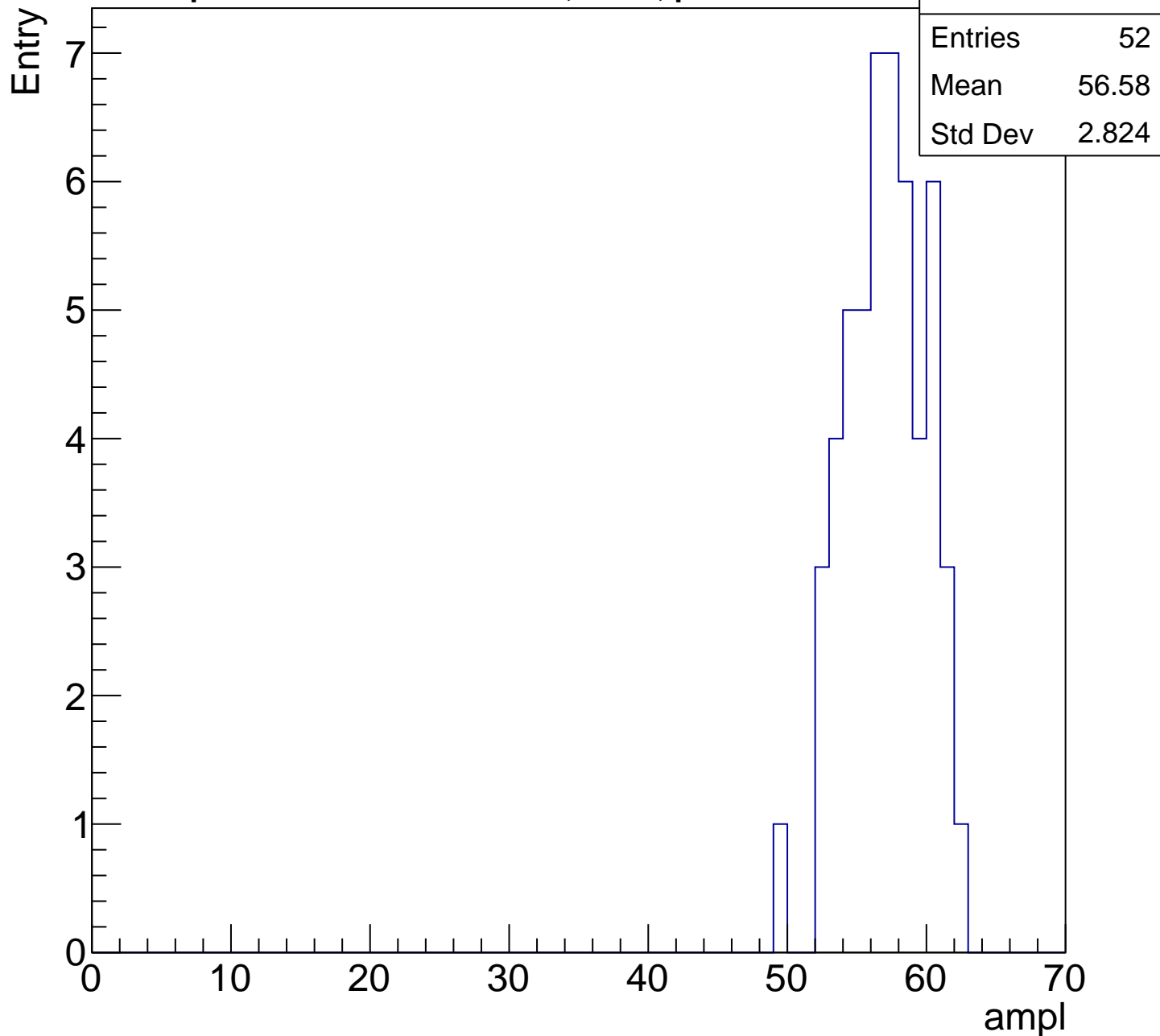
6

8

10

B0L001S, U7-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

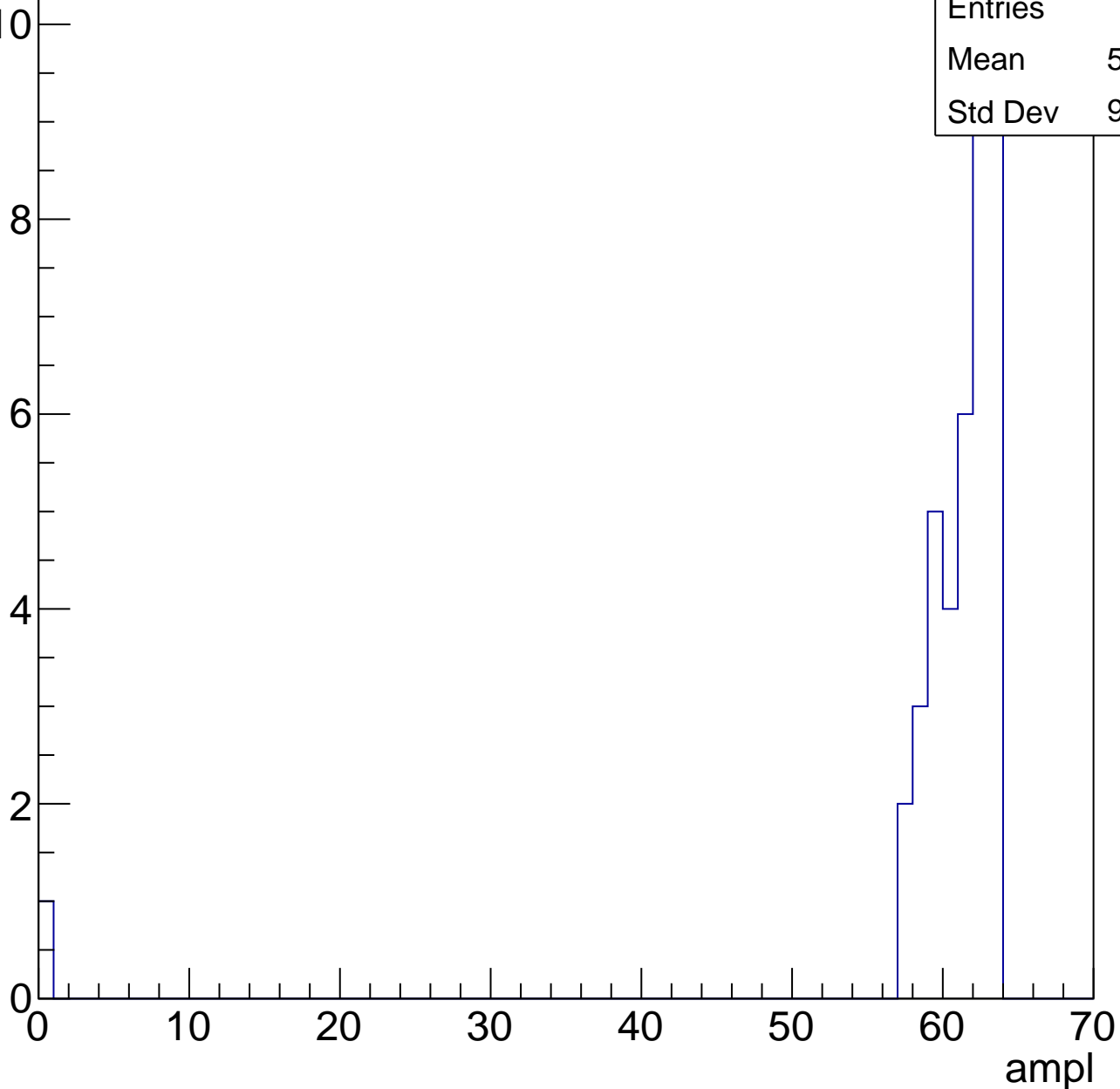


B0L001S, U7-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 40 |
| Mean | 59.42 |
| Std Dev | 9.687 |



B0L001S, U7-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

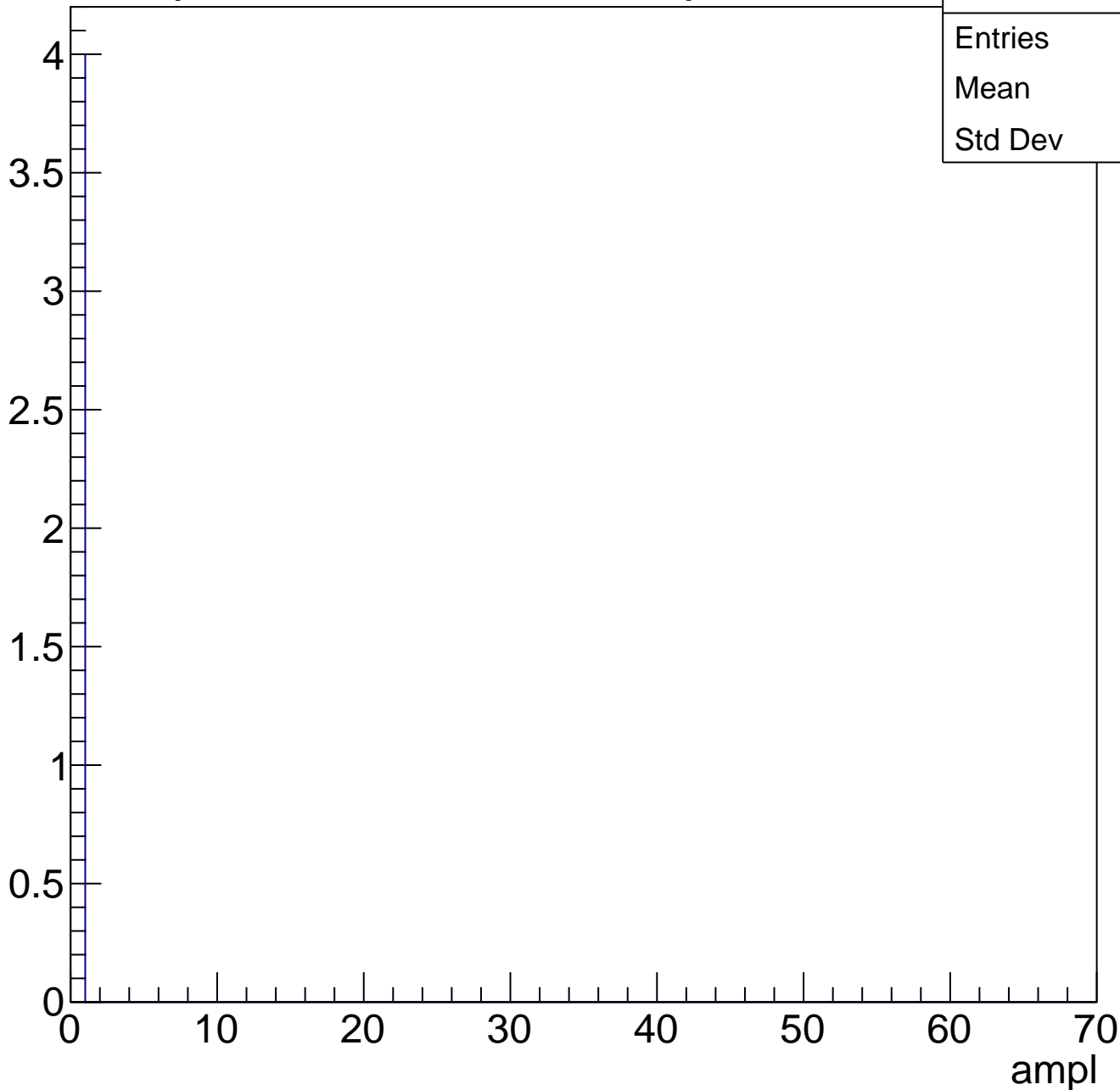
Entry



B0L001S, U7-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch72, adc0

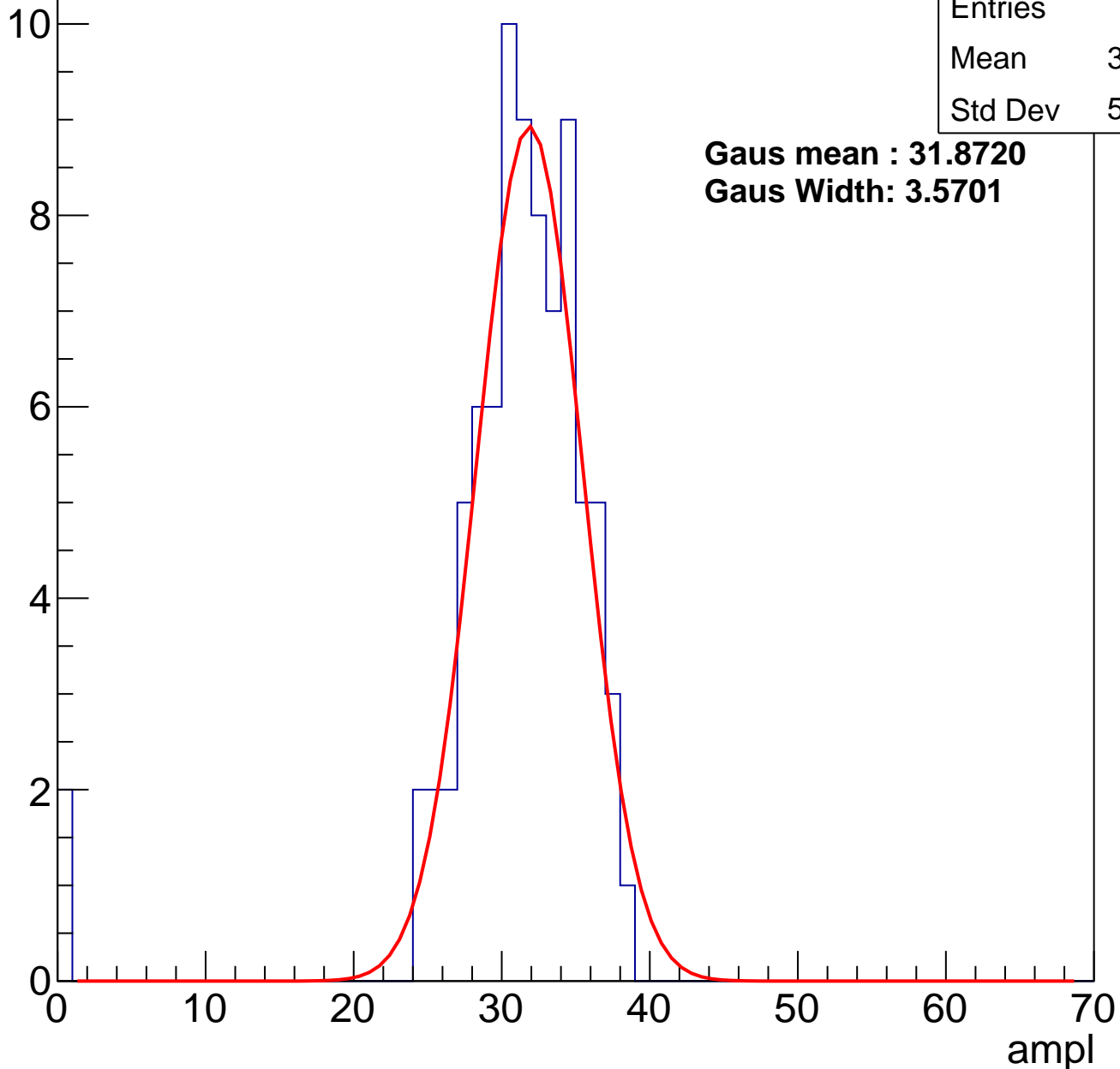
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 82 |
| Mean | 30.52 |
| Std Dev | 5.813 |

Gaus mean : 31.8720

Gaus Width: 3.5701

Entry



B0L001S, U7-ch72, adc1

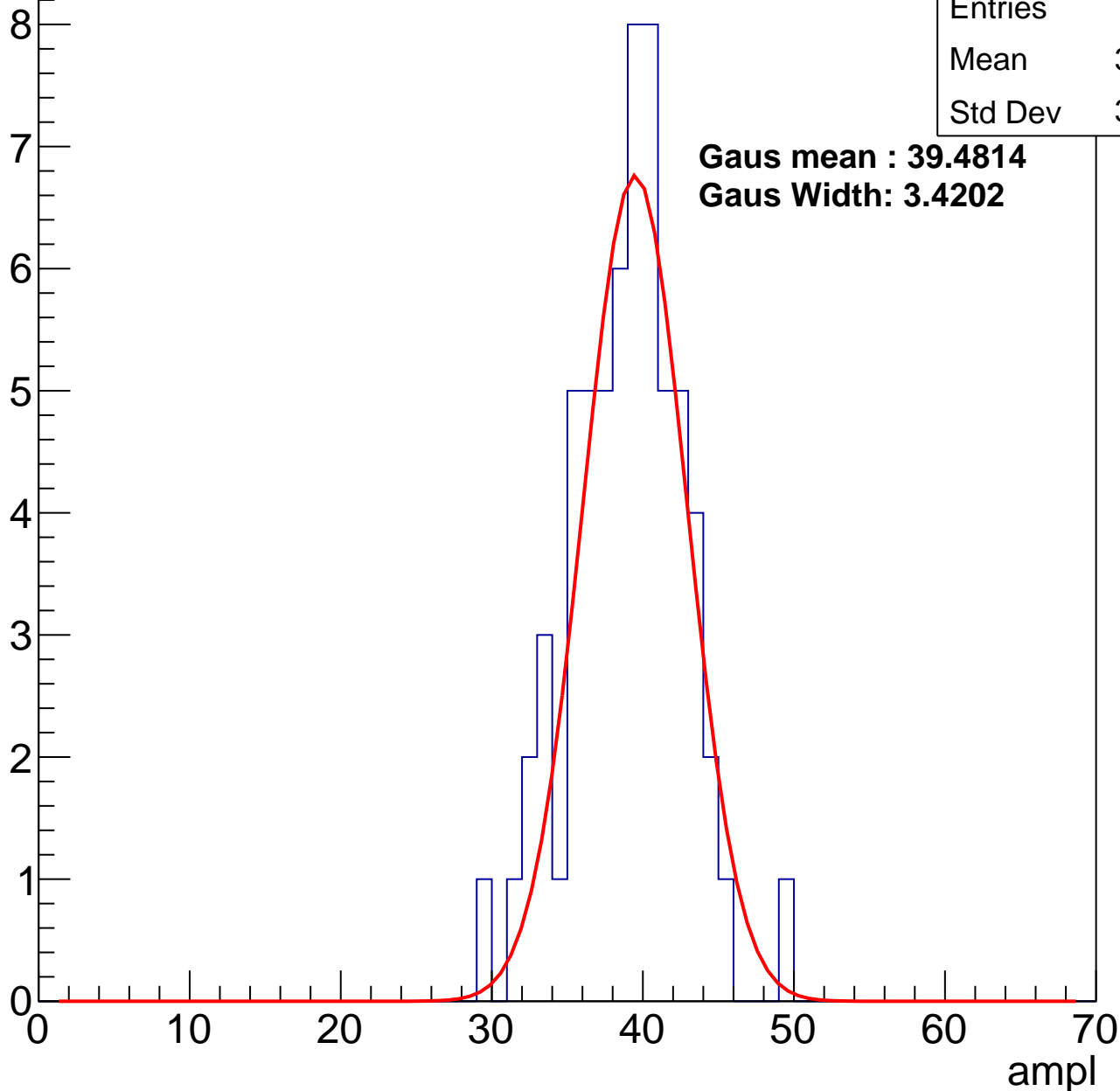
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 38.51 |
| Std Dev | 3.681 |

Gaus mean : 39.4814

Gaus Width: 3.4202



B0L001S, U7-ch72, adc2

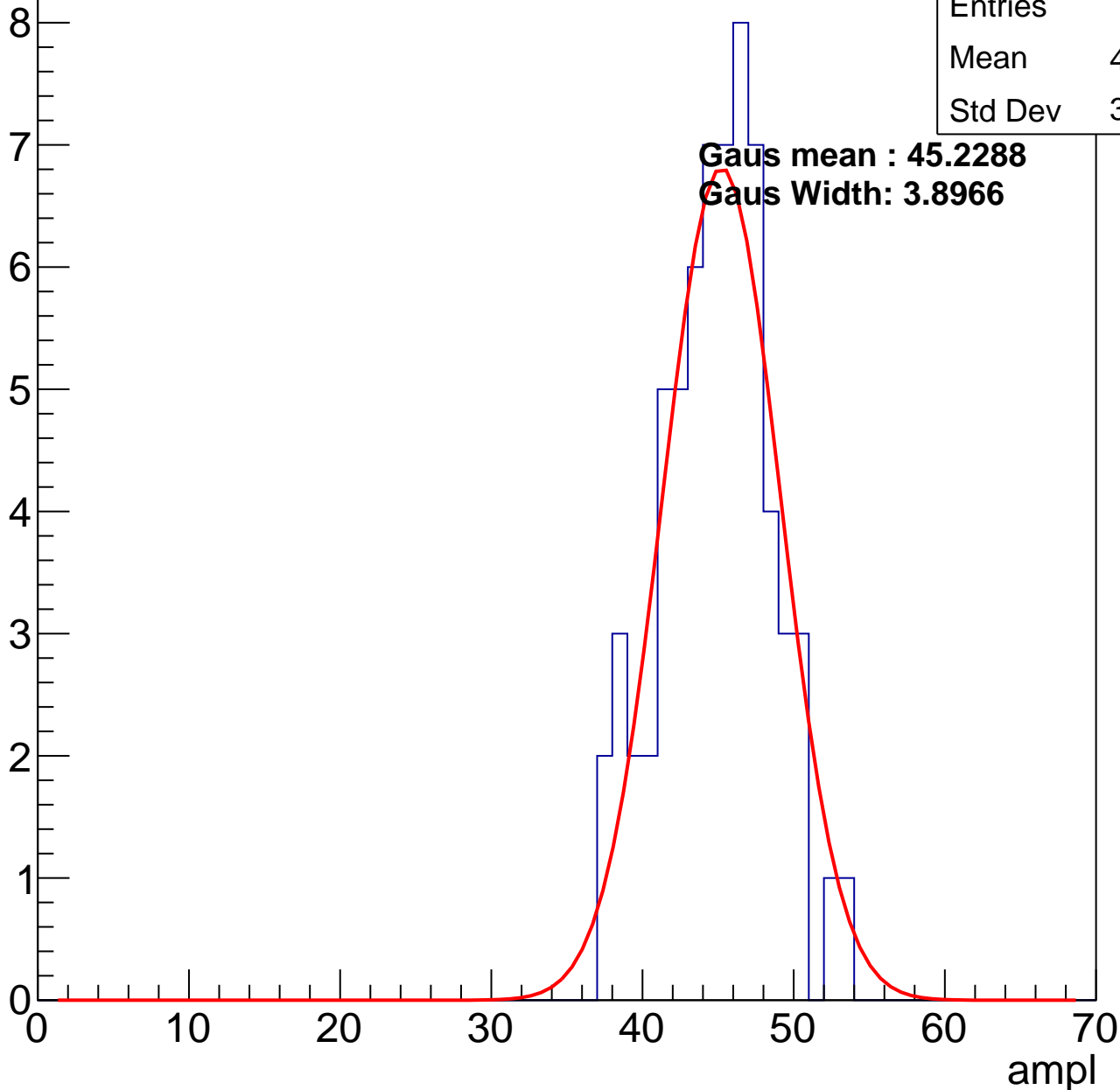
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 44.44 |
| Std Dev | 3.568 |

Gaus mean : 45.2288

Gaus Width: 3.8966

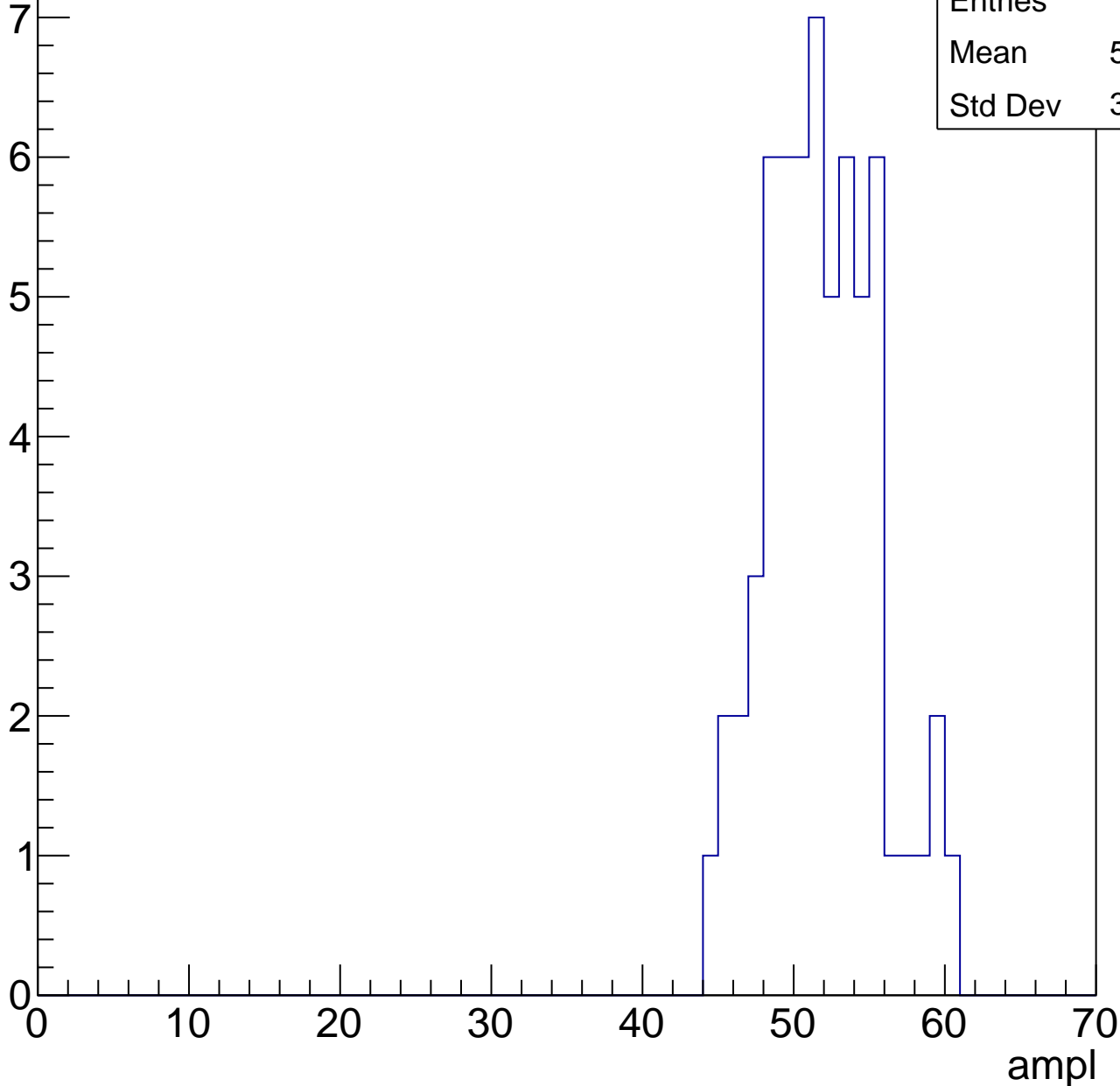


B0L001S, U7-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 51.36 |
| Std Dev | 3.585 |

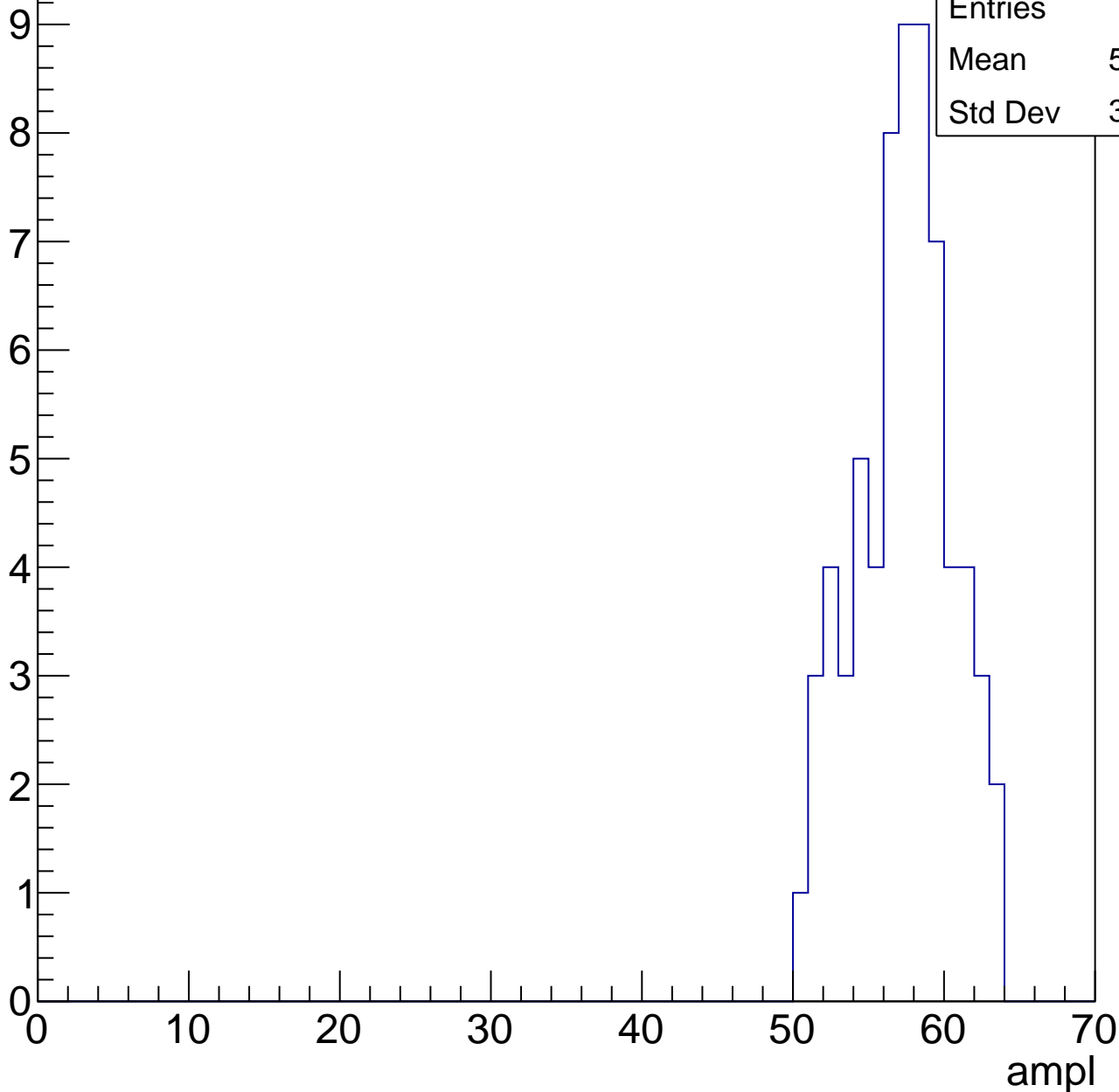


B0L001S, U7-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 56.85 |
| Std Dev | 3.154 |

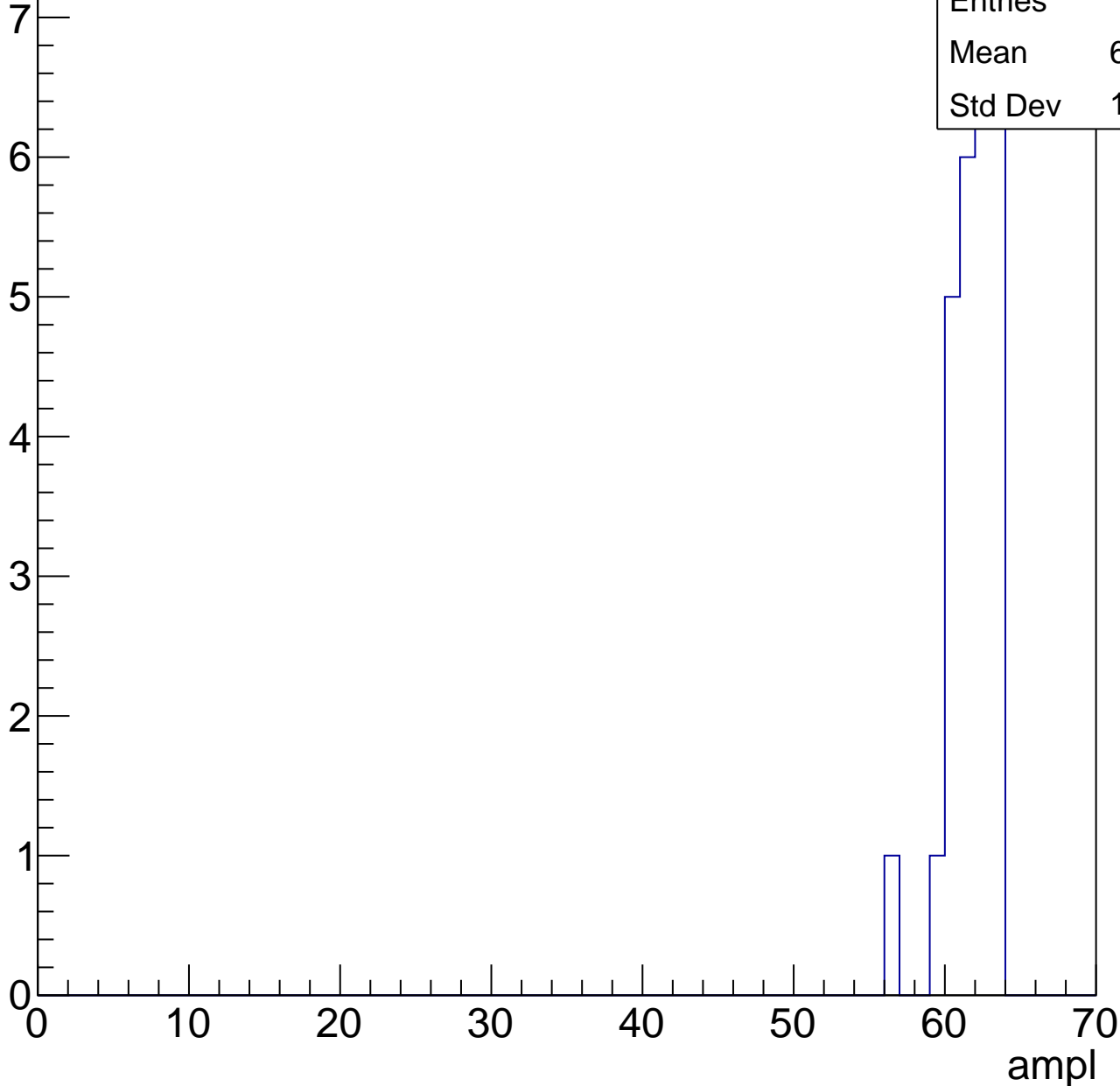


B0L001S, U7-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 27 |
| Mean | 61.33 |
| Std Dev | 1.563 |



B0L001S, U7-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch73, adc0

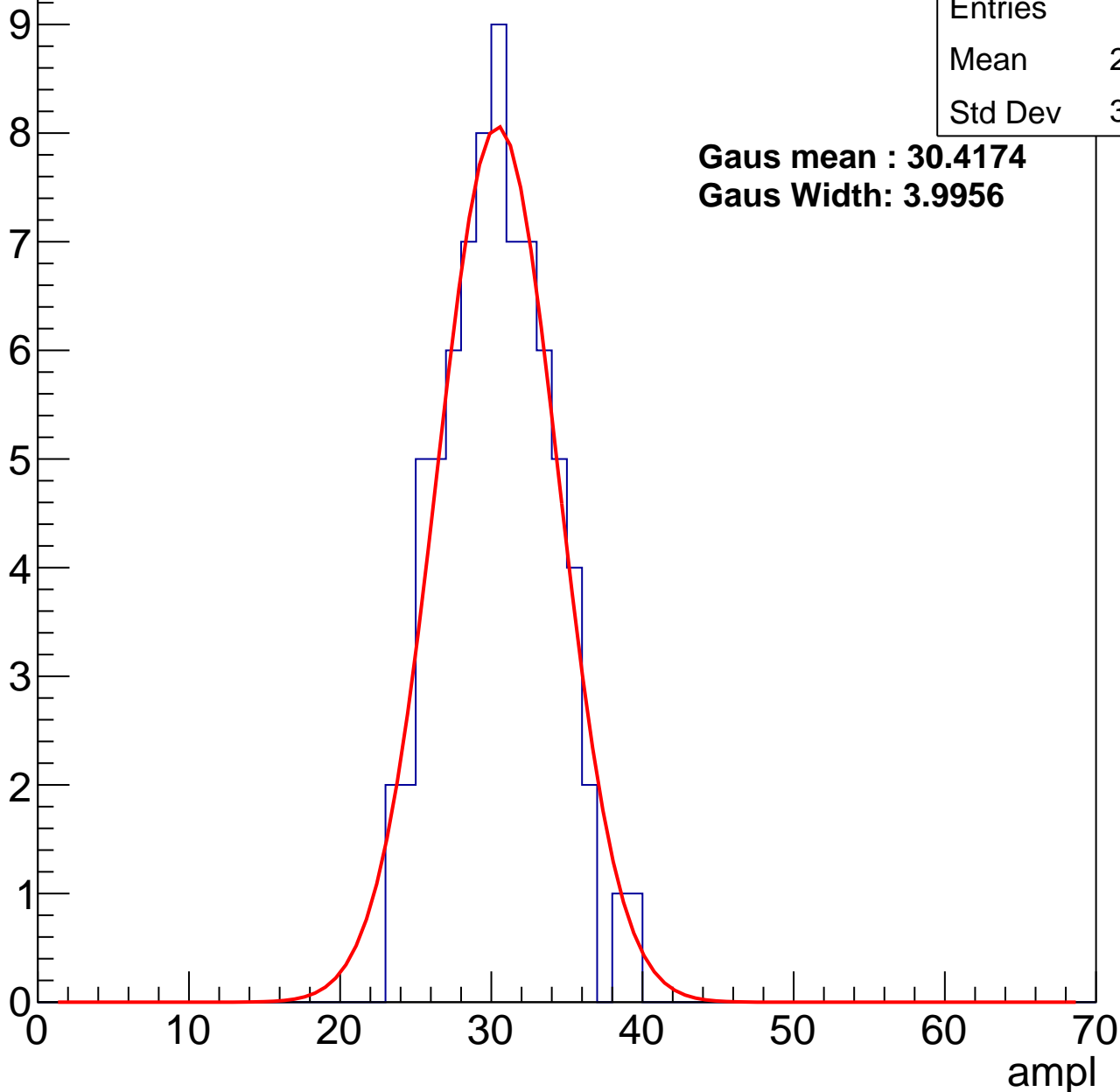
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 29.96 |
| Std Dev | 3.514 |

Gaus mean : 30.4174

Gaus Width: 3.9956



B0L001S, U7-ch73, adc1

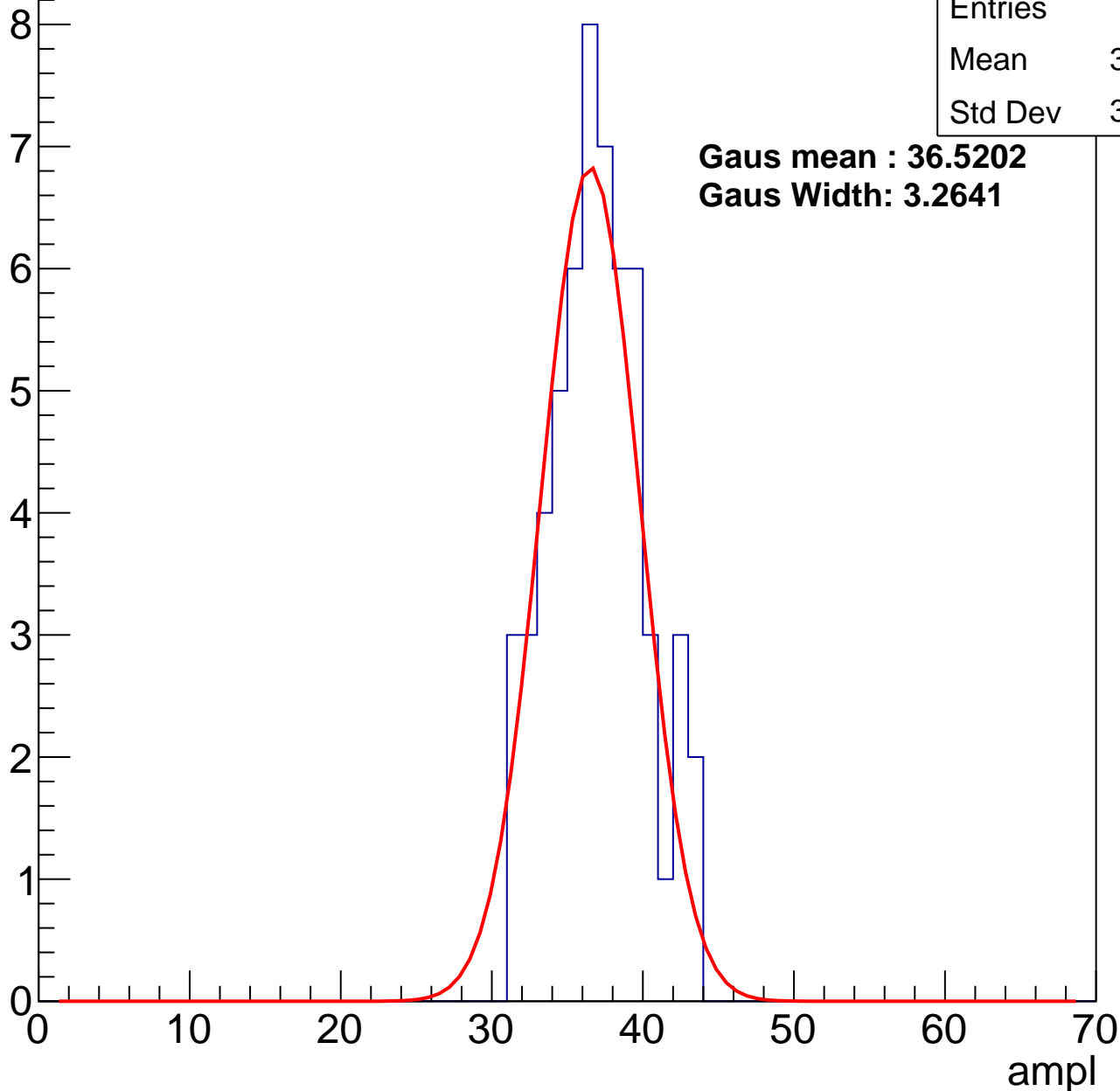
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 36.54 |
| Std Dev | 3.055 |

Gaus mean : 36.5202

Gaus Width: 3.2641



B0L001S, U7-ch73, adc2

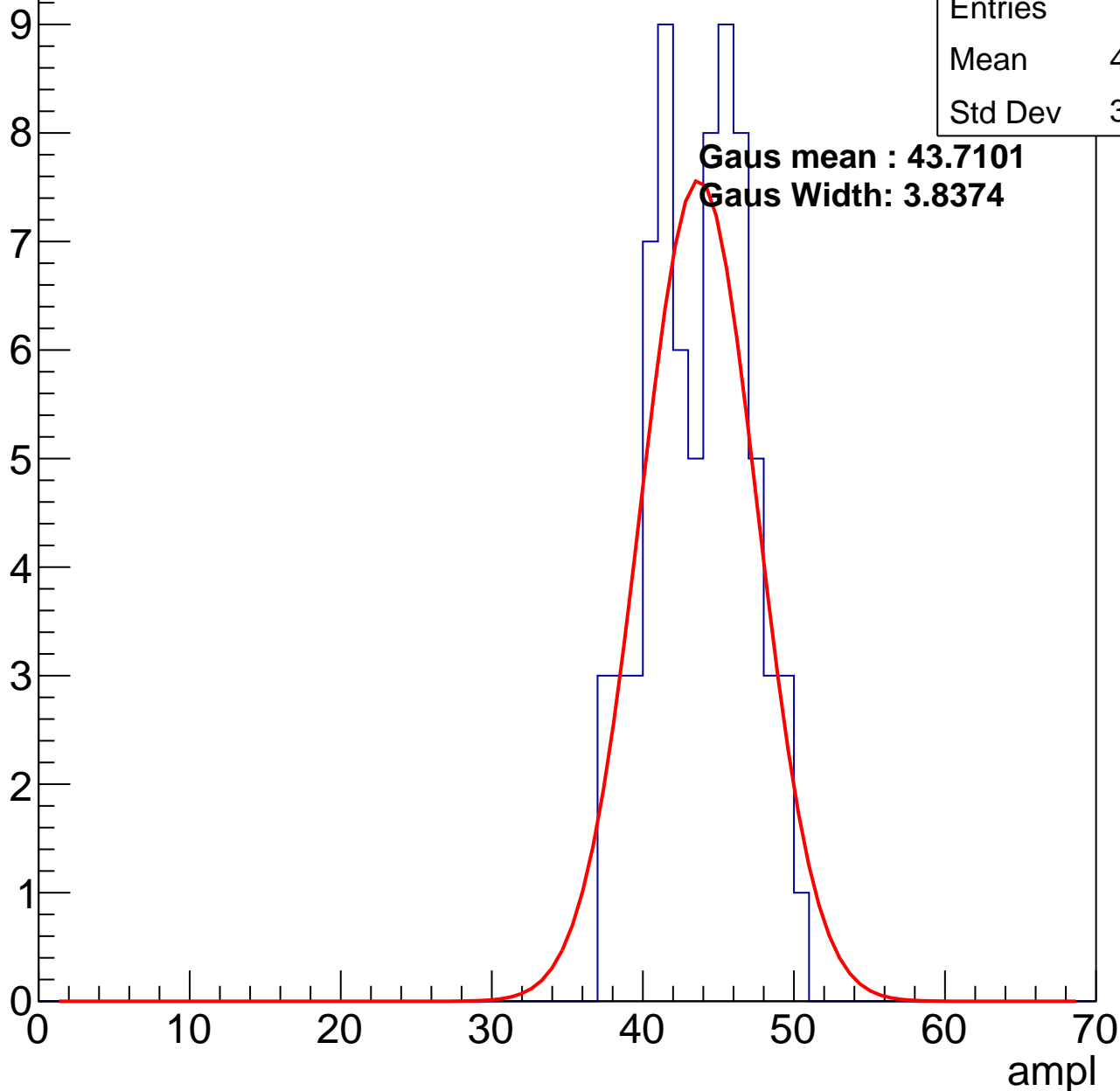
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.27 |
| Std Dev | 3.223 |

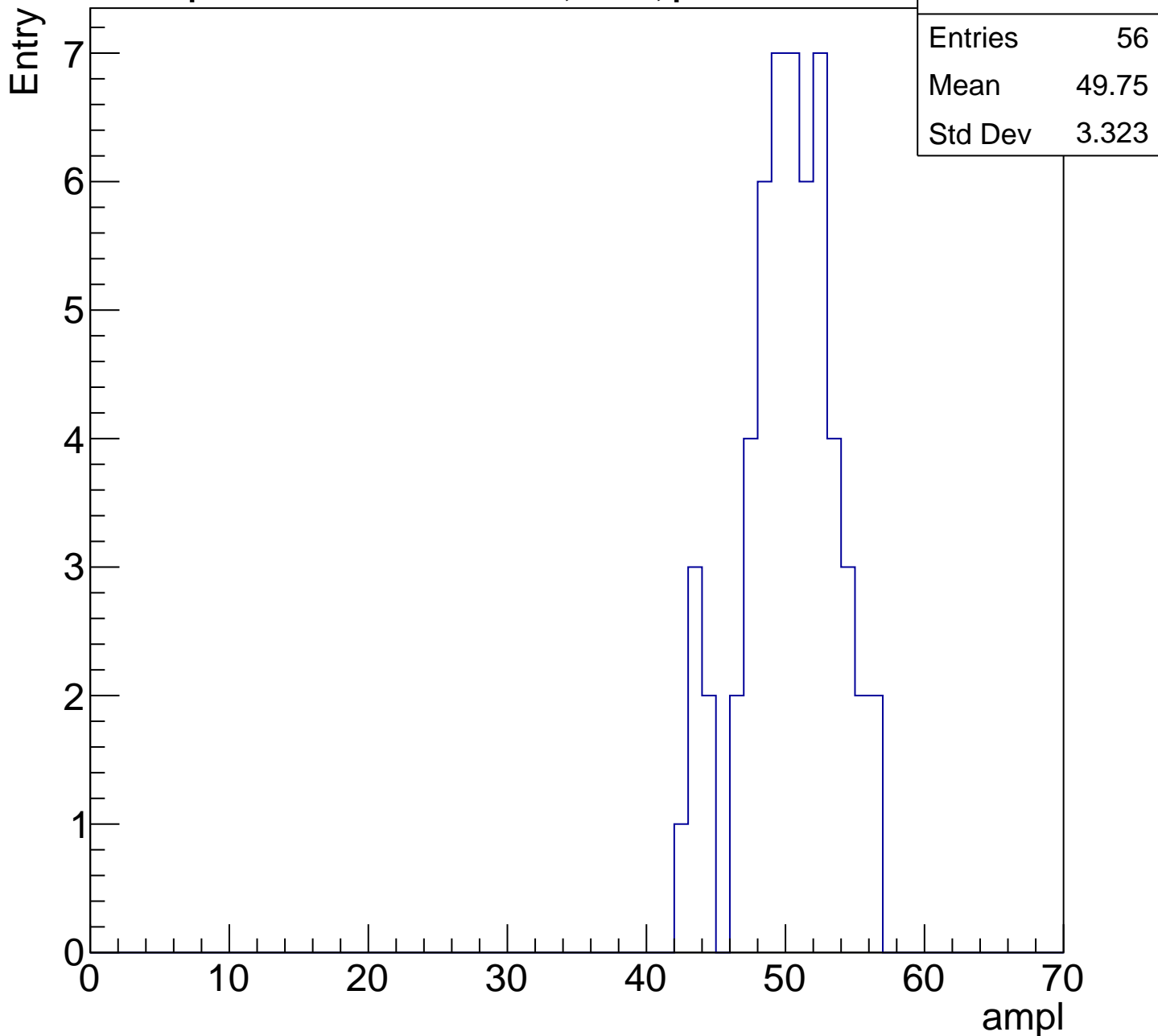
Gaus mean : 43.7101

Gaus Width: 3.8374



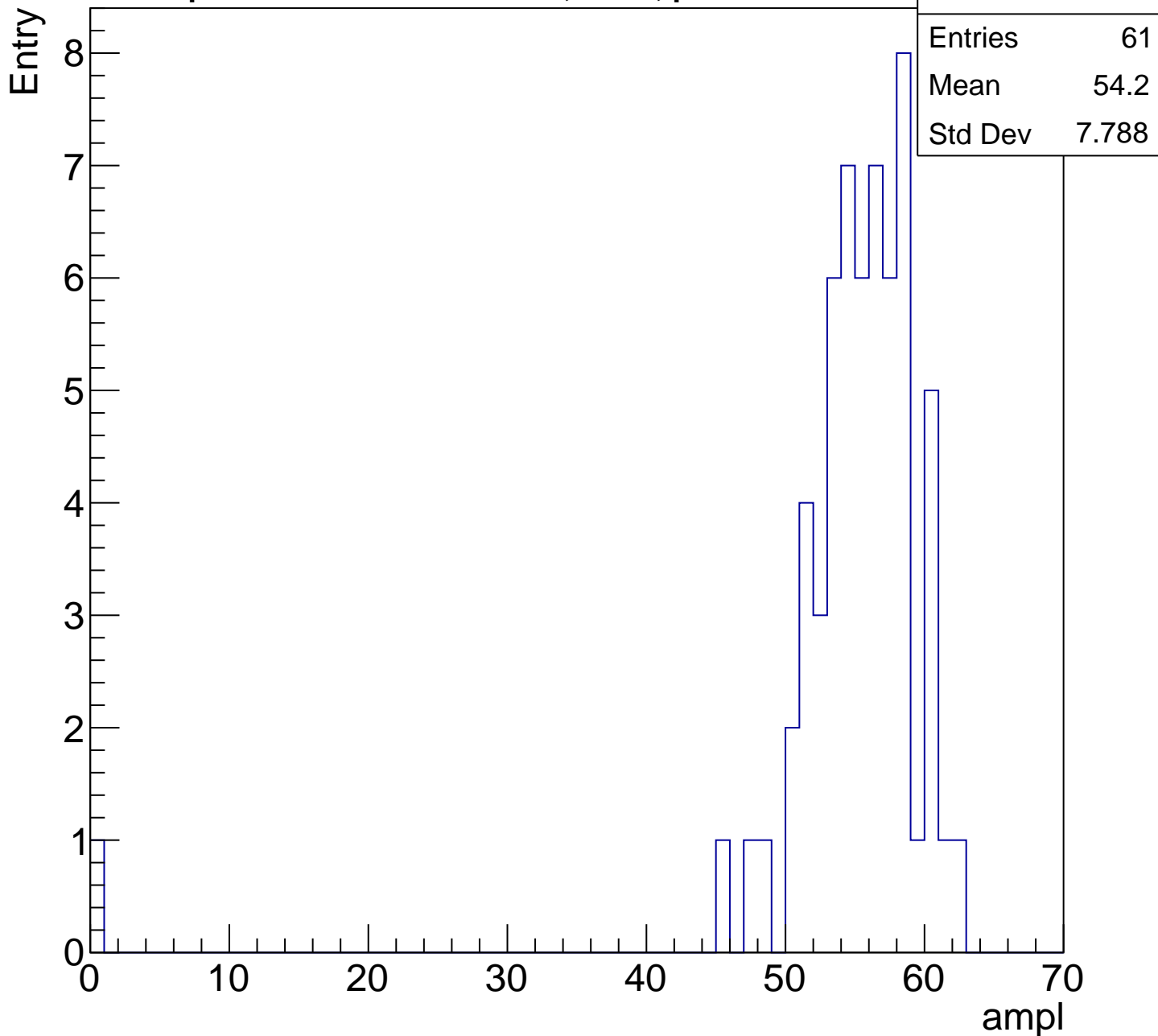
B0L001S, U7-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 47 |
| Mean | 60.23 |
| Std Dev | 2.205 |

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

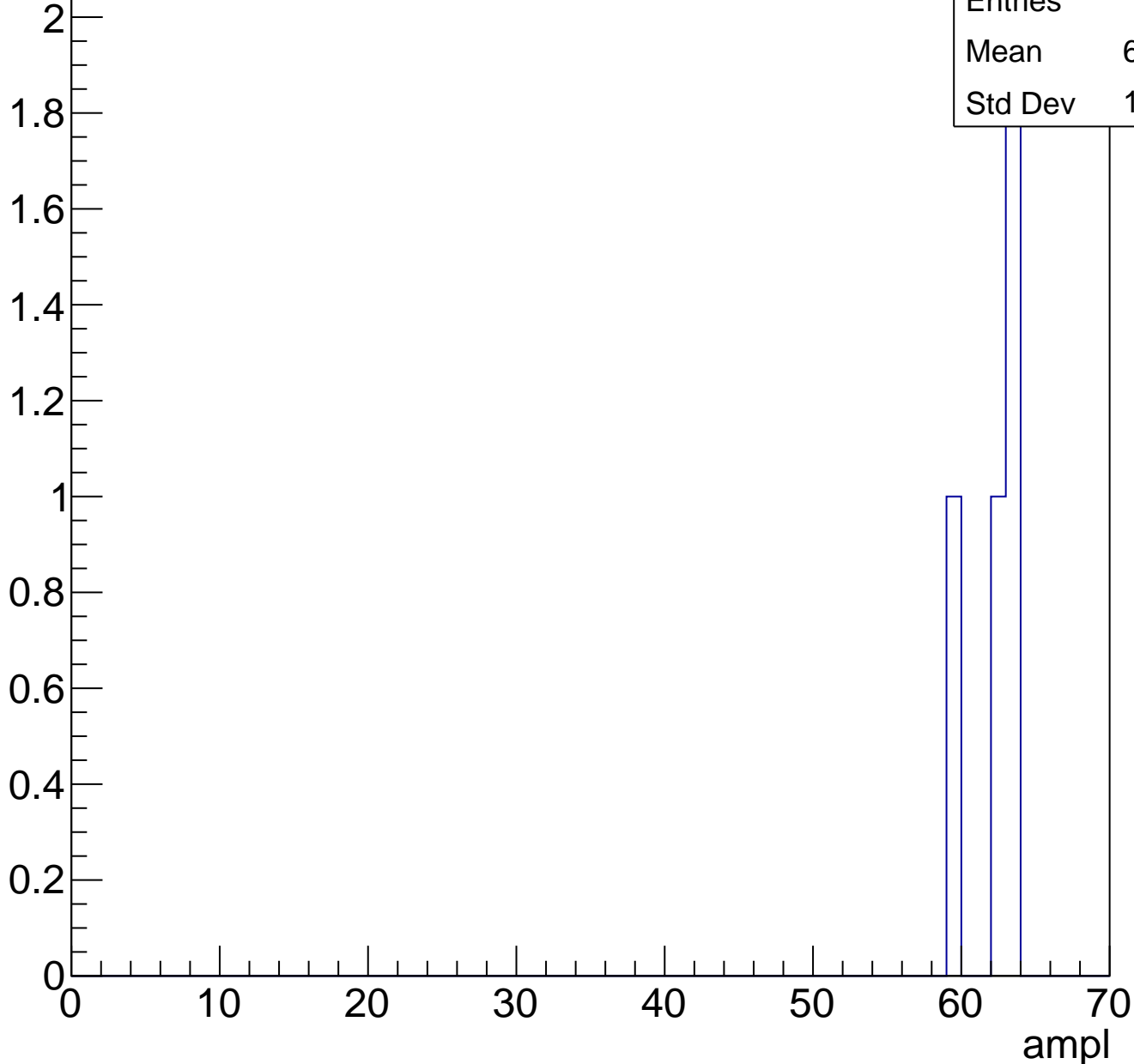
8

9

B0L001S, U7-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch74, adc0

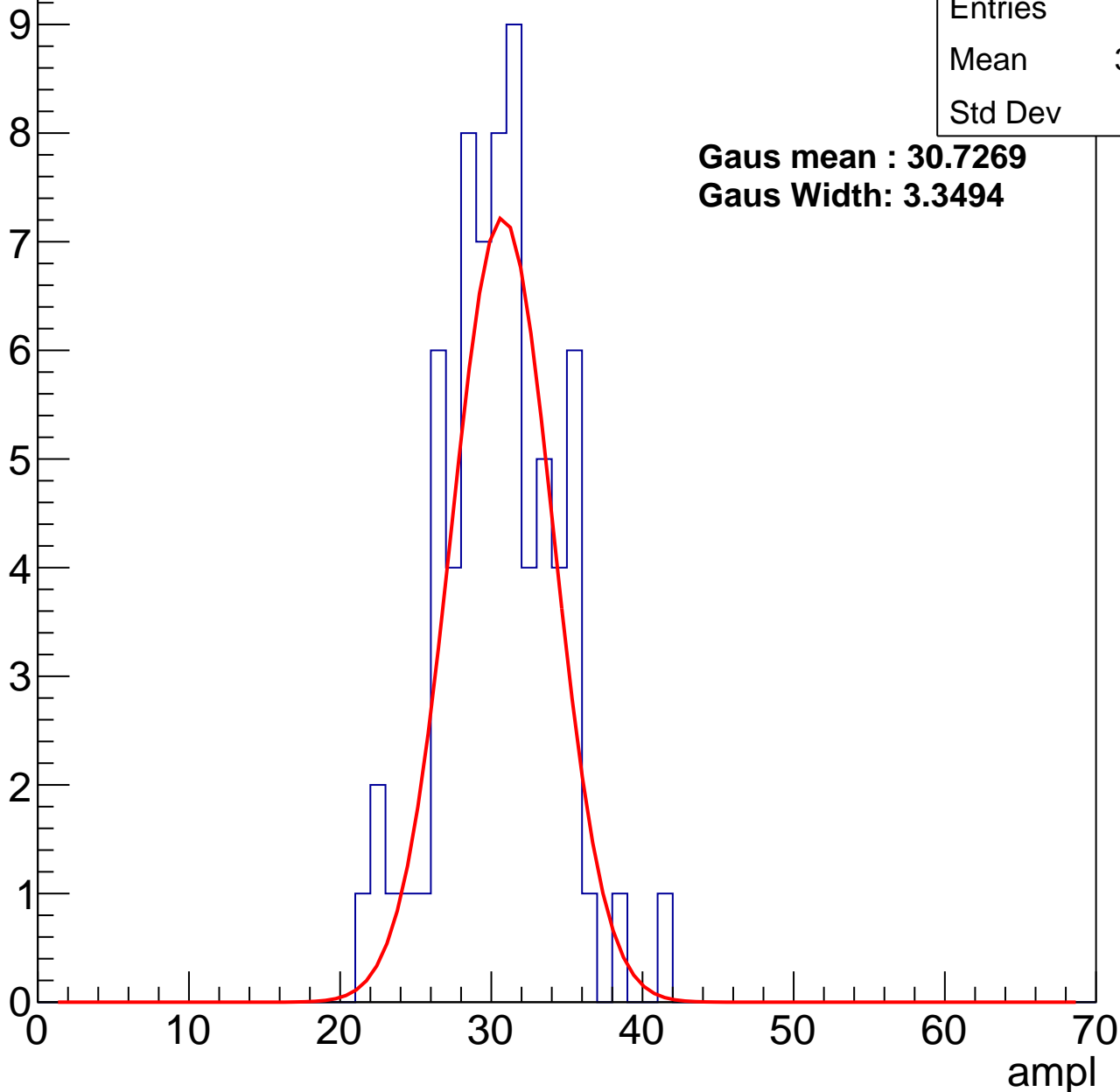
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 30.01 |
| Std Dev | 3.77 |

Gaus mean : 30.7269

Gaus Width: 3.3494



B0L001S, U7-ch74, adc1

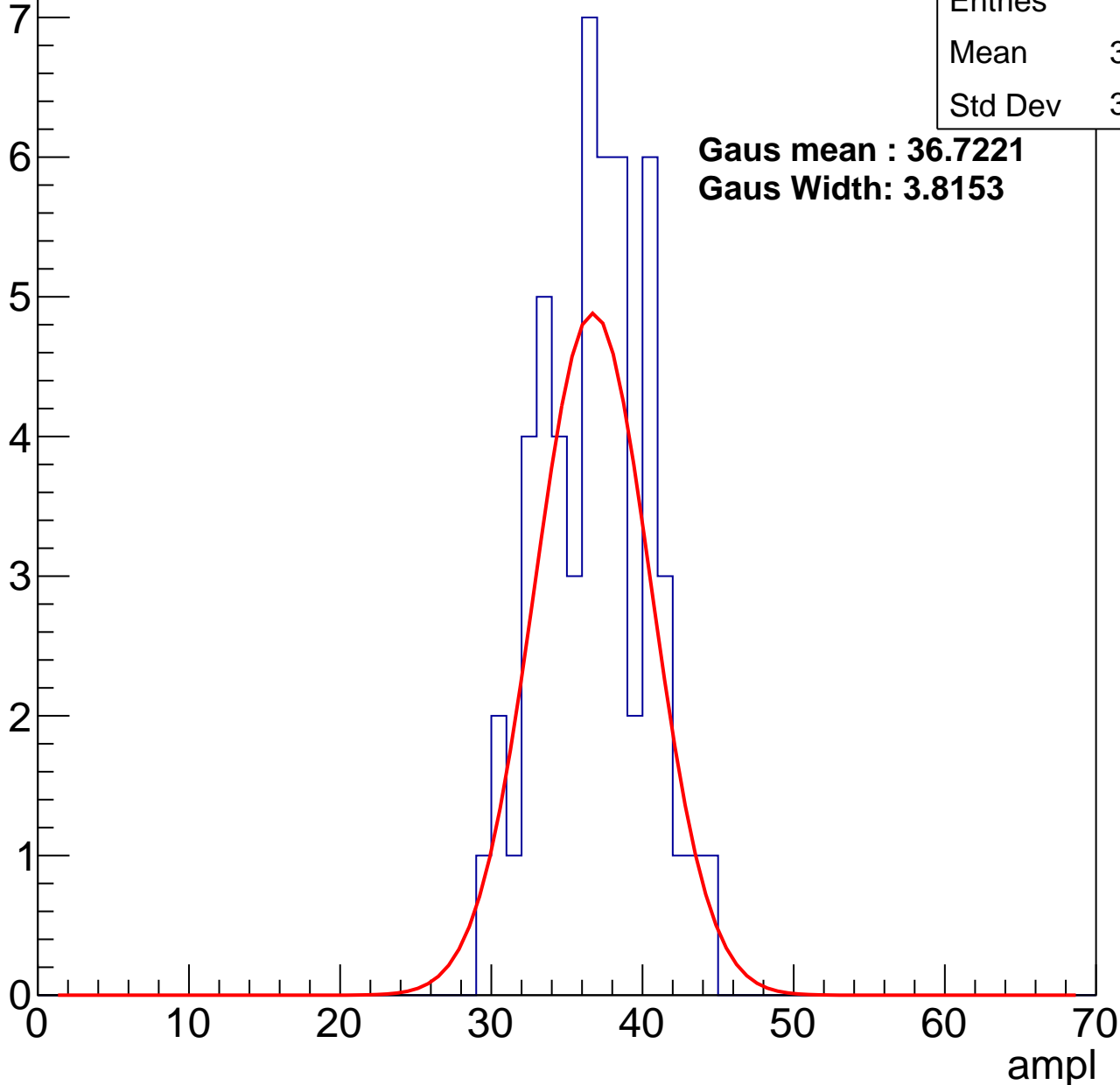
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 36.34 |
| Std Dev | 3.464 |

Gaus mean : 36.7221

Gaus Width: 3.8153



B0L001S, U7-ch74, adc2

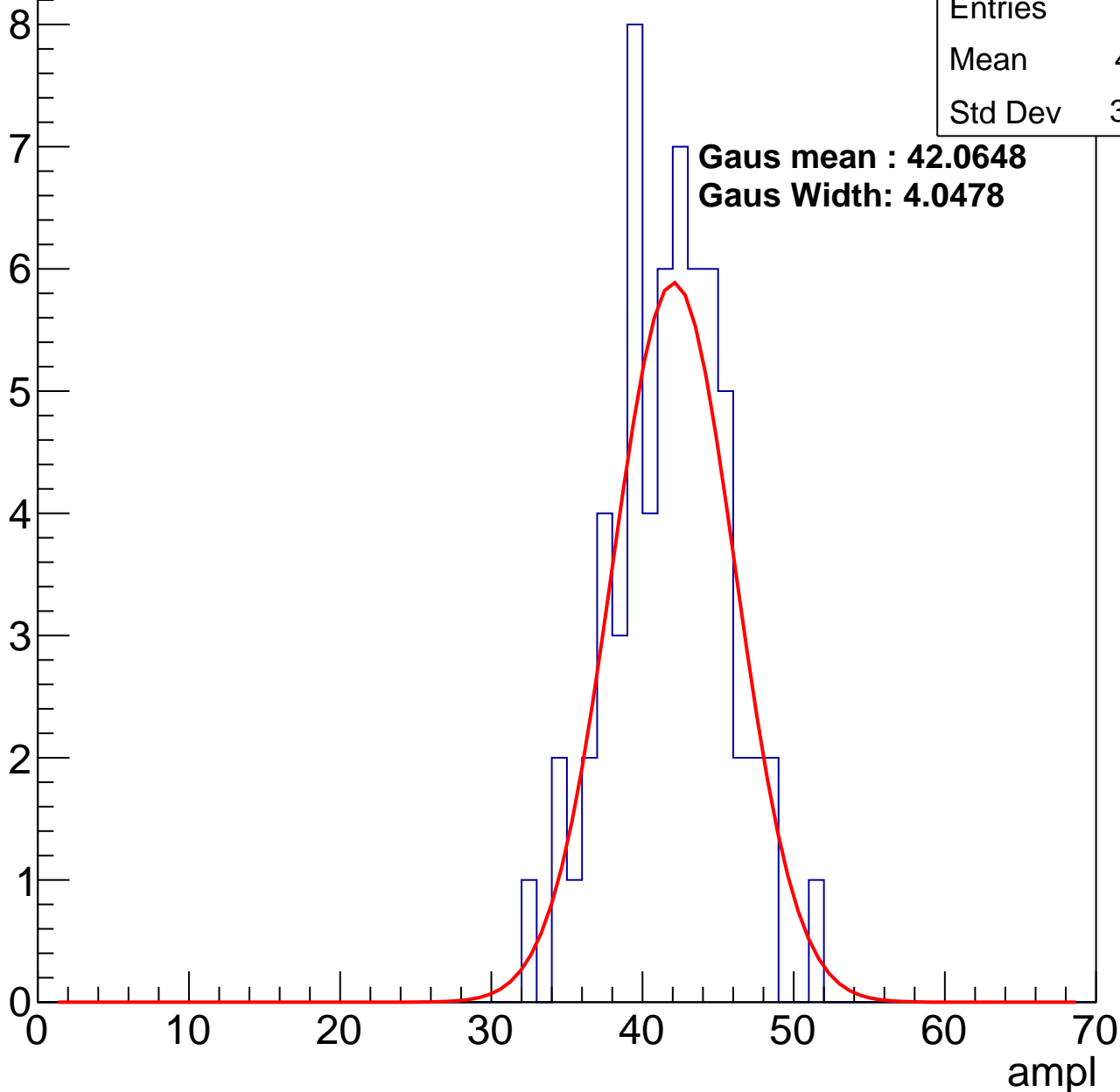
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.31 |
| Std Dev | 3.748 |

Gaus mean : 42.0648

Gaus Width: 4.0478



B0L001S, U7-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

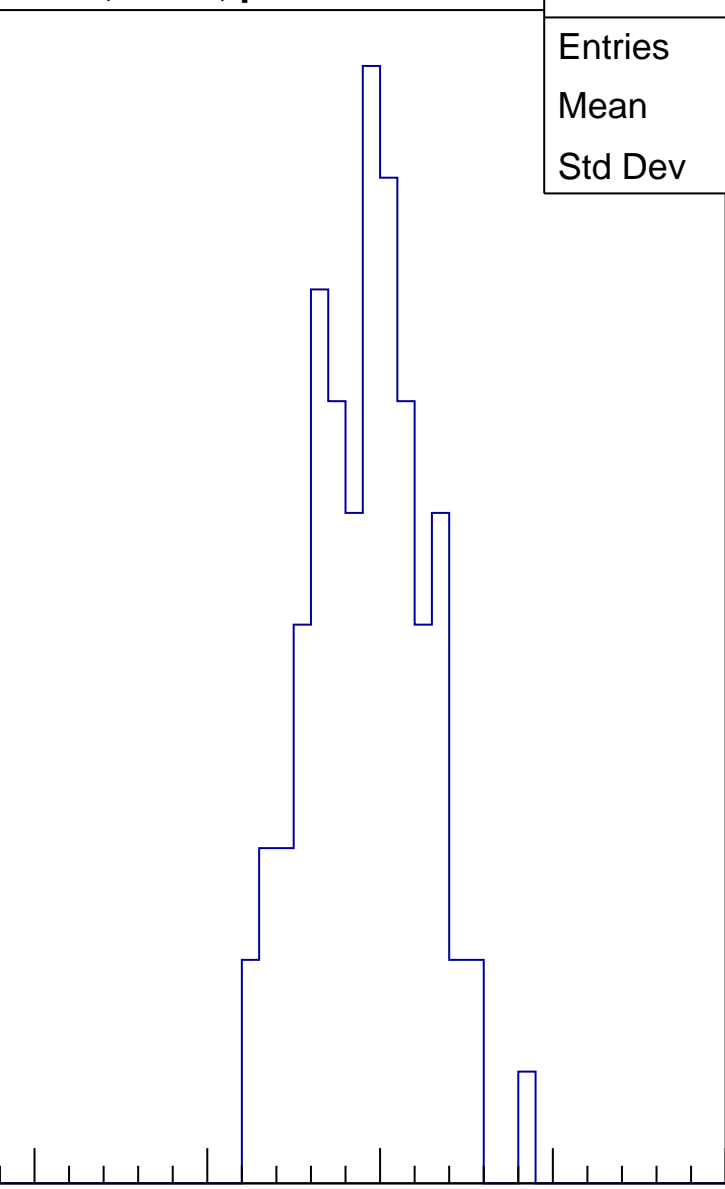
| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.76 |
| Std Dev | 3.332 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

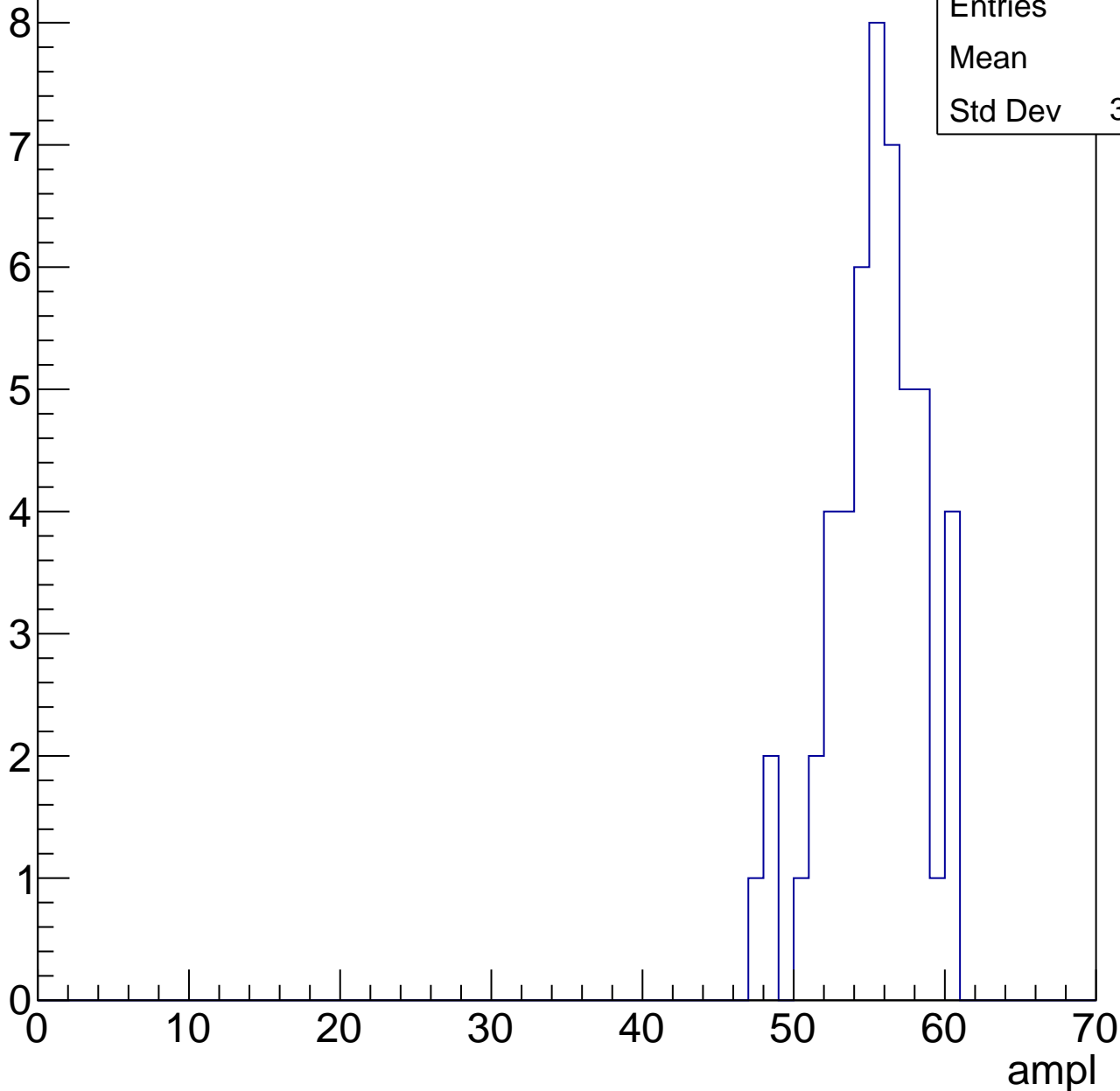


B0L001S, U7-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 54.9 |
| Std Dev | 3.048 |

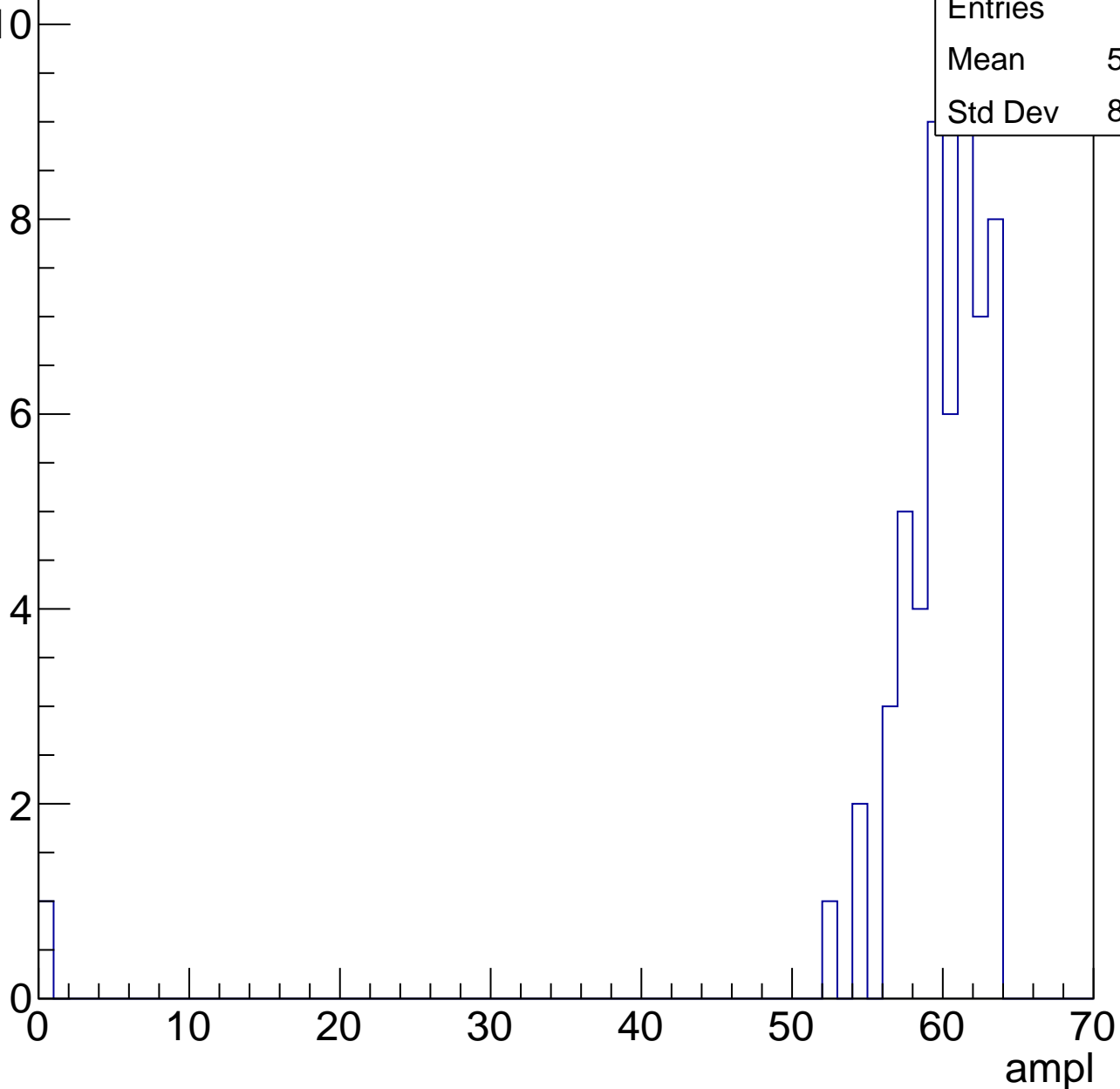


B0L001S, U7-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.64 |
| Std Dev | 8.303 |



B0L001S, U7-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch75, adc0

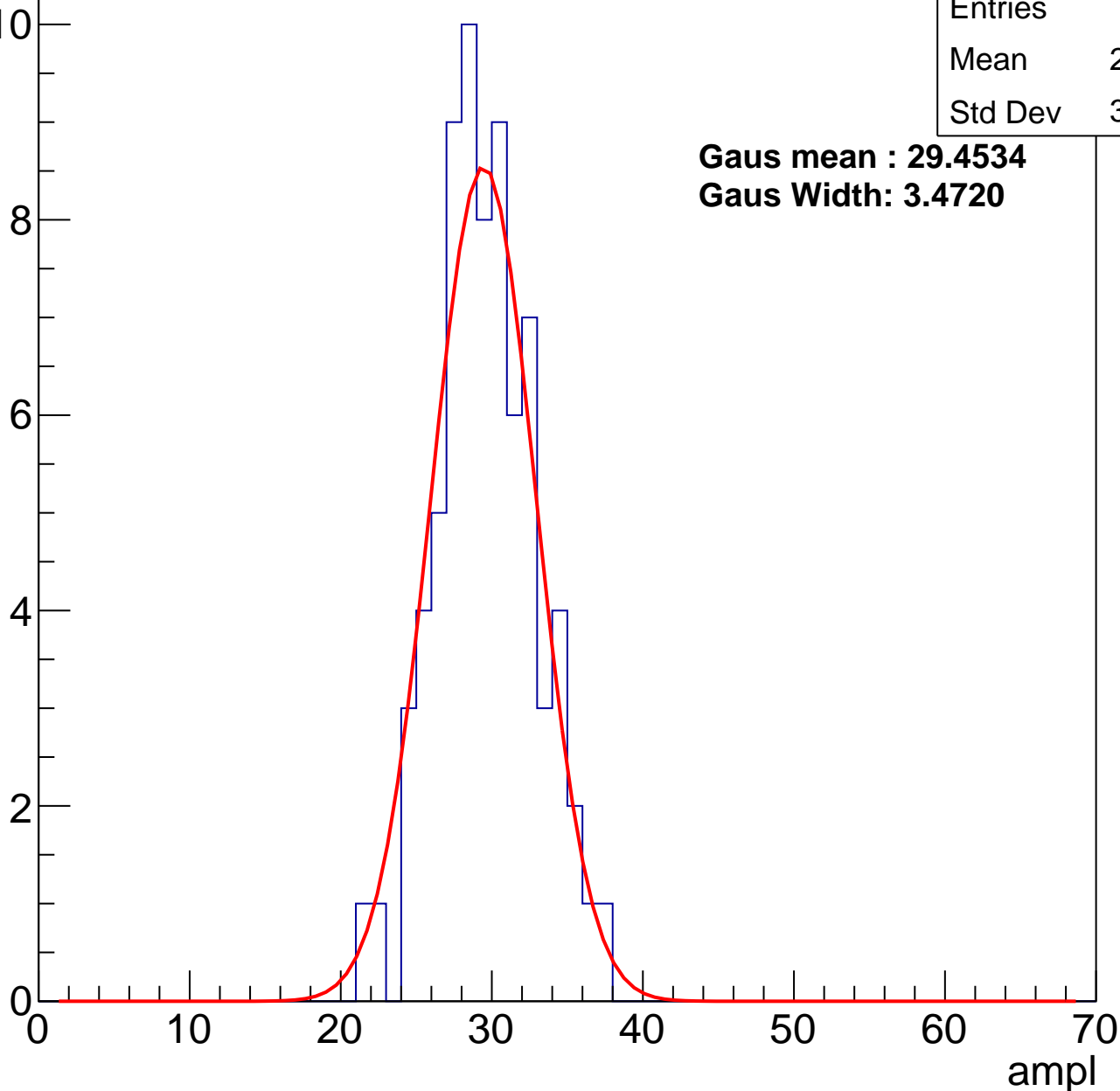
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.16 |
| Std Dev | 3.226 |

Gaus mean : 29.4534

Gaus Width: 3.4720



B0L001S, U7-ch75, adc1

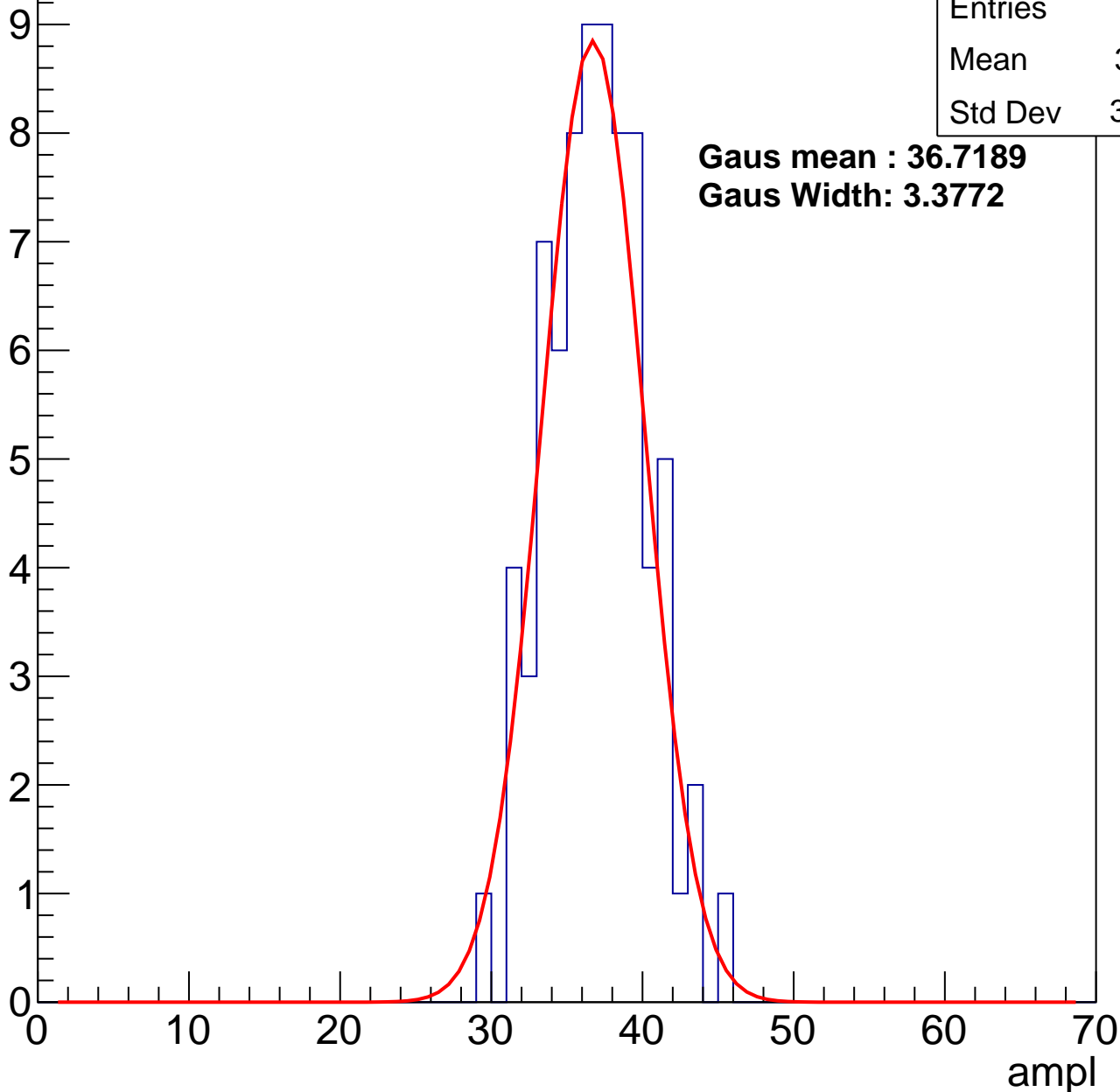
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.51 |
| Std Dev | 3.222 |

Gaus mean : 36.7189

Gaus Width: 3.3772



B0L001S, U7-ch75, adc2

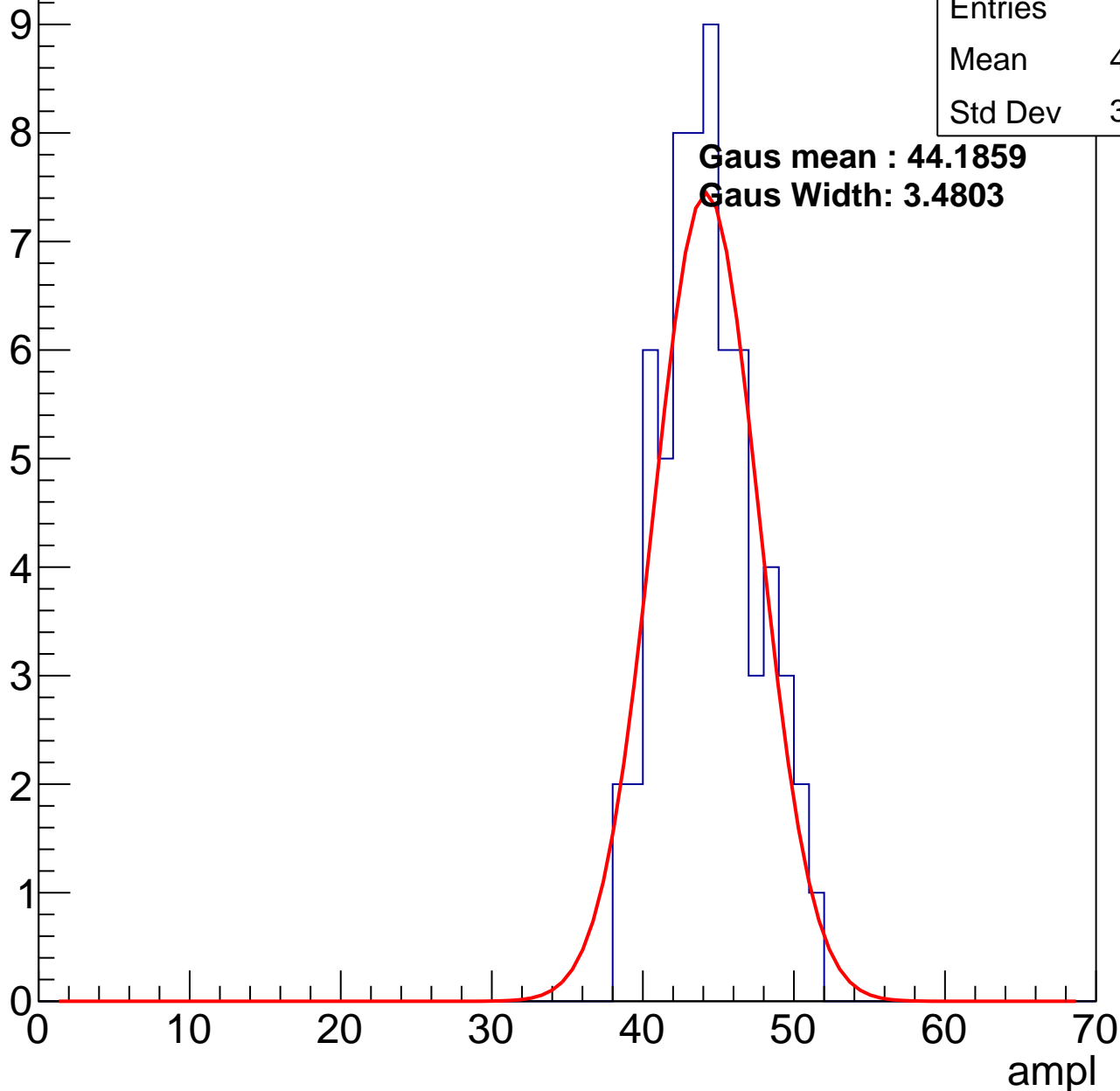
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 43.88 |
| Std Dev | 3.086 |

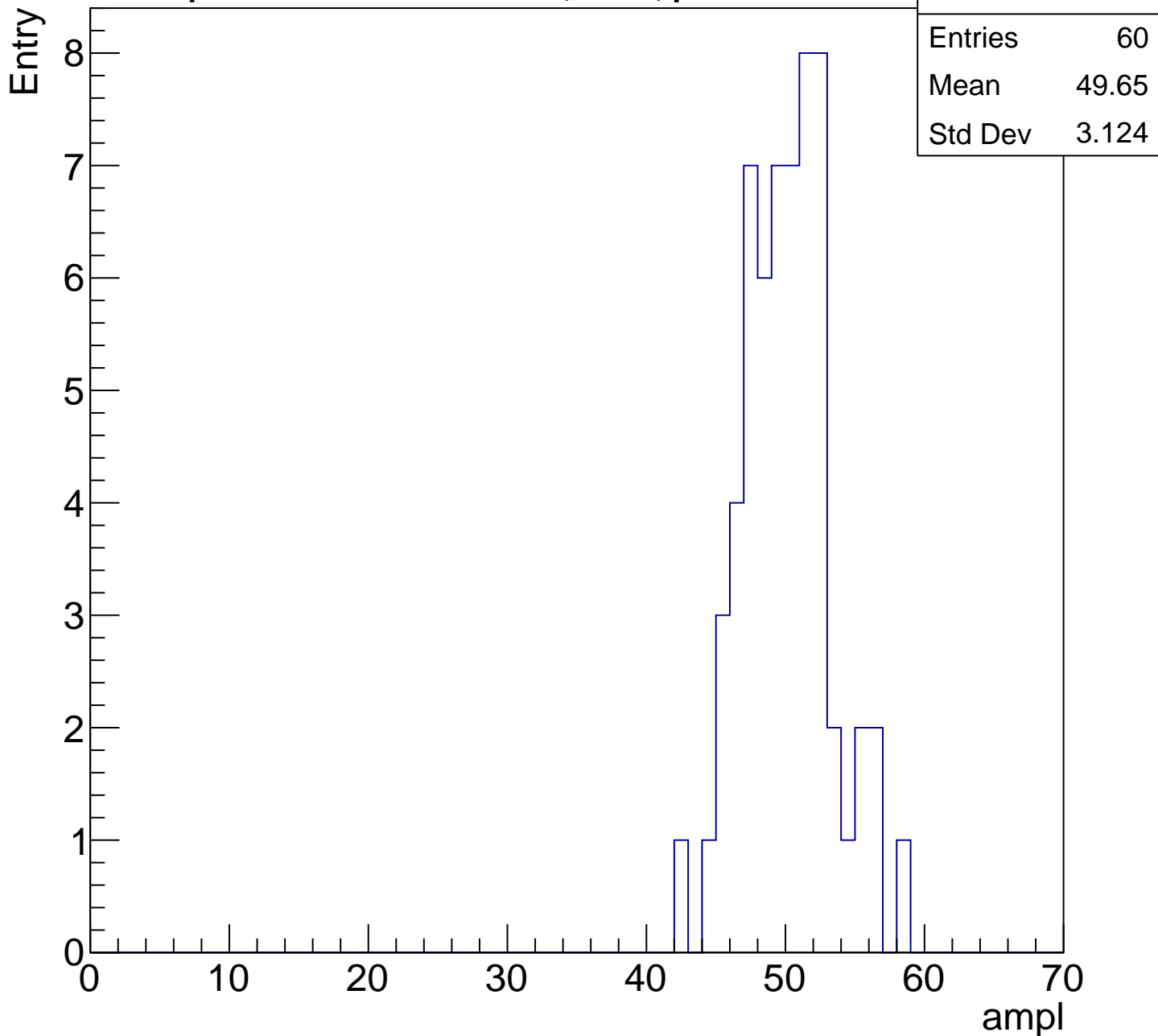
Gaus mean : 44.1859

Gaus Width: 3.4803



B0L001S, U7-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

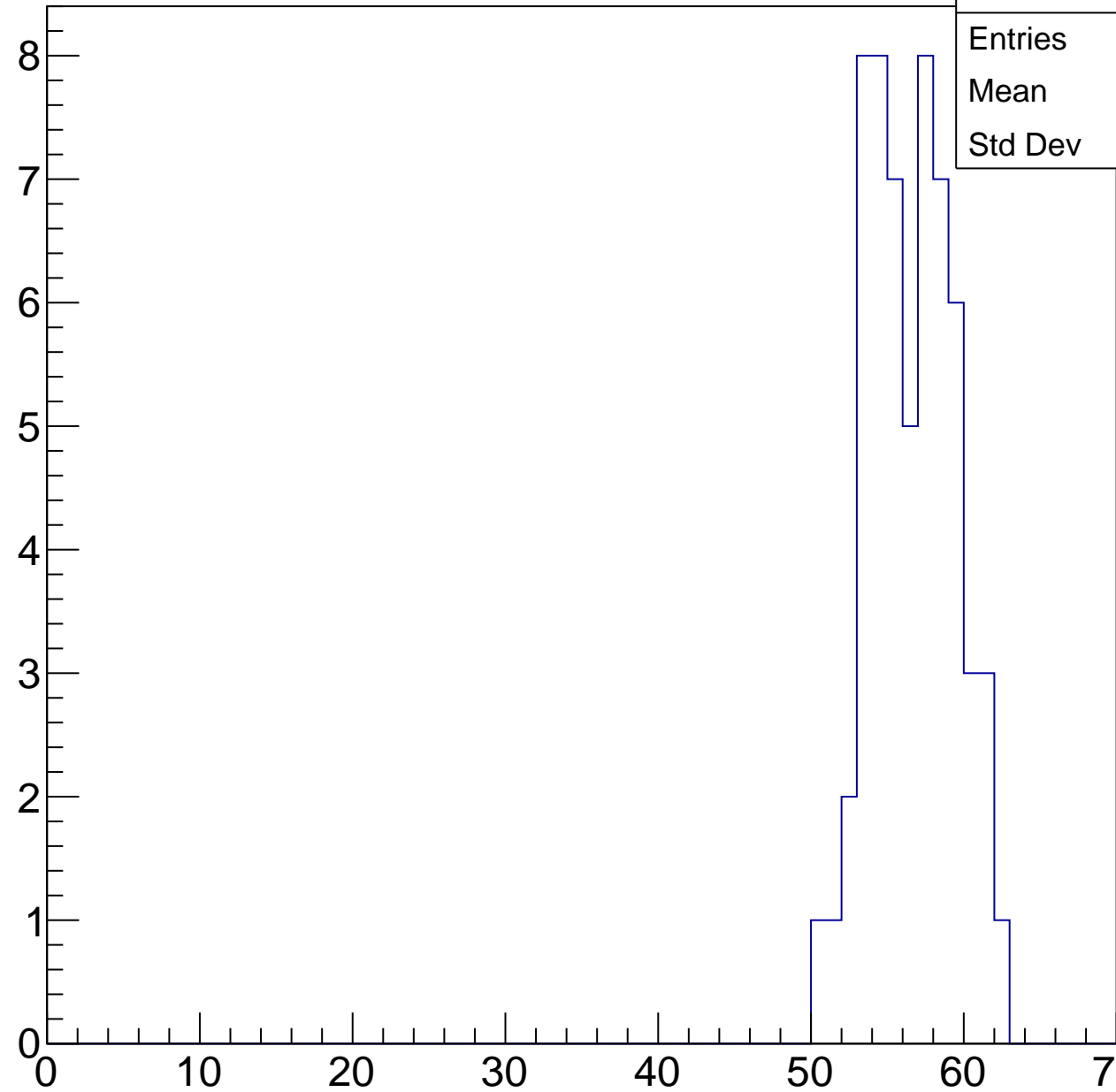
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 60 |
| Mean | 56.12 |
| Std Dev | 2.745 |

ampl

0 10 20 30 40 50 60 70

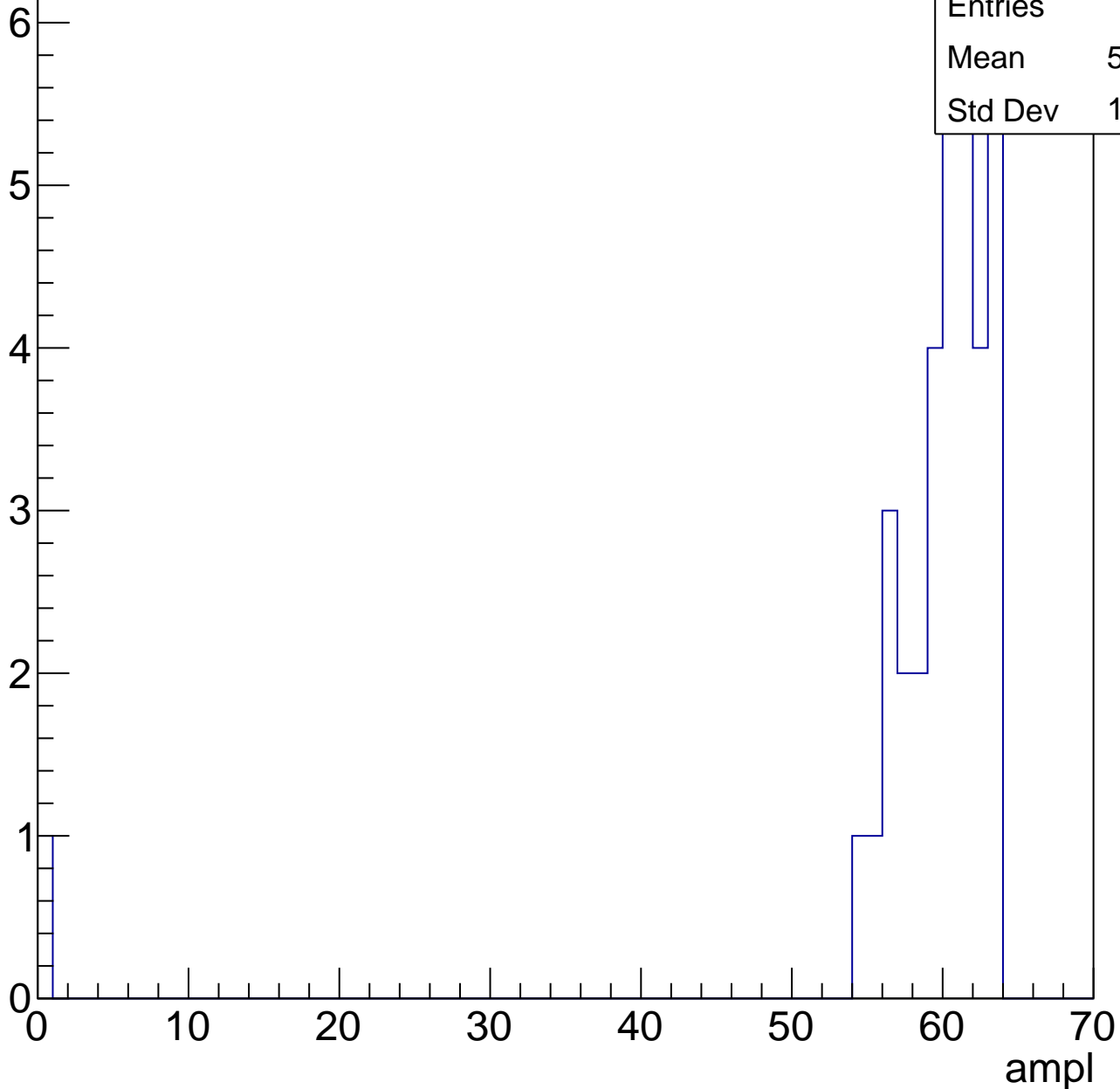


B0L001S, U7-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 36 |
| Mean | 58.19 |
| Std Dev | 10.13 |

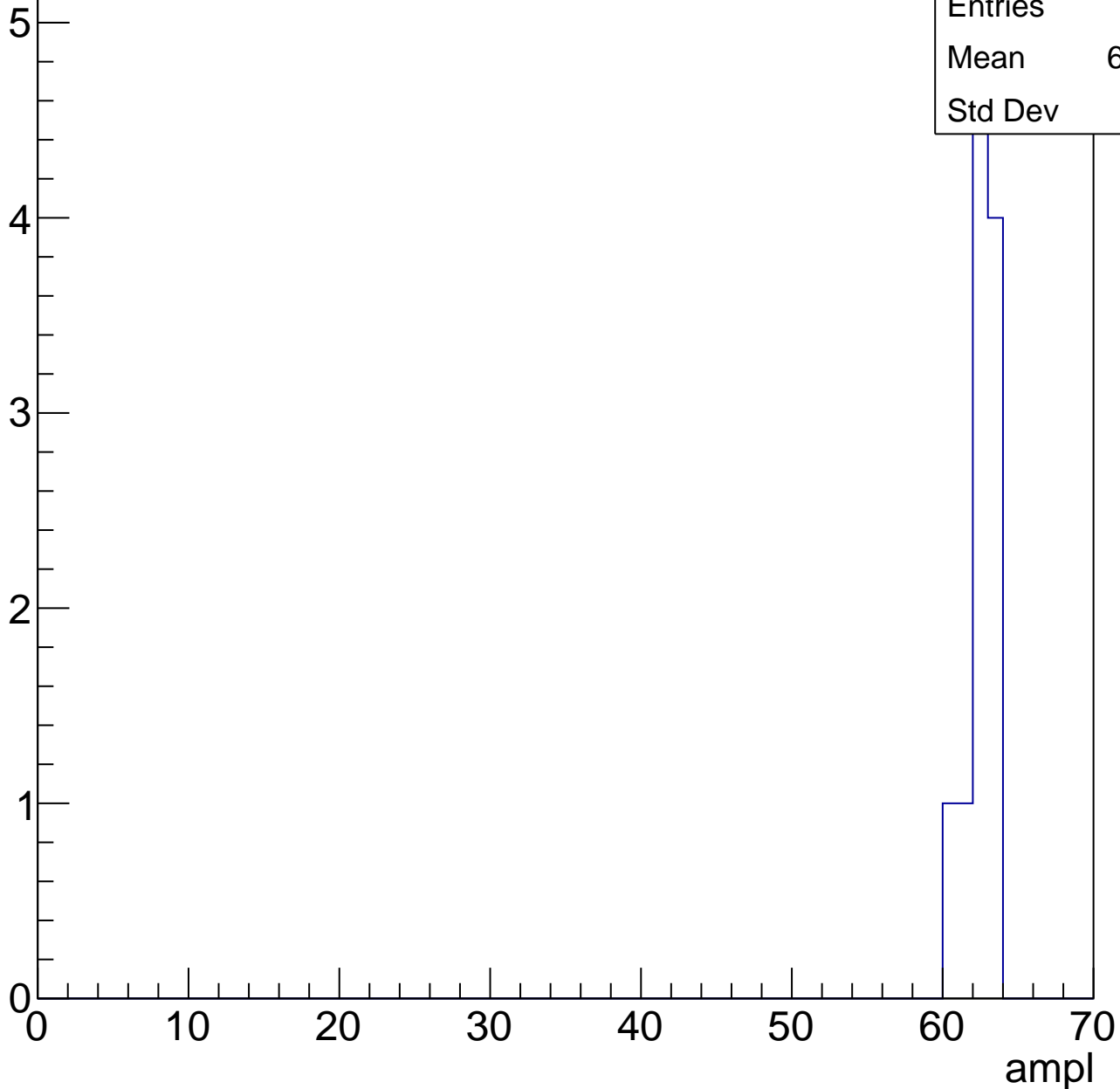


B0L001S, U7-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 62.09 |
| Std Dev | 0.9 |



B0L001S, U7-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 20 |
| Std Dev | 0 |

B0L001S, U7-ch76, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 74 |
| Mean | 31.53 |
| Std Dev | 3.662 |

Gaus mean : 31.5972

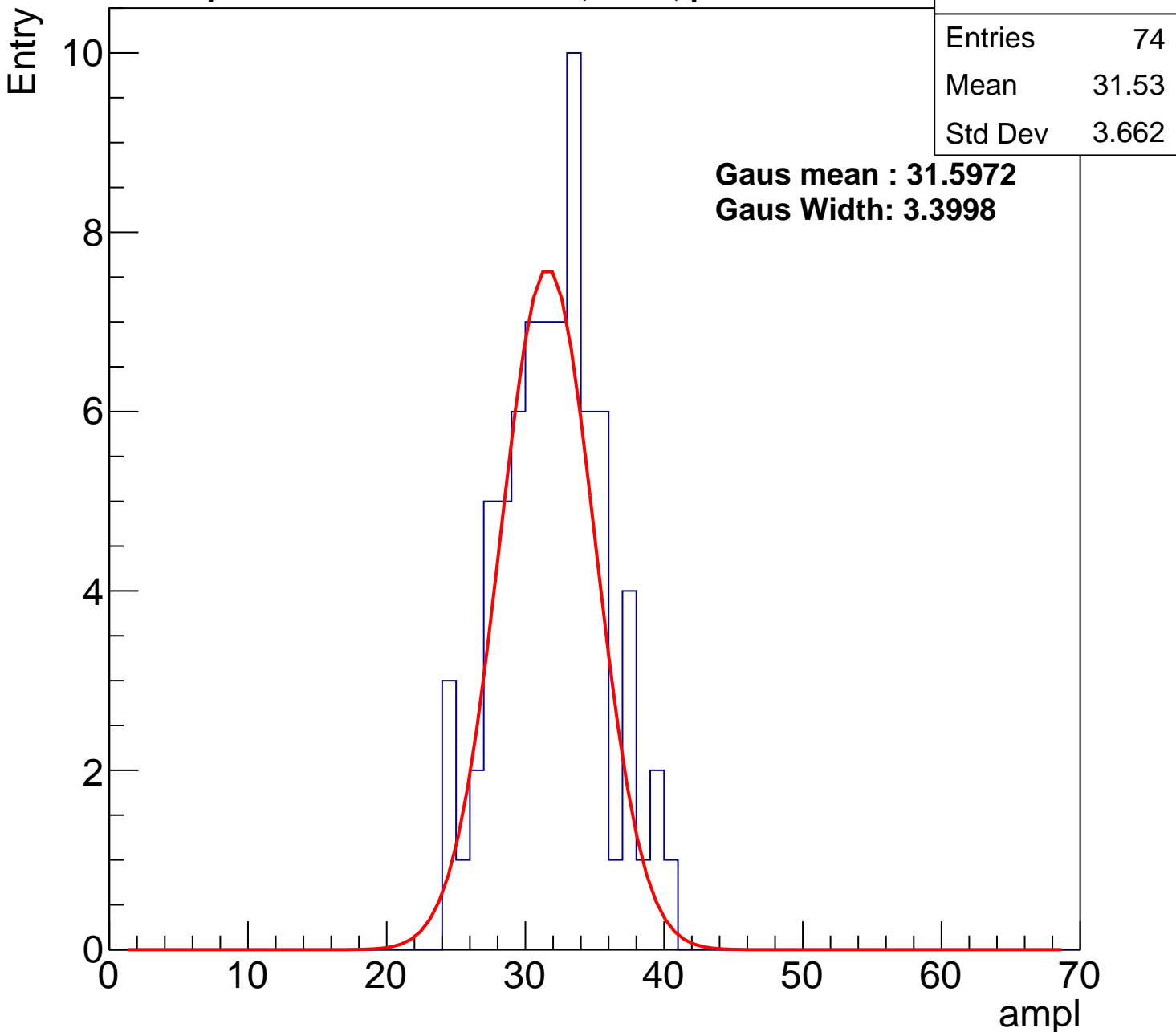
Gaus Width: 3.3998

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch76, adc1

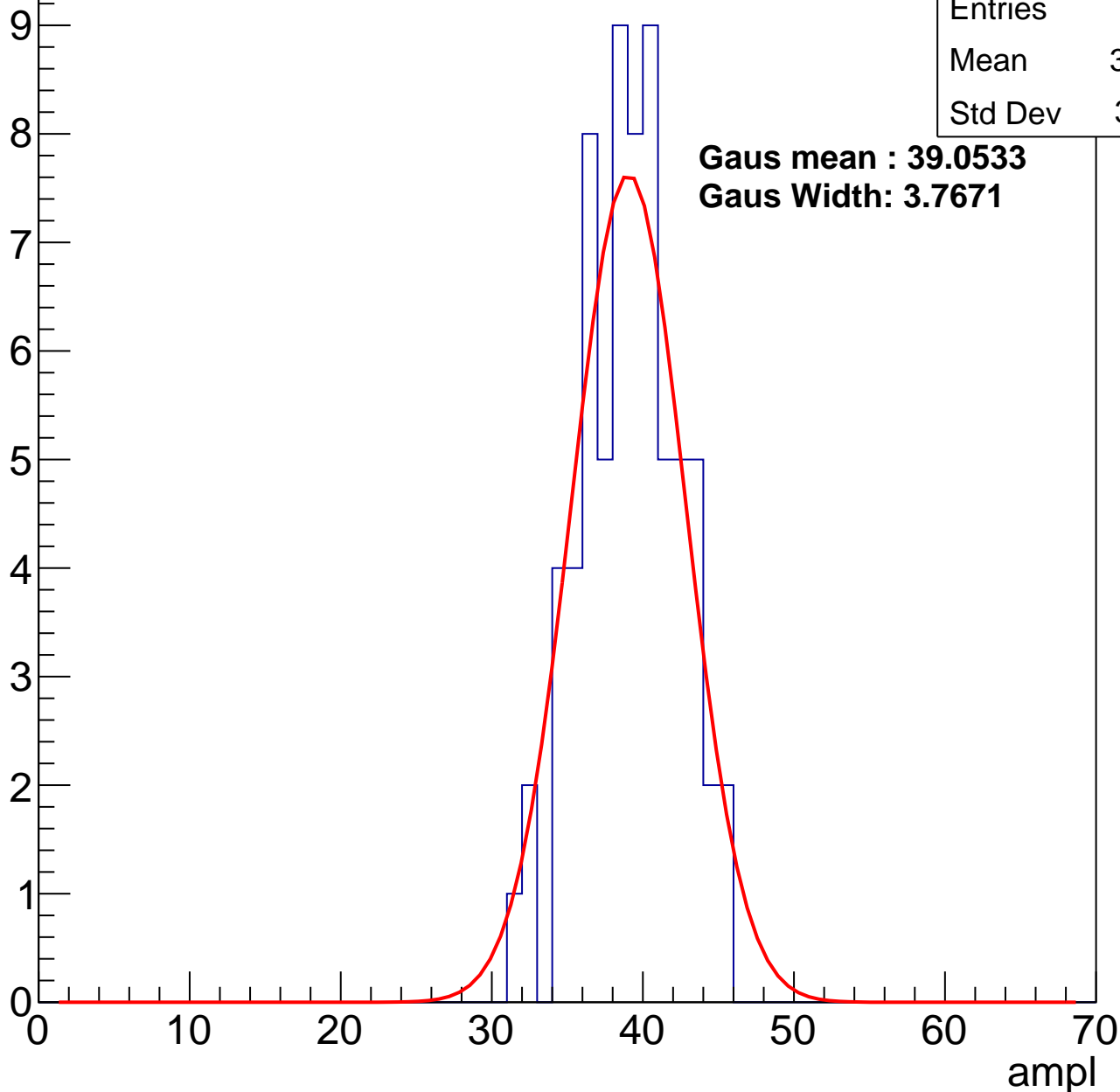
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 38.64 |
| Std Dev | 3.171 |

Gaus mean : 39.0533

Gaus Width: 3.7671



B0L001S, U7-ch76, adc2

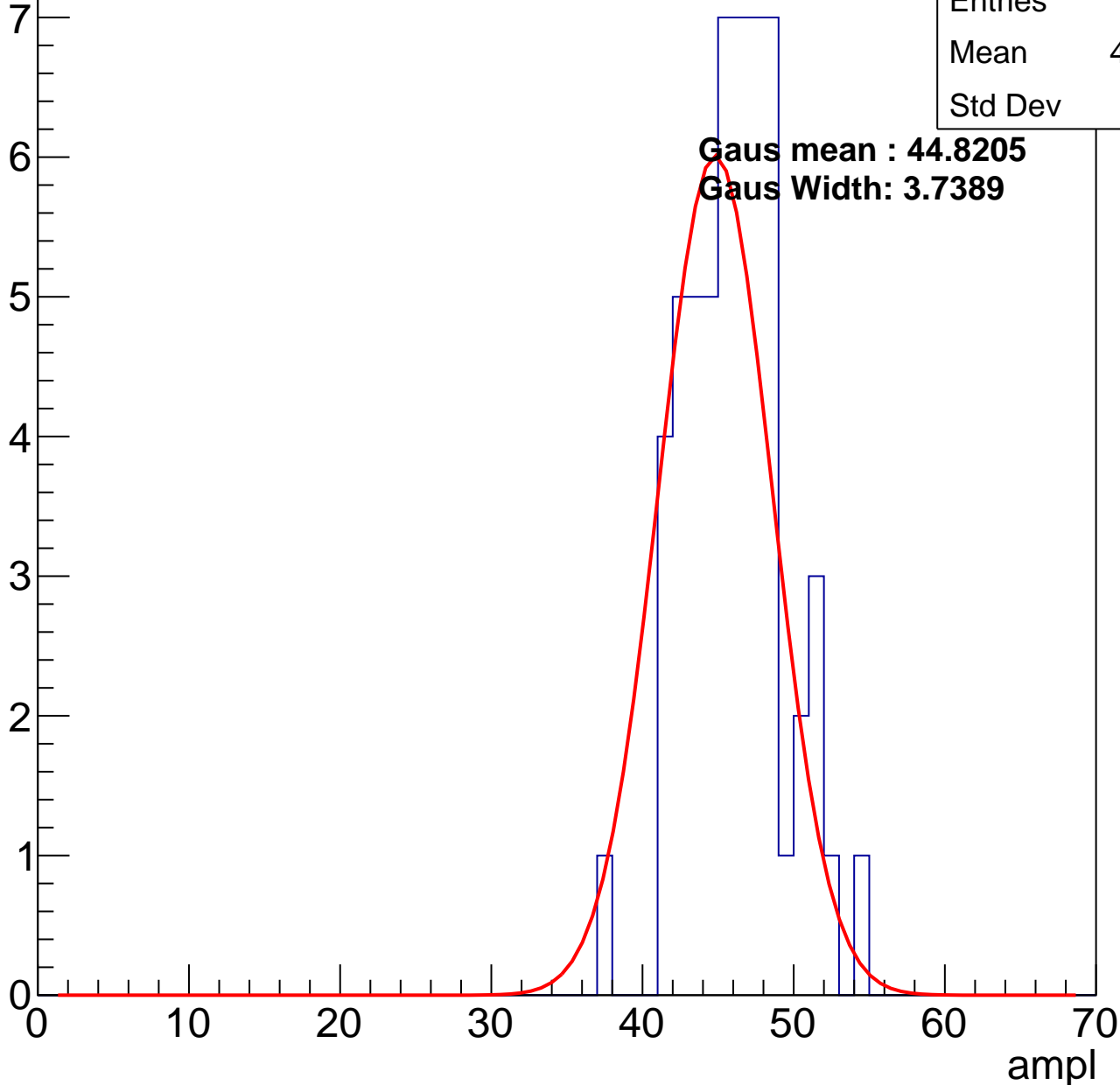
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 45.64 |
| Std Dev | 3.21 |

Gaus mean : 44.8205

Gaus Width: 3.7389

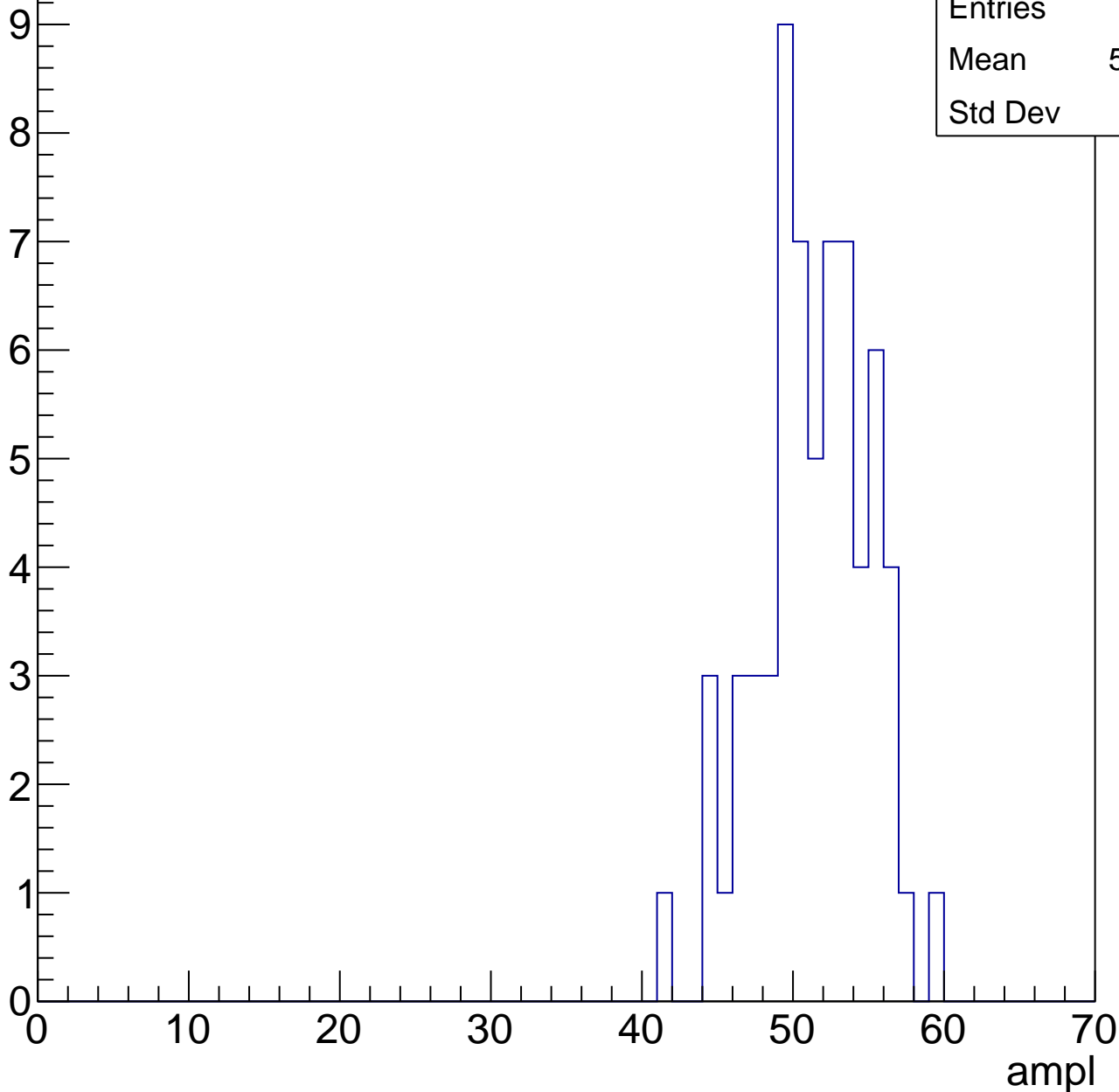


B0L001S, U7-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

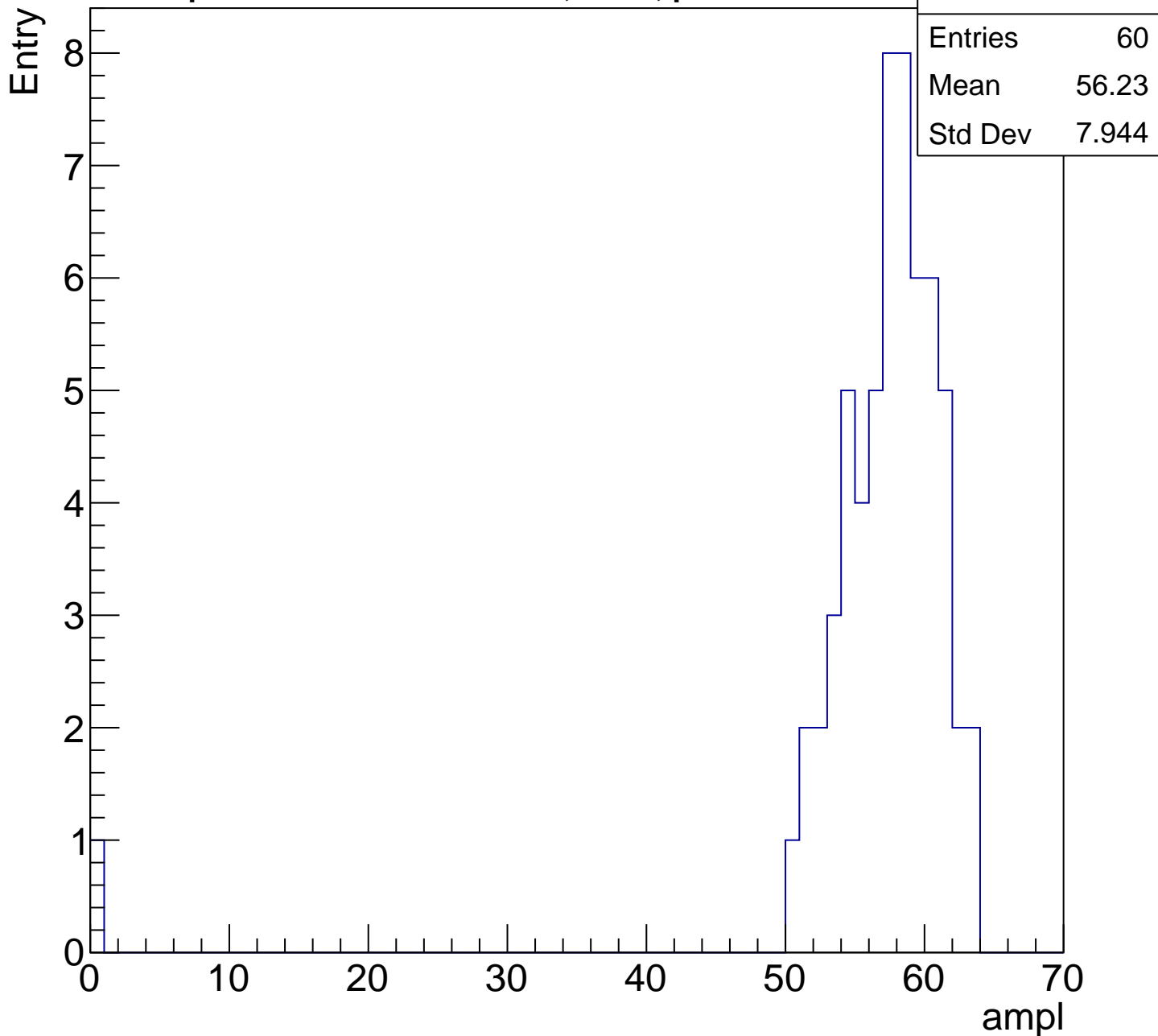
Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.89 |
| Std Dev | 3.6 |



B0L001S, U7-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

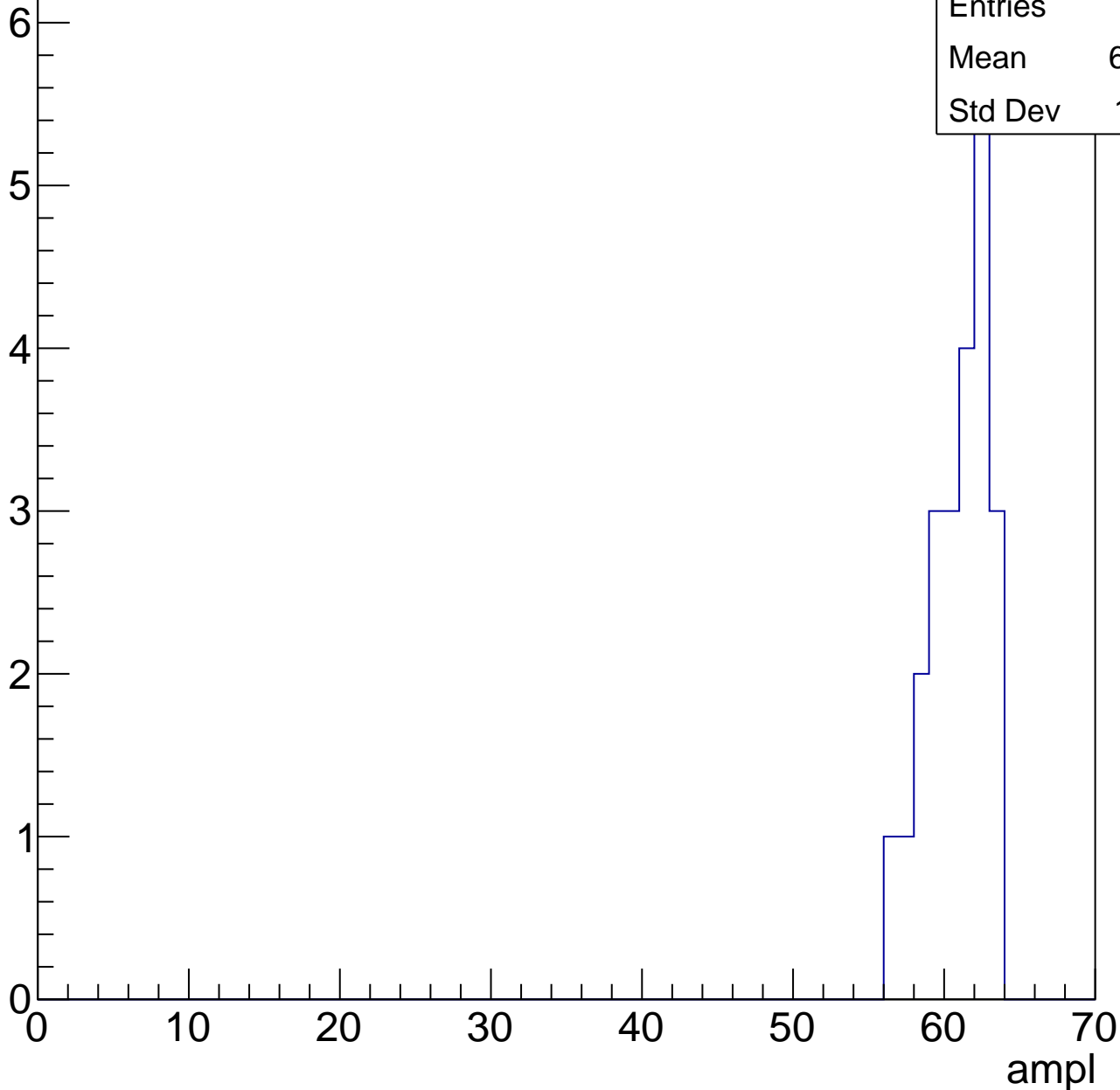


B0L001S, U7-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 23 |
| Mean | 60.48 |
| Std Dev | 1.931 |

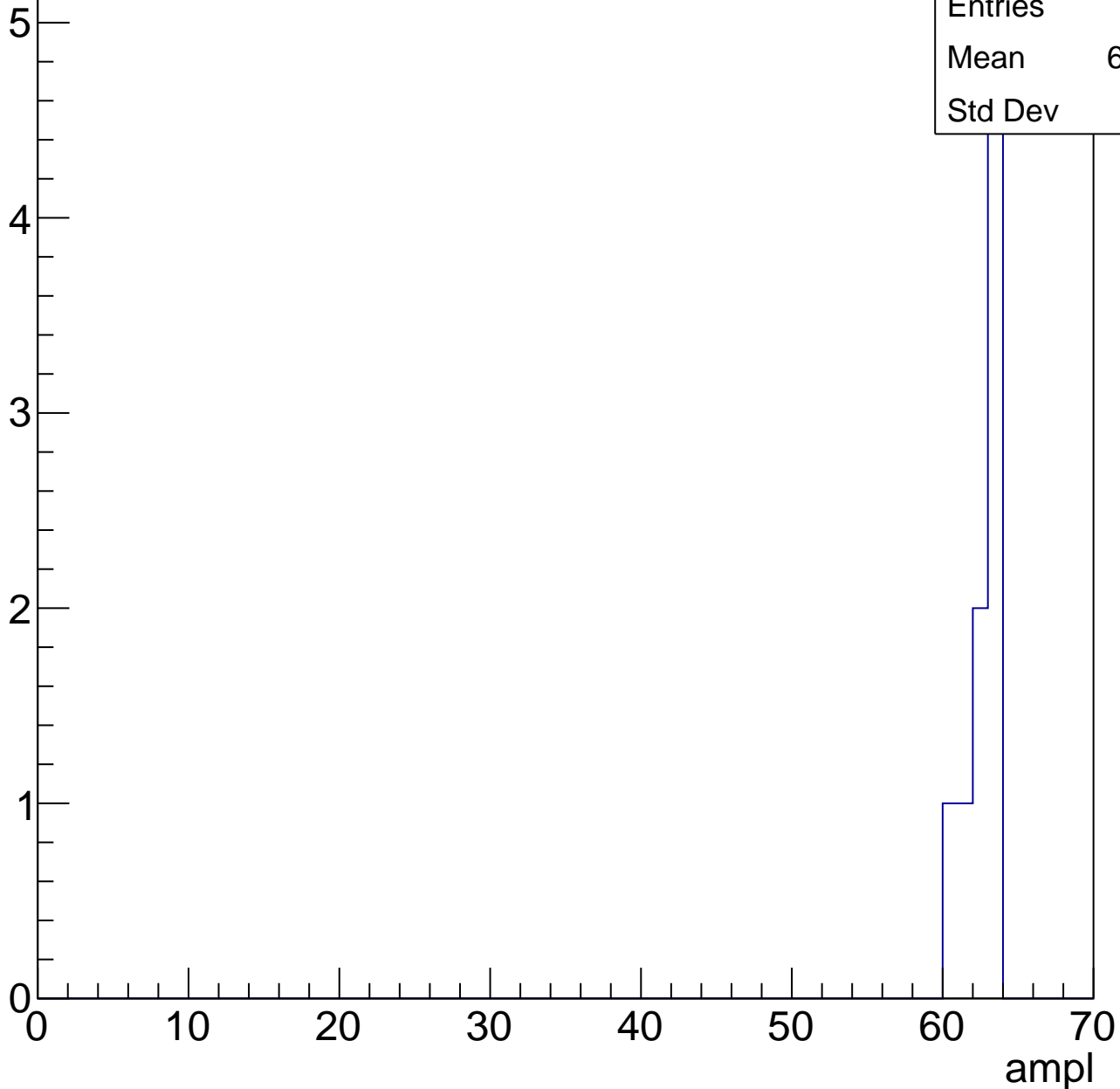


B0L001S, U7-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 62.22 |
| Std Dev | 1.03 |



B0L001S, U7-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch77, adc0

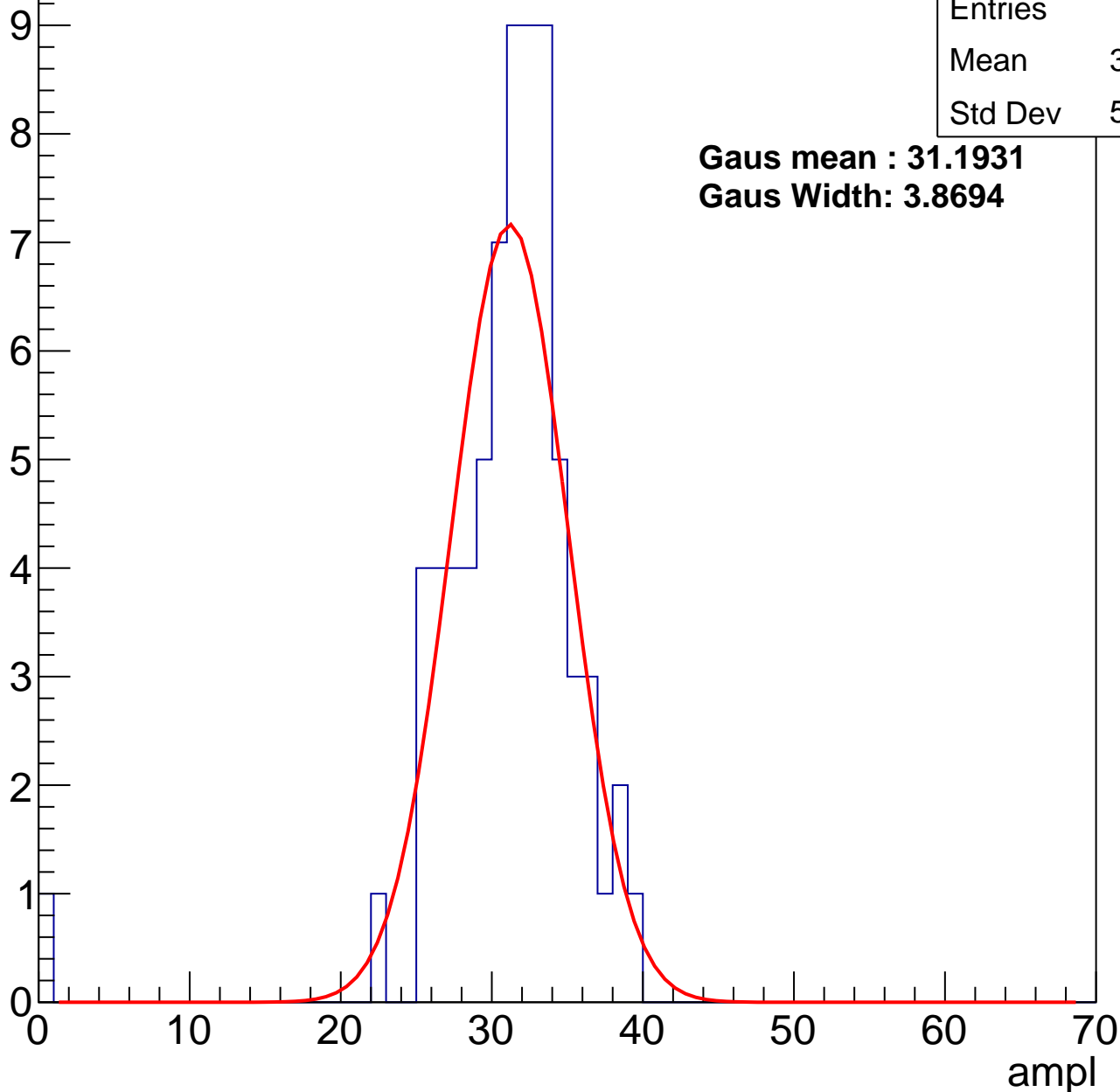
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 30.56 |
| Std Dev | 5.014 |

Gaus mean : 31.1931

Gaus Width: 3.8694



B0L001S, U7-ch77, adc1

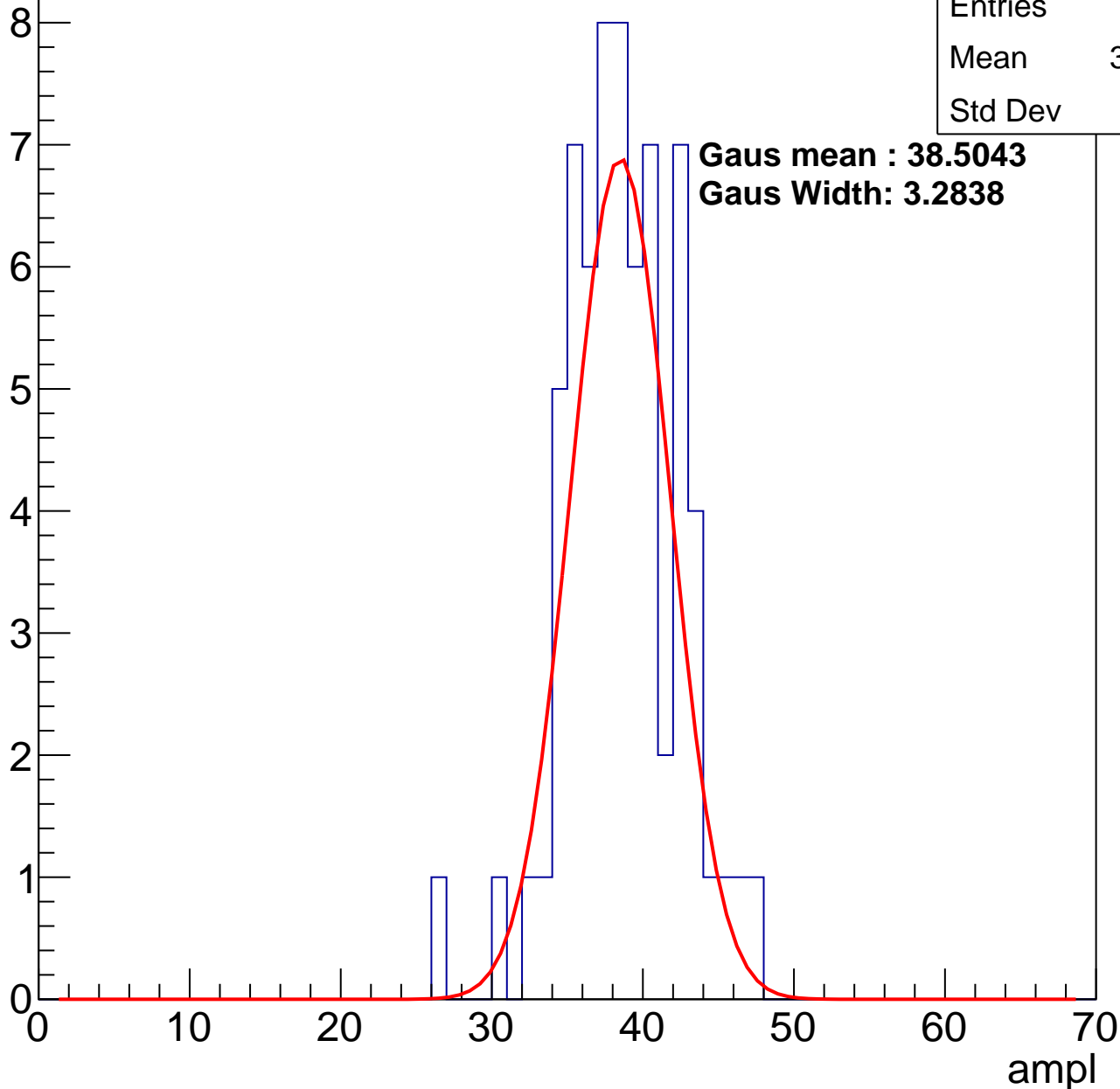
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 38.18 |
| Std Dev | 3.71 |

Gaus mean : 38.5043

Gaus Width: 3.2838



B0L001S, U7-ch77, adc2

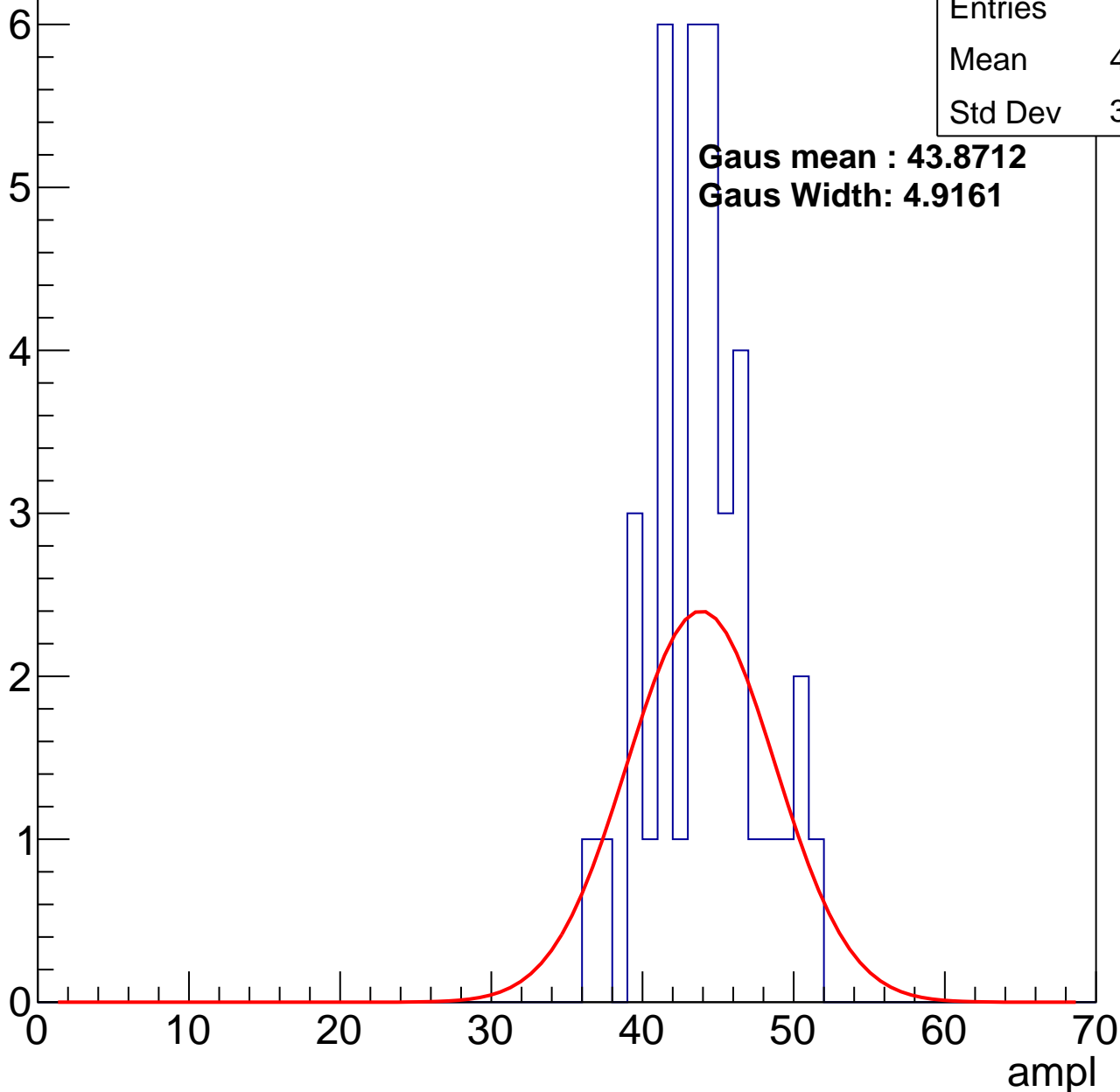
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 43.53 |
| Std Dev | 3.439 |

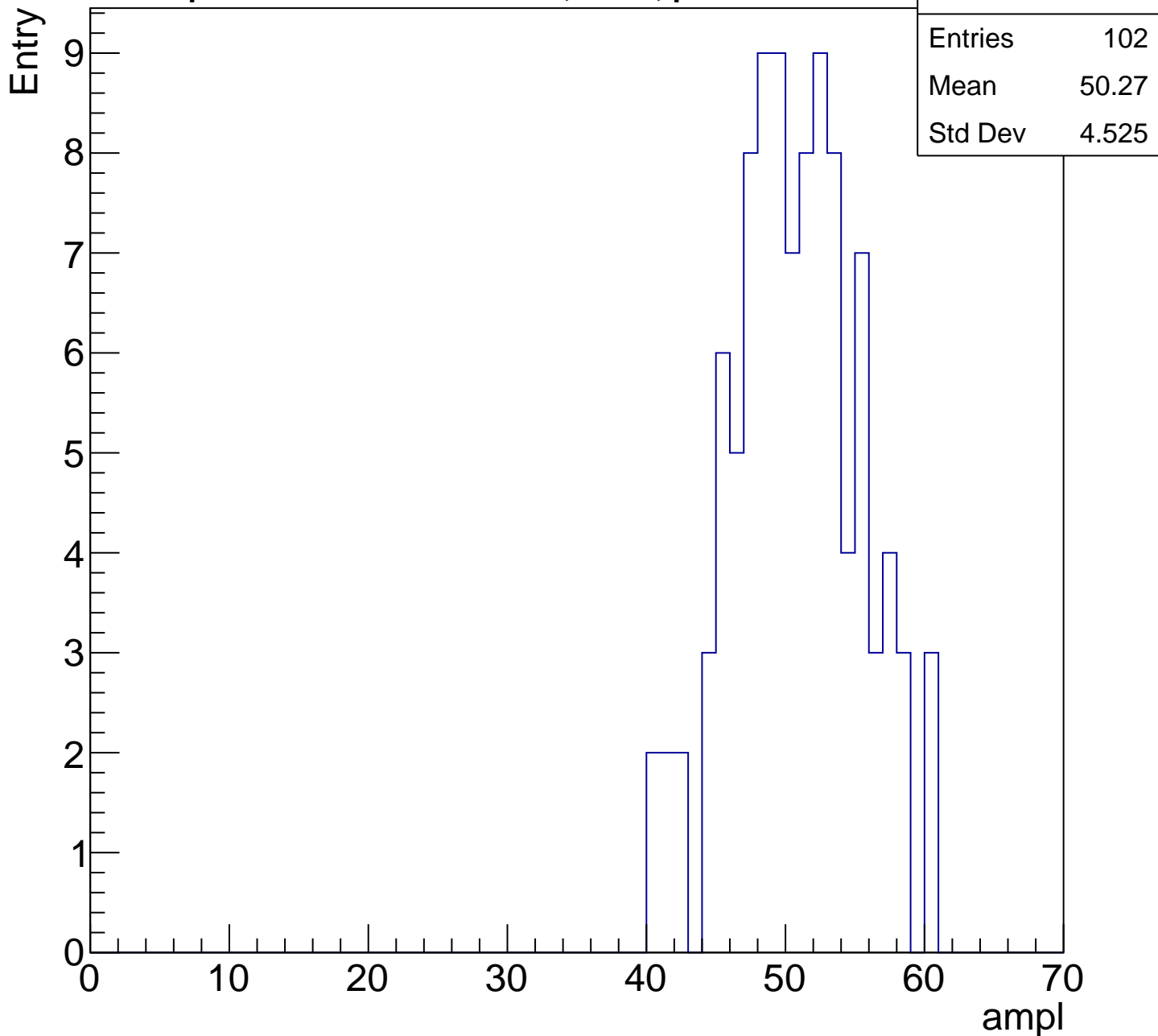
Gaus mean : 43.8712

Gaus Width: 4.9161



B0L001S, U7-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

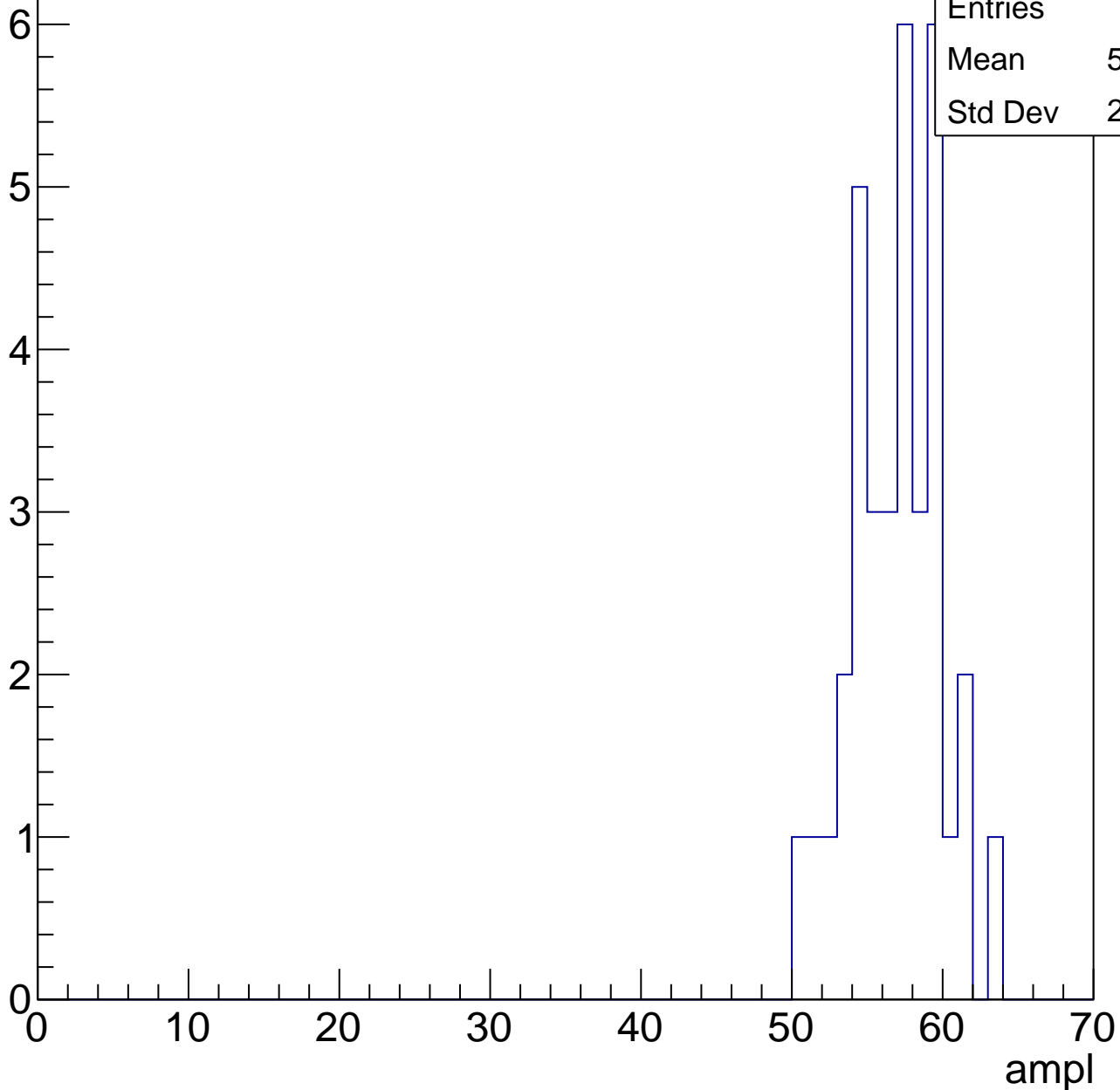


B0L001S, U7-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 35 |
| Mean | 56.49 |
| Std Dev | 2.912 |

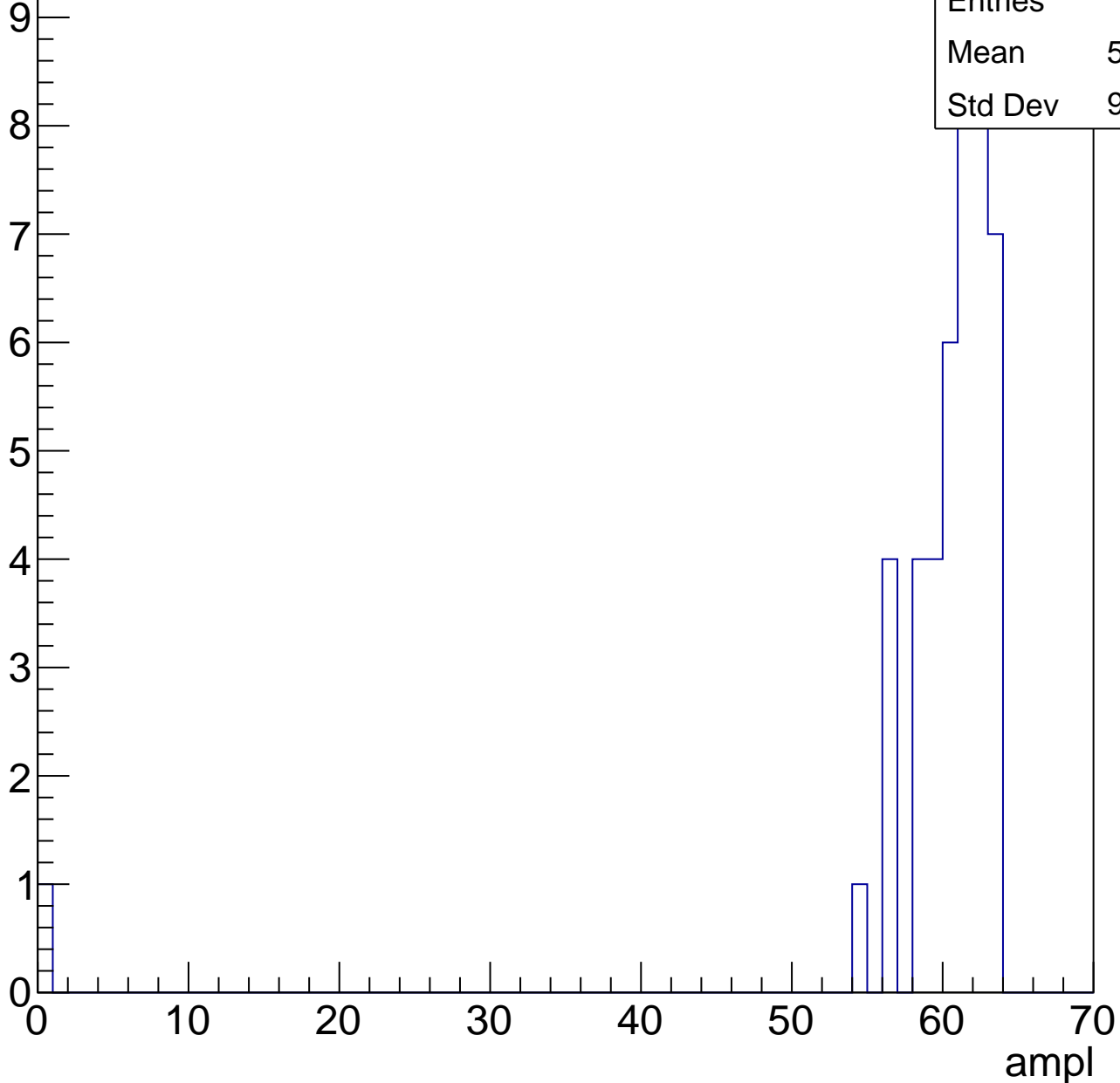


B0L001S, U7-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.93 |
| Std Dev | 9.265 |



B0L001S, U7-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

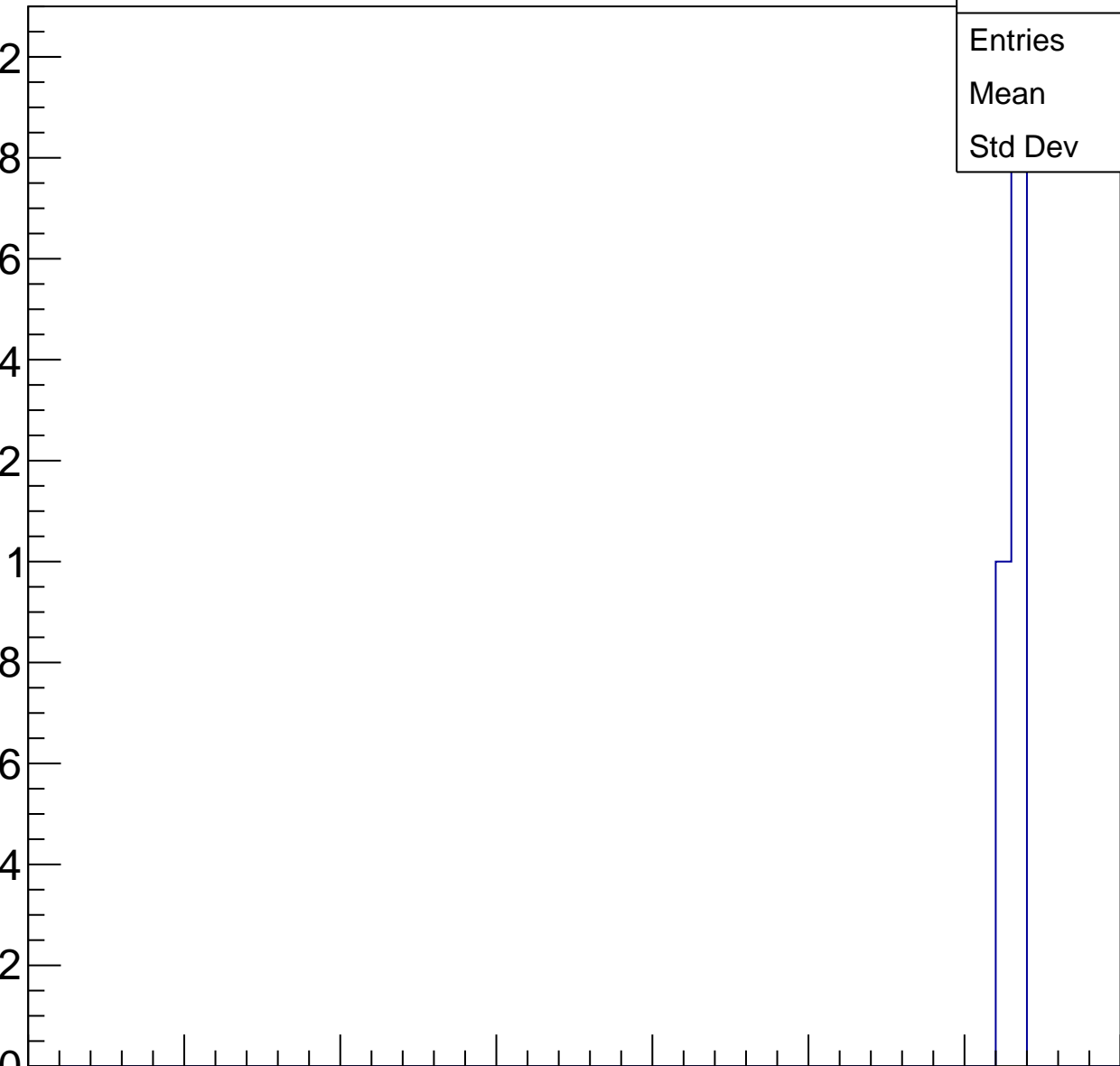
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch78, adc0

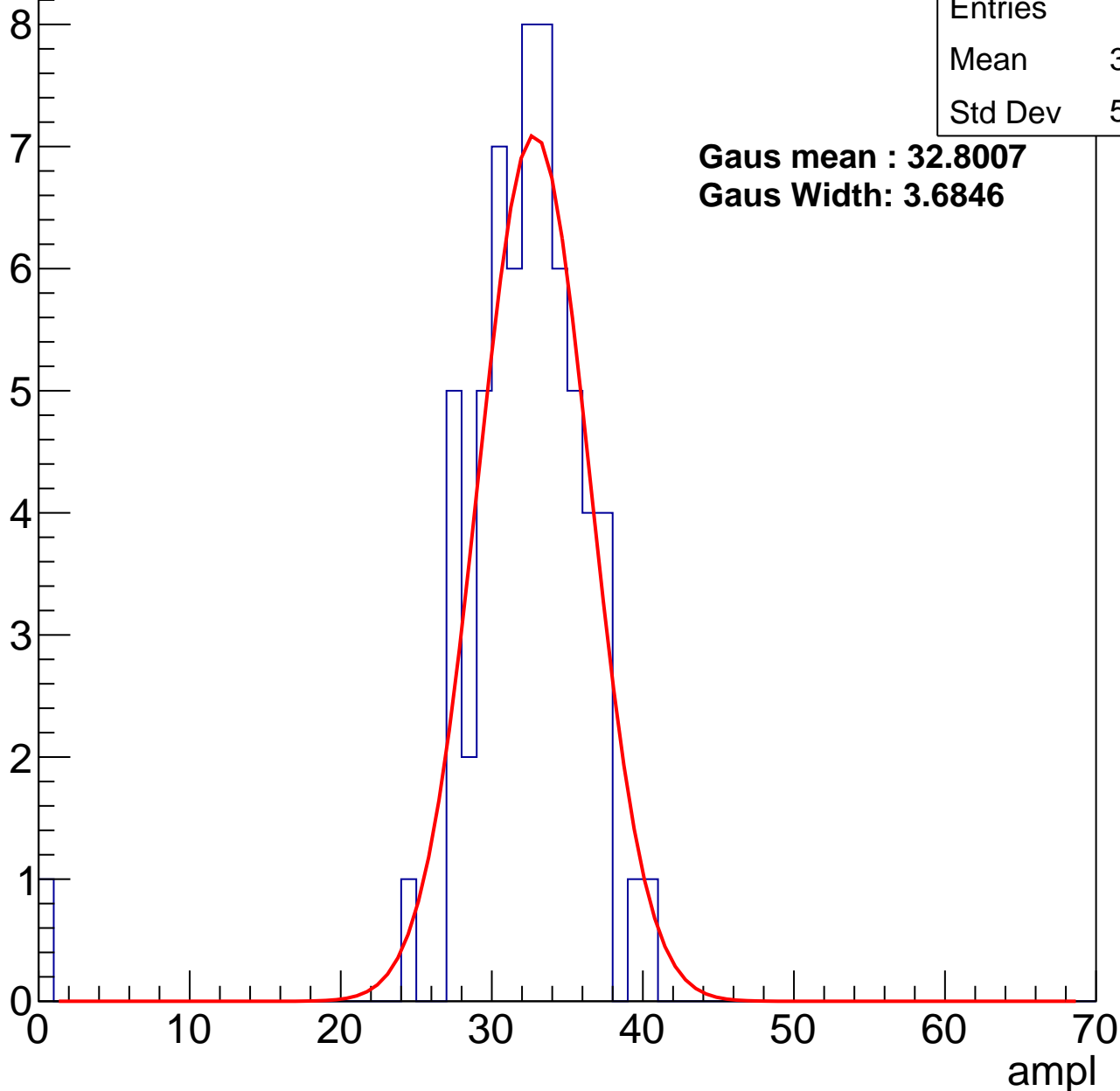
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 31.66 |
| Std Dev | 5.109 |

Gaus mean : 32.8007

Gaus Width: 3.6846



B0L001S, U7-ch78, adc1

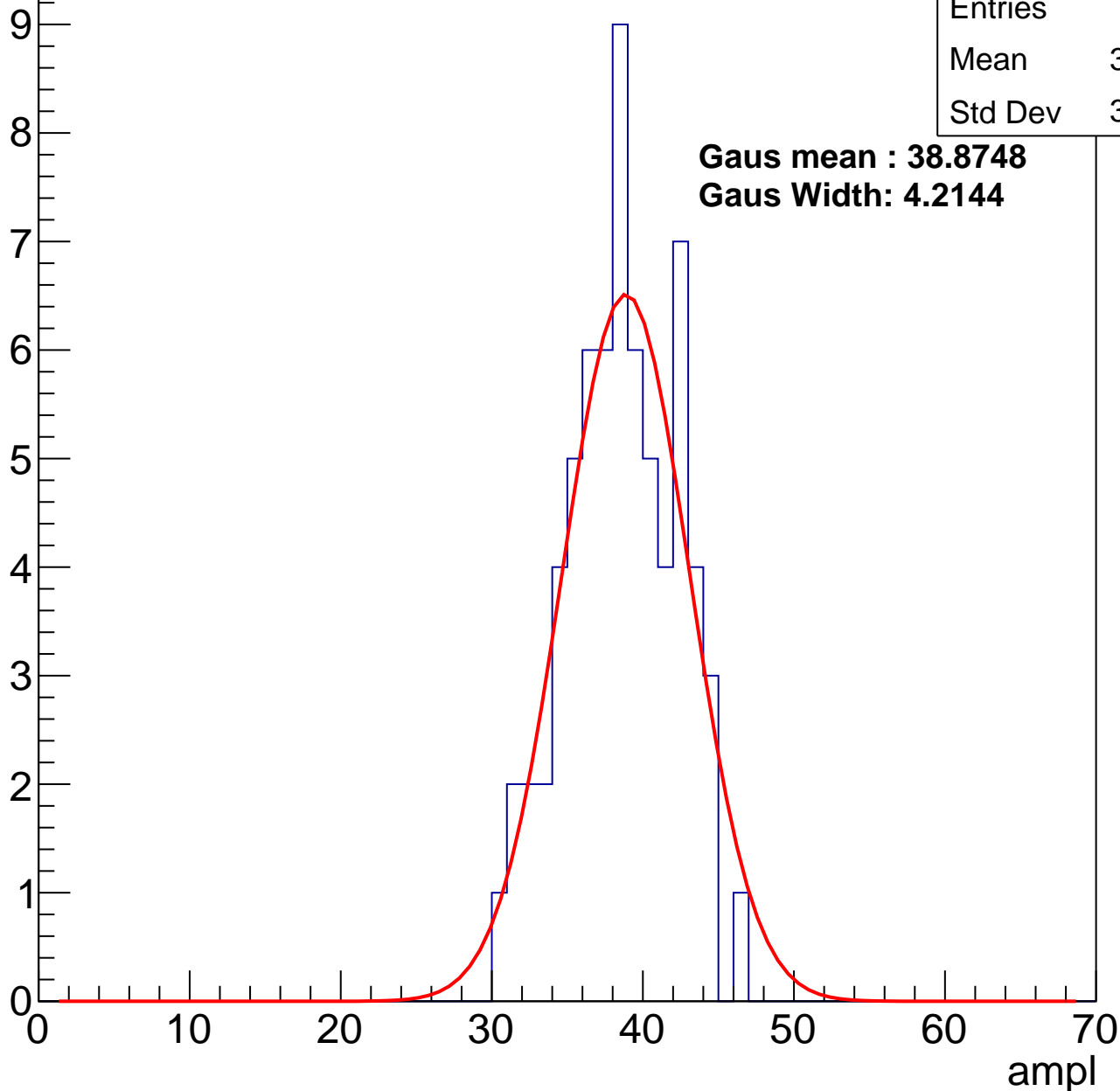
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 38.13 |
| Std Dev | 3.599 |

Gaus mean : 38.8748

Gaus Width: 4.2144



B0L001S, U7-ch78, adc2

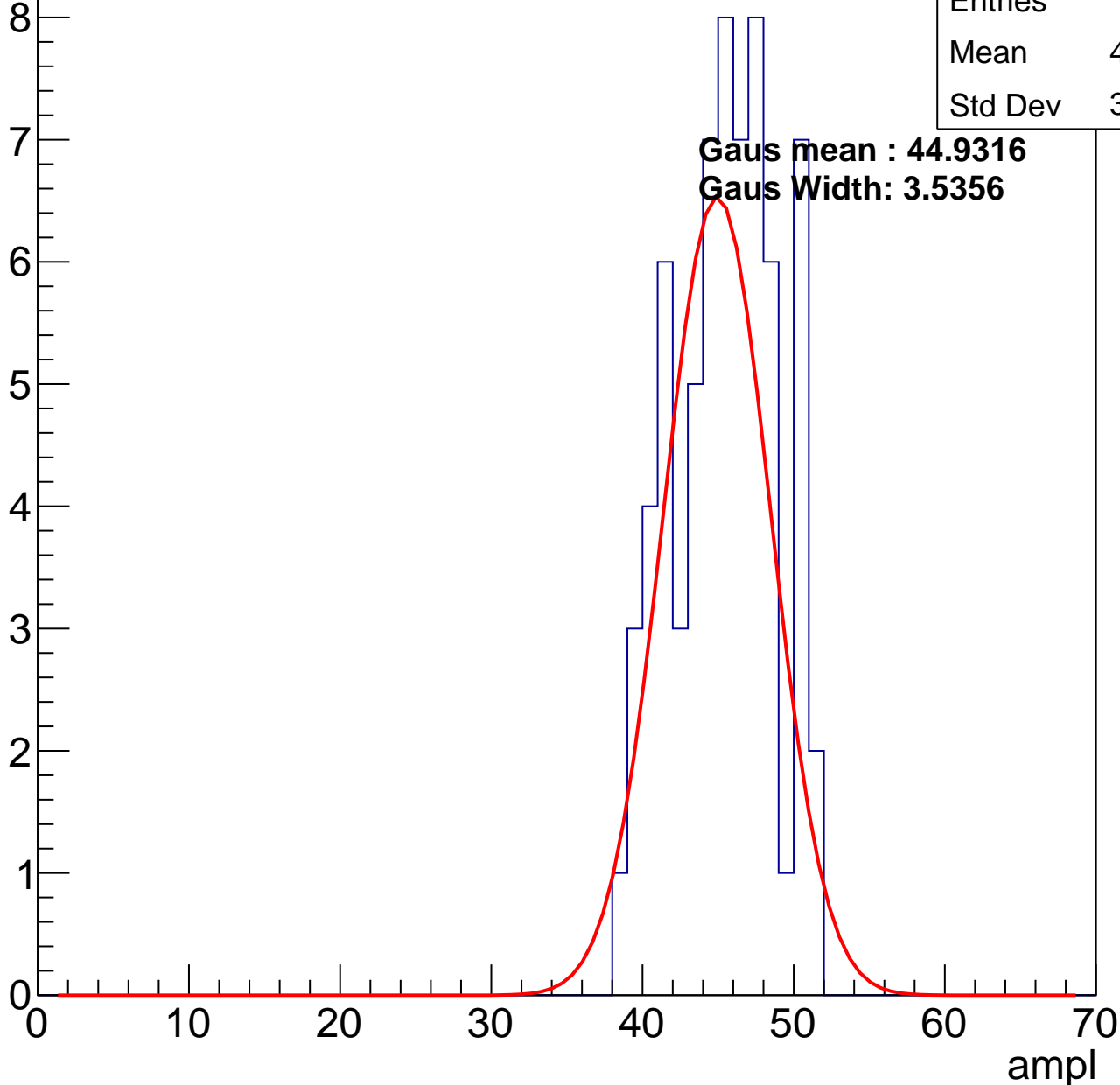
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 44.96 |
| Std Dev | 3.349 |

Gaus mean : 44.9316

Gaus Width: 3.5356

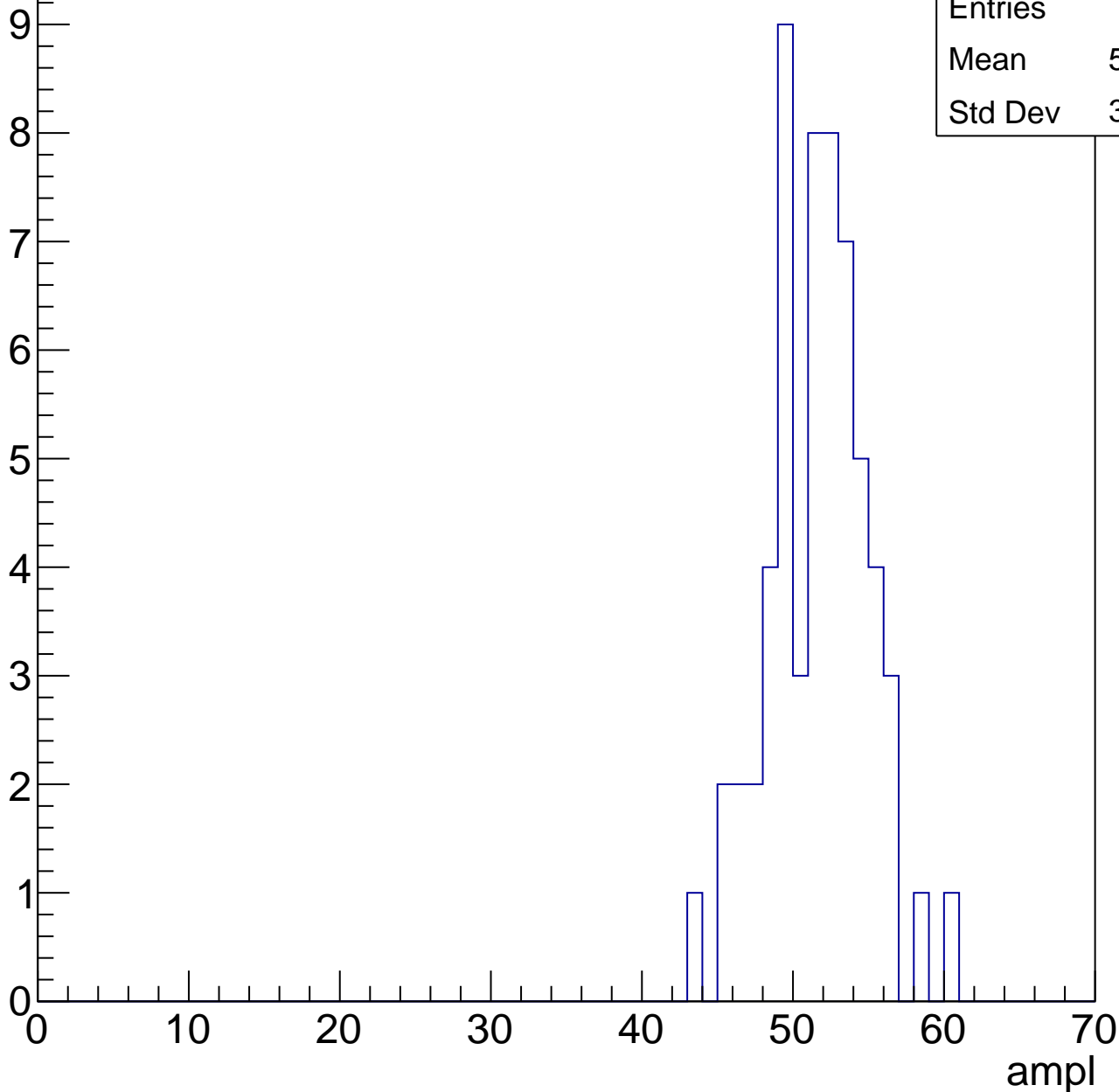


B0L001S, U7-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

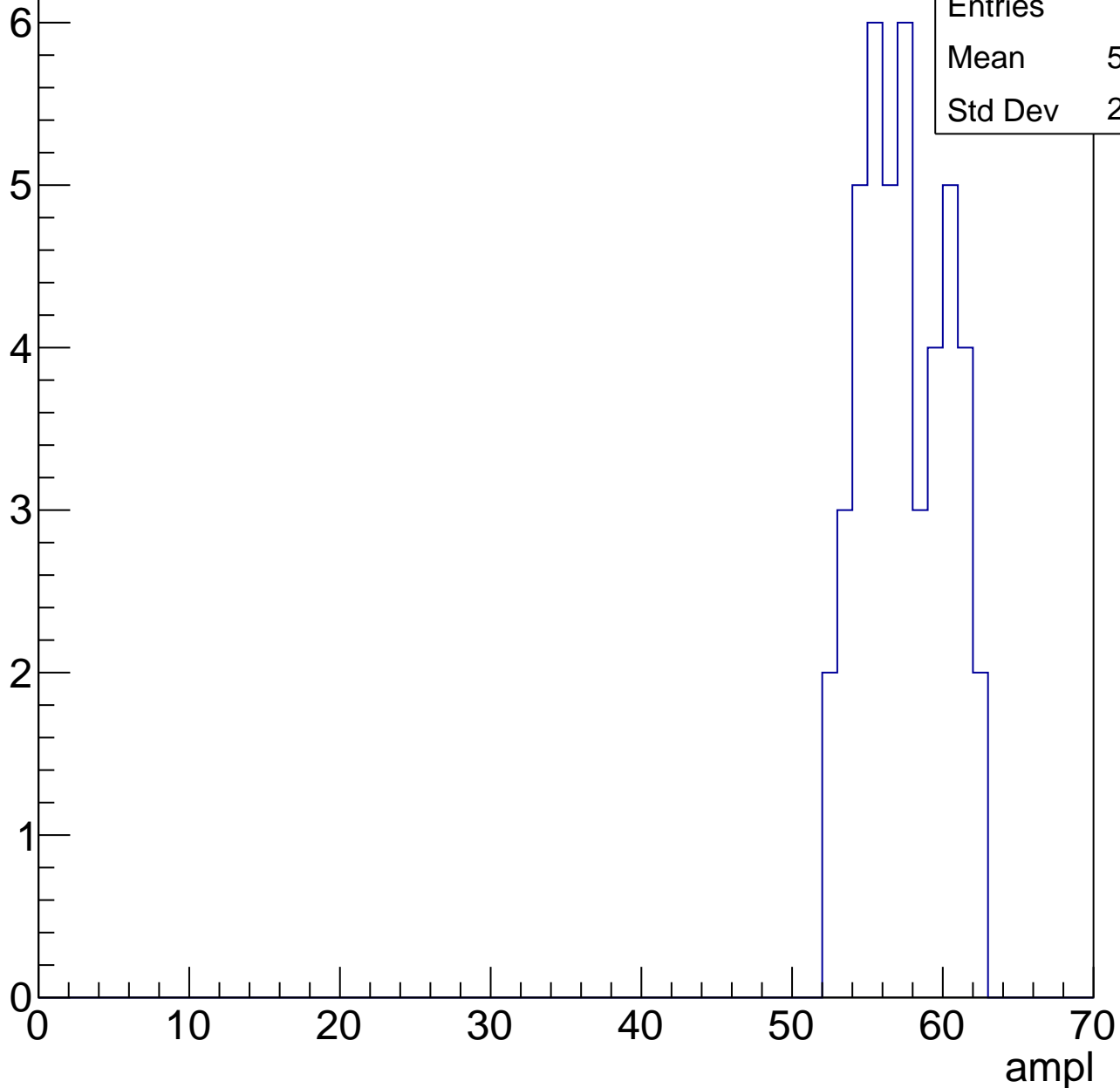
| | |
|---------|-------|
| Entries | 60 |
| Mean | 51.22 |
| Std Dev | 3.266 |



B0L001S, U7-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

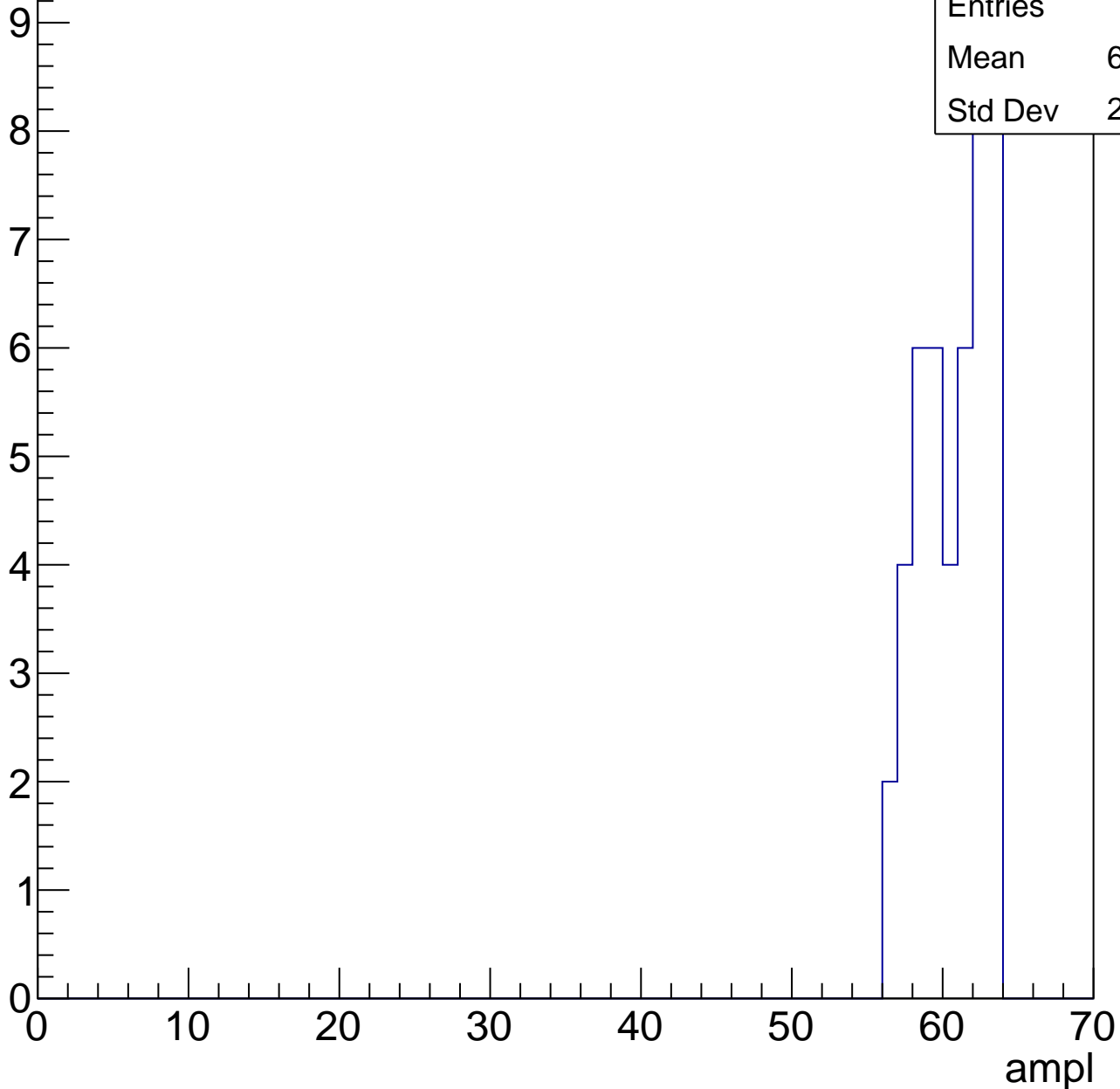


B0L001S, U7-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

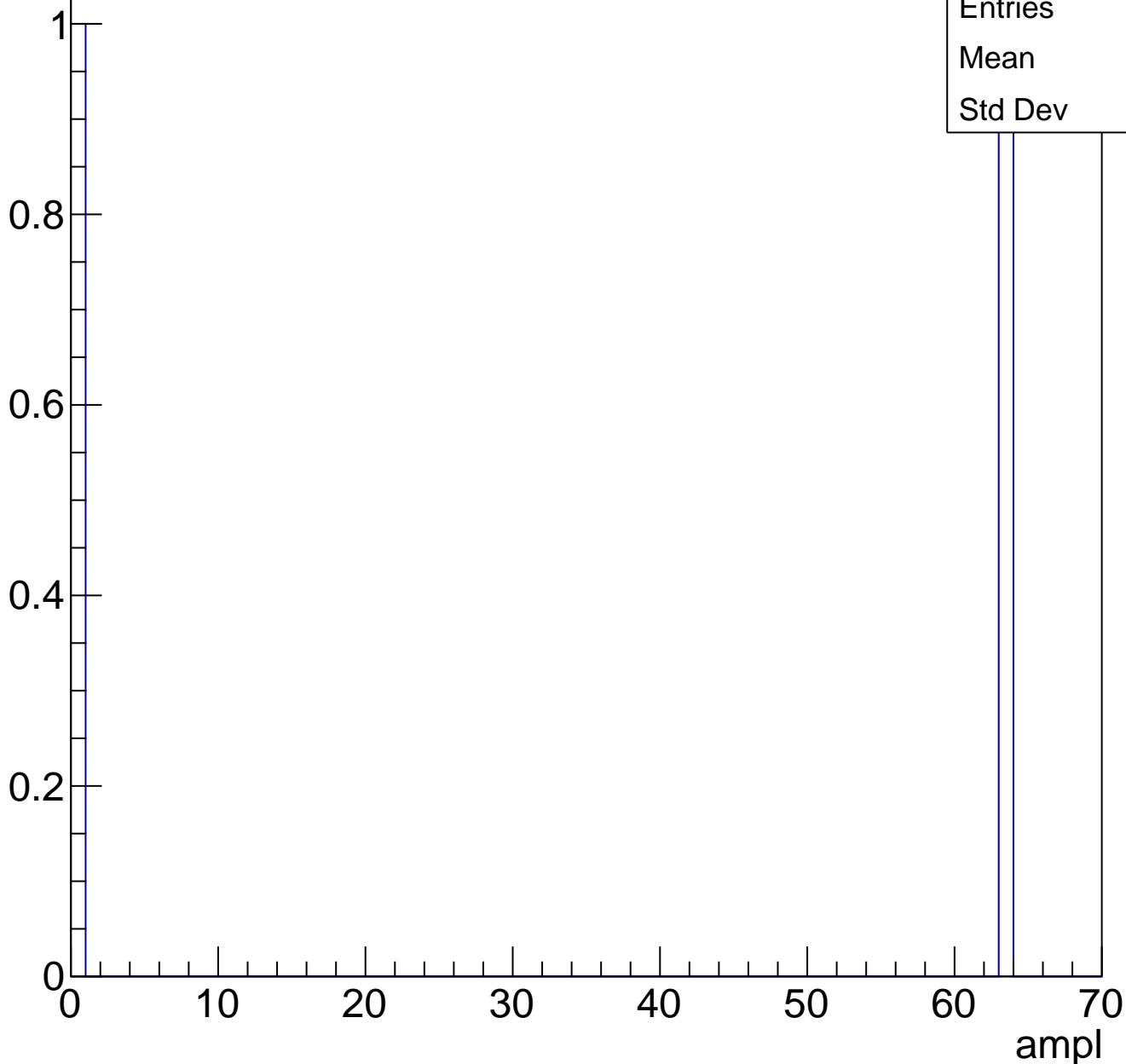
| | |
|---------|-------|
| Entries | 45 |
| Mean | 60.24 |
| Std Dev | 2.182 |



B0L001S, U7-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch79, adc0

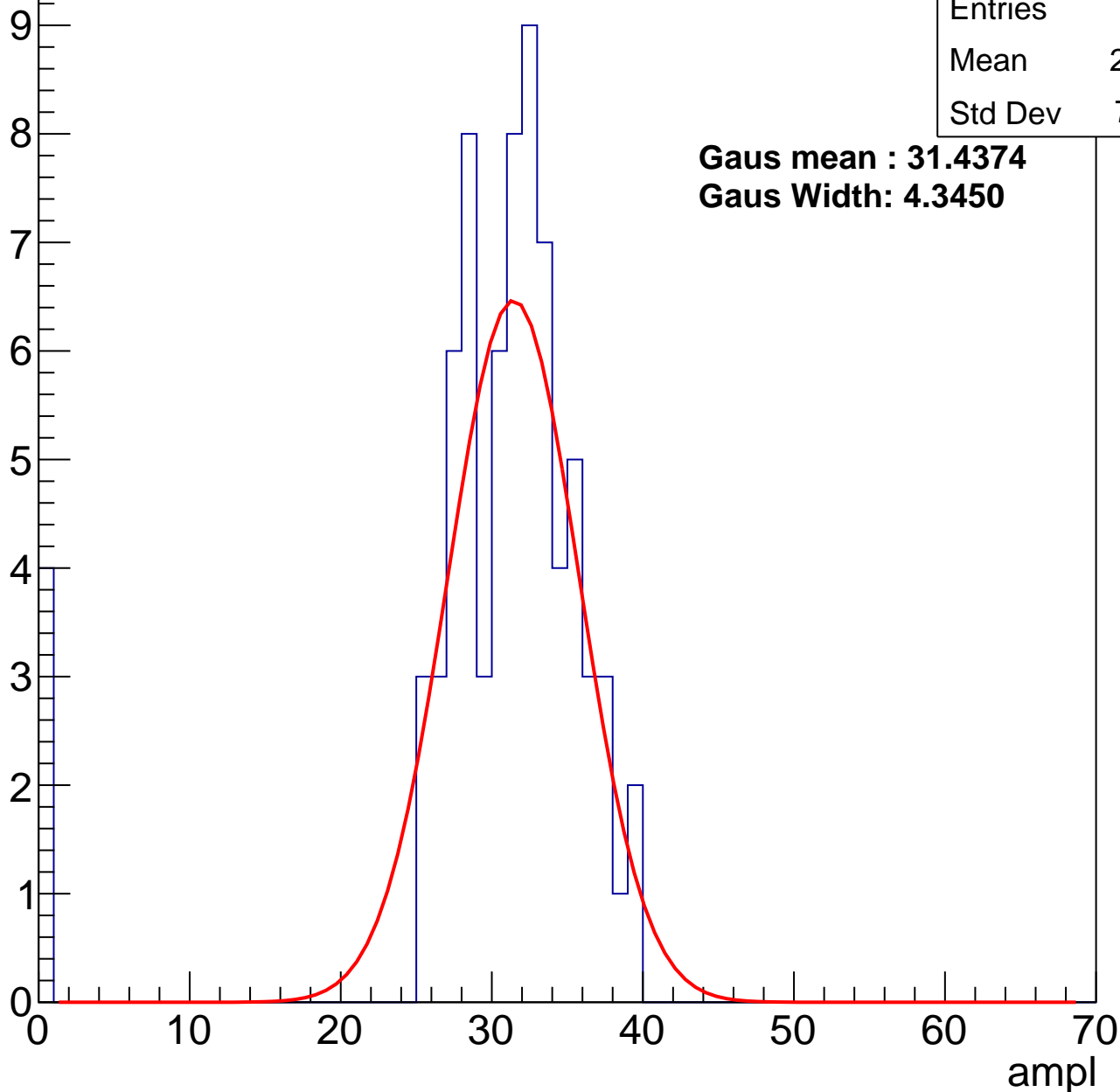
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.59 |
| Std Dev | 7.811 |

Gaus mean : 31.4374

Gaus Width: 4.3450



B0L001S, U7-ch79, adc1

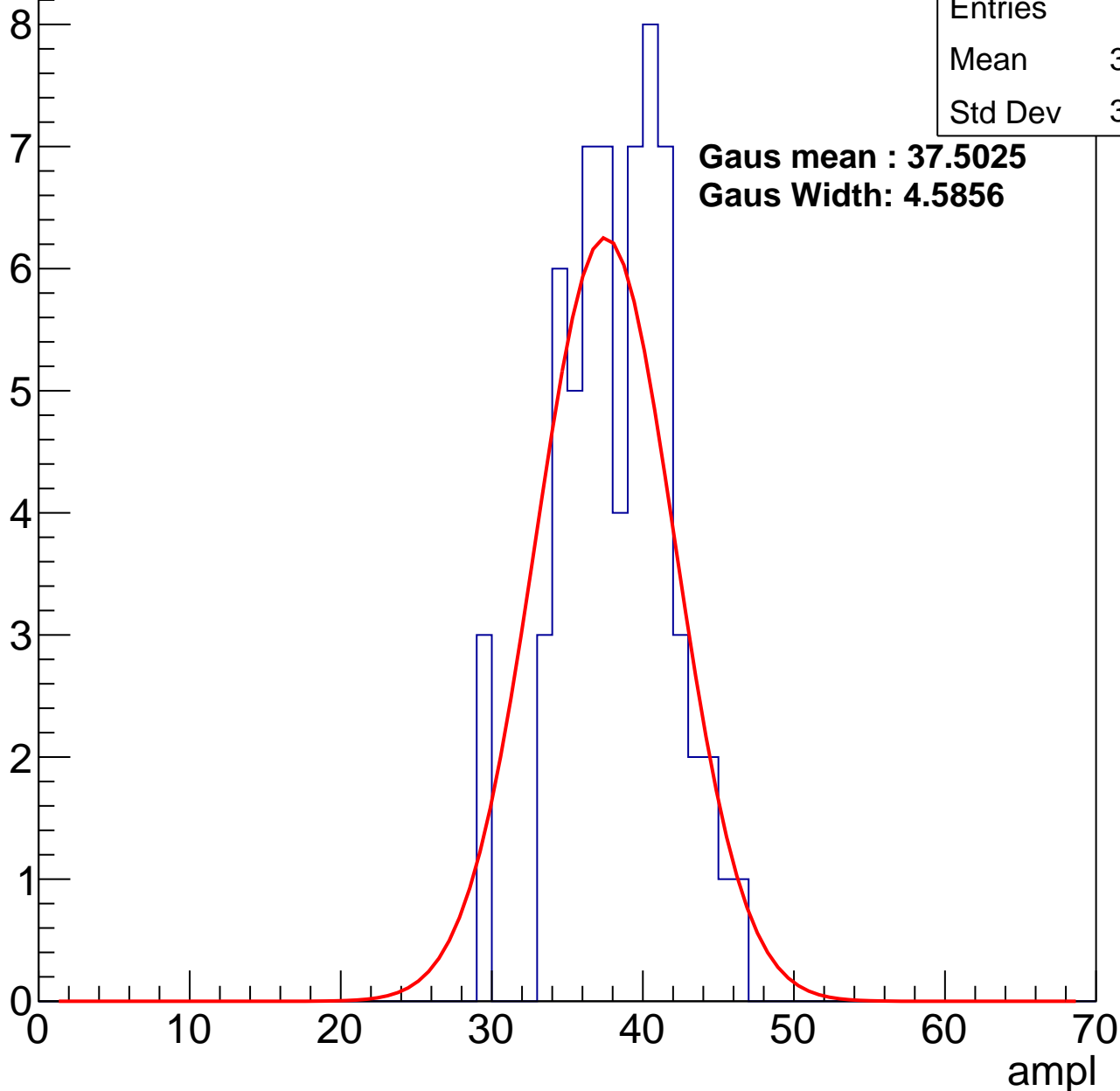
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 37.86 |
| Std Dev | 3.647 |

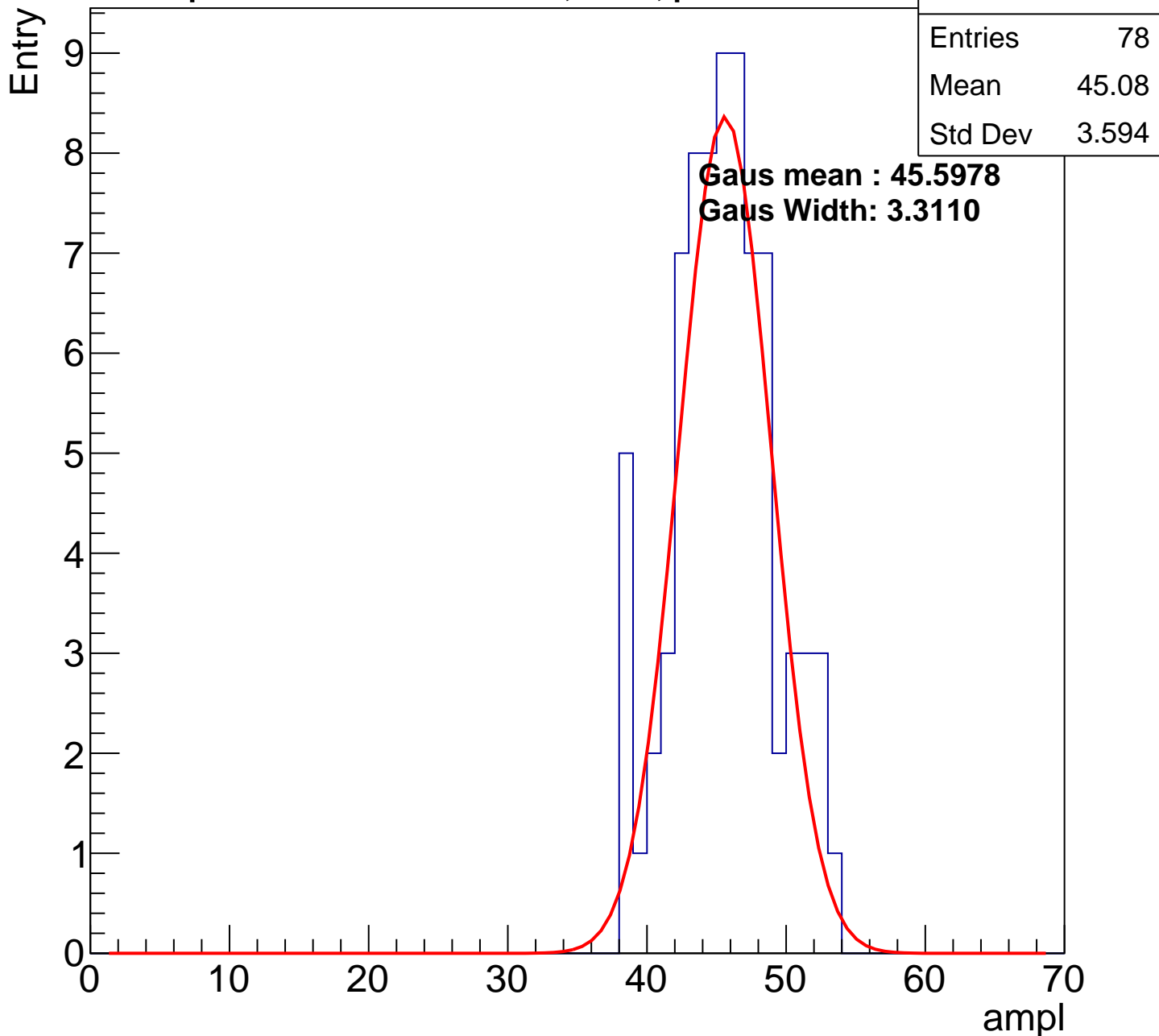
Gaus mean : 37.5025

Gaus Width: 4.5856



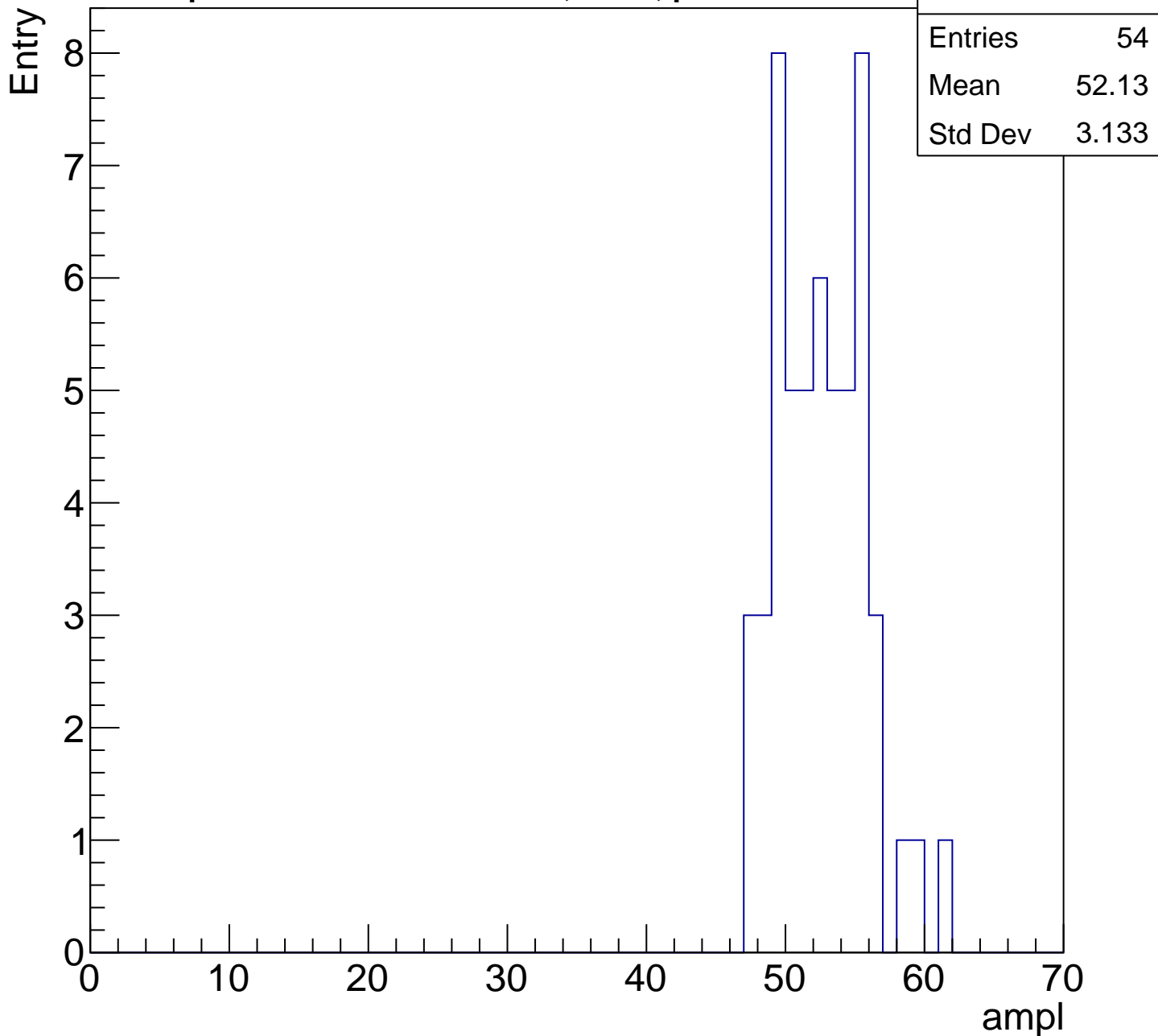
B0L001S, U7-ch79, adc2

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

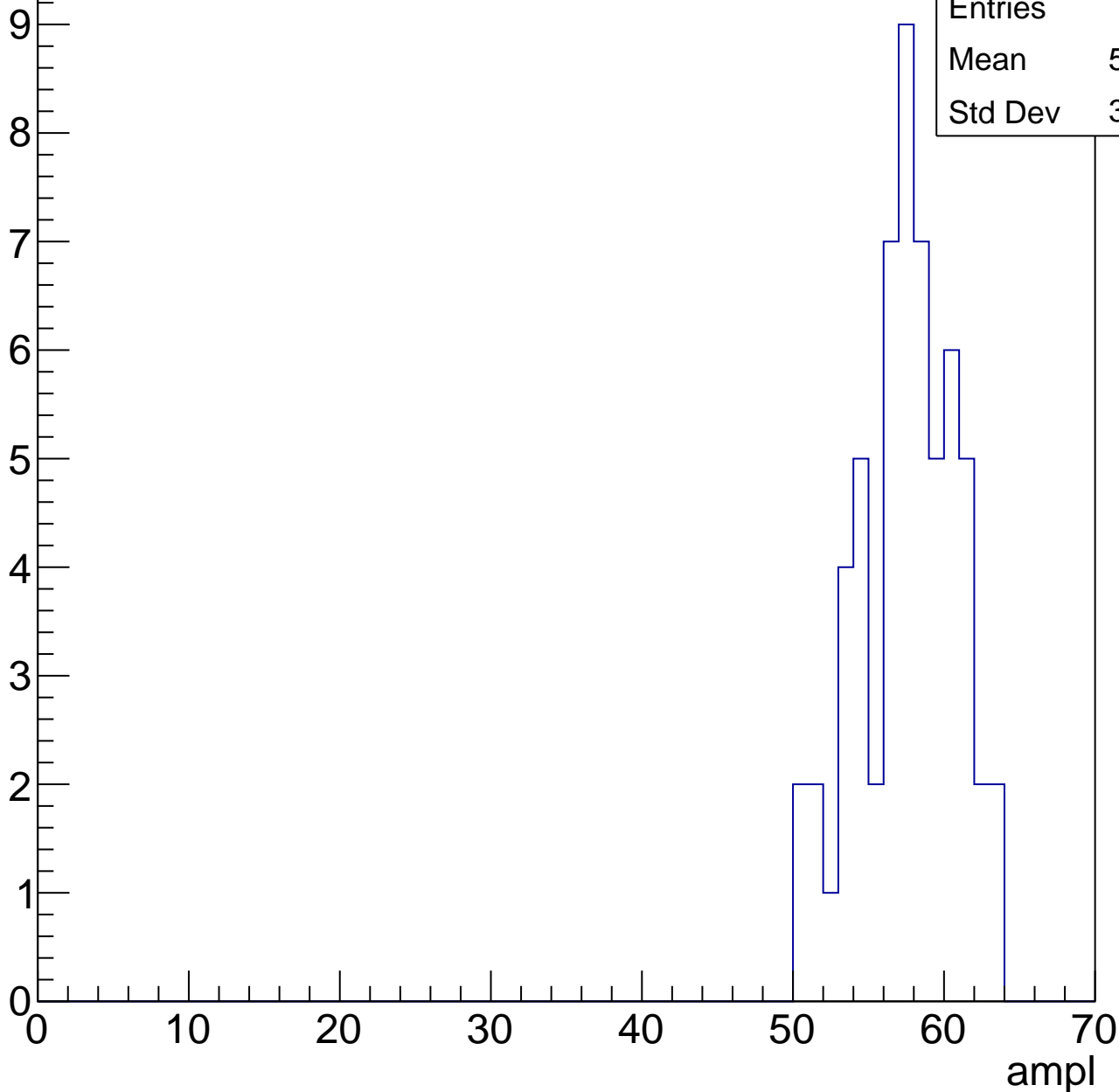


B0L001S, U7-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 57.07 |
| Std Dev | 3.194 |

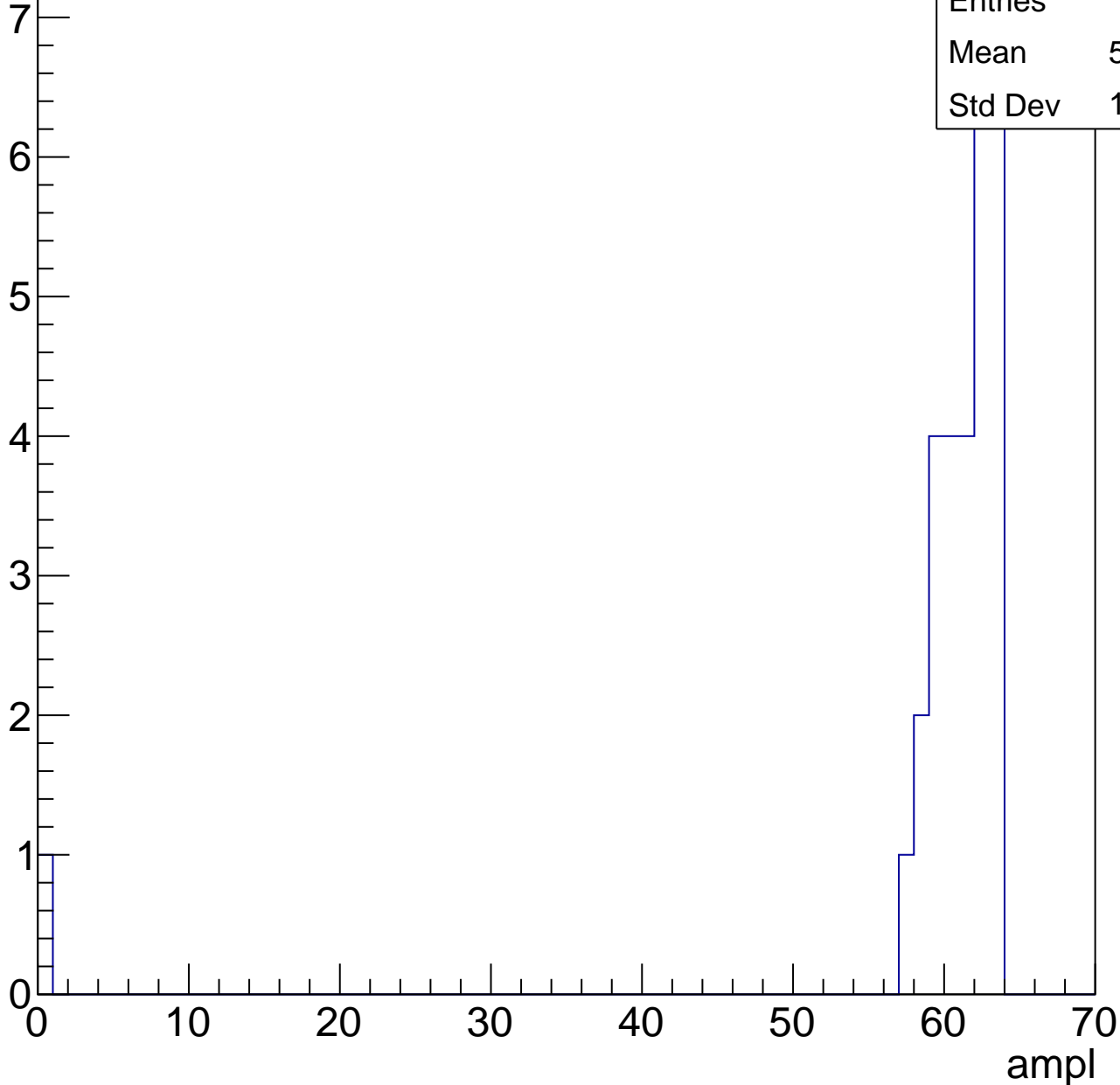


B0L001S, U7-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 30 |
| Mean | 58.93 |
| Std Dev | 11.08 |



B0L001S, U7-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch80, adc0

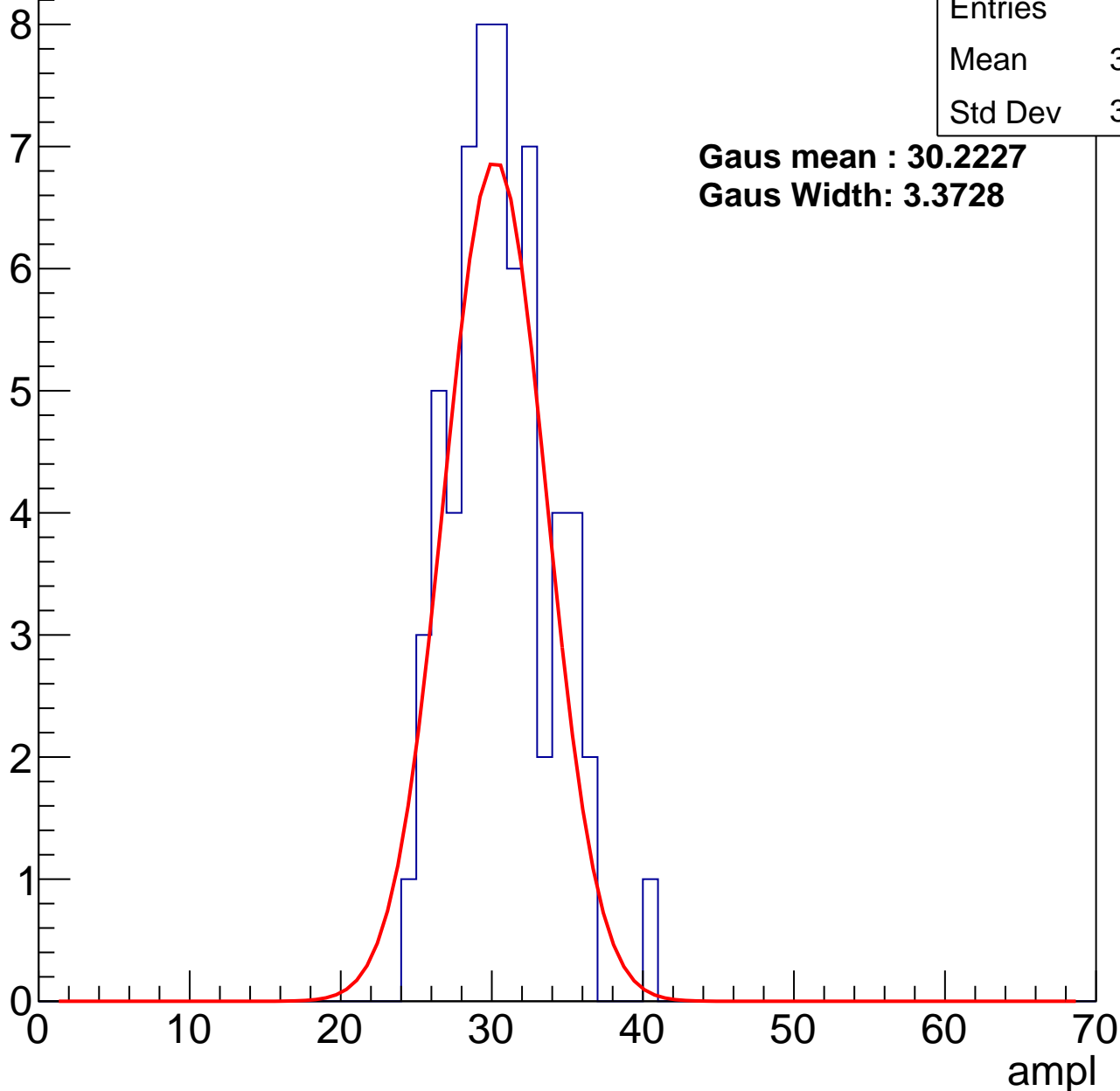
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 30.15 |
| Std Dev | 3.237 |

Gaus mean : 30.2227

Gaus Width: 3.3728



B0L001S, U7-ch80, adc1

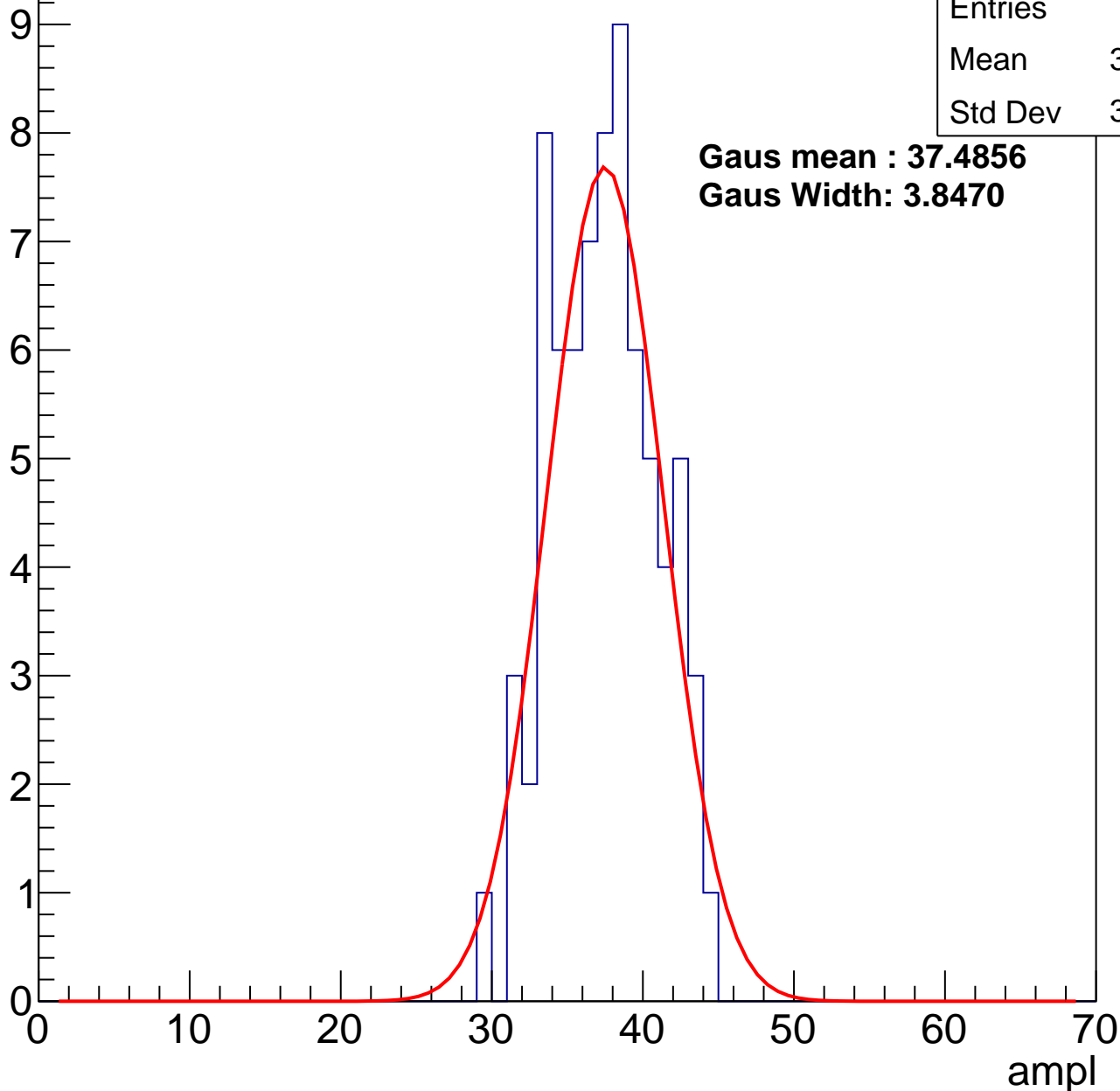
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.96 |
| Std Dev | 3.407 |

Gaus mean : 37.4856

Gaus Width: 3.8470



B0L001S, U7-ch80, adc2

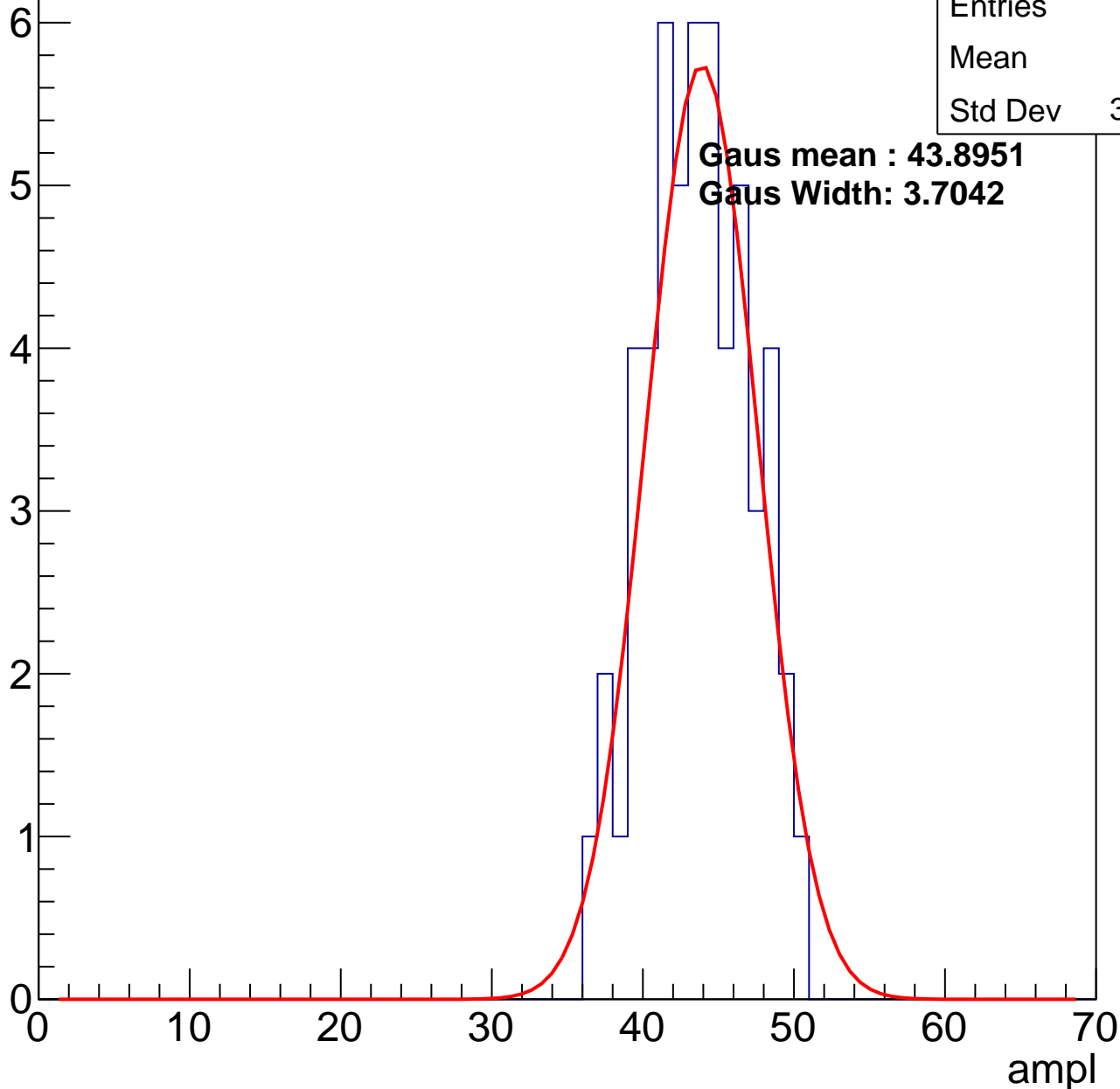
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 43.2 |
| Std Dev | 3.358 |

Gaus mean : 43.8951

Gaus Width: 3.7042

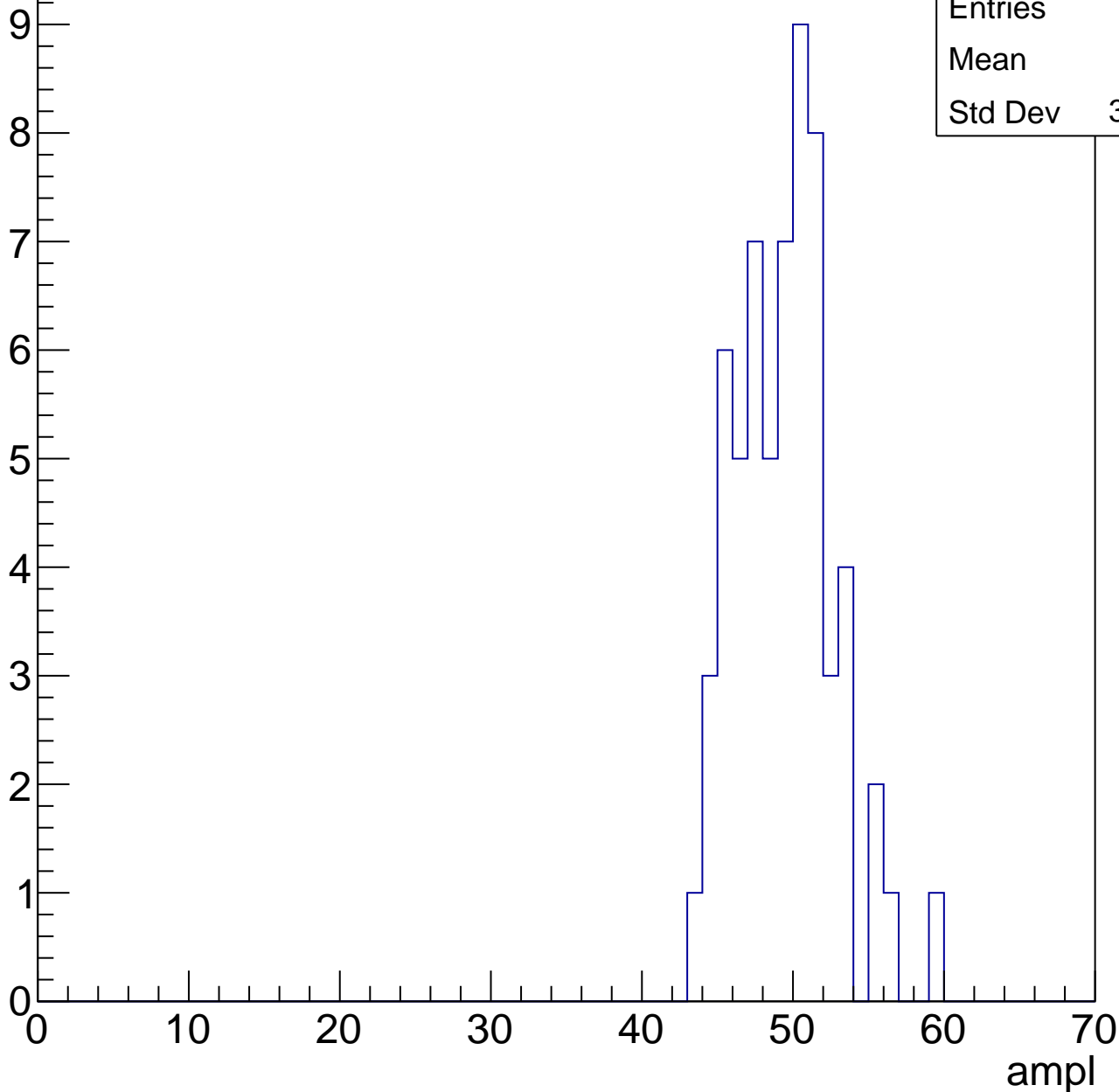


B0L001S, U7-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49 |
| Std Dev | 3.208 |

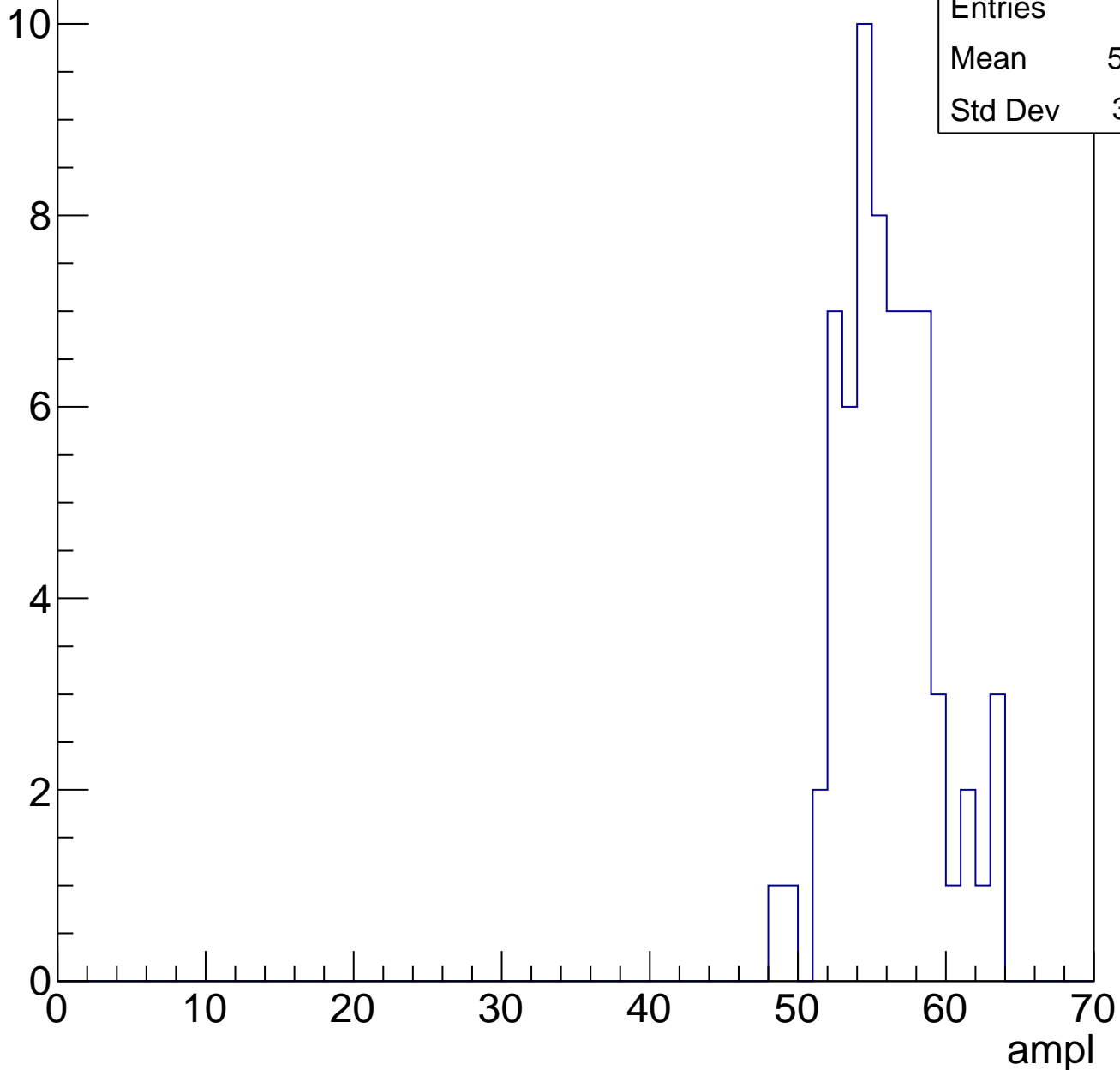


B0L001S, U7-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 66 |
| Mean | 55.58 |
| Std Dev | 3.201 |

Entry



B0L001S, U7-ch80, adc5

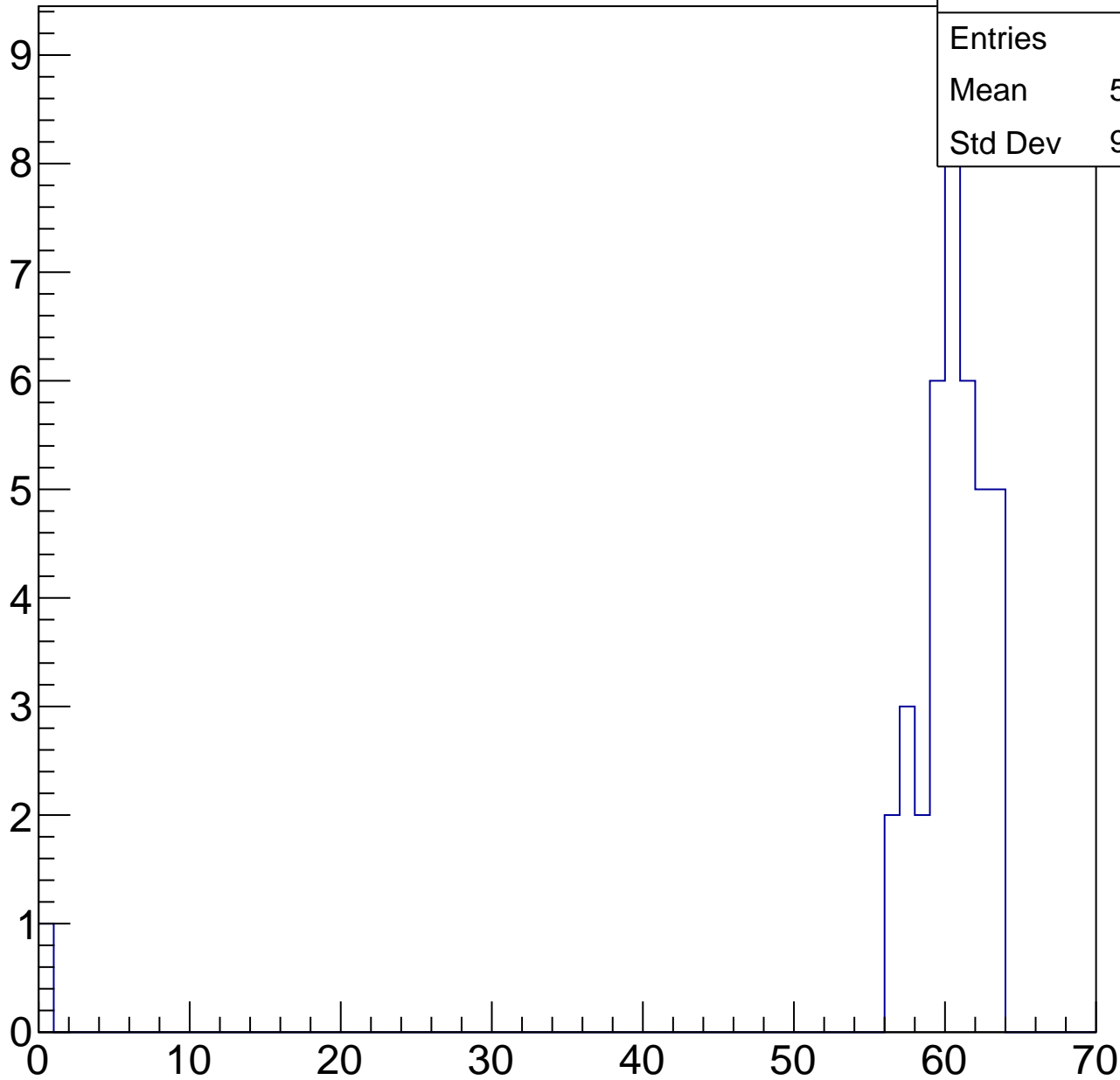
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.56 |
| Std Dev | 9.692 |

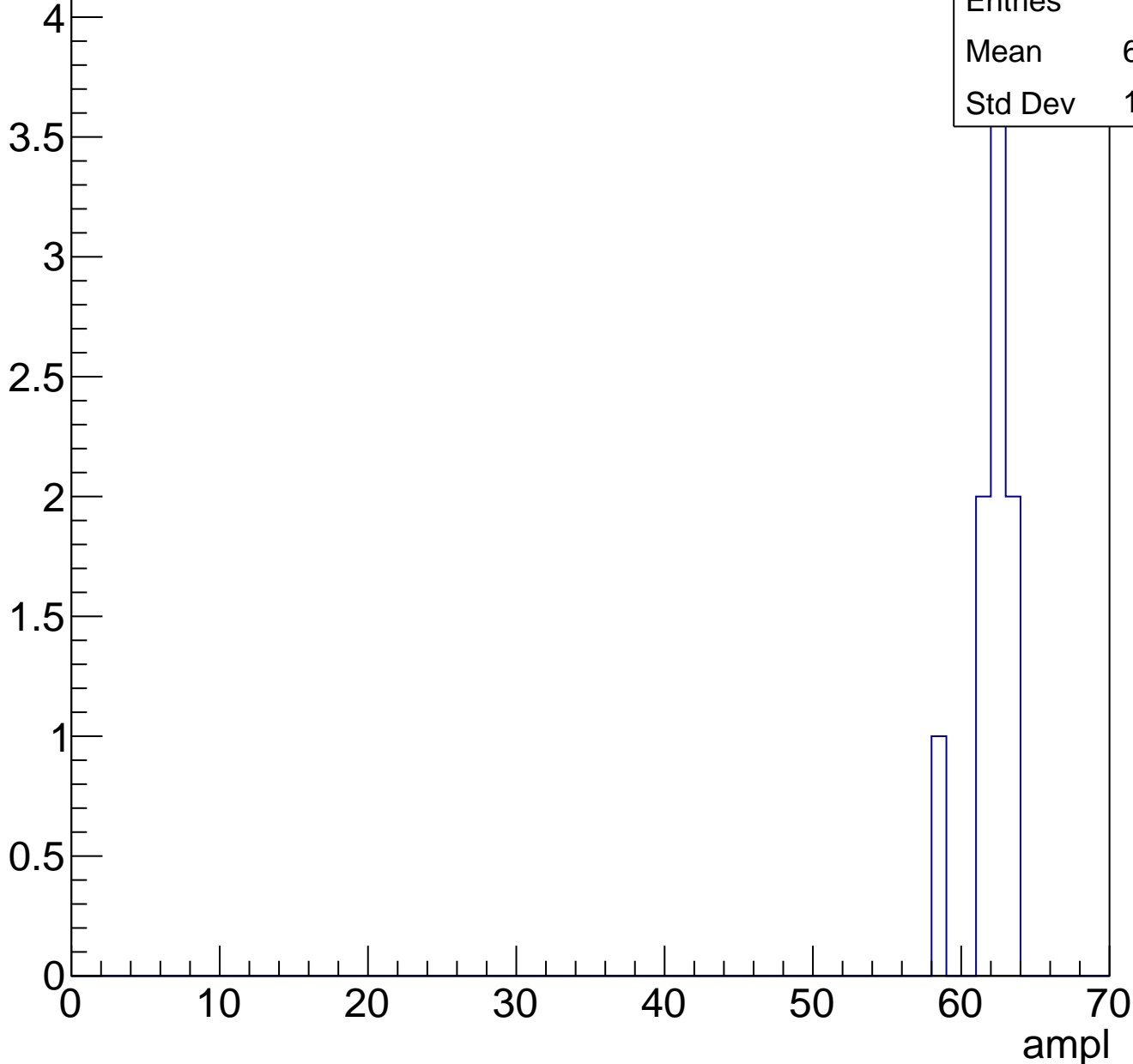
ampl



B0L001S, U7-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch81, adc0

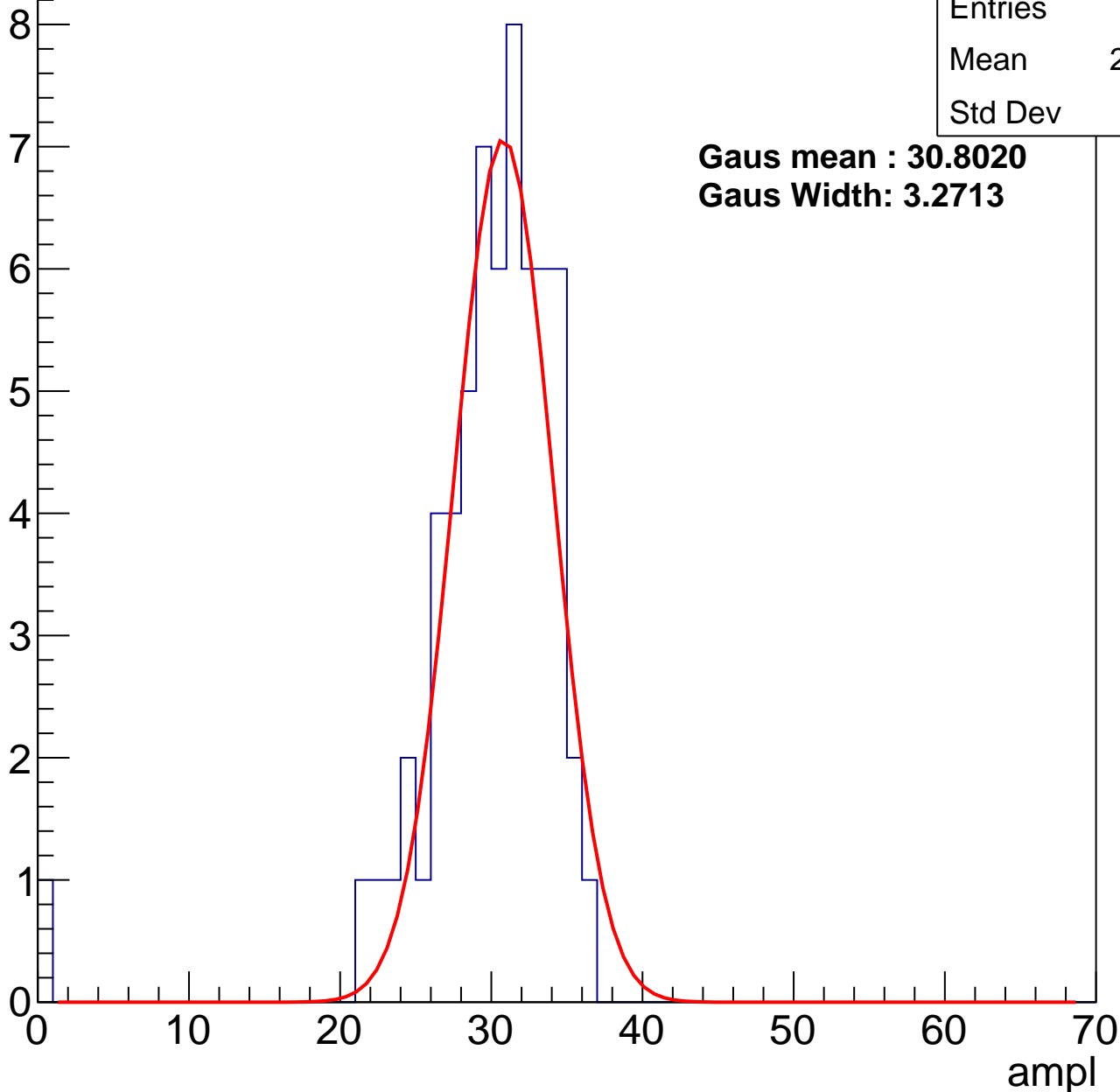
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 29.39 |
| Std Dev | 5.02 |

Gaus mean : 30.8020

Gaus Width: 3.2713



B0L001S, U7-ch81, adc1

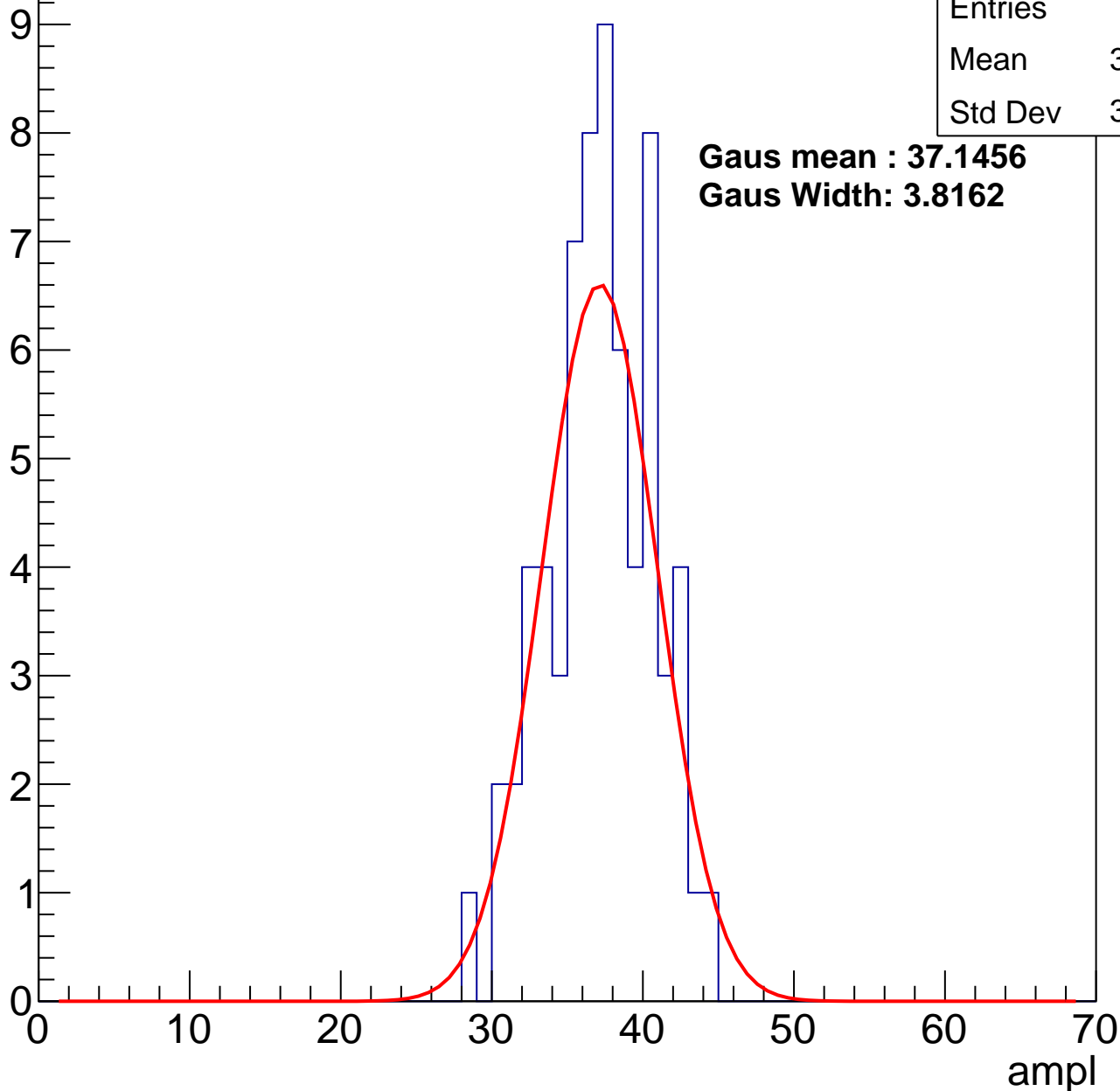
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 36.72 |
| Std Dev | 3.455 |

Gaus mean : 37.1456

Gaus Width: 3.8162



B0L001S, U7-ch81, adc2

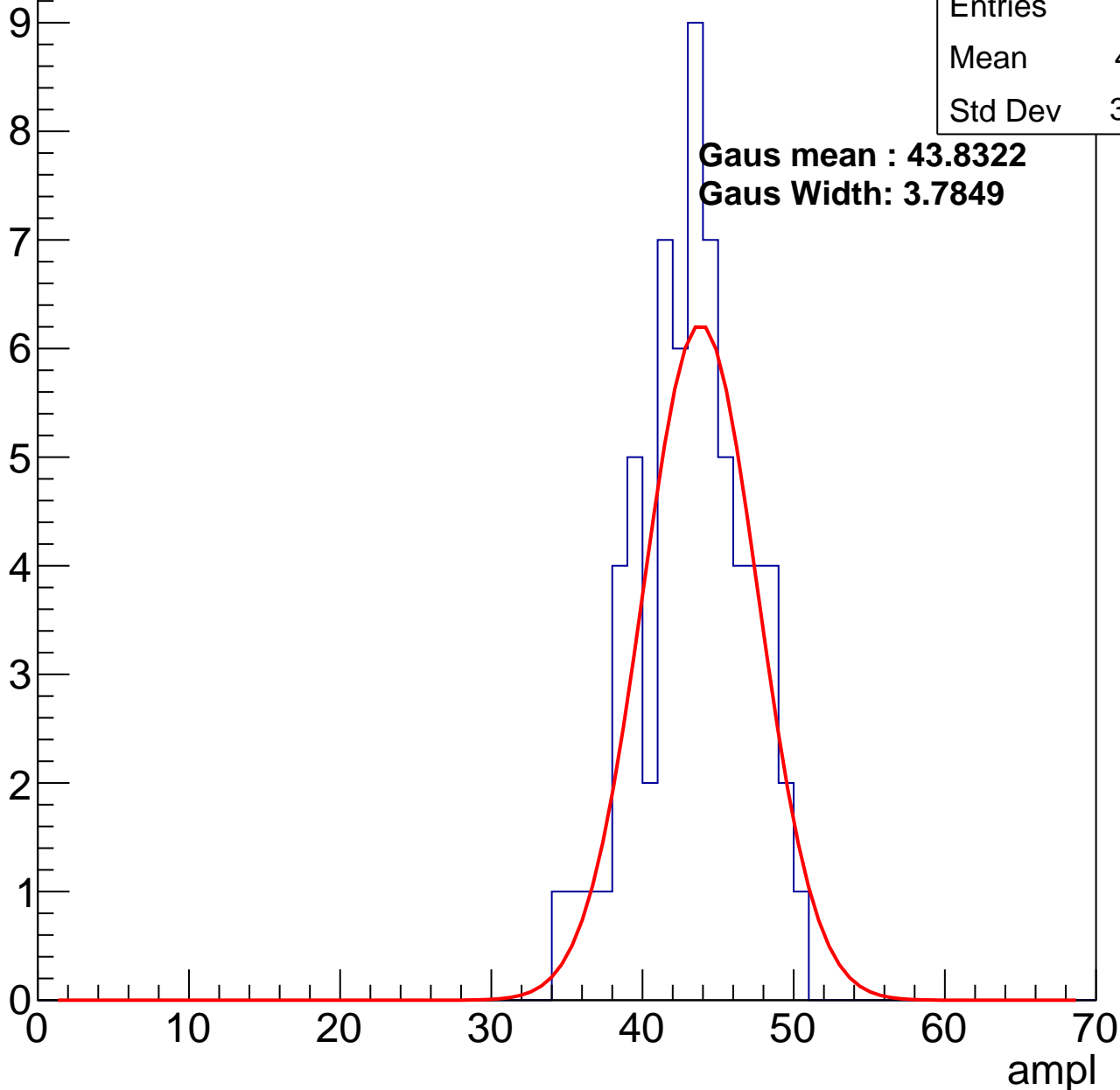
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.81 |
| Std Dev | 3.553 |

Gaus mean : 43.8322

Gaus Width: 3.7849



B0L001S, U7-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

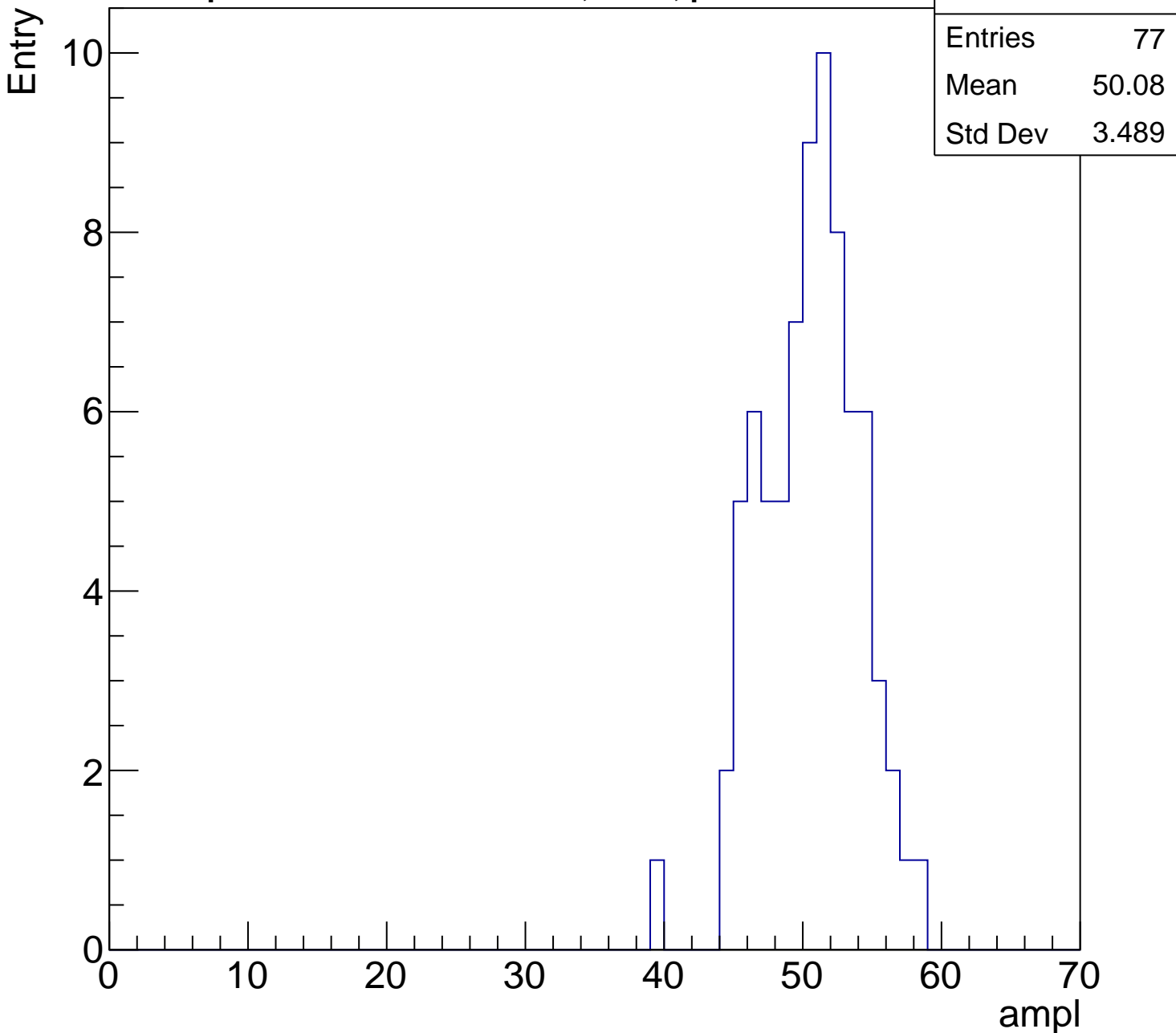
| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.08 |
| Std Dev | 3.489 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

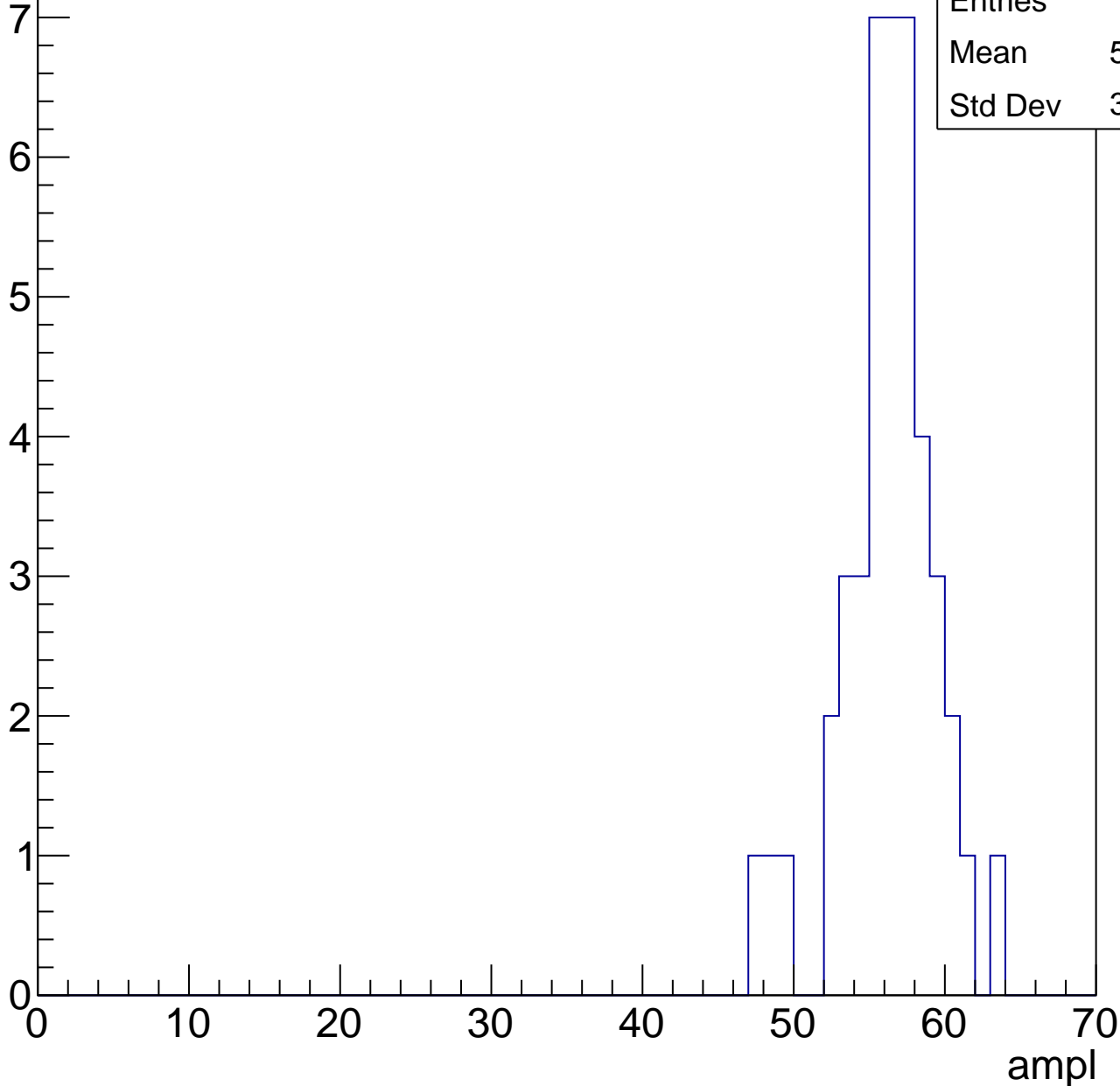


B0L001S, U7-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 55.77 |
| Std Dev | 3.146 |

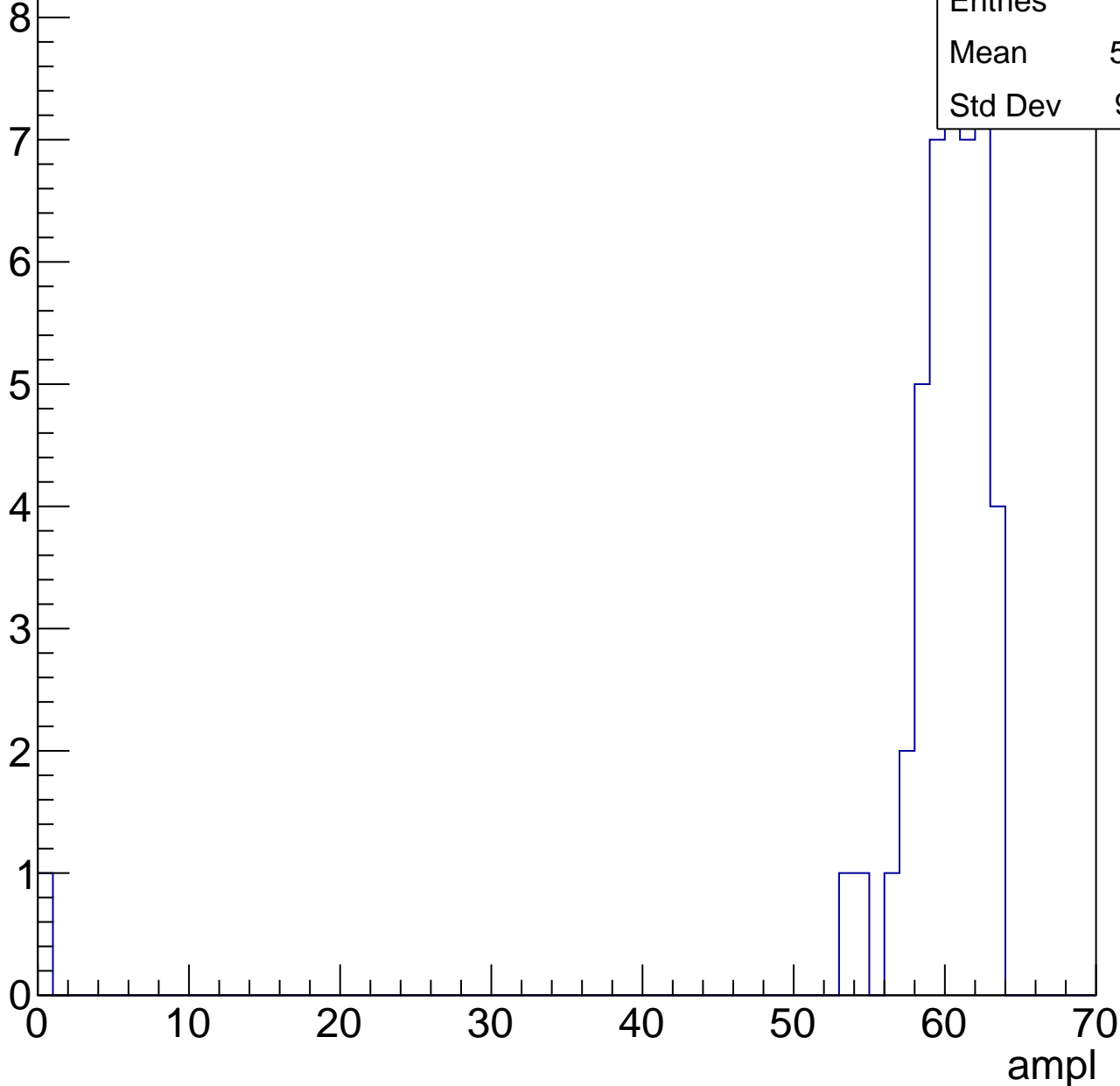


B0L001S, U7-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.56 |
| Std Dev | 9.101 |

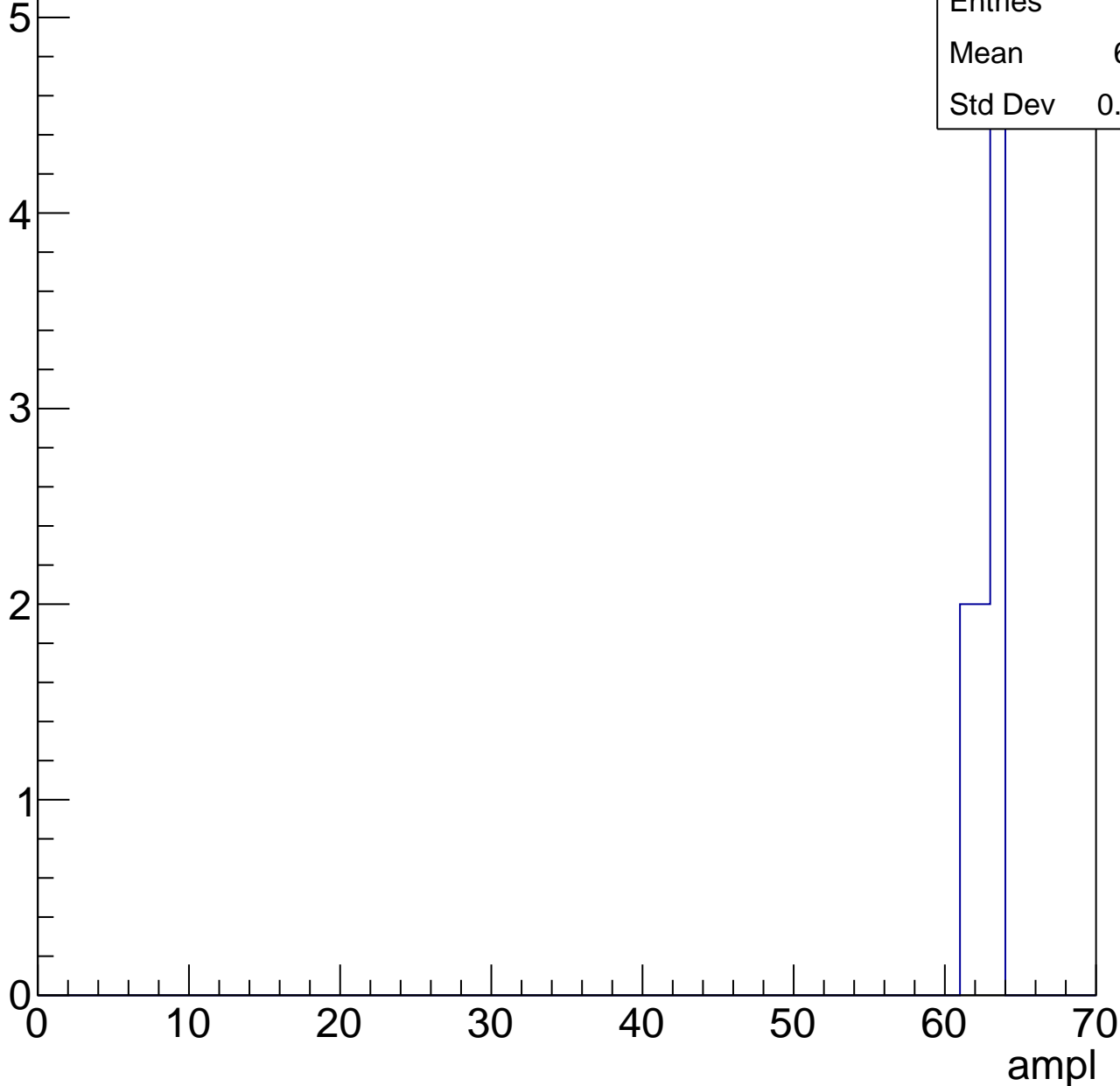


B0L001S, U7-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|--------|
| Entries | 9 |
| Mean | 62.33 |
| Std Dev | 0.8165 |



B0L001S, U7-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch82, adc0

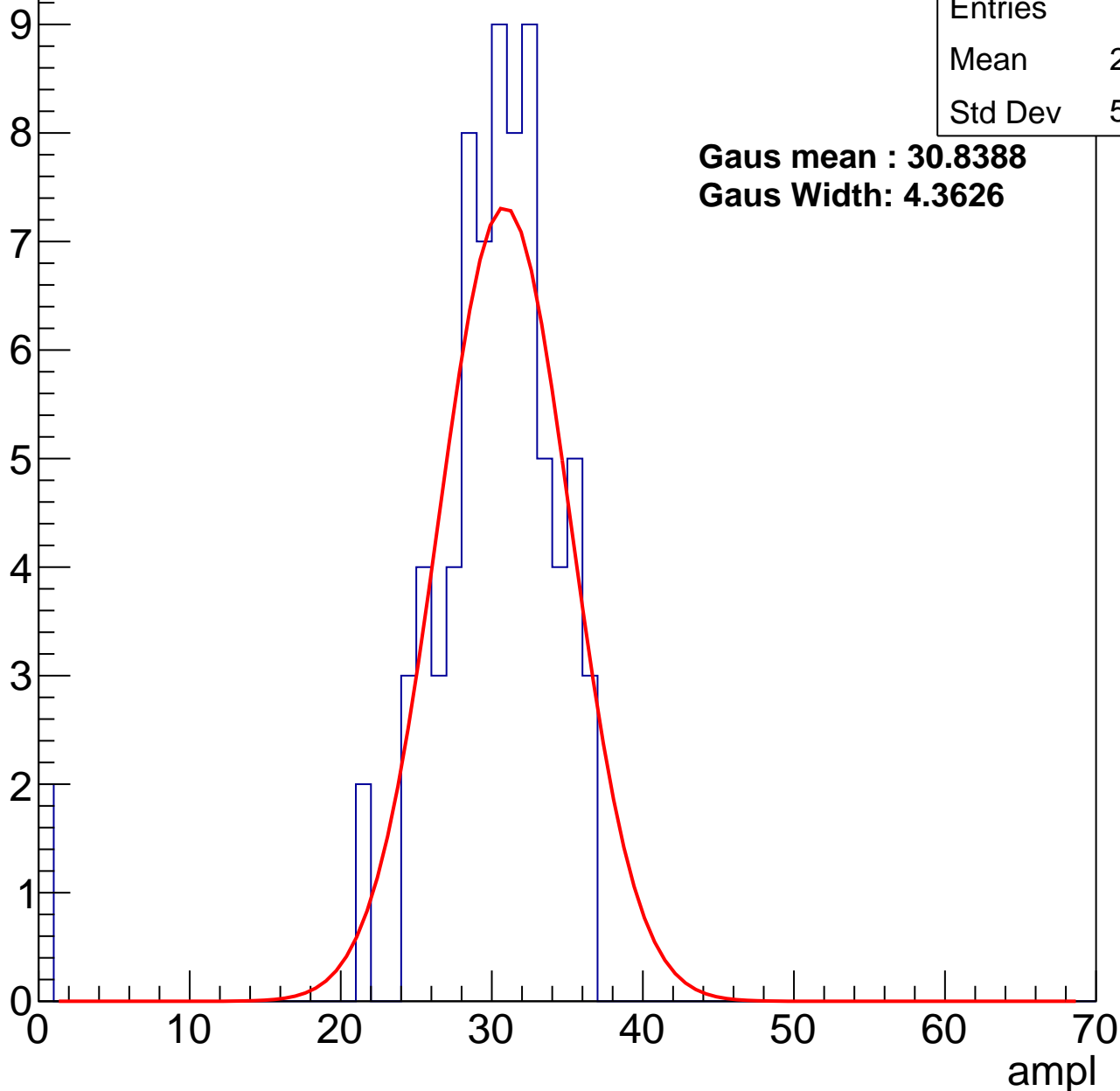
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.17 |
| Std Dev | 5.879 |

Gaus mean : 30.8388

Gaus Width: 4.3626



B0L001S, U7-ch82, adc1

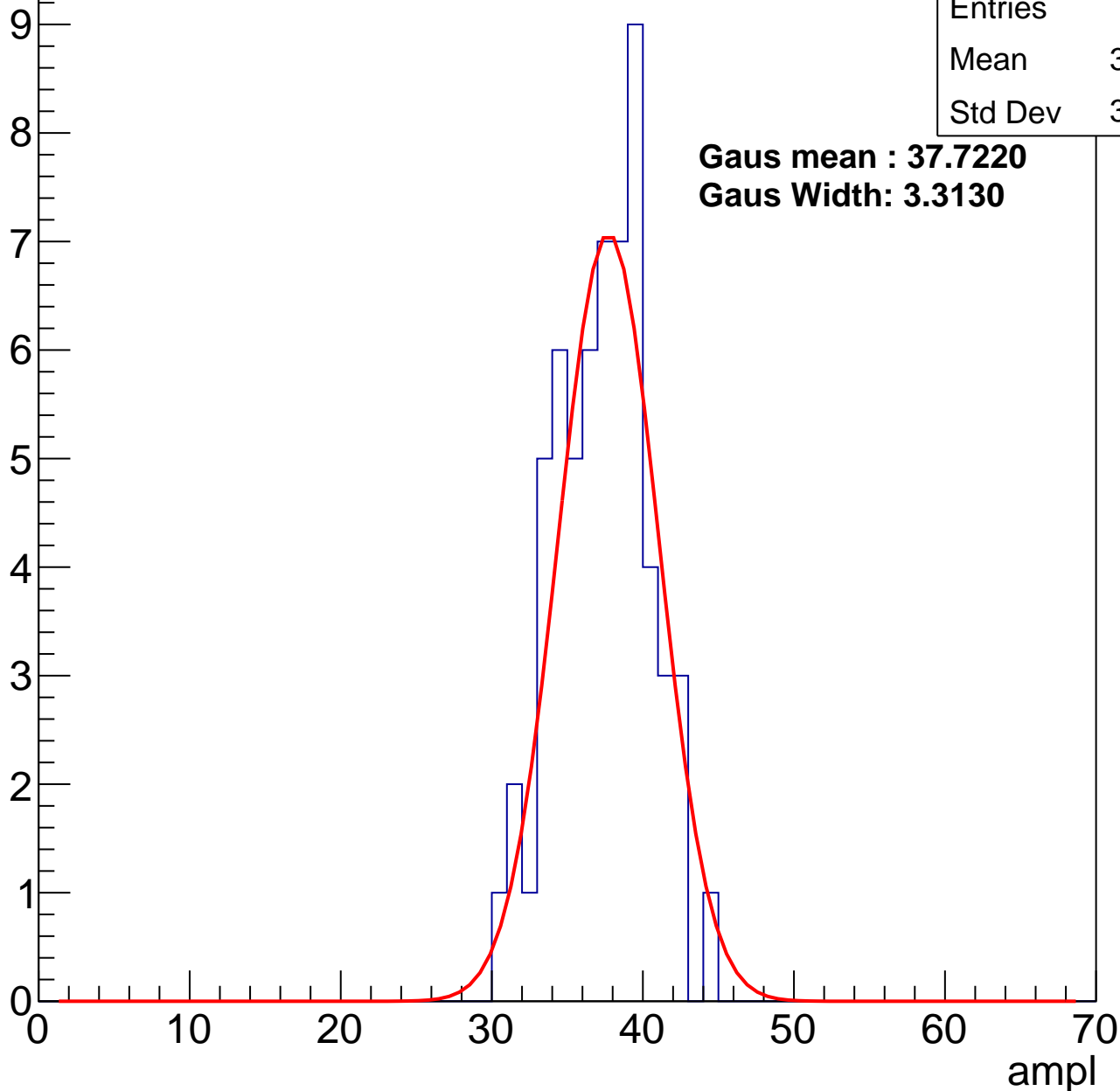
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 36.88 |
| Std Dev | 3.045 |

Gaus mean : 37.7220

Gaus Width: 3.3130

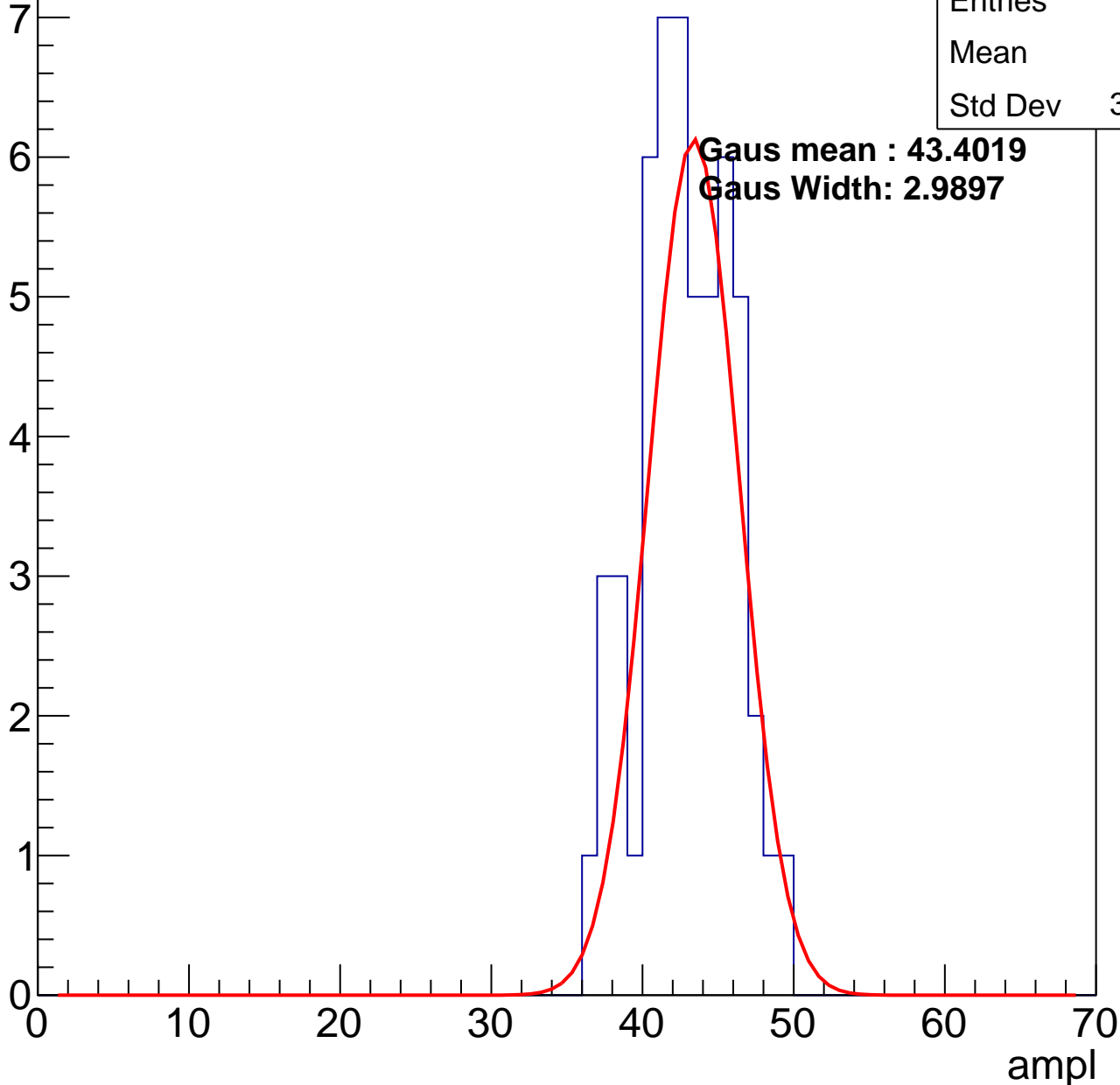


B0L001S, U7-ch82, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

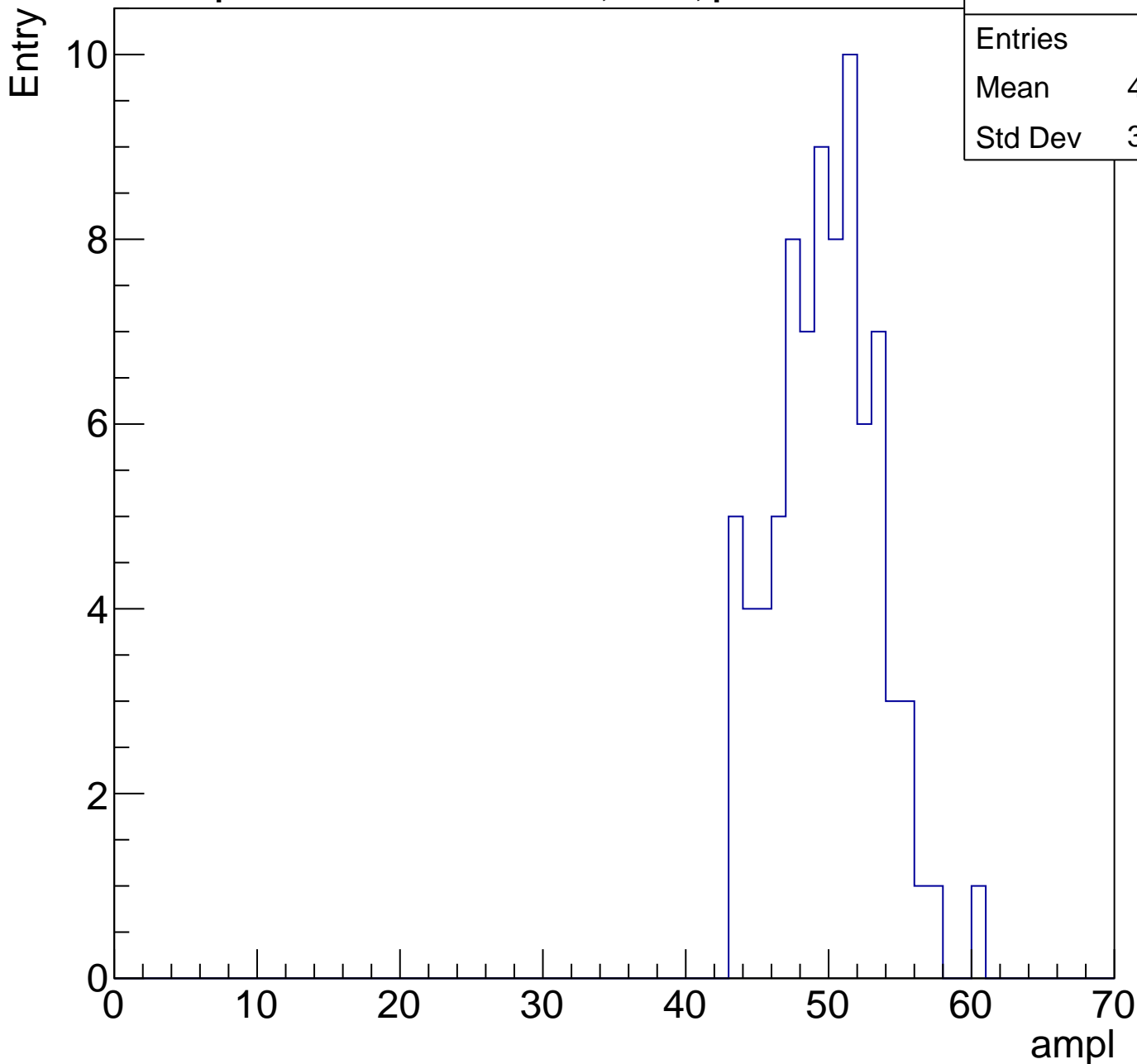
| | |
|---------|-------|
| Entries | 53 |
| Mean | 42.4 |
| Std Dev | 3.024 |



B0L001S, U7-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

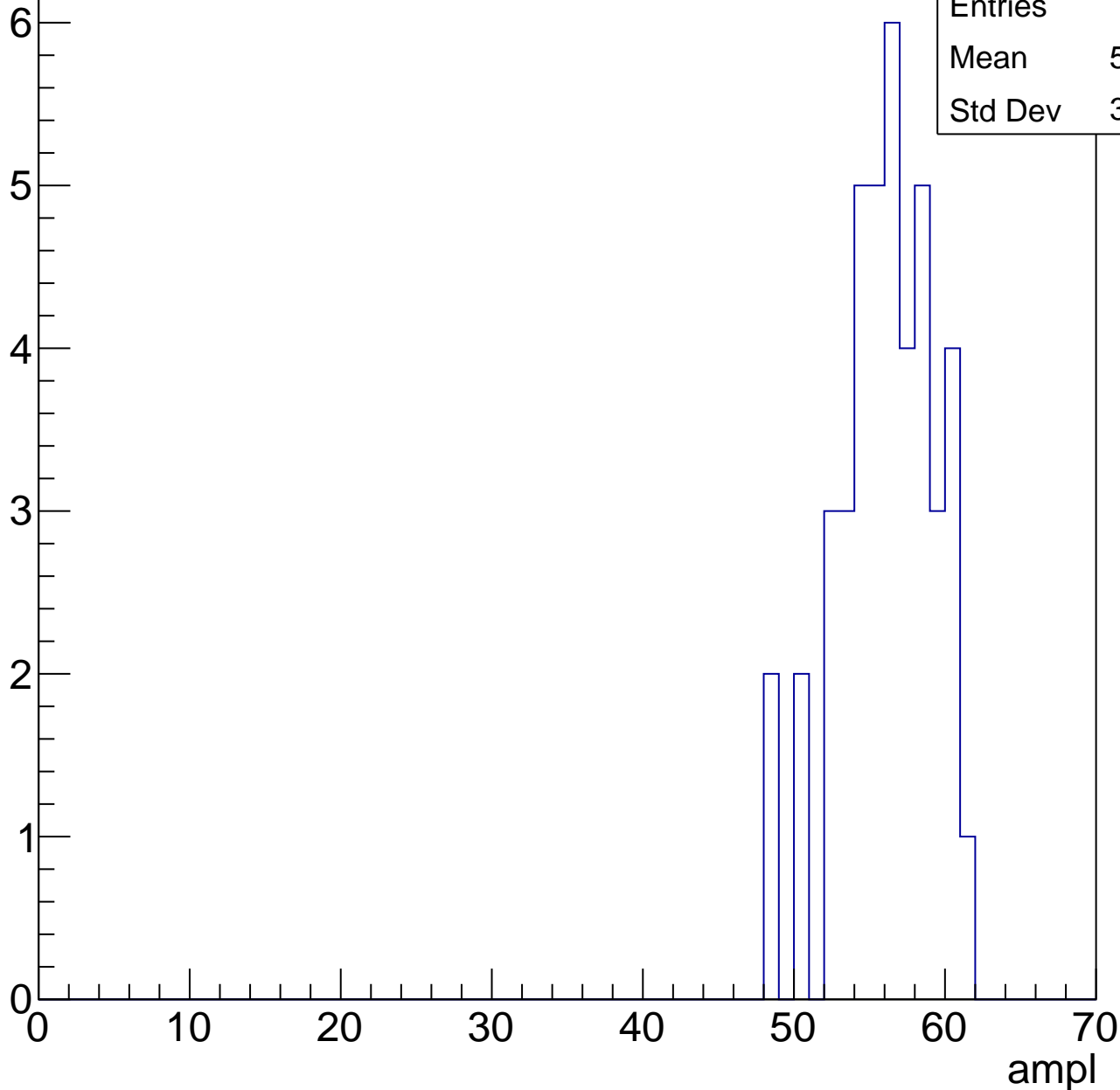
| | |
|---------|-------|
| Entries | 82 |
| Mean | 49.35 |
| Std Dev | 3.576 |



B0L001S, U7-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



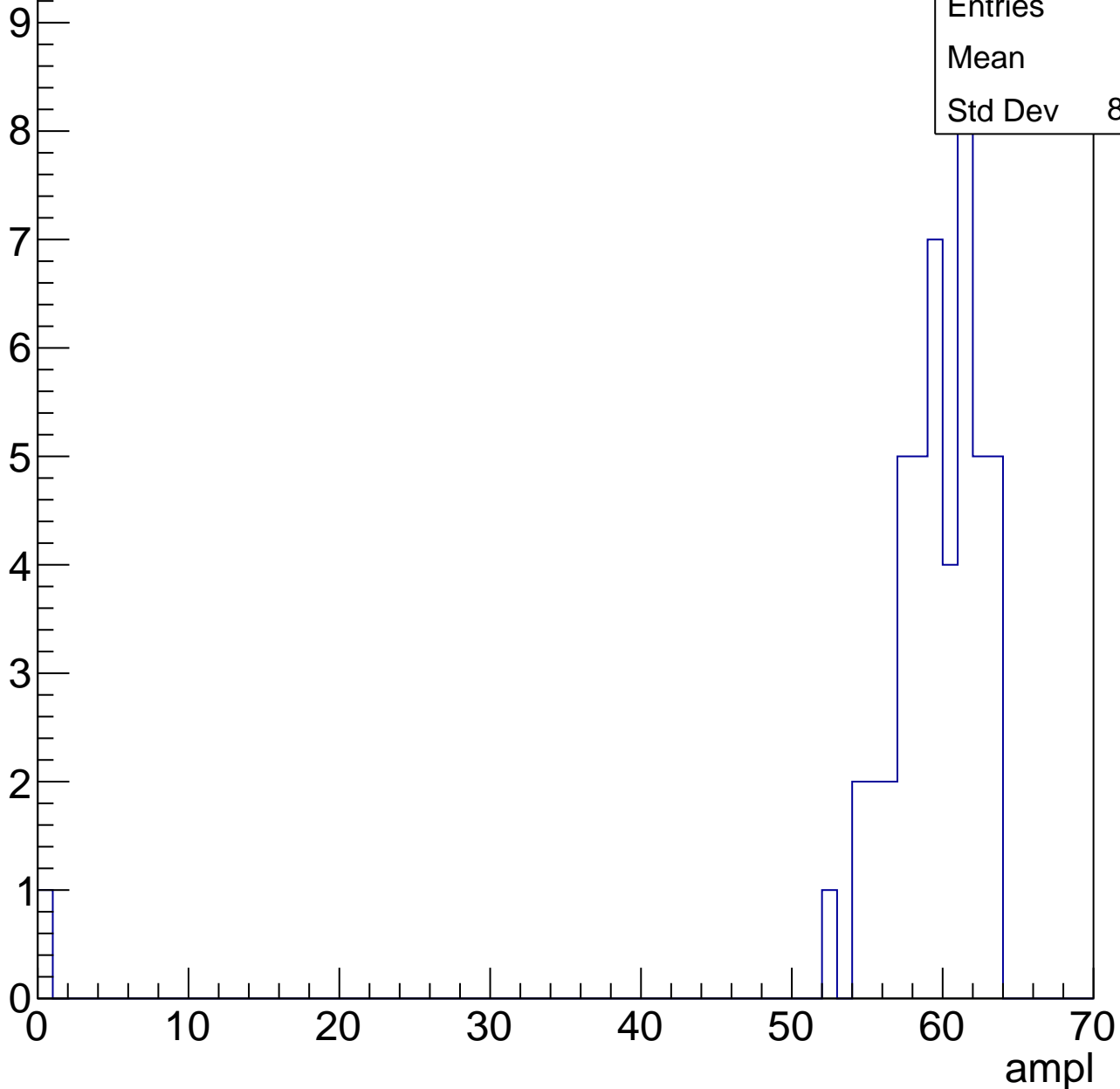
| | |
|---------|-------|
| Entries | 43 |
| Mean | 55.53 |
| Std Dev | 3.165 |

B0L001S, U7-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58 |
| Std Dev | 8.865 |

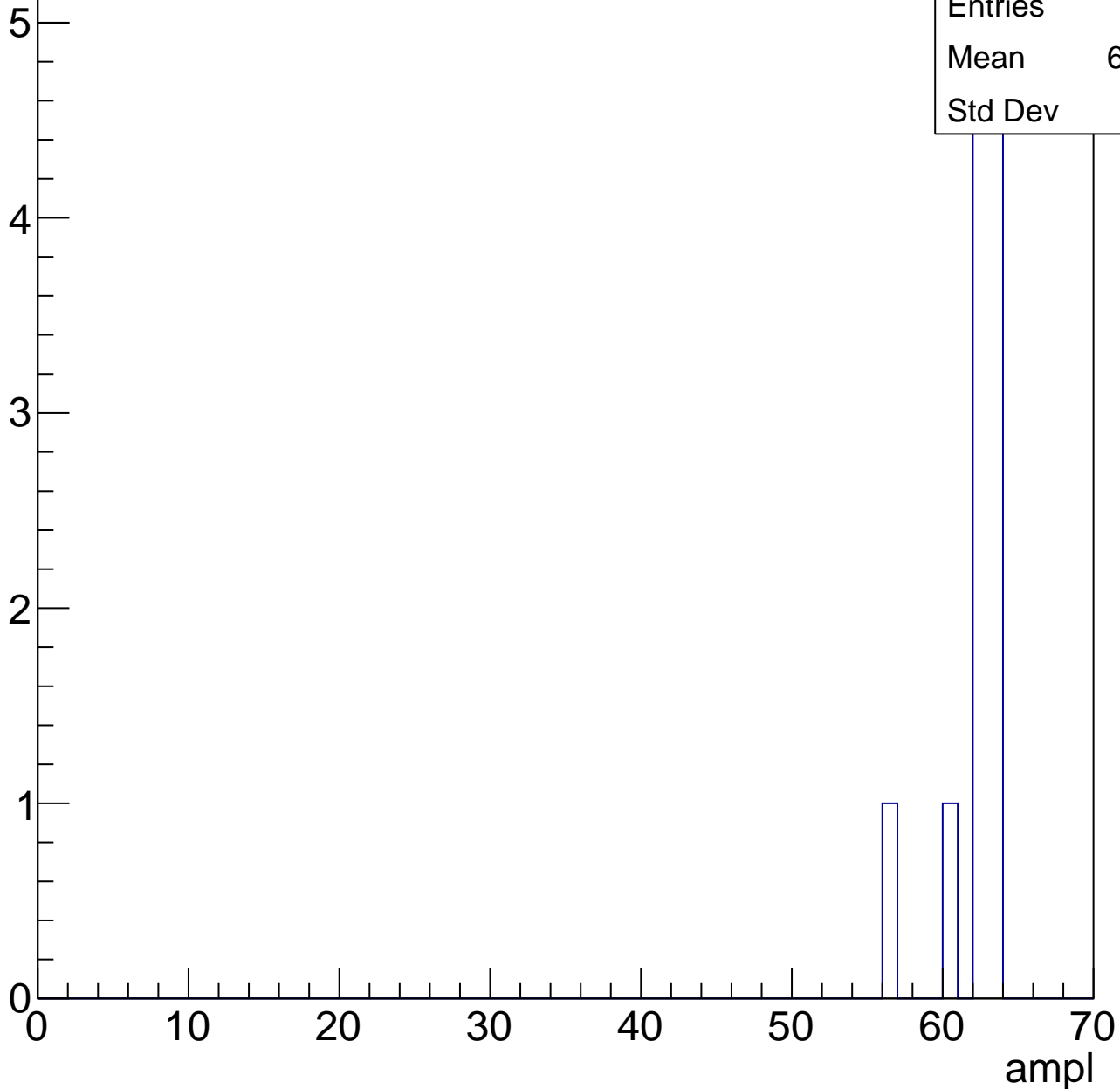


B0L001S, U7-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 61.75 |
| Std Dev | 1.92 |



B0L001S, U7-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch83, adc0

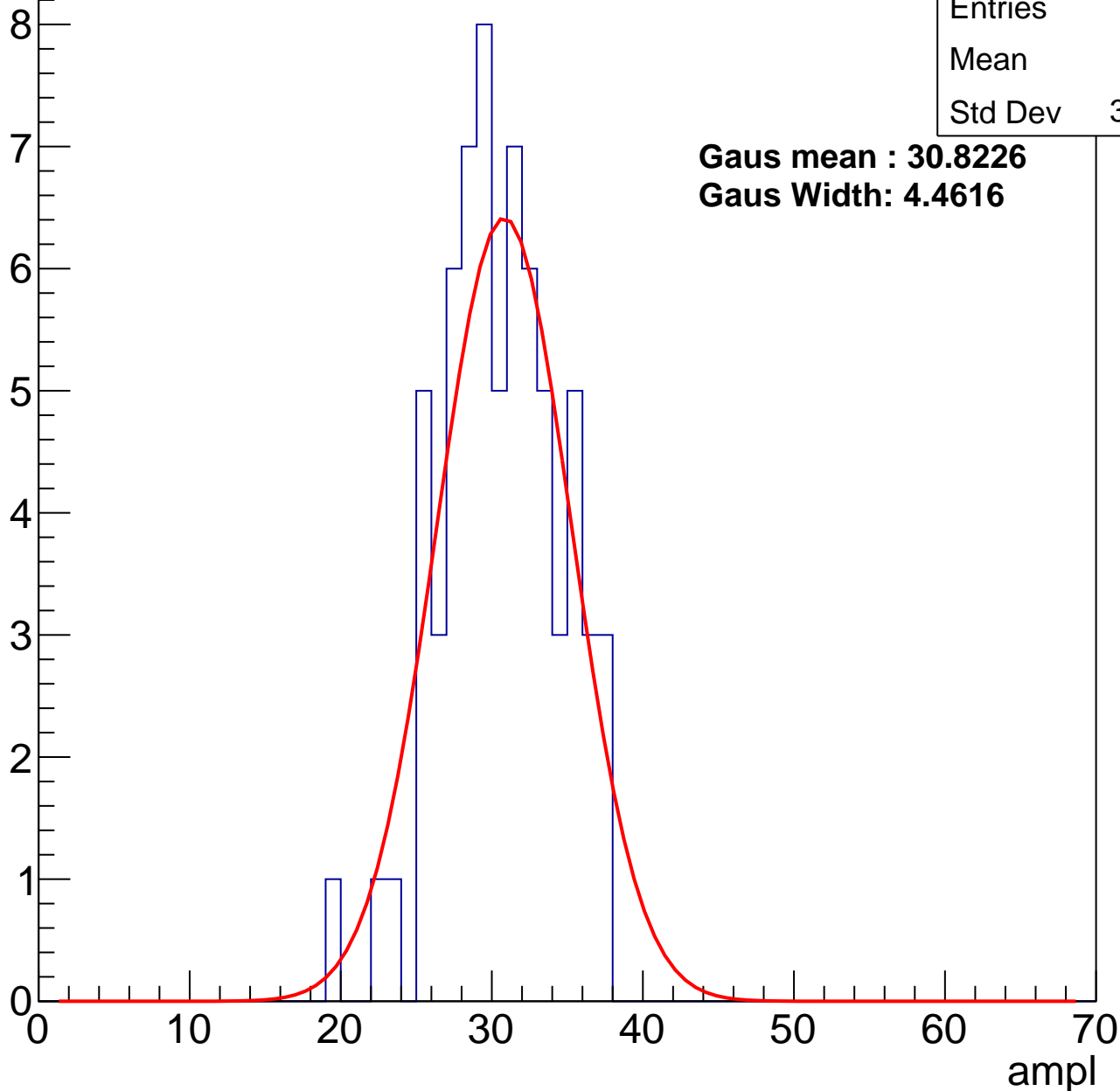
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 30.1 |
| Std Dev | 3.807 |

Gaus mean : 30.8226

Gaus Width: 4.4616



B0L001S, U7-ch83, adc1

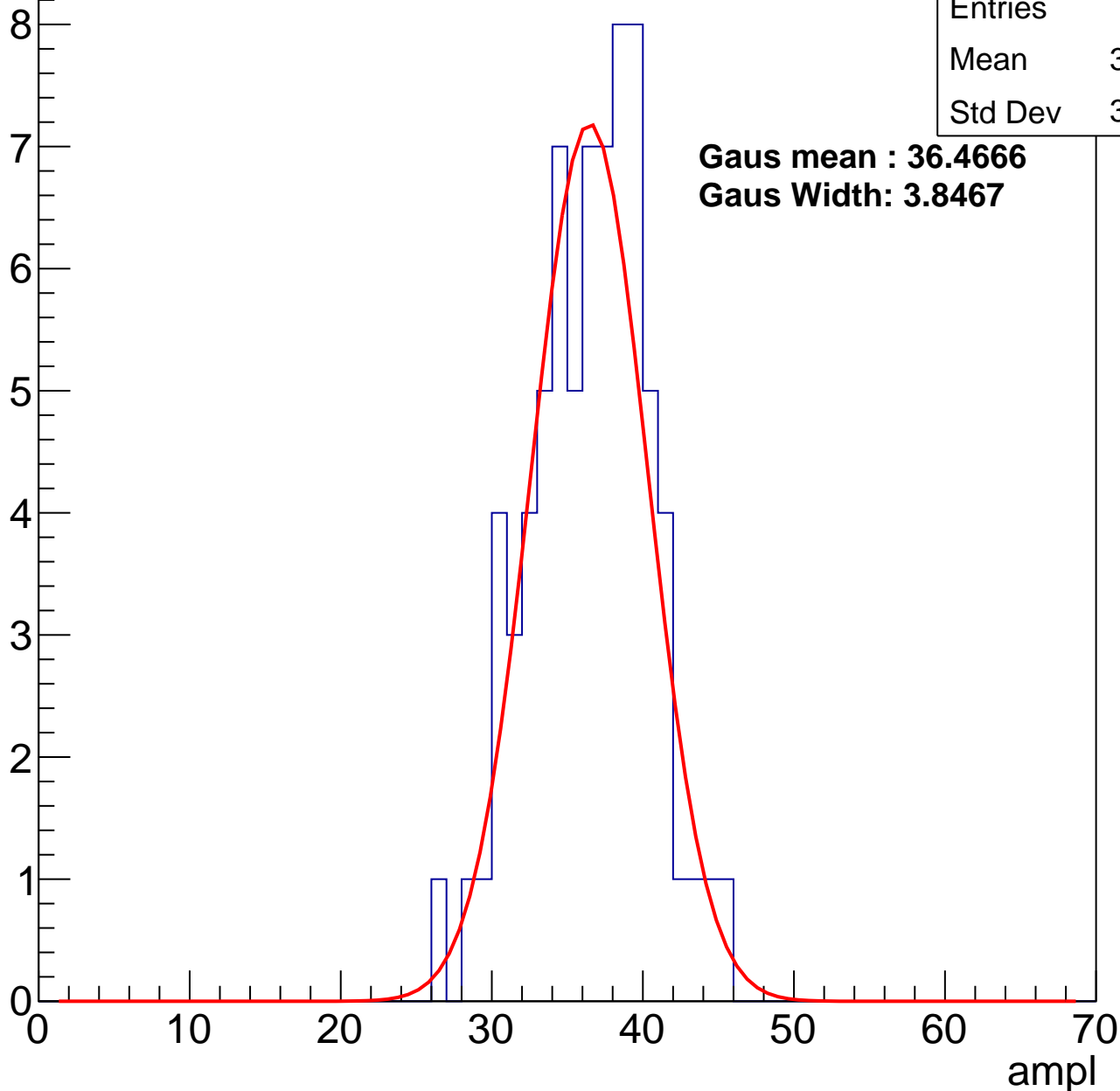
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.04 |
| Std Dev | 3.847 |

Gaus mean : 36.4666

Gaus Width: 3.8467

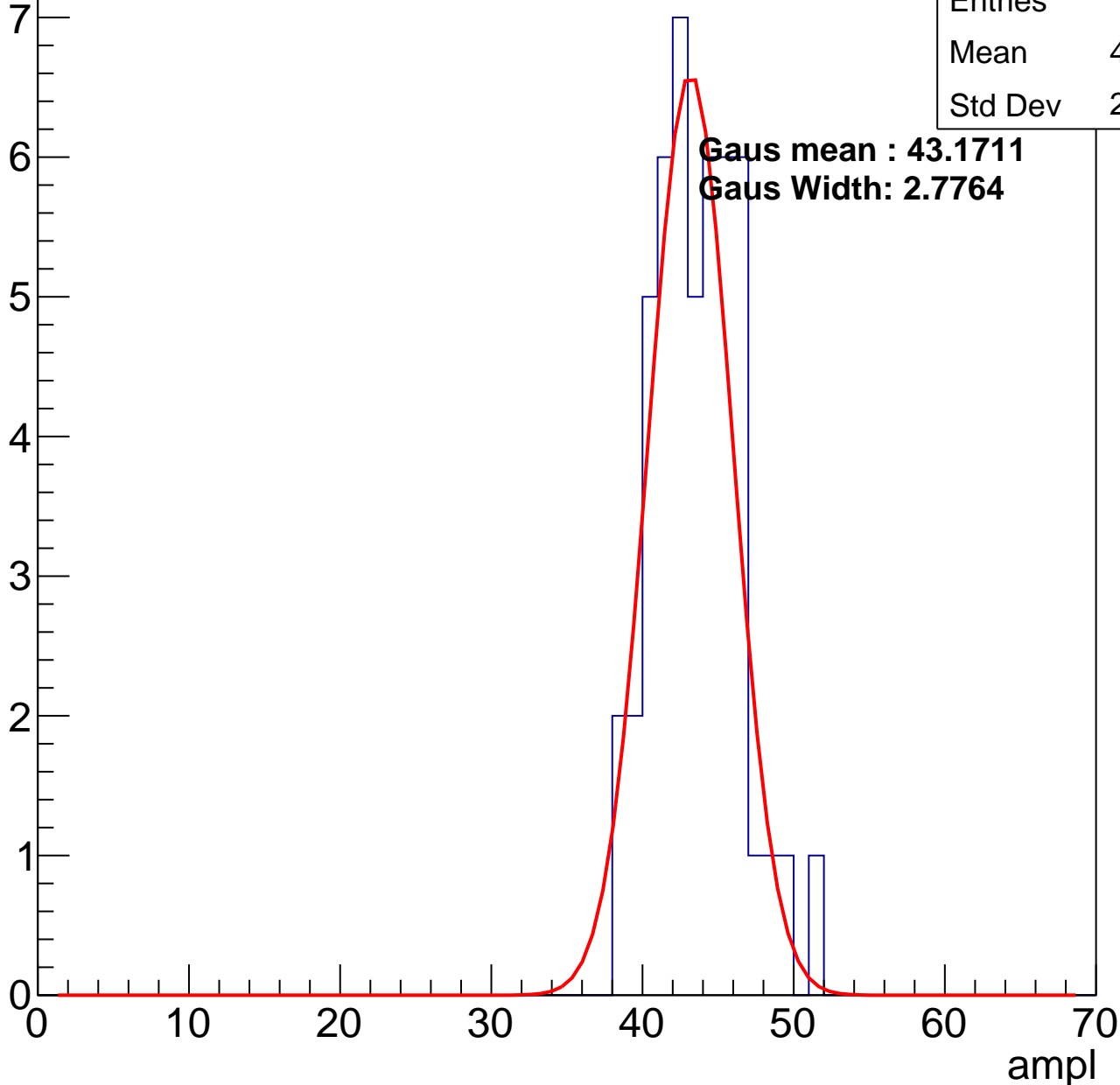


B0L001S, U7-ch83, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

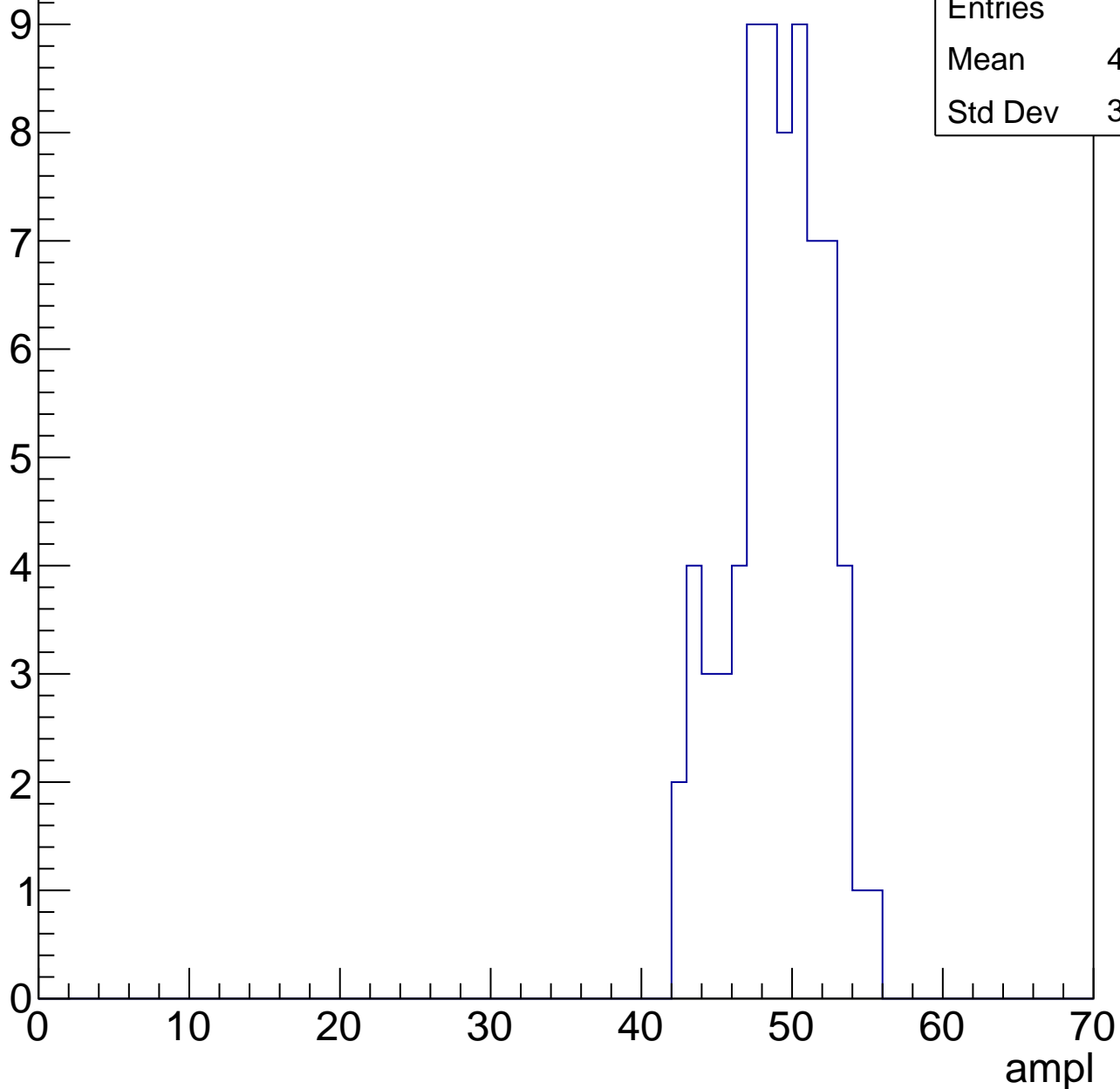
| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.14 |
| Std Dev | 2.792 |



B0L001S, U7-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



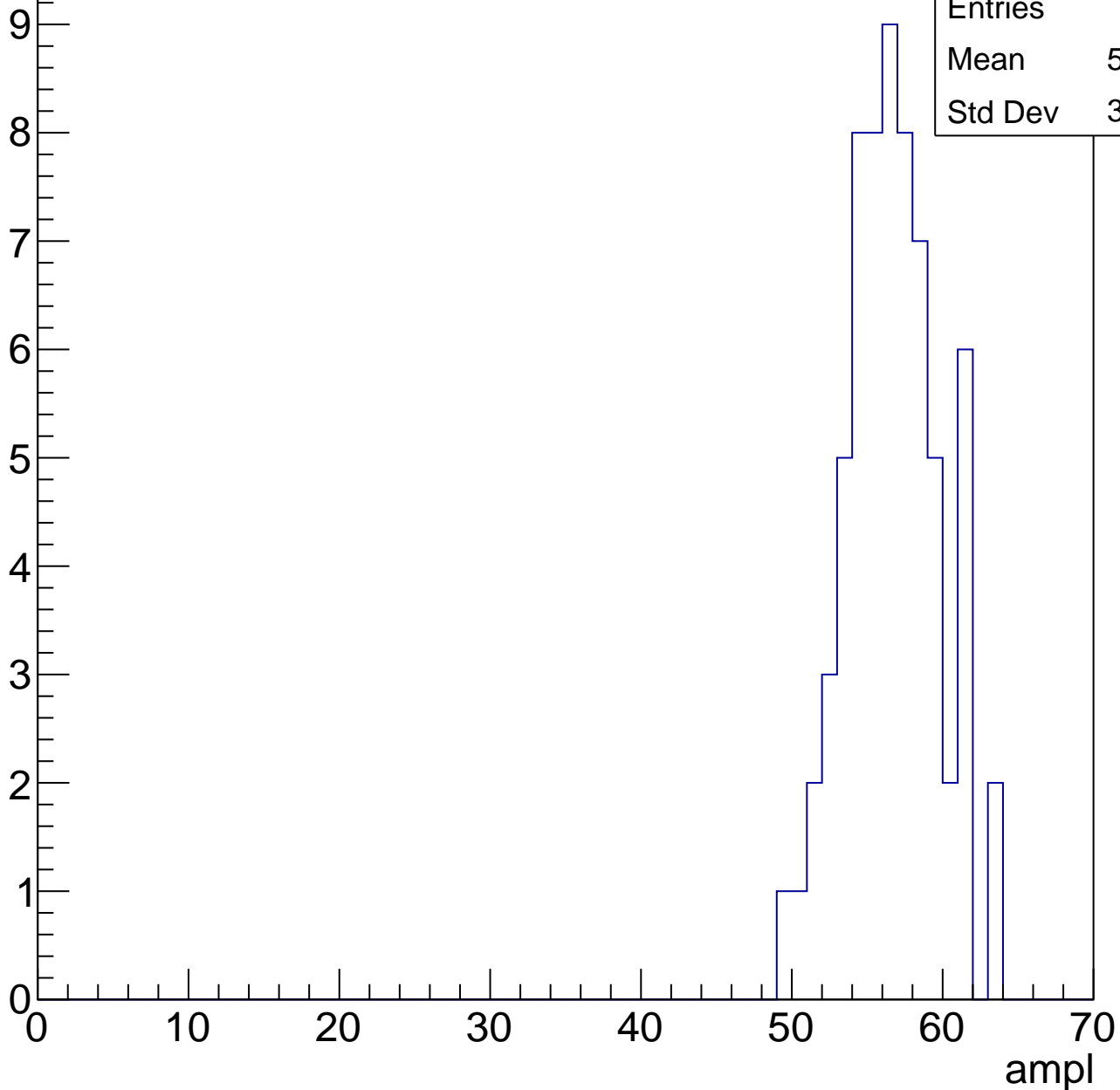
| | |
|---------|-------|
| Entries | 71 |
| Mean | 48.54 |
| Std Dev | 3.039 |

B0L001S, U7-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

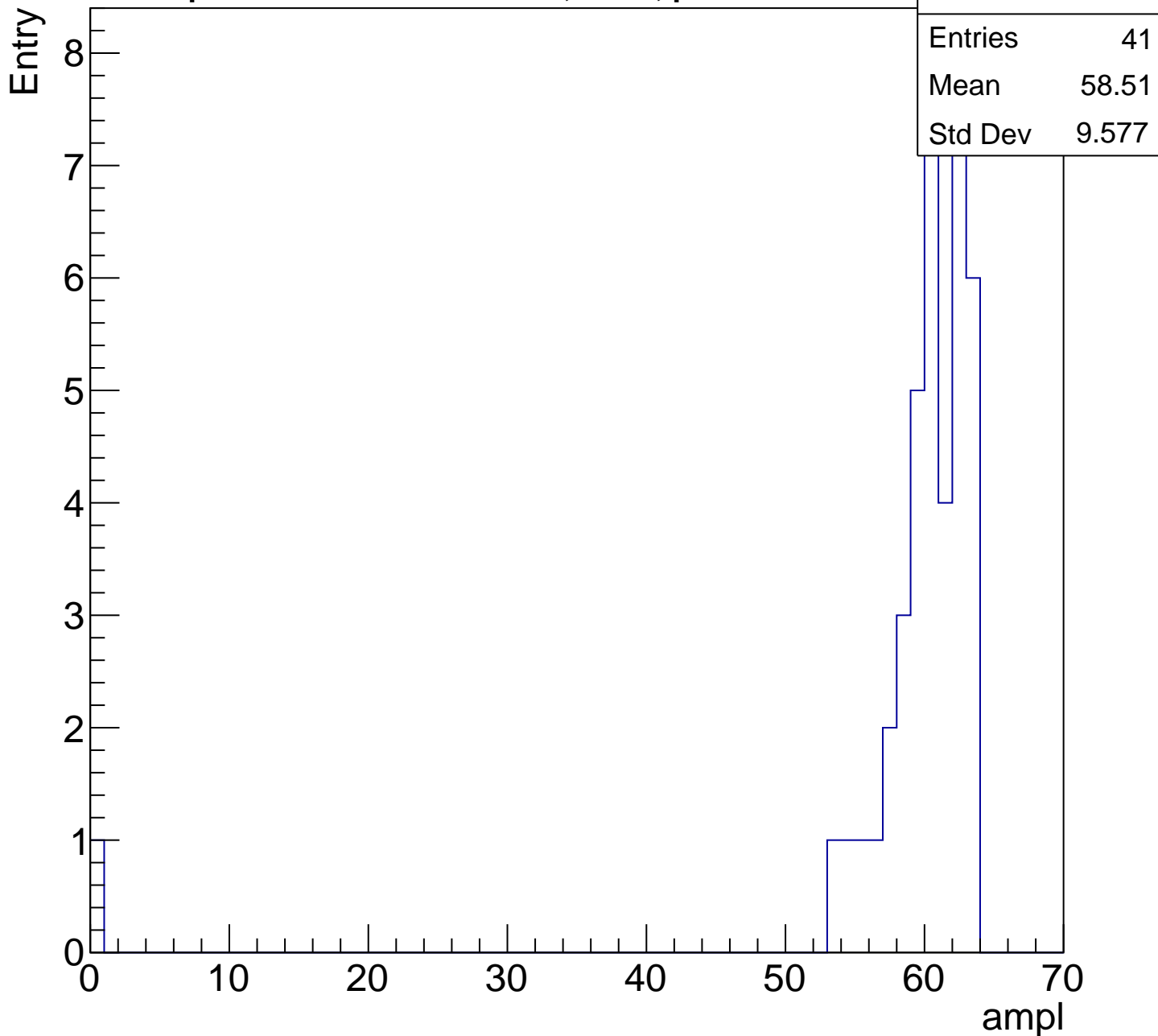
Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 56.22 |
| Std Dev | 3.056 |



B0L001S, U7-ch83, adc5

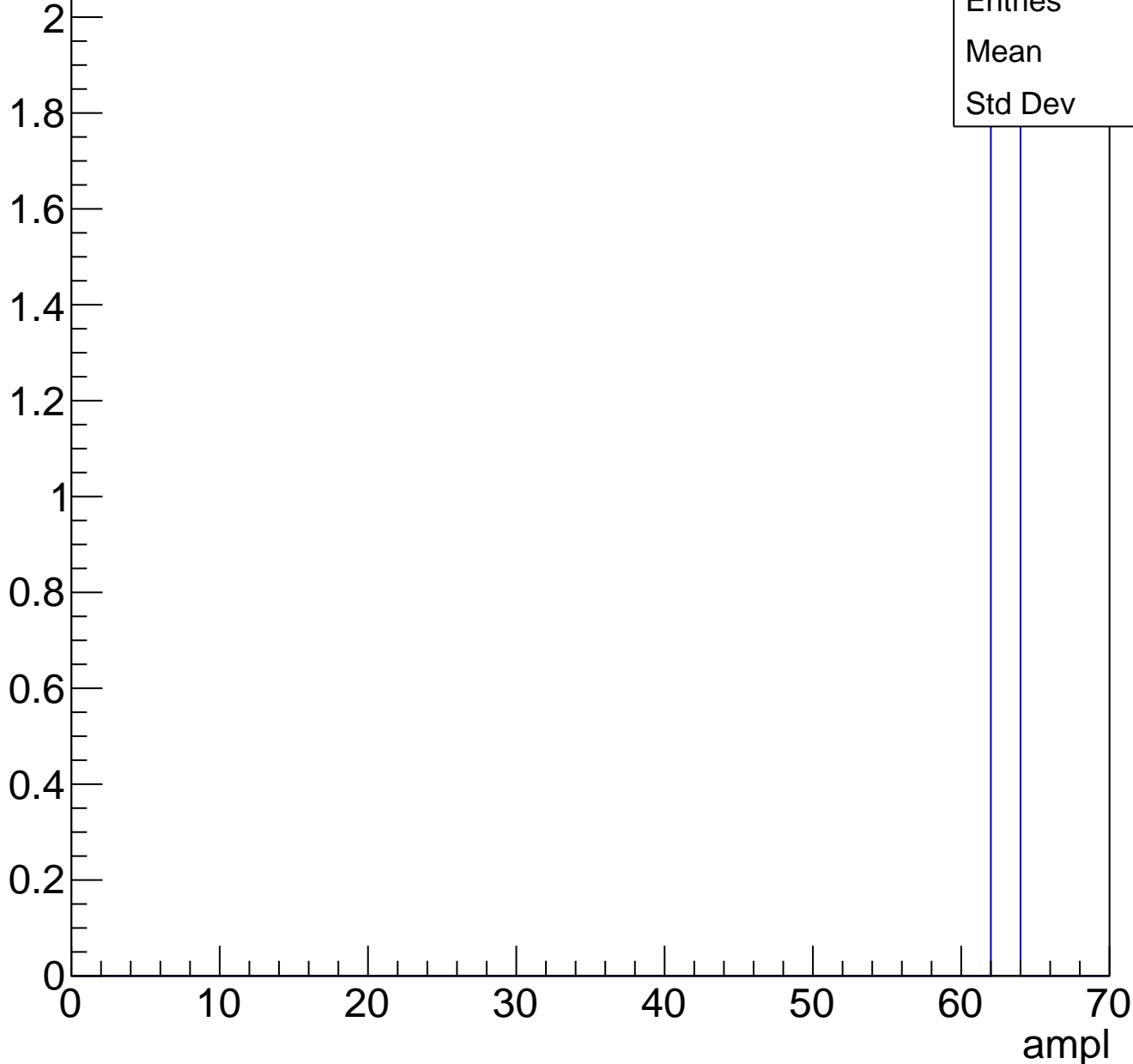
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch84, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 94 |
| Mean | 27.22 |
| Std Dev | 4.875 |

Gaus mean : 28.0391

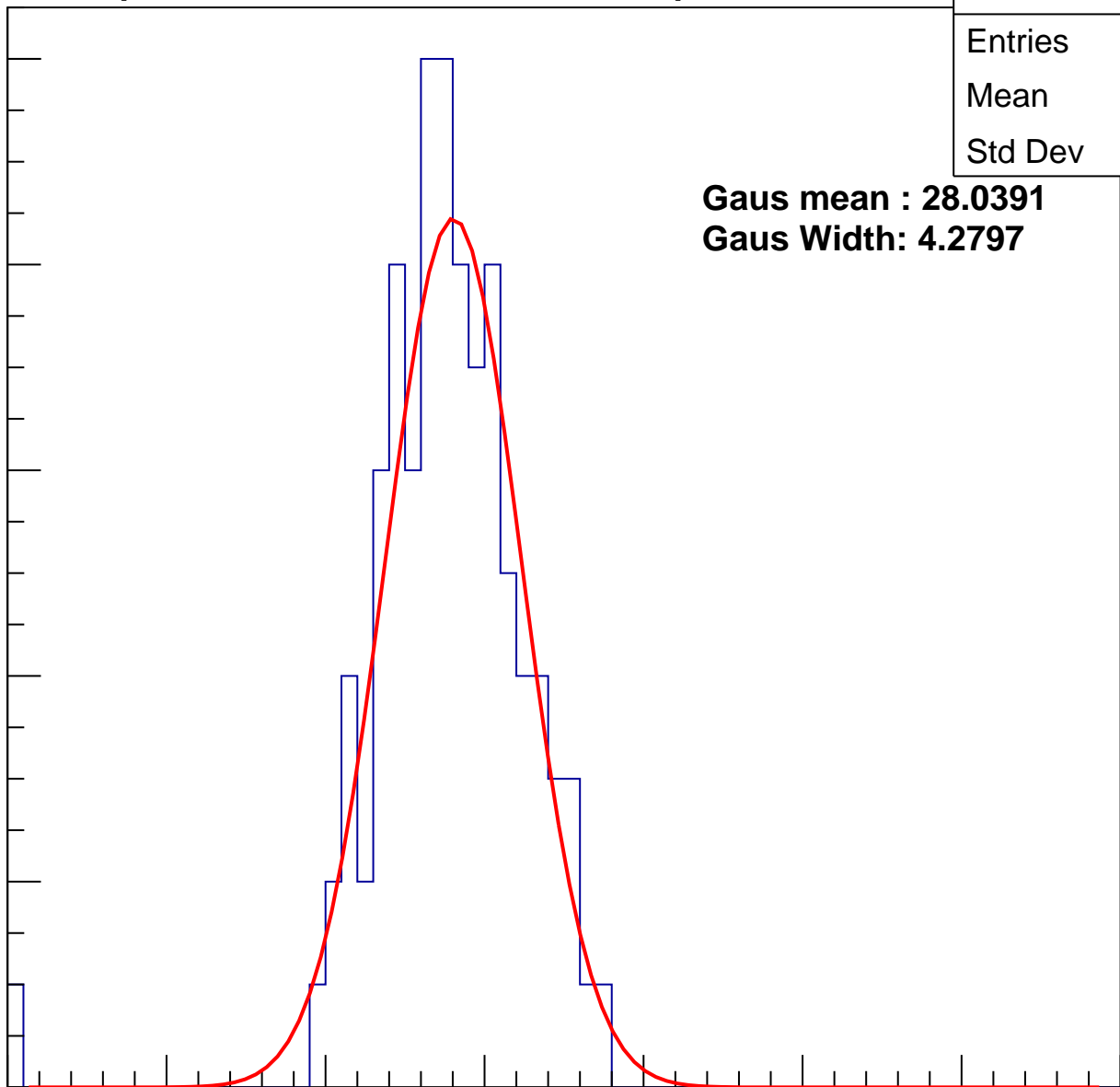
Gaus Width: 4.2797

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch84, adc1

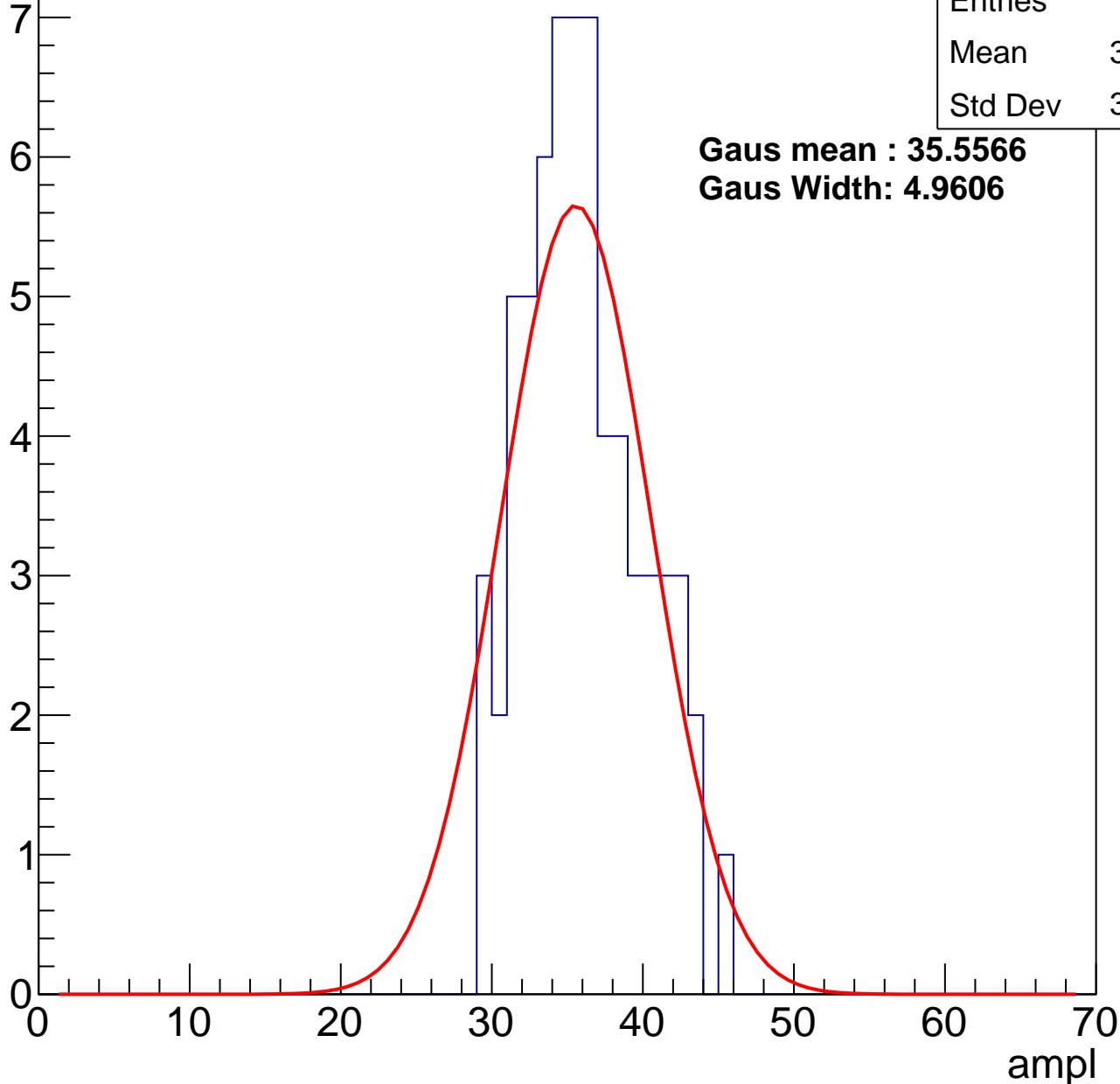
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.57 |
| Std Dev | 3.839 |

Gaus mean : 35.5566

Gaus Width: 4.9606



B0L001S, U7-ch84, adc2

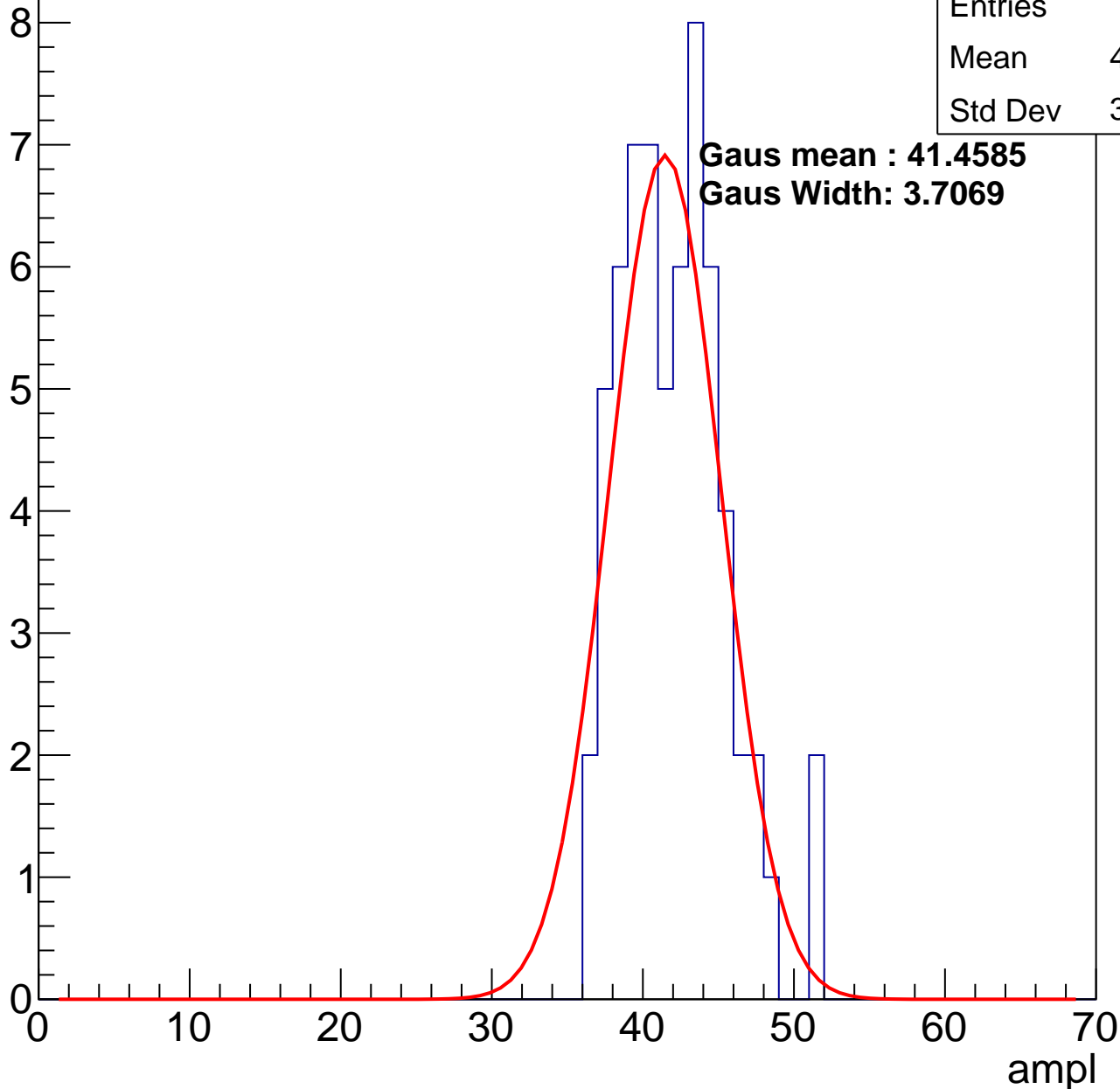
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.57 |
| Std Dev | 3.393 |

Gaus mean : 41.4585

Gaus Width: 3.7069

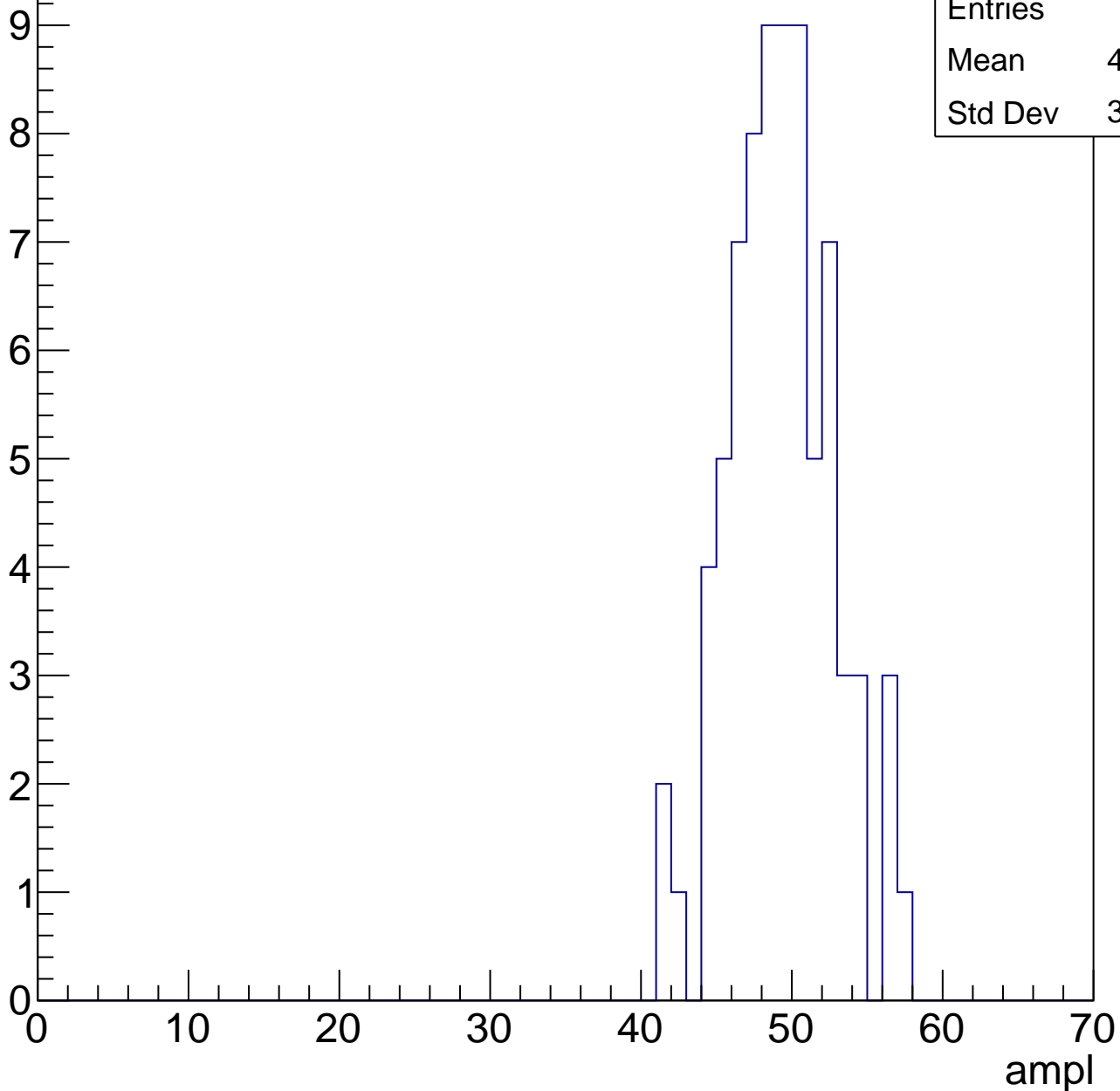


B0L001S, U7-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.83 |
| Std Dev | 3.412 |

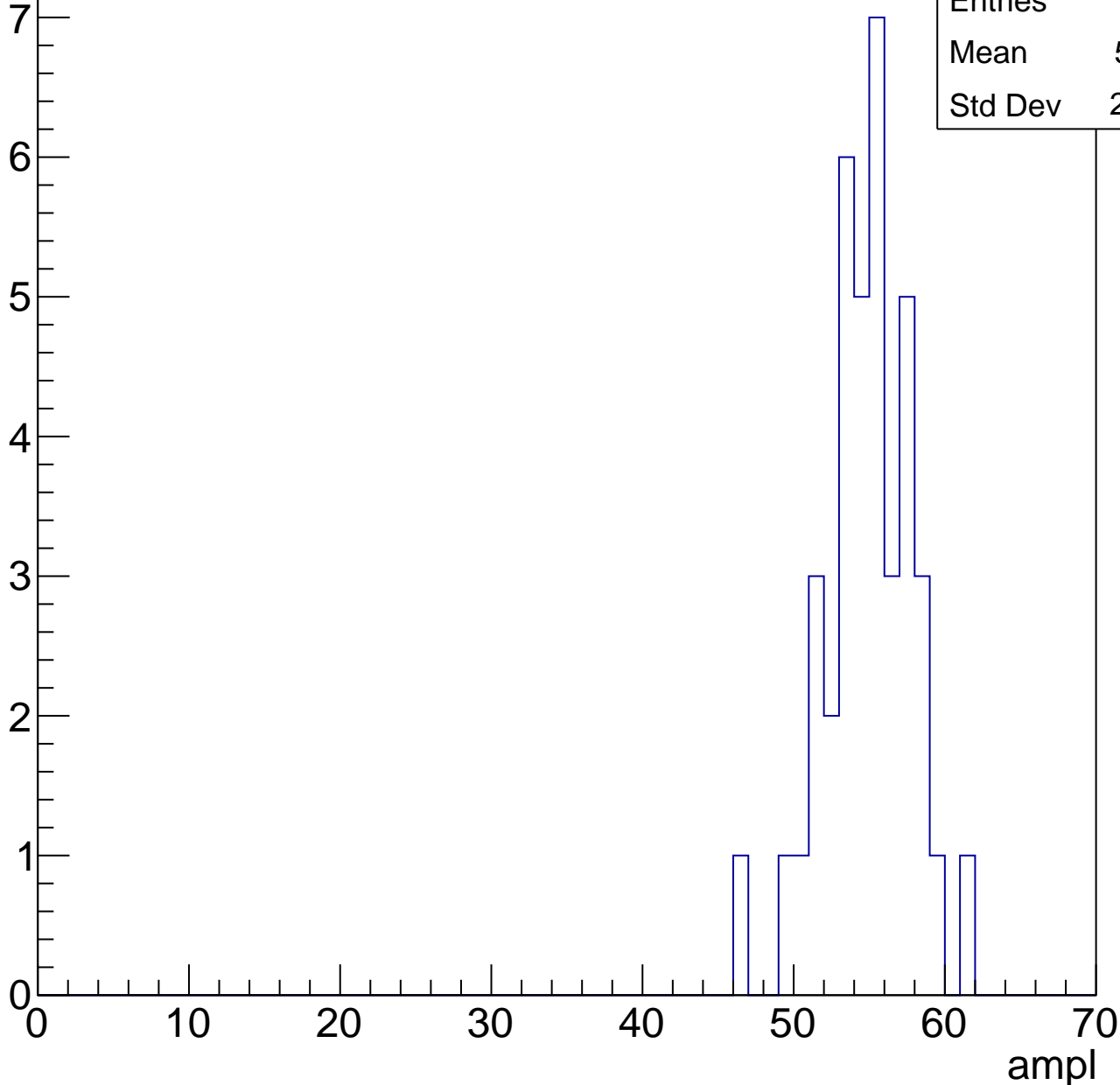


B0L001S, U7-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 54.41 |
| Std Dev | 2.889 |



B0L001S, U7-ch84, adc5

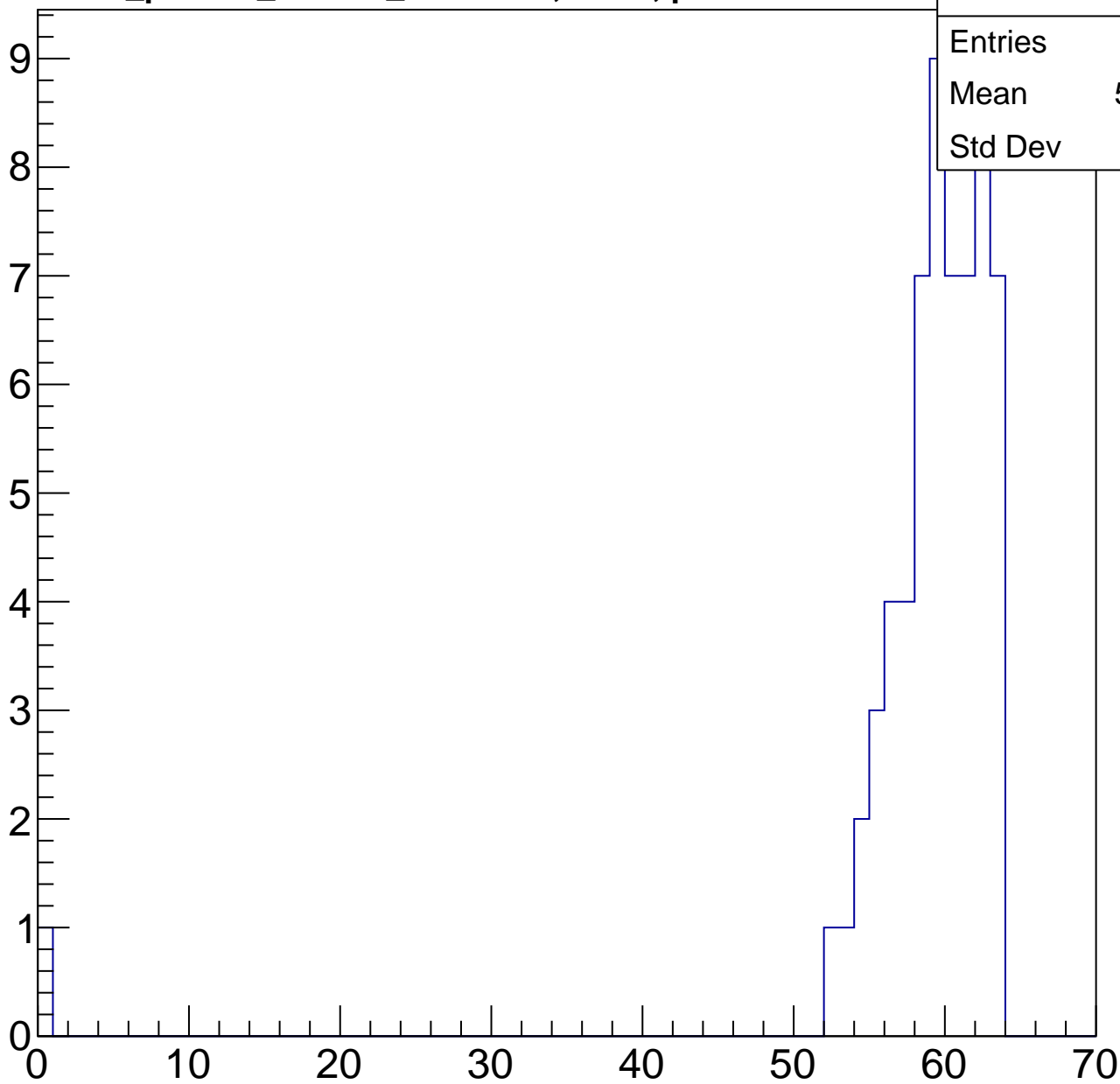
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 61 |
| Mean | 58.21 |
| Std Dev | 8 |

ampl



B0L001S, U7-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

| | |
|---------|-------|
| Entries | 10 |
| Mean | 61.5 |
| Std Dev | 1.204 |

B0L001S, U7-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch85, adc0

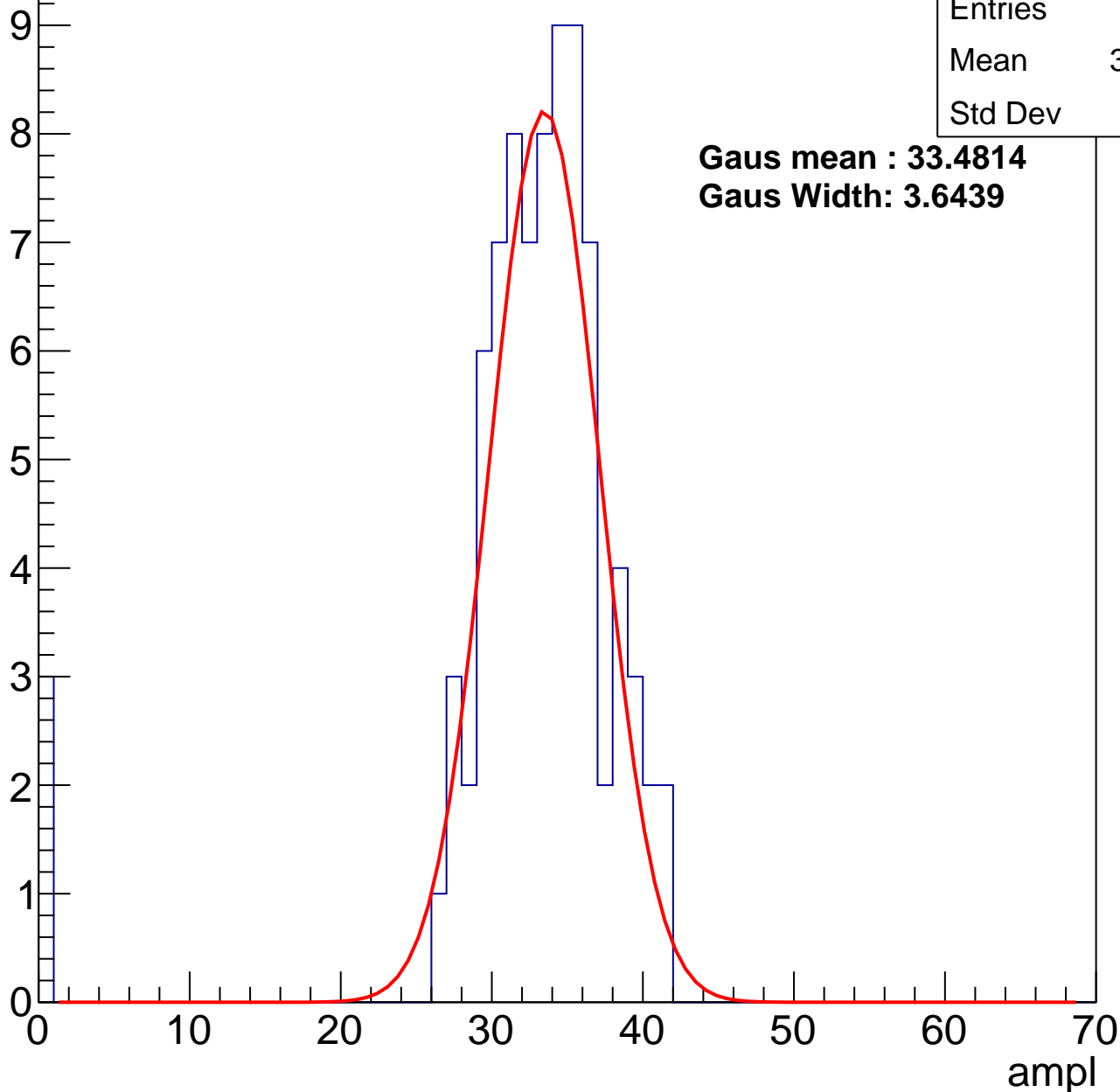
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 32.06 |
| Std Dev | 7.09 |

Gaus mean : 33.4814

Gaus Width: 3.6439



B0L001S, U7-ch85, adc1

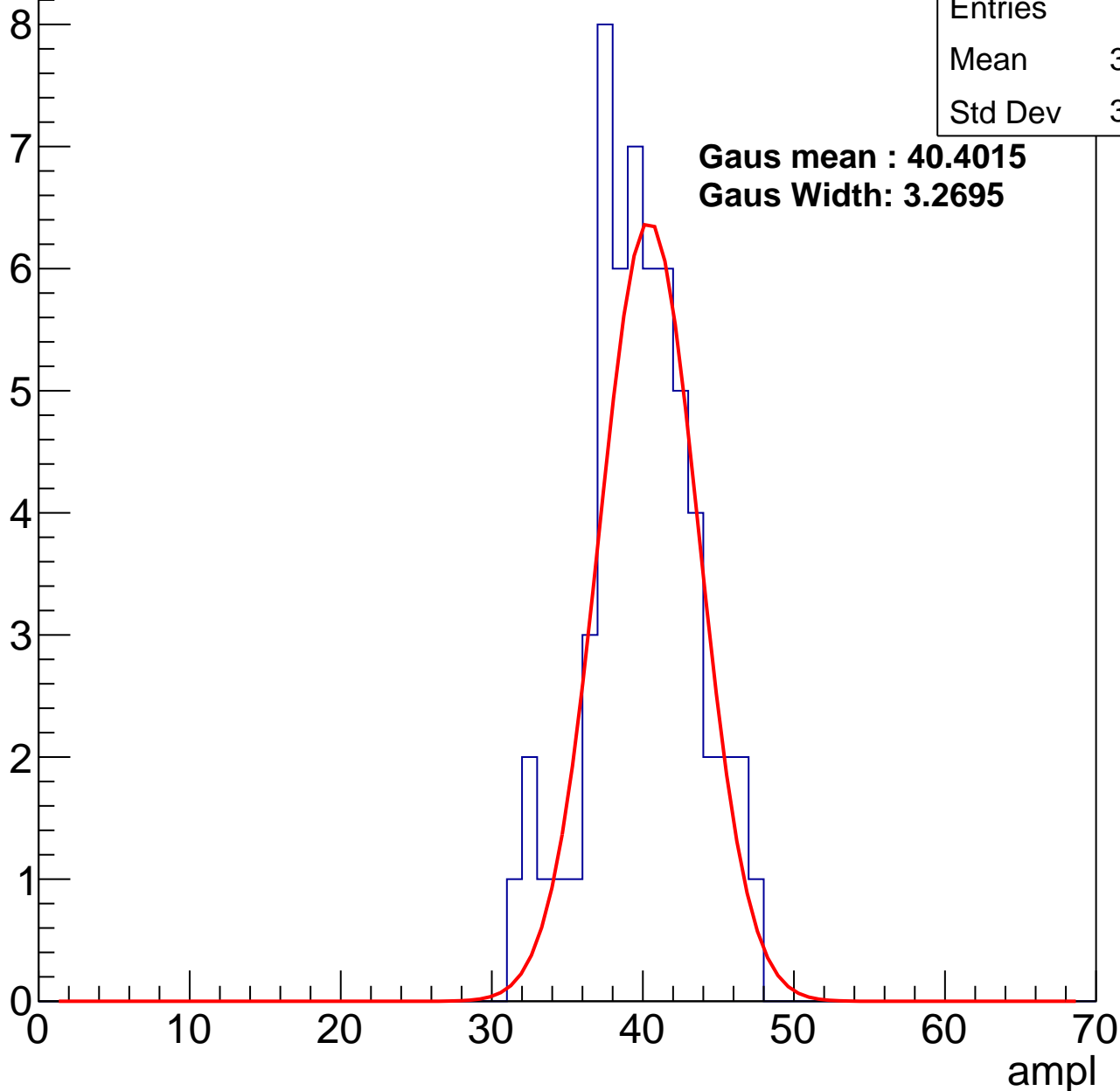
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 39.43 |
| Std Dev | 3.514 |

Gaus mean : 40.4015

Gaus Width: 3.2695



B0L001S, U7-ch85, adc2

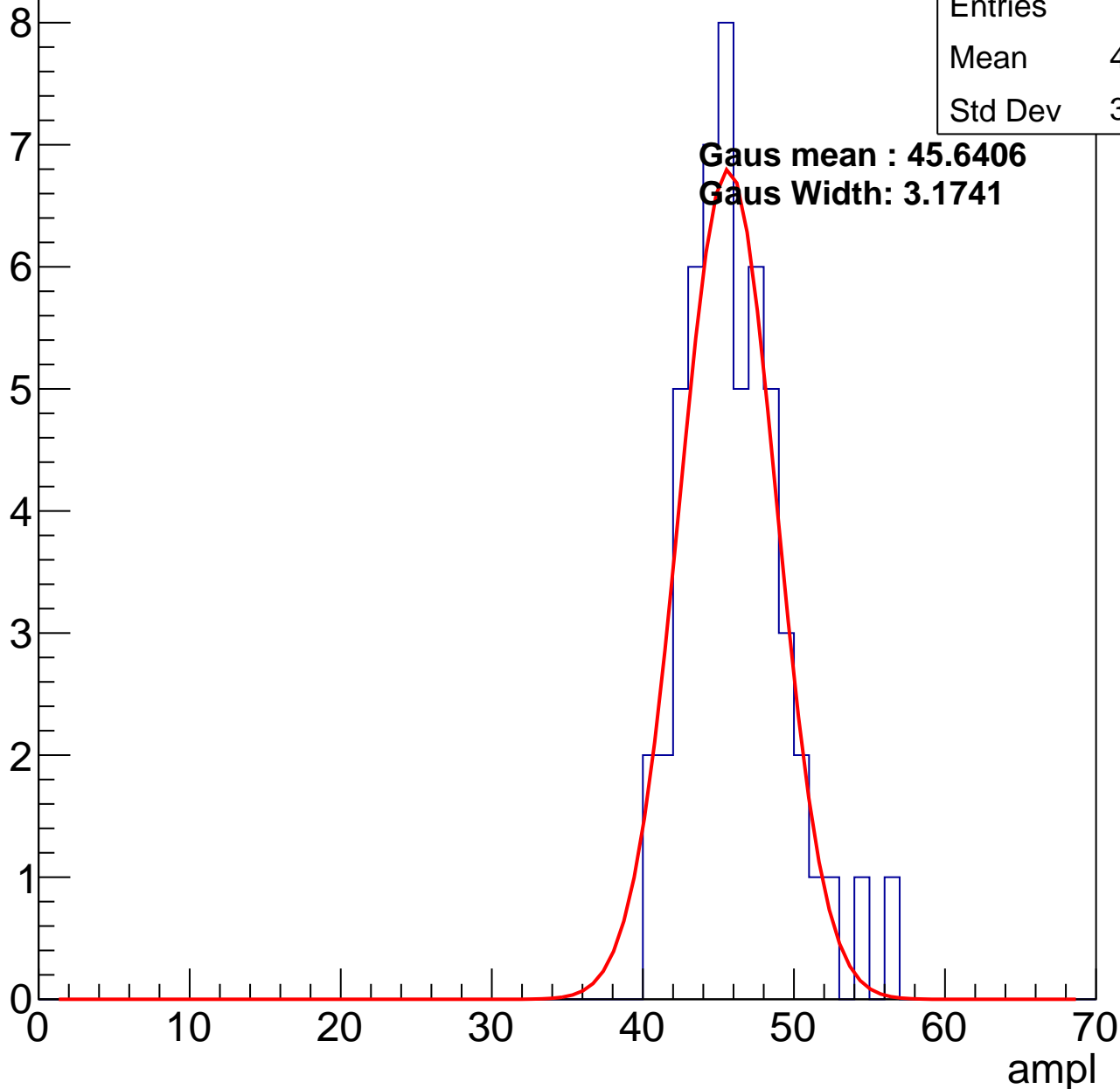
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 45.64 |
| Std Dev | 3.288 |

Gaus mean : 45.6406

Gaus Width: 3.1741

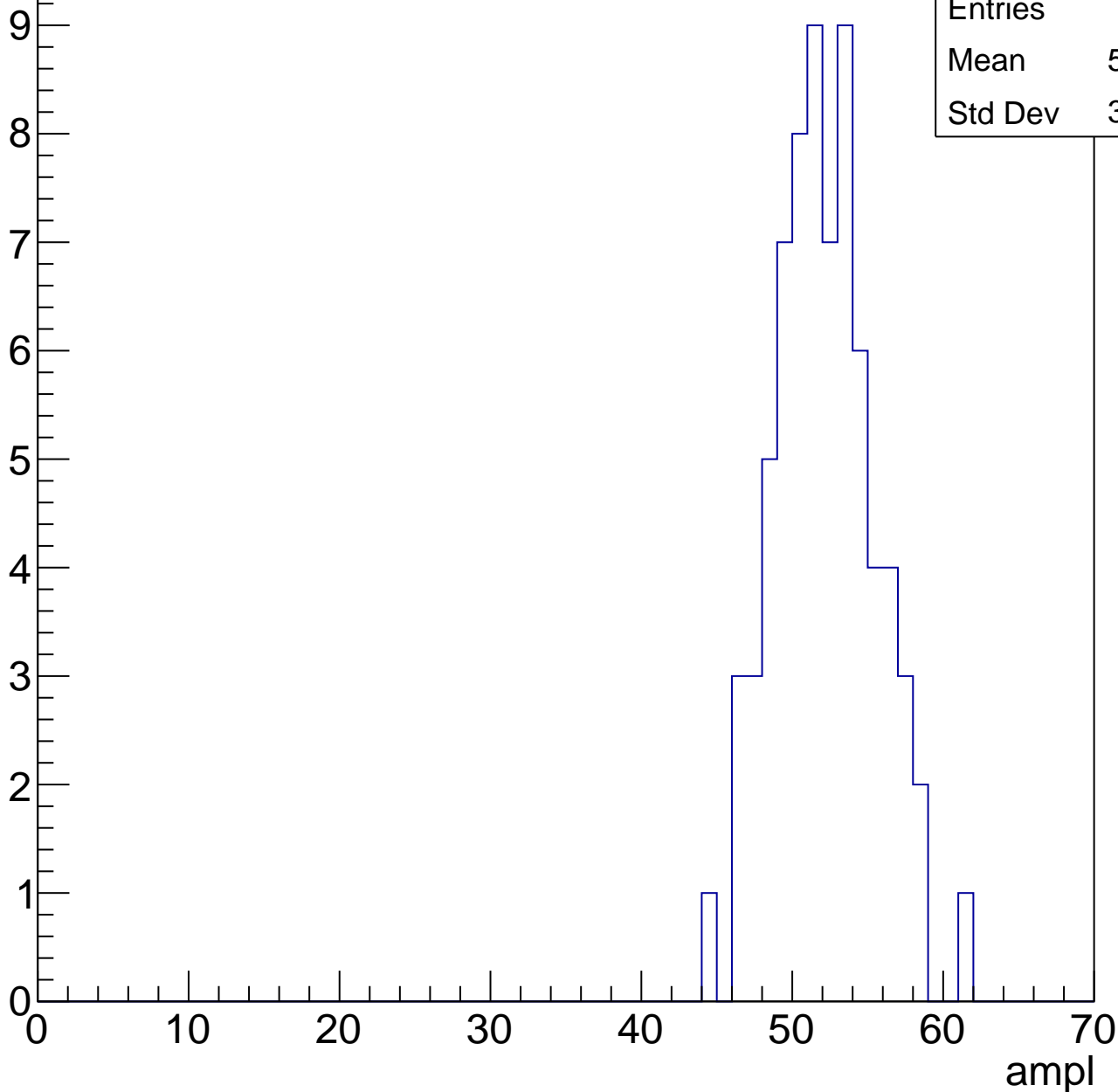


B0L001S, U7-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 51.69 |
| Std Dev | 3.303 |

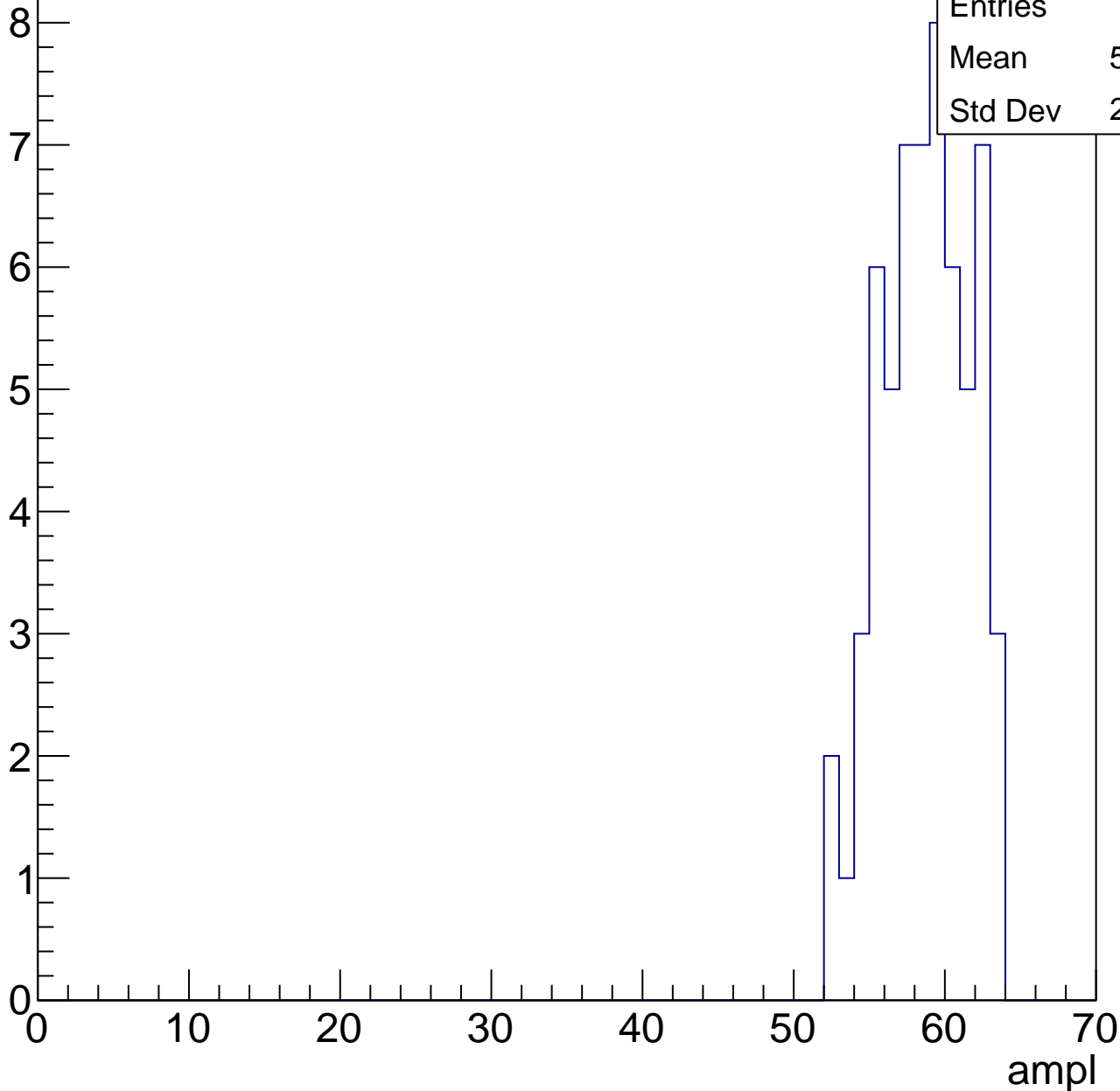


B0L001S, U7-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 58.23 |
| Std Dev | 2.848 |

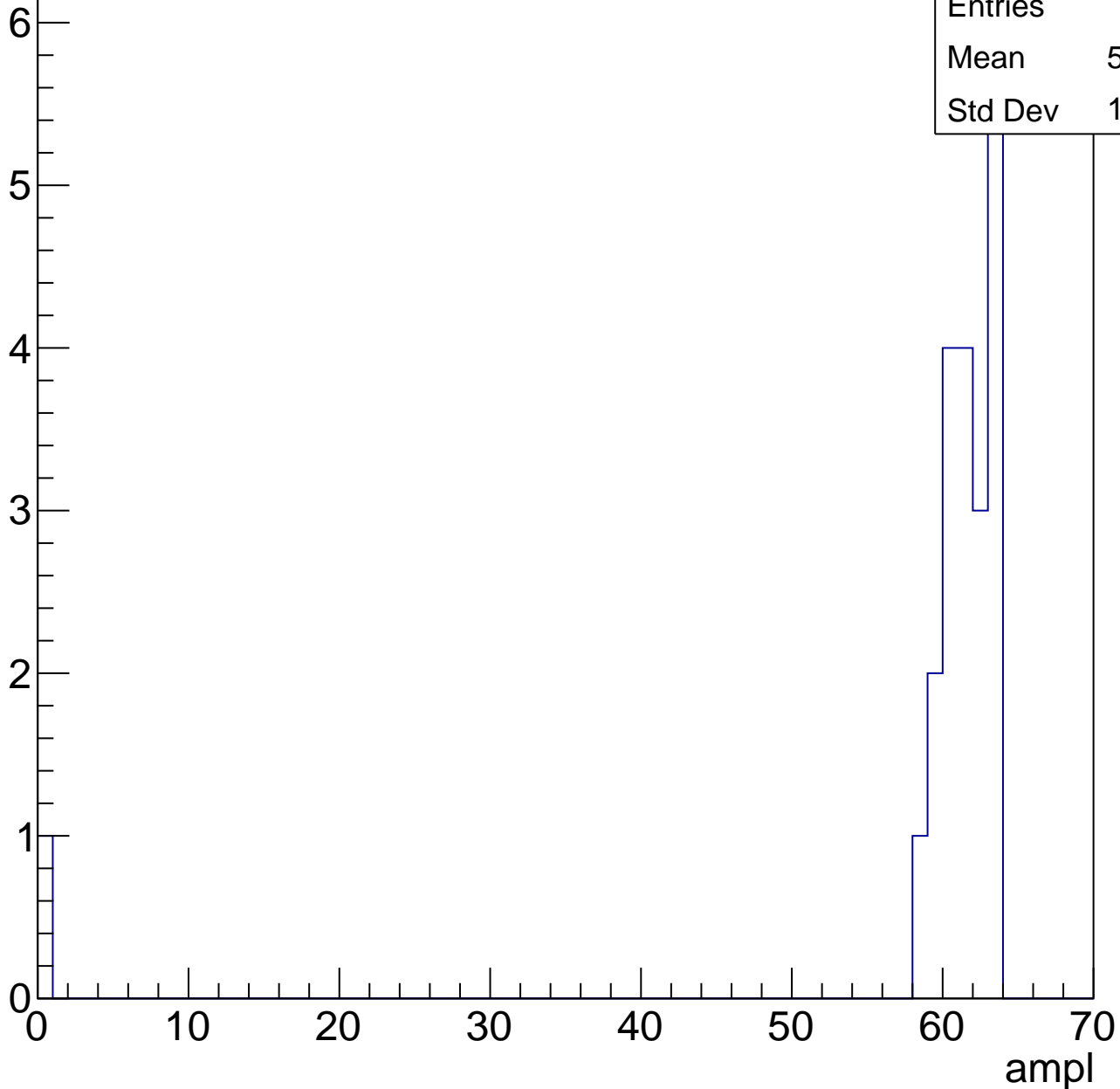


B0L001S, U7-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 21 |
| Mean | 58.29 |
| Std Dev | 13.12 |



B0L001S, U7-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

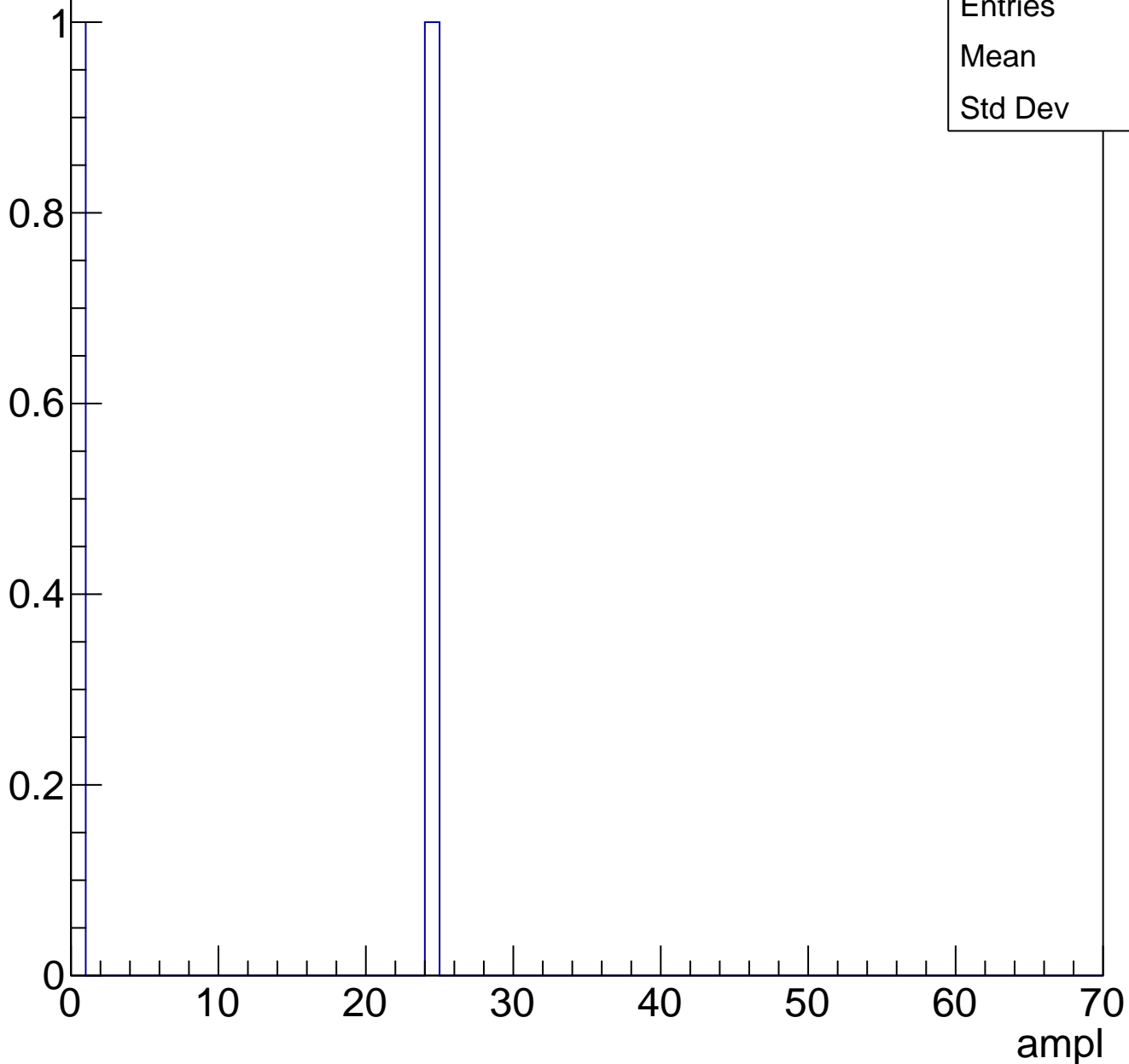


| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

B0L001S, U7-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch86, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 82 |
| Mean | 28.44 |
| Std Dev | 3.531 |

Gaus mean : 29.3014

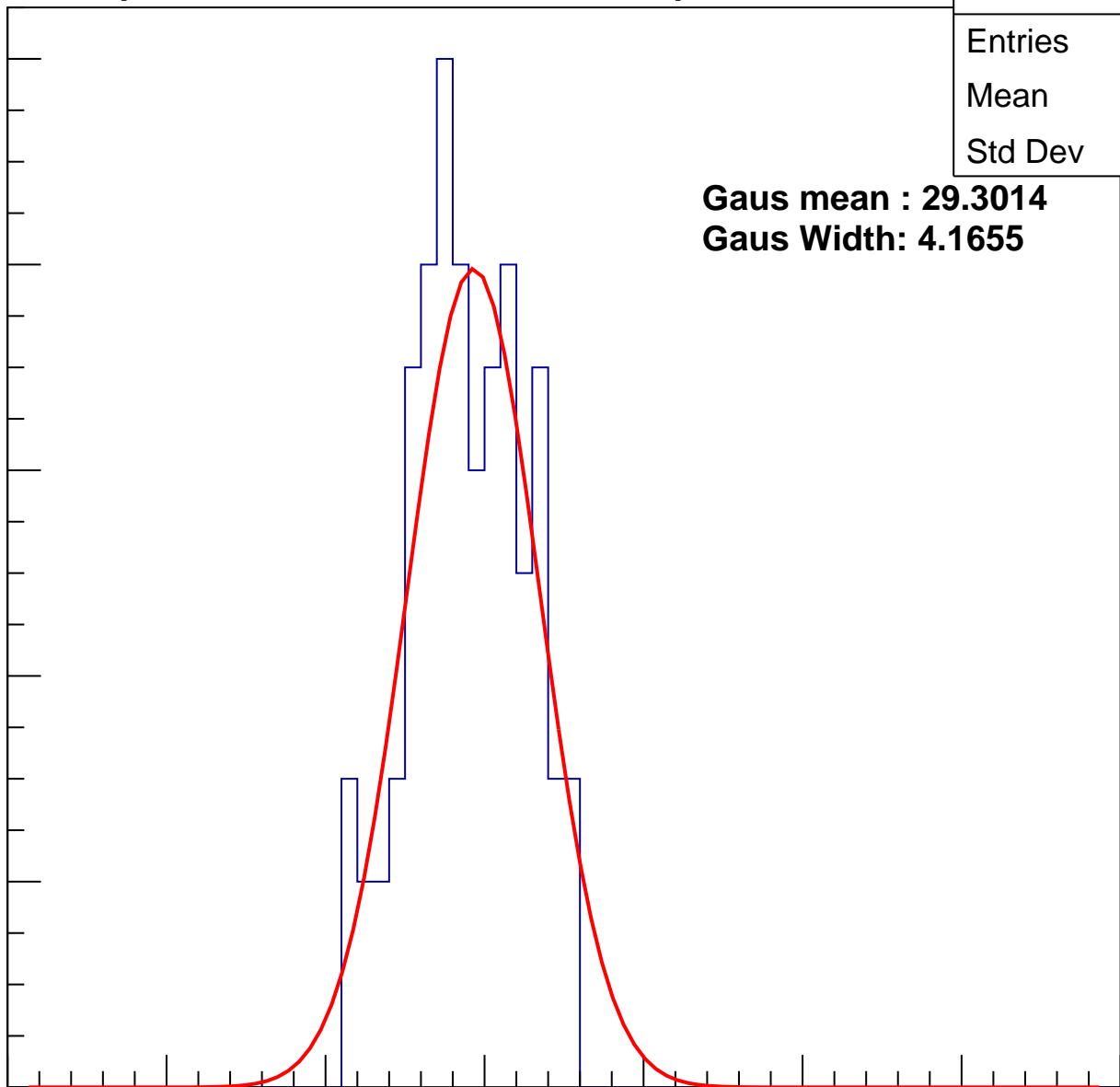
Gaus Width: 4.1655

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch86, adc1

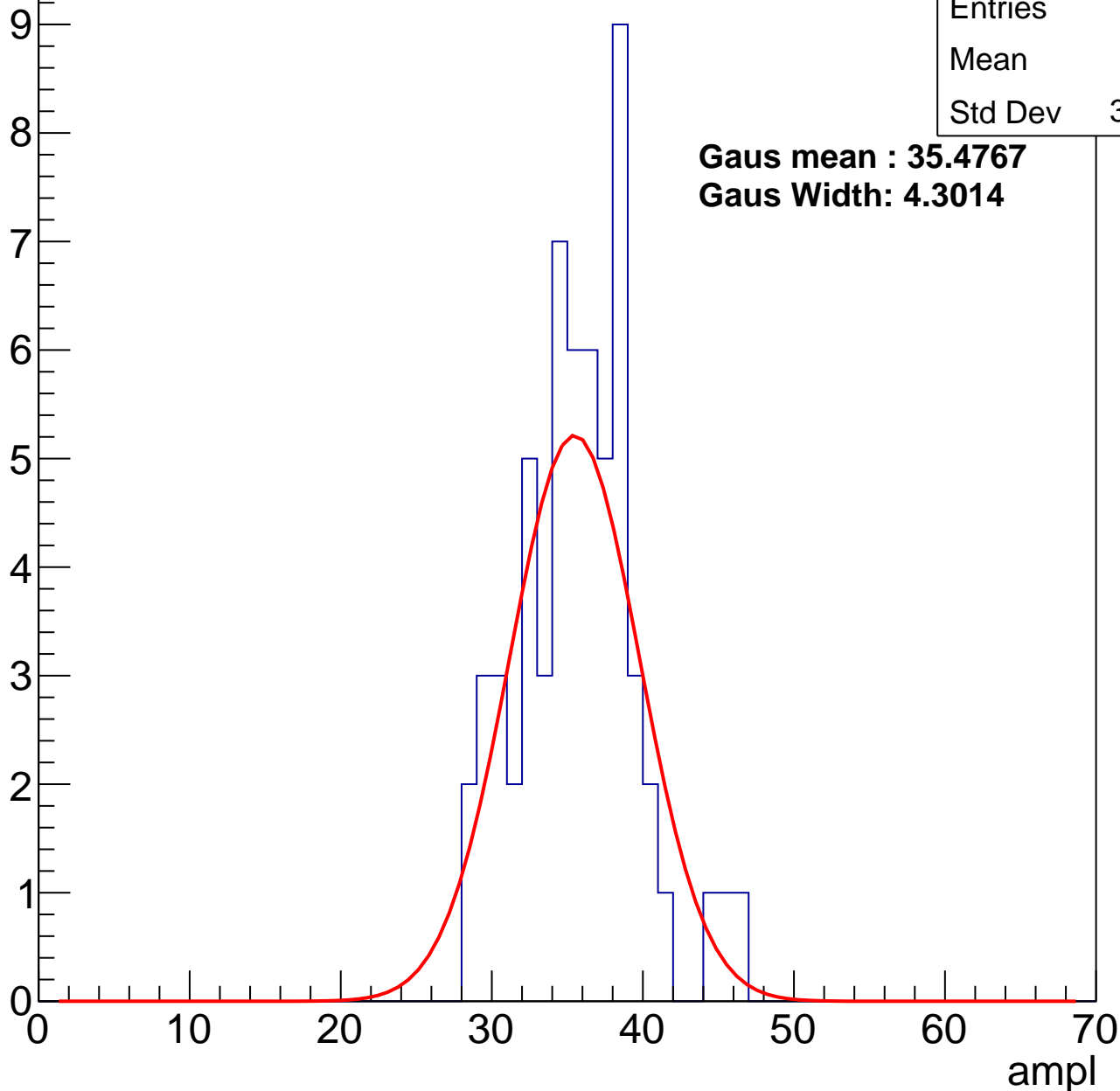
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 35.3 |
| Std Dev | 3.904 |

Gaus mean : 35.4767

Gaus Width: 4.3014



B0L001S, U7-ch86, adc2

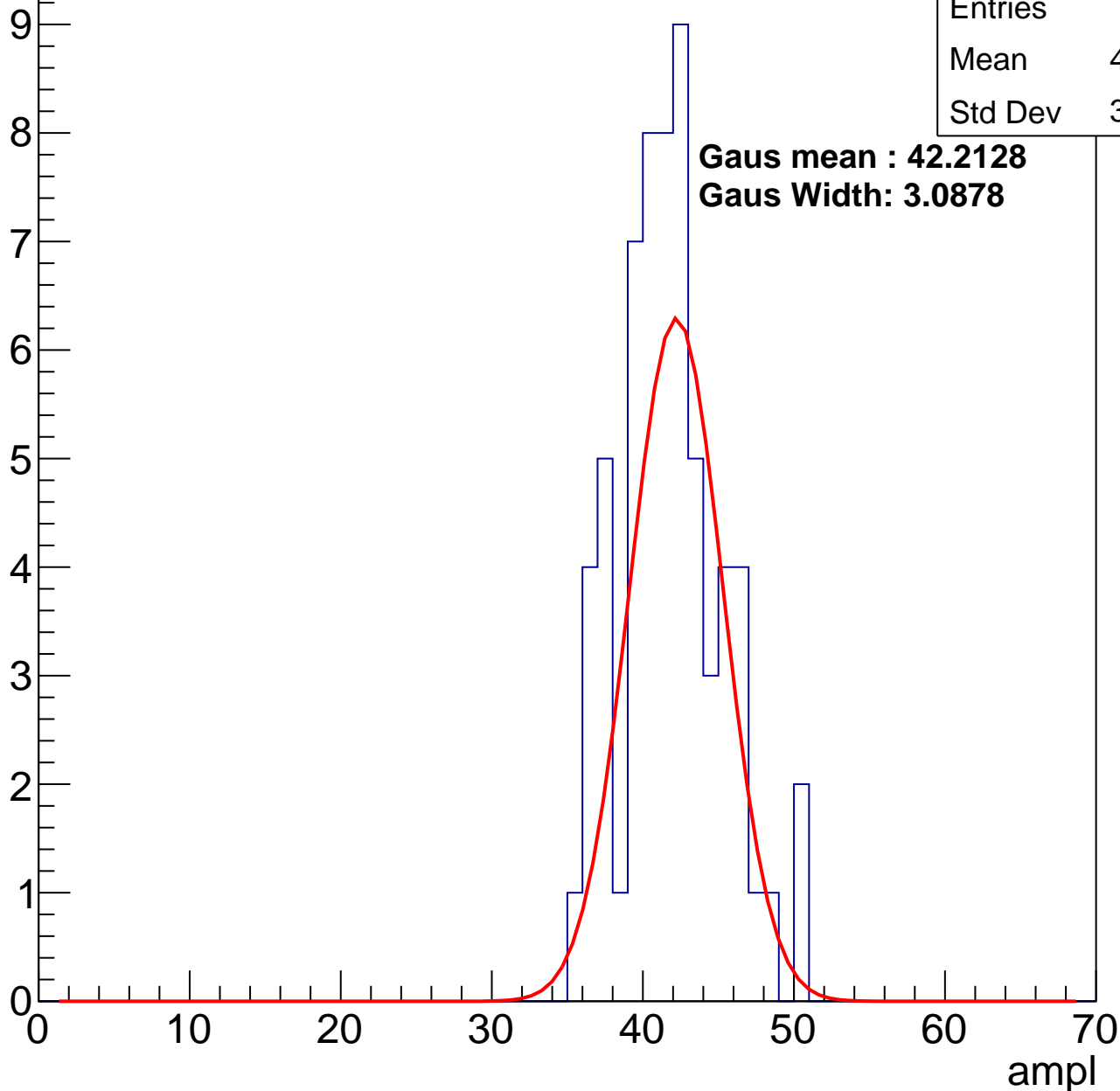
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.38 |
| Std Dev | 3.378 |

Gaus mean : 42.2128

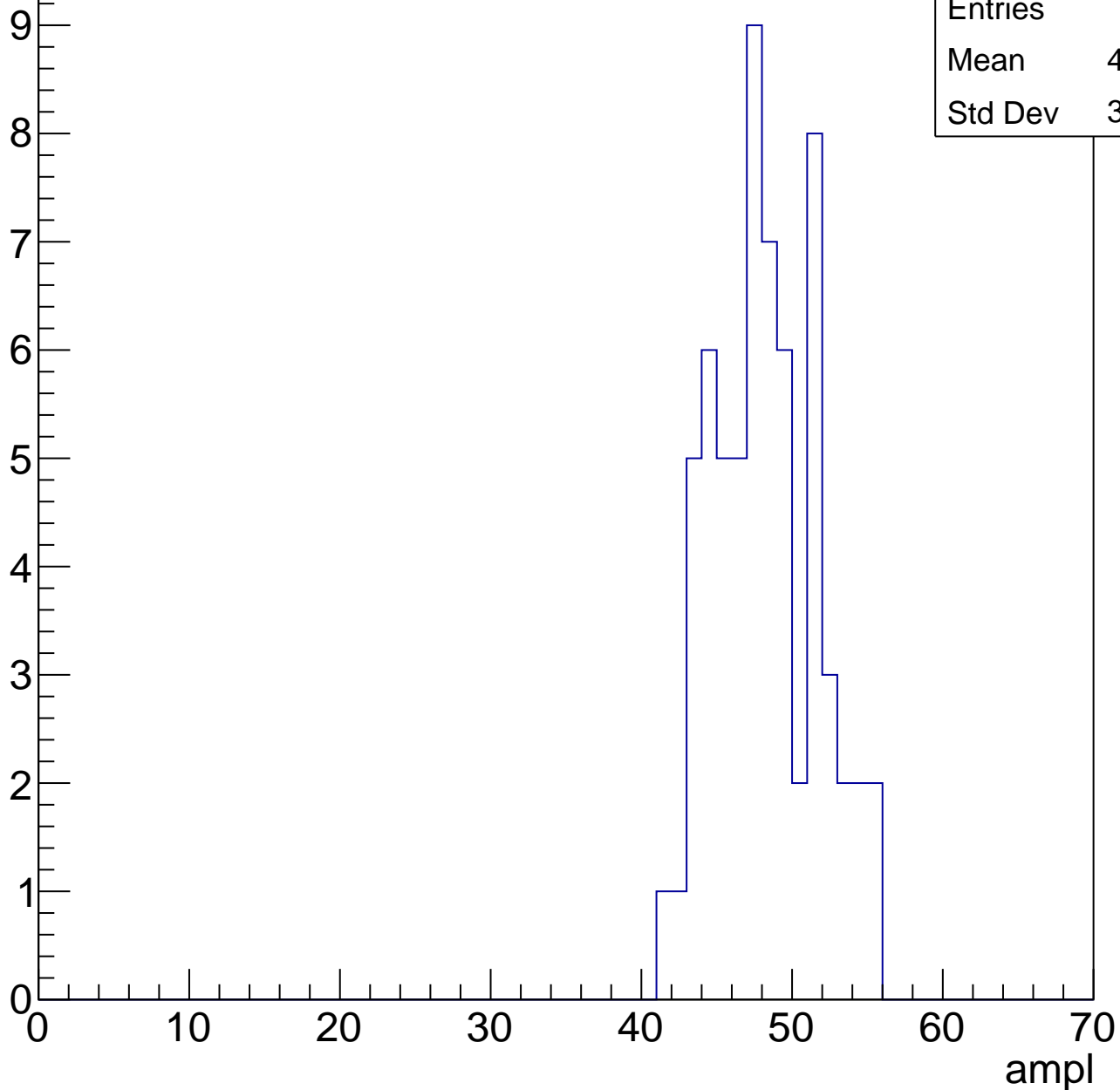
Gaus Width: 3.0878



B0L001S, U7-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

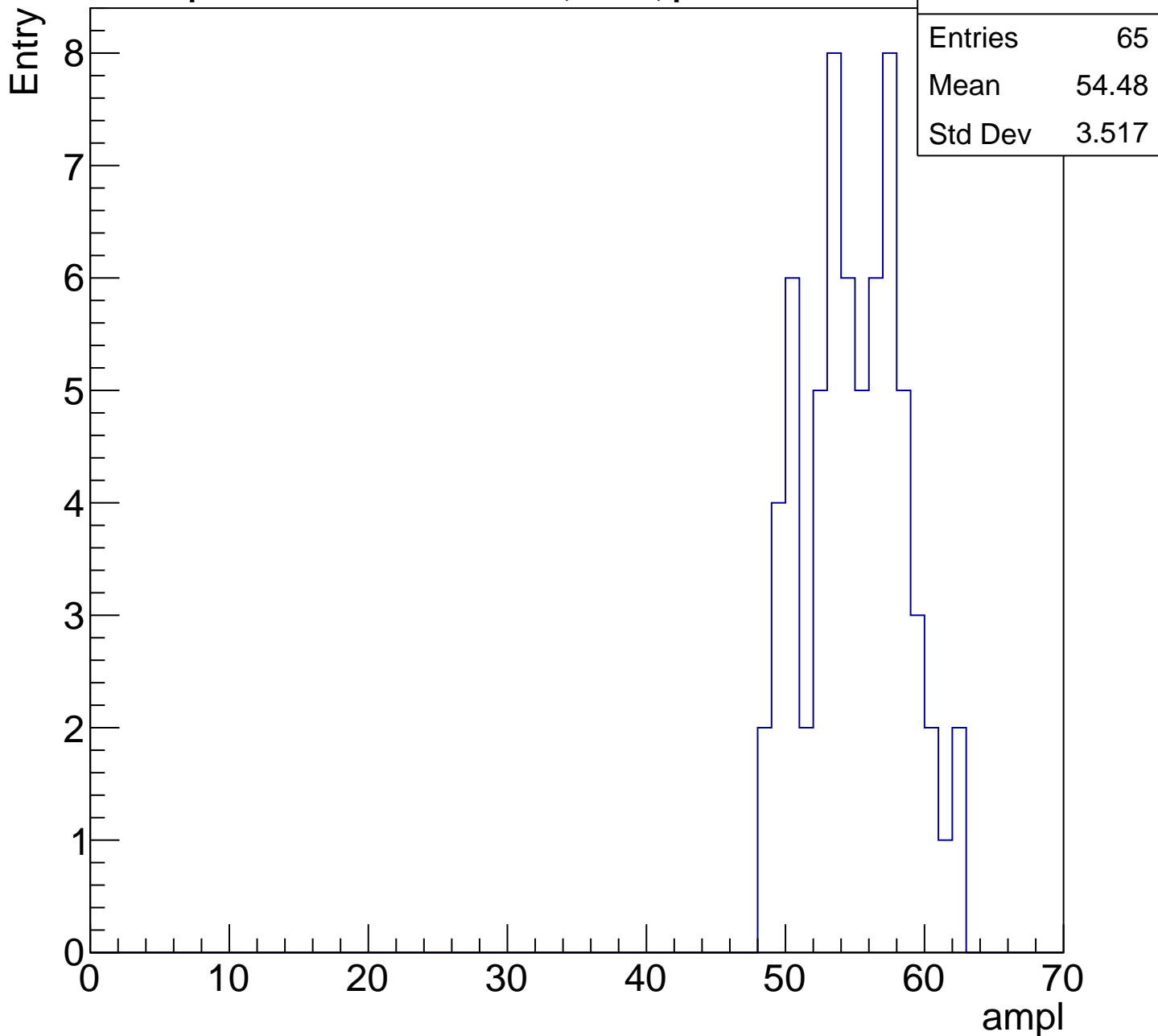
Entry



| | |
|---------|-------|
| Entries | 64 |
| Mean | 47.78 |
| Std Dev | 3.379 |

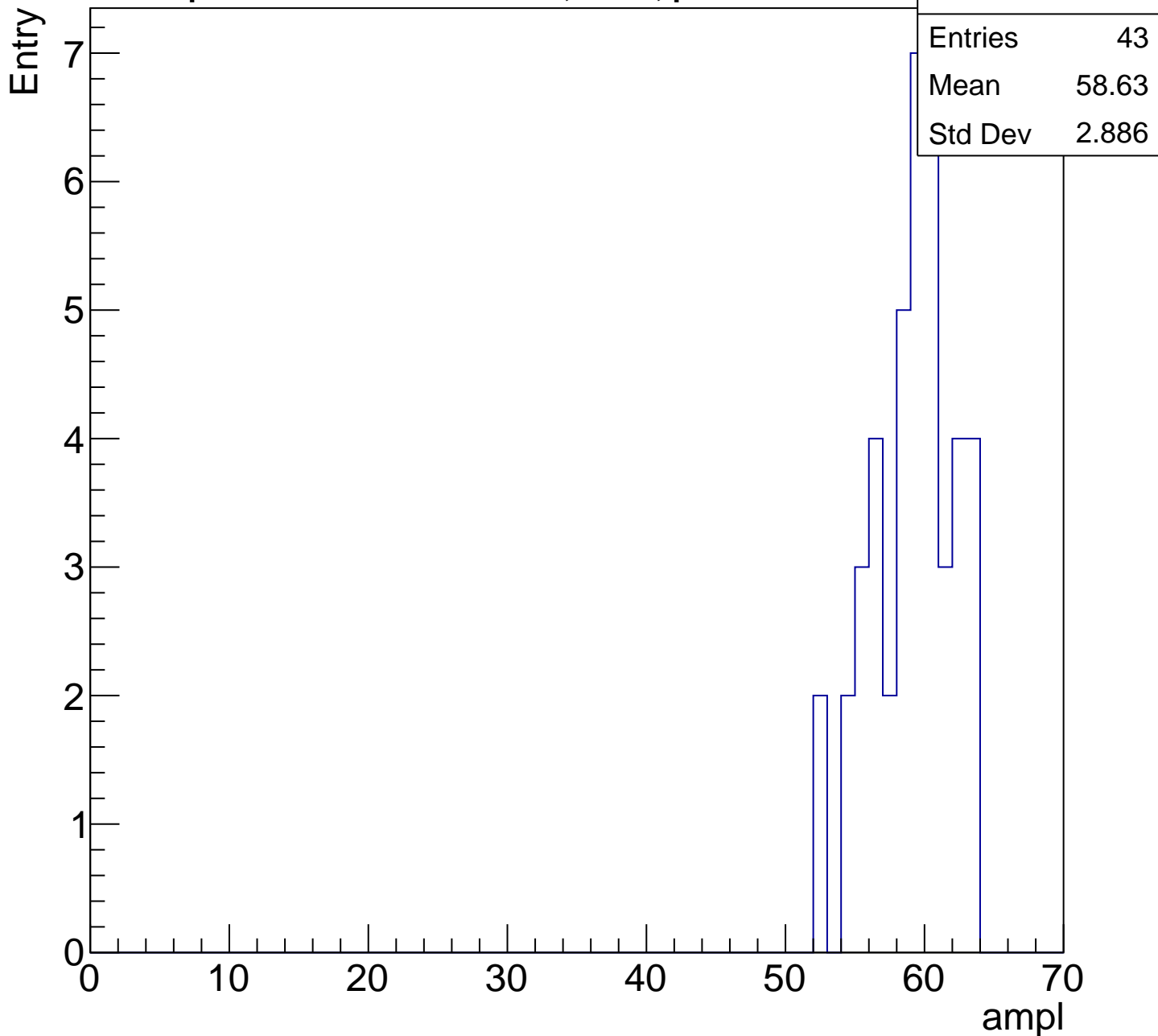
B0L001S, U7-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

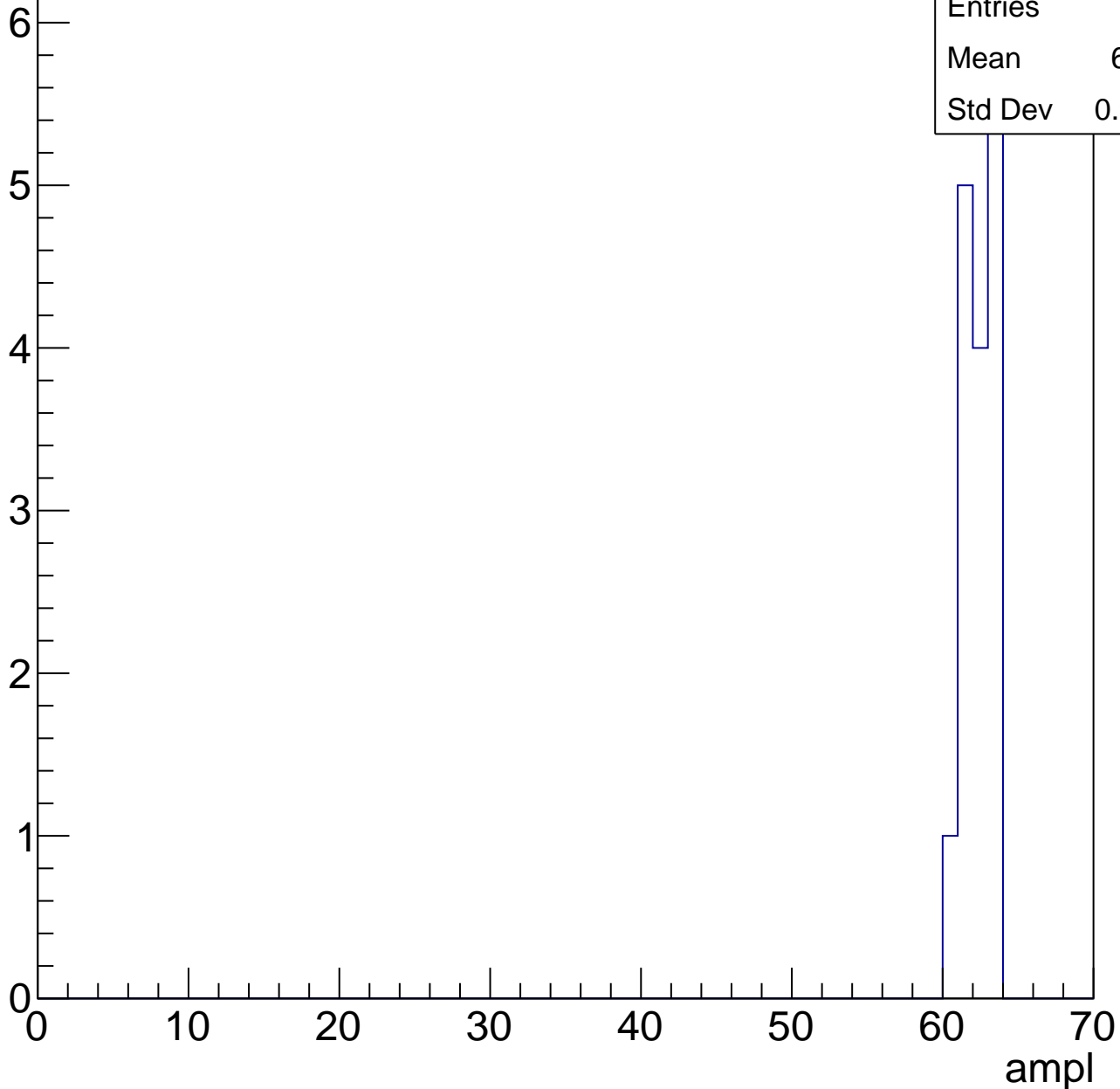


B0L001S, U7-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

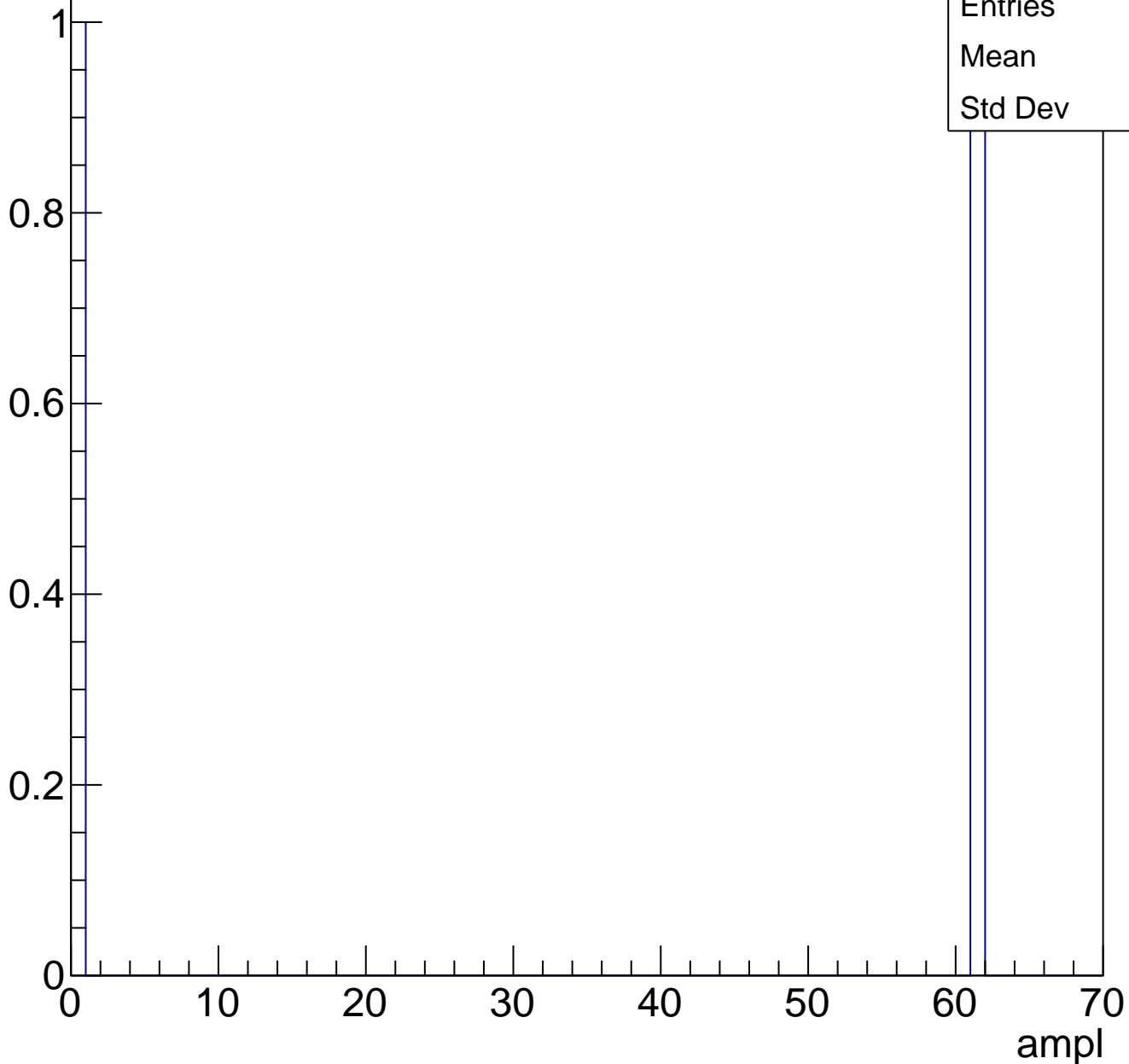
| | |
|---------|--------|
| Entries | 16 |
| Mean | 61.94 |
| Std Dev | 0.9662 |



B0L001S, U7-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch87, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 29.33 |
| Std Dev | 3.906 |

Gaus mean : 30.0890

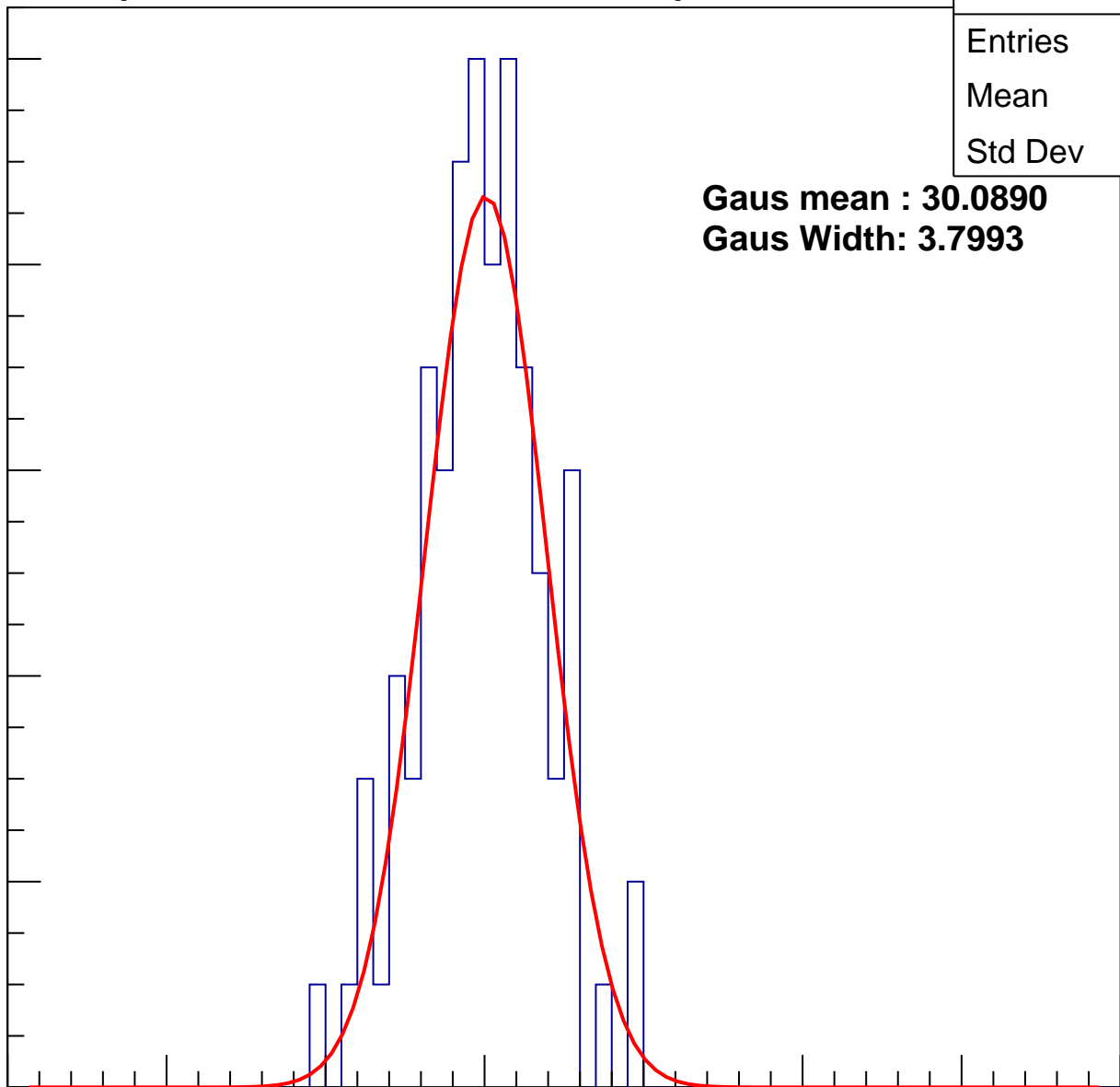
Gaus Width: 3.7993

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch87, adc1

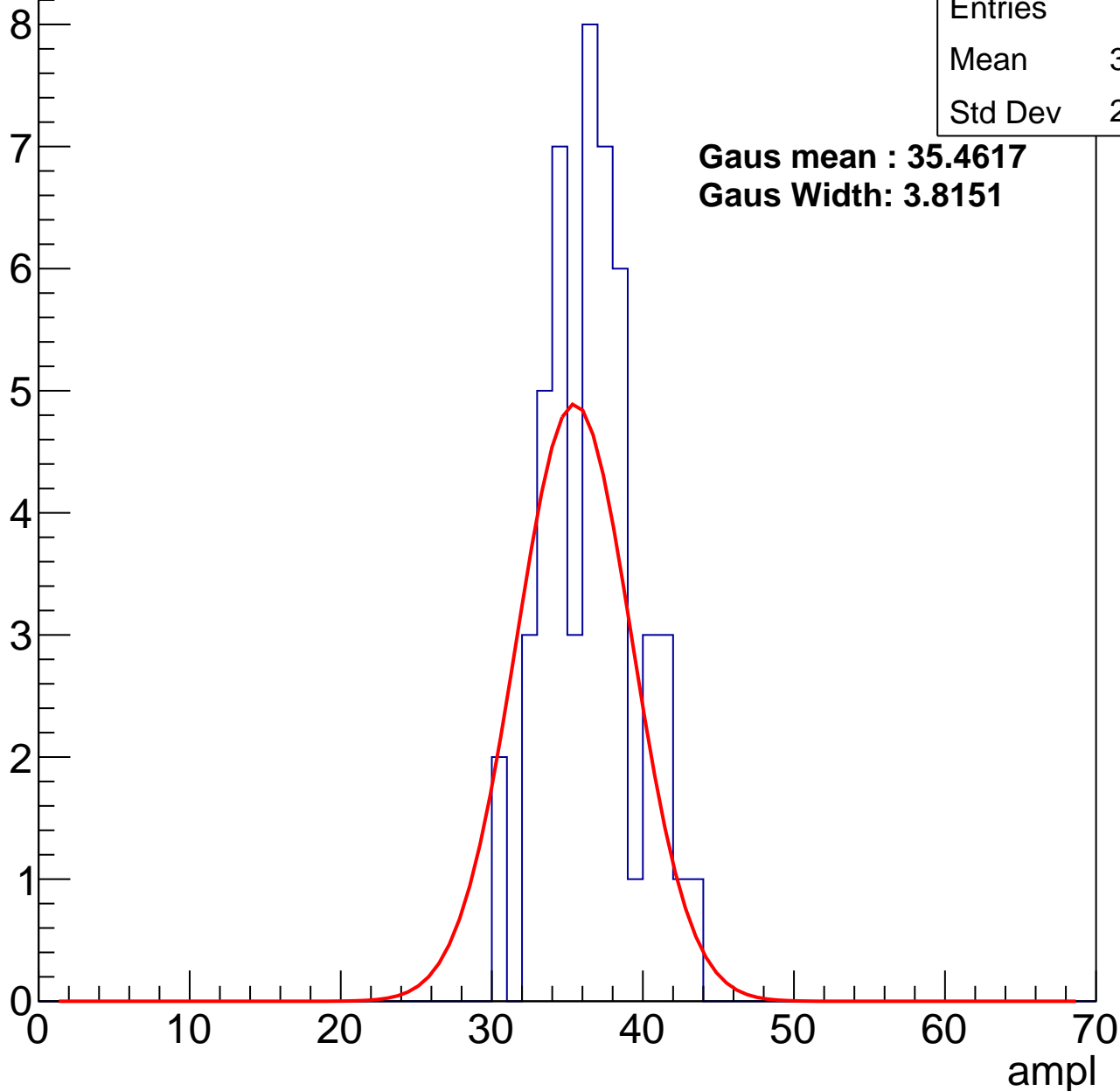
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 36.12 |
| Std Dev | 2.978 |

Gaus mean : 35.4617

Gaus Width: 3.8151



B0L001S, U7-ch87, adc2

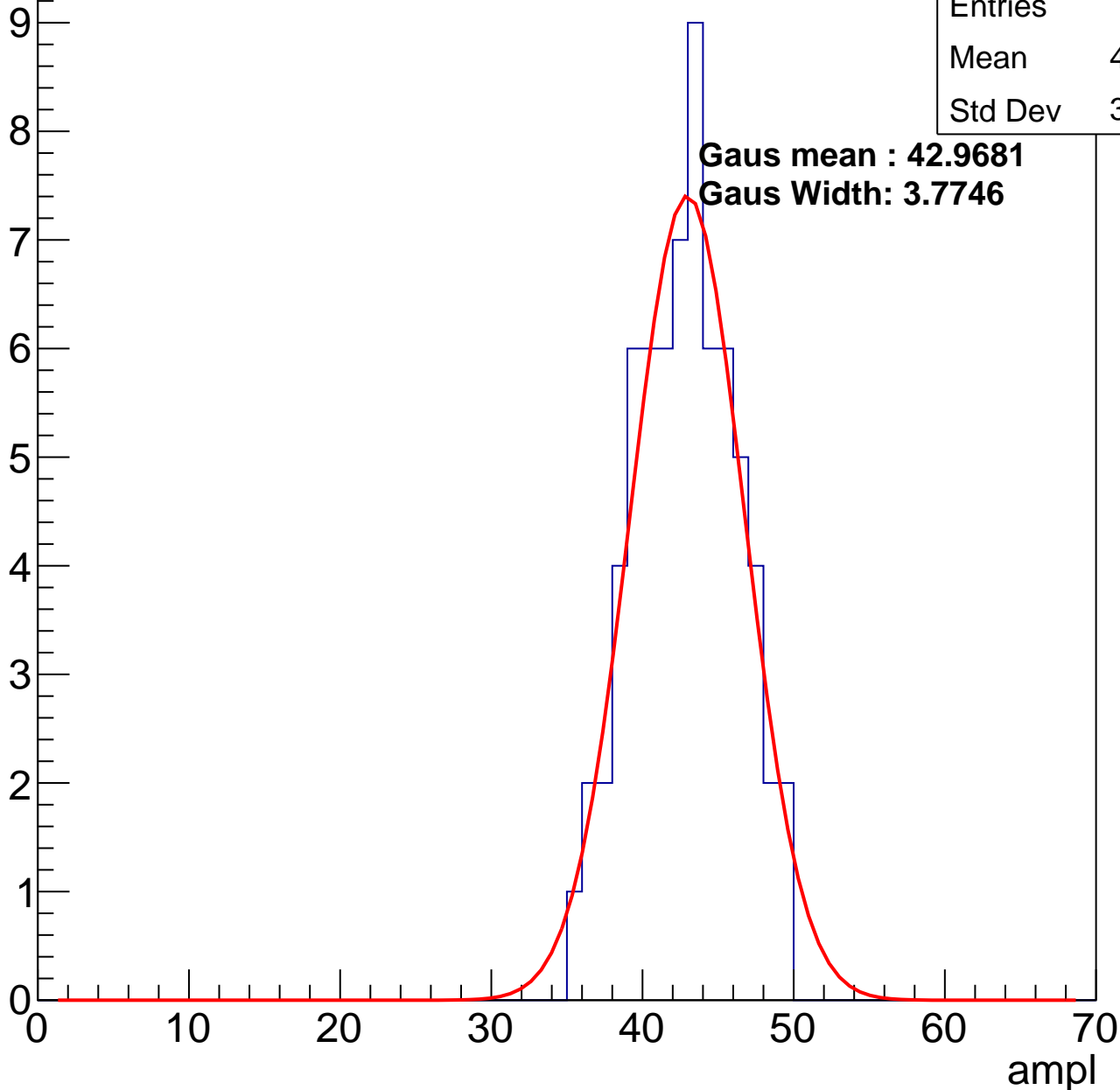
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.35 |
| Std Dev | 3.316 |

Gaus mean : 42.9681

Gaus Width: 3.7746



B0L001S, U7-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

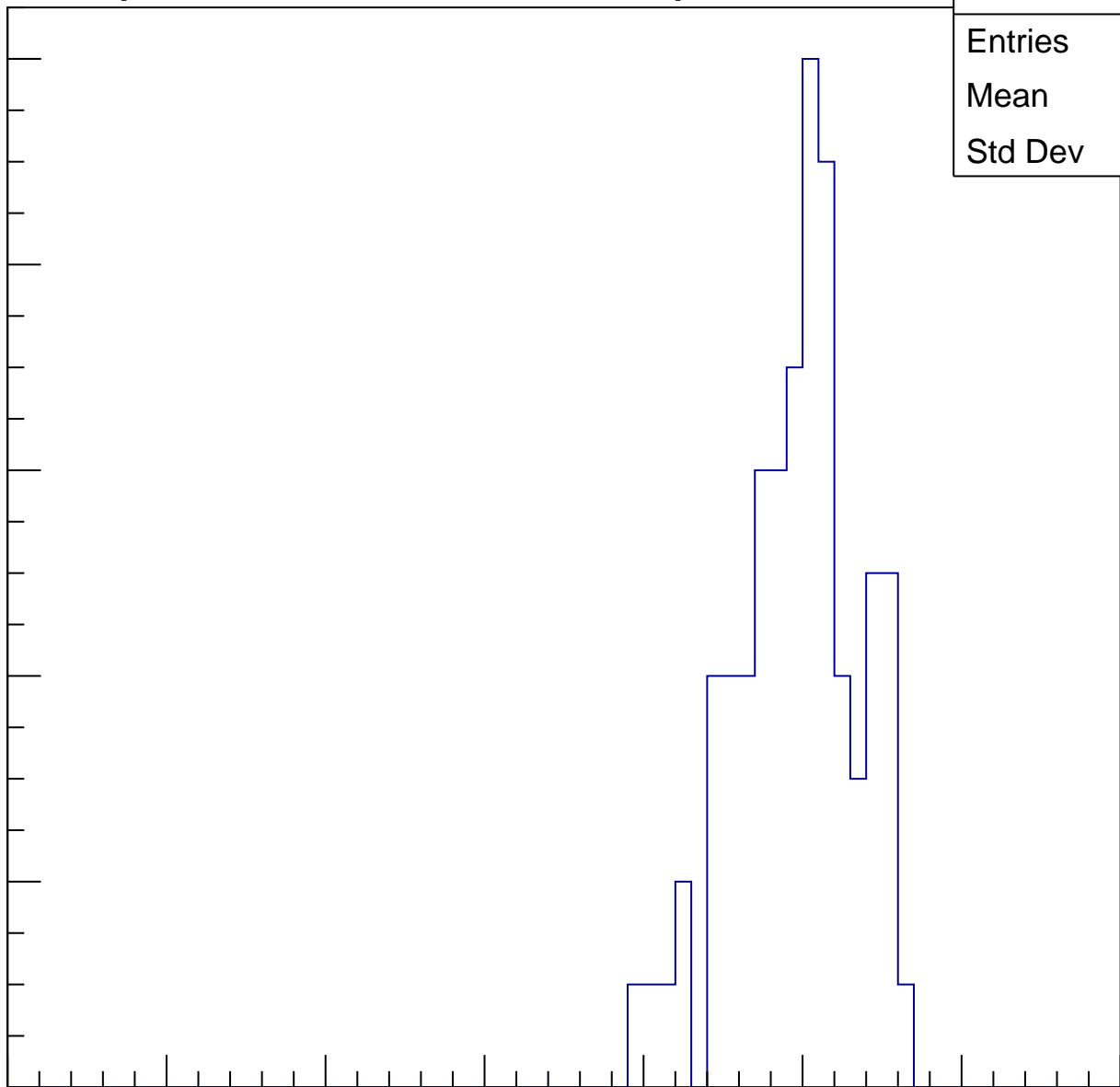
| | |
|---------|-------|
| Entries | 73 |
| Mean | 49.1 |
| Std Dev | 3.804 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

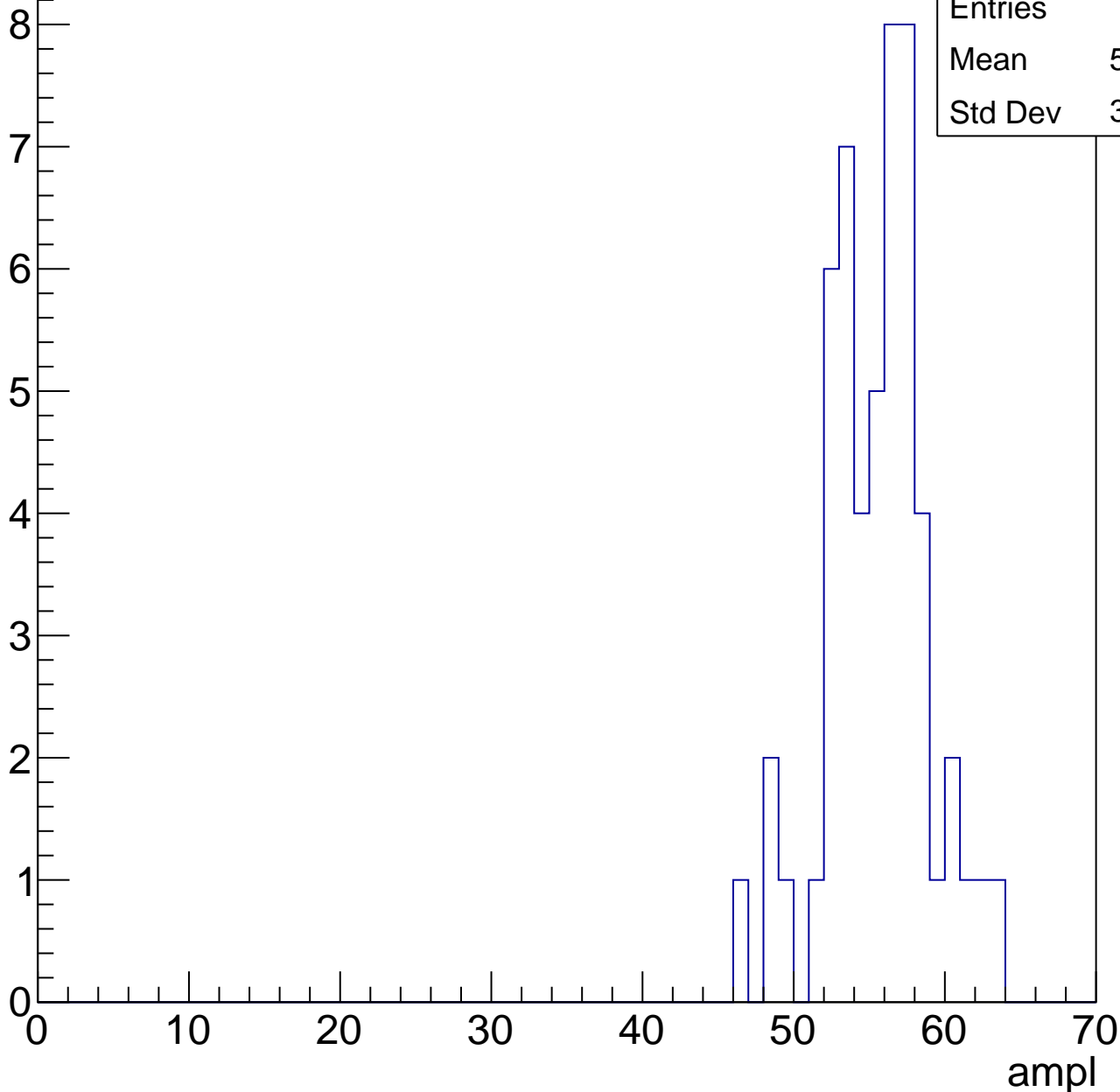


B0L001S, U7-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.04 |
| Std Dev | 3.387 |

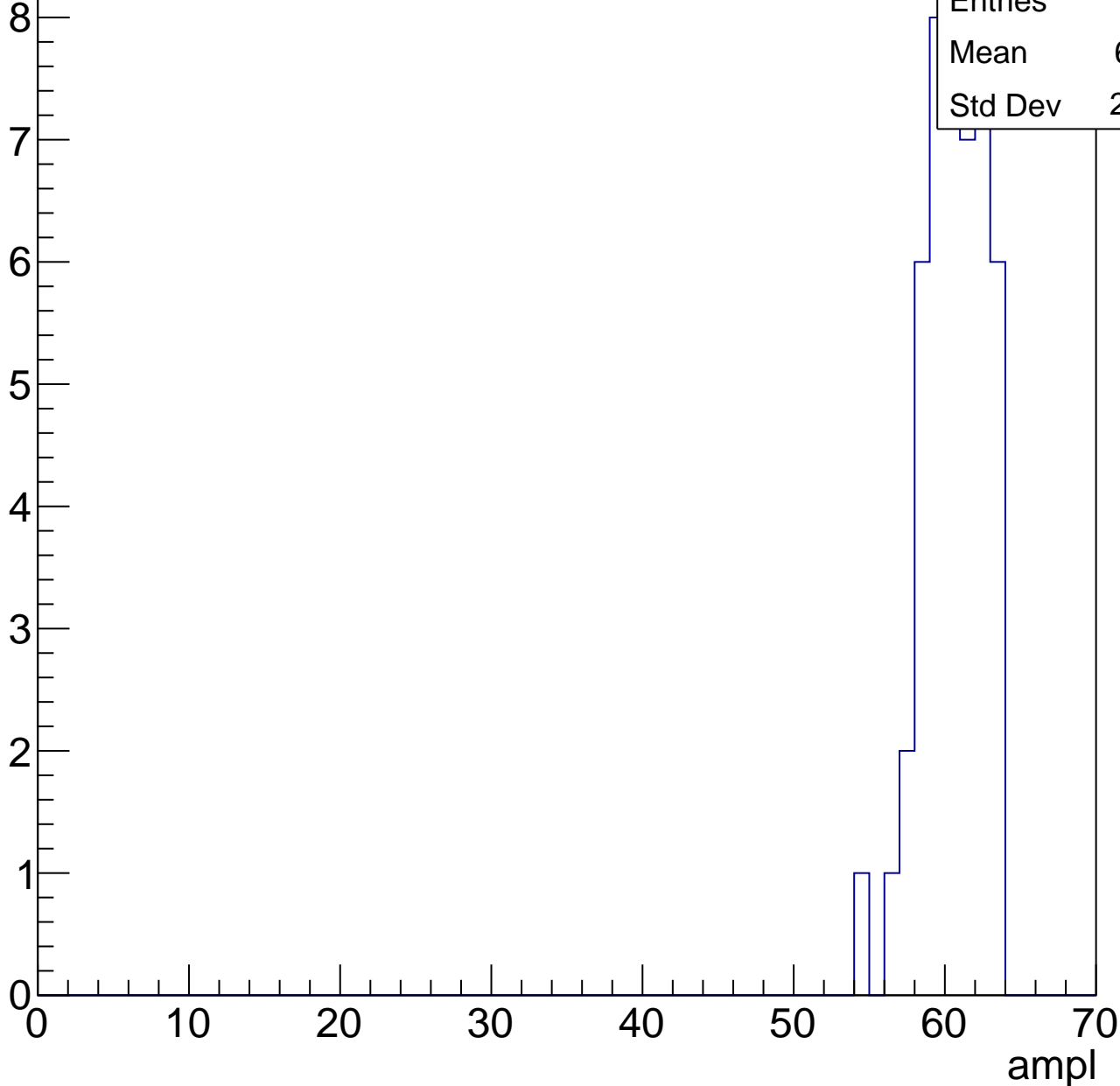


B0L001S, U7-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

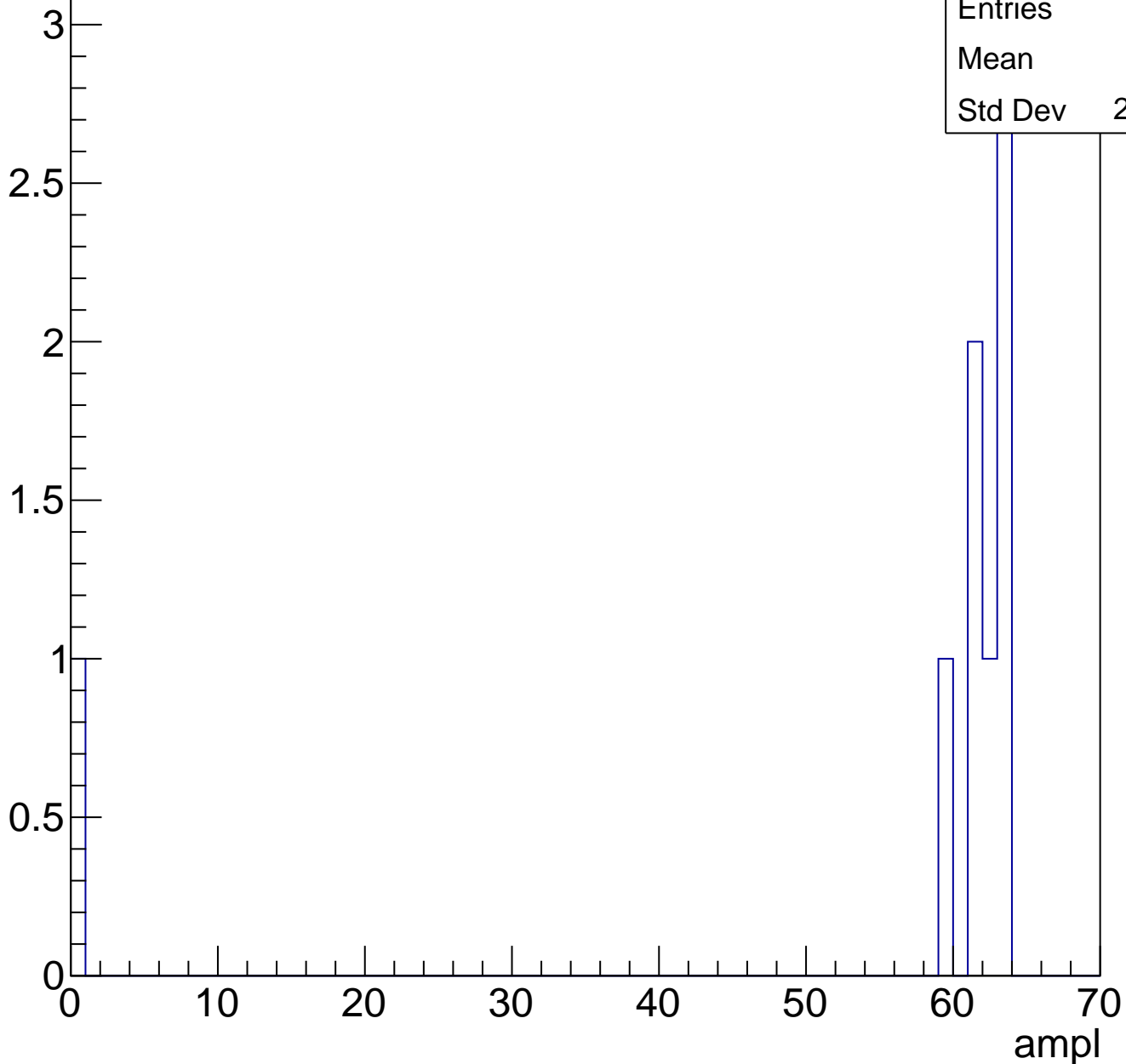
| | |
|---------|-------|
| Entries | 47 |
| Mean | 60.11 |
| Std Dev | 2.034 |



B0L001S, U7-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



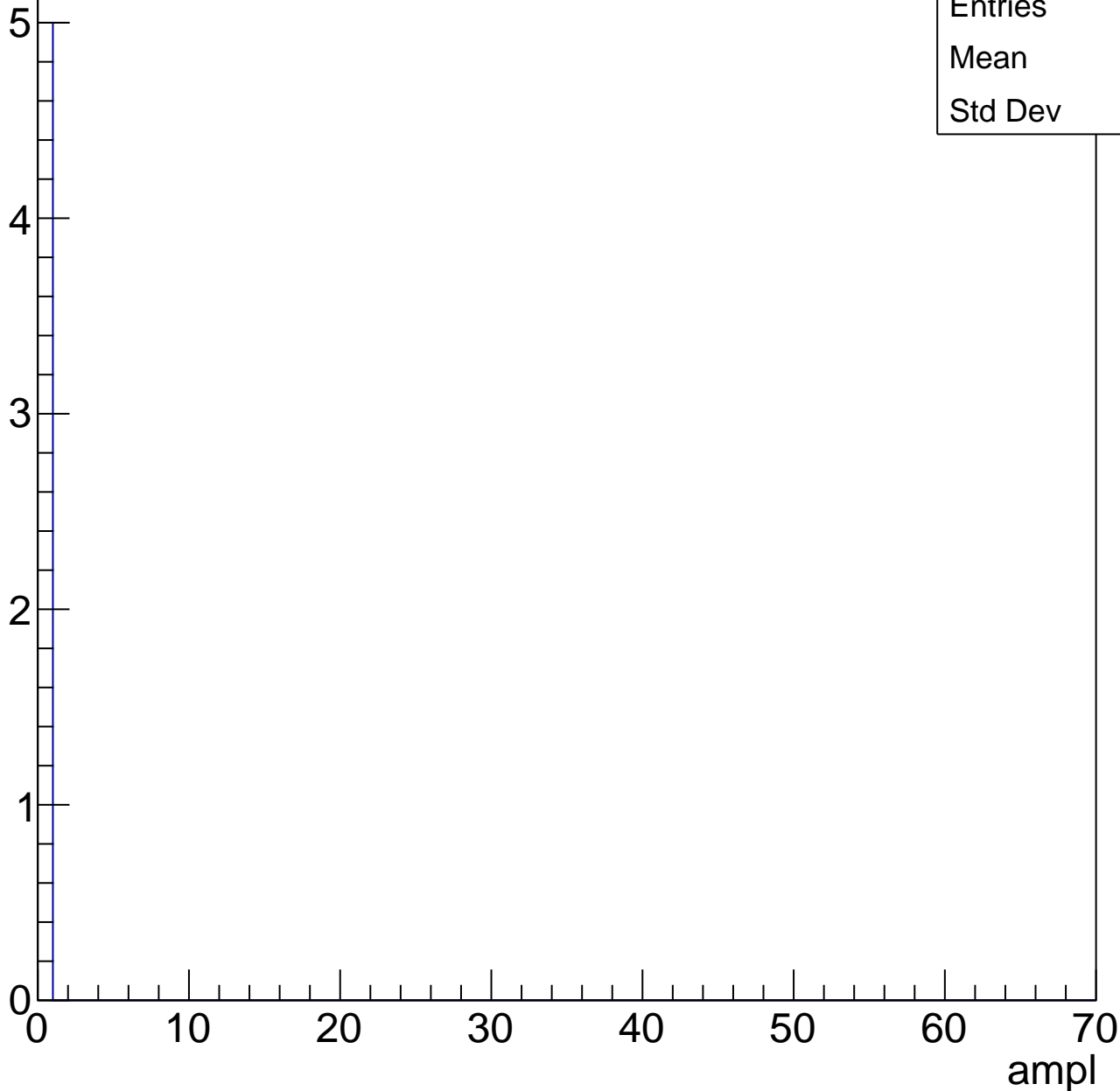
| | |
|---------|-------|
| Entries | 8 |
| Mean | 54 |
| Std Dev | 20.45 |

B0L001S, U7-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|---|
| Entries | 5 |
| Mean | 0 |
| Std Dev | 0 |



B0L001S, U7-ch88, adc0

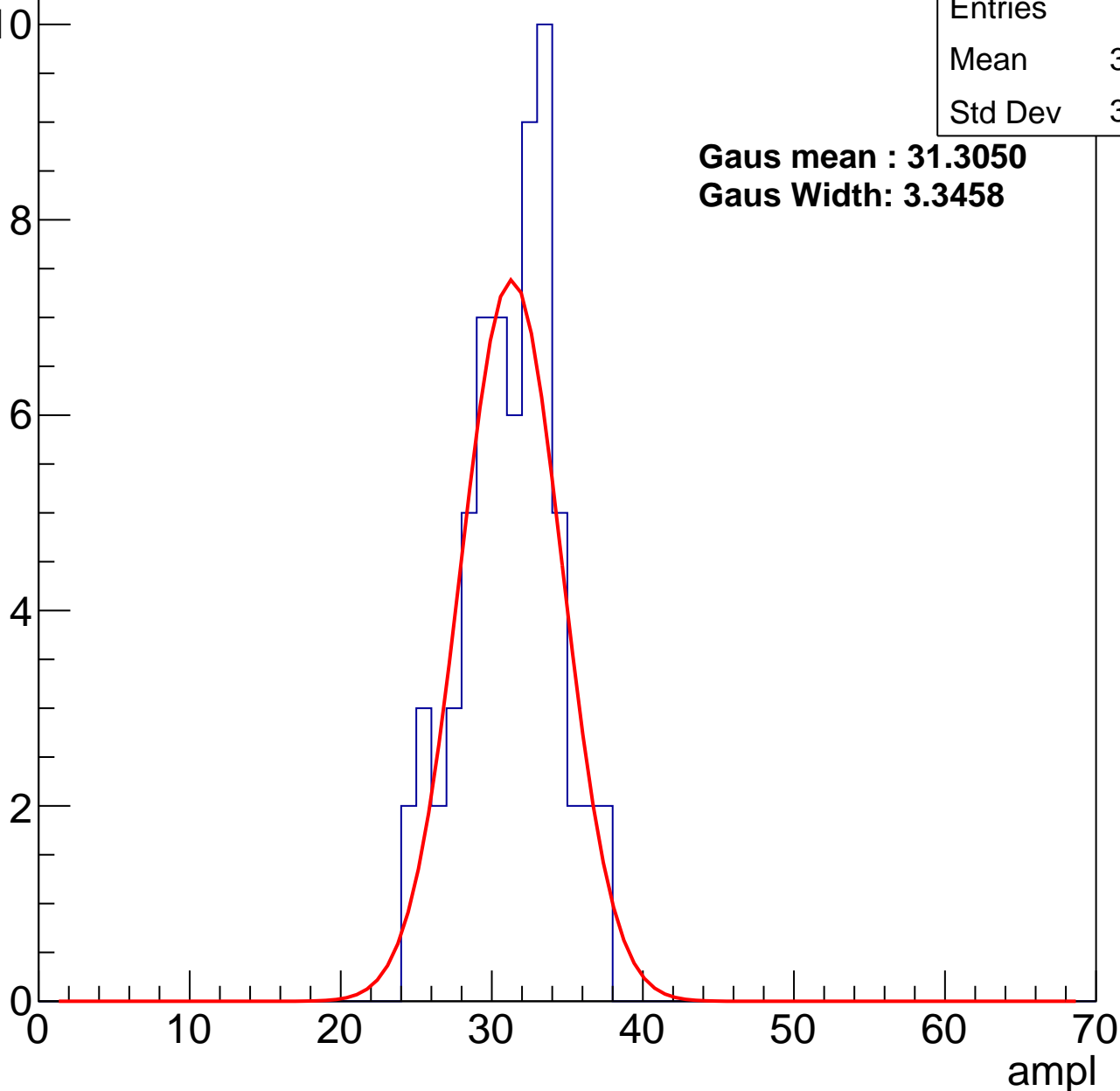
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 30.75 |
| Std Dev | 3.108 |

Gaus mean : 31.3050

Gaus Width: 3.3458



B0L001S, U7-ch88, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 70 |
| Mean | 37.54 |
| Std Dev | 3.396 |

Gaus mean : 37.9913

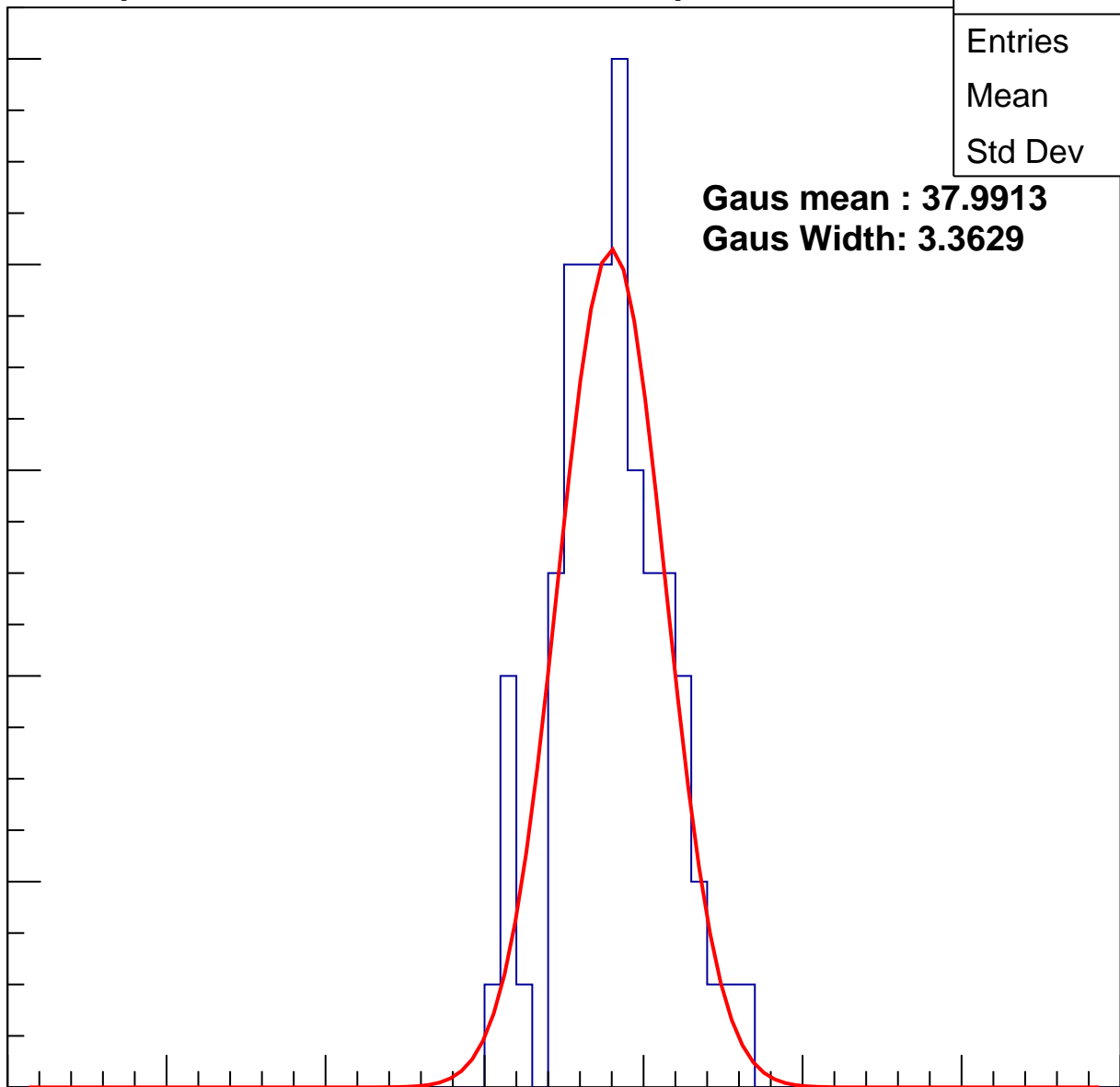
Gaus Width: 3.3629

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch88, adc2

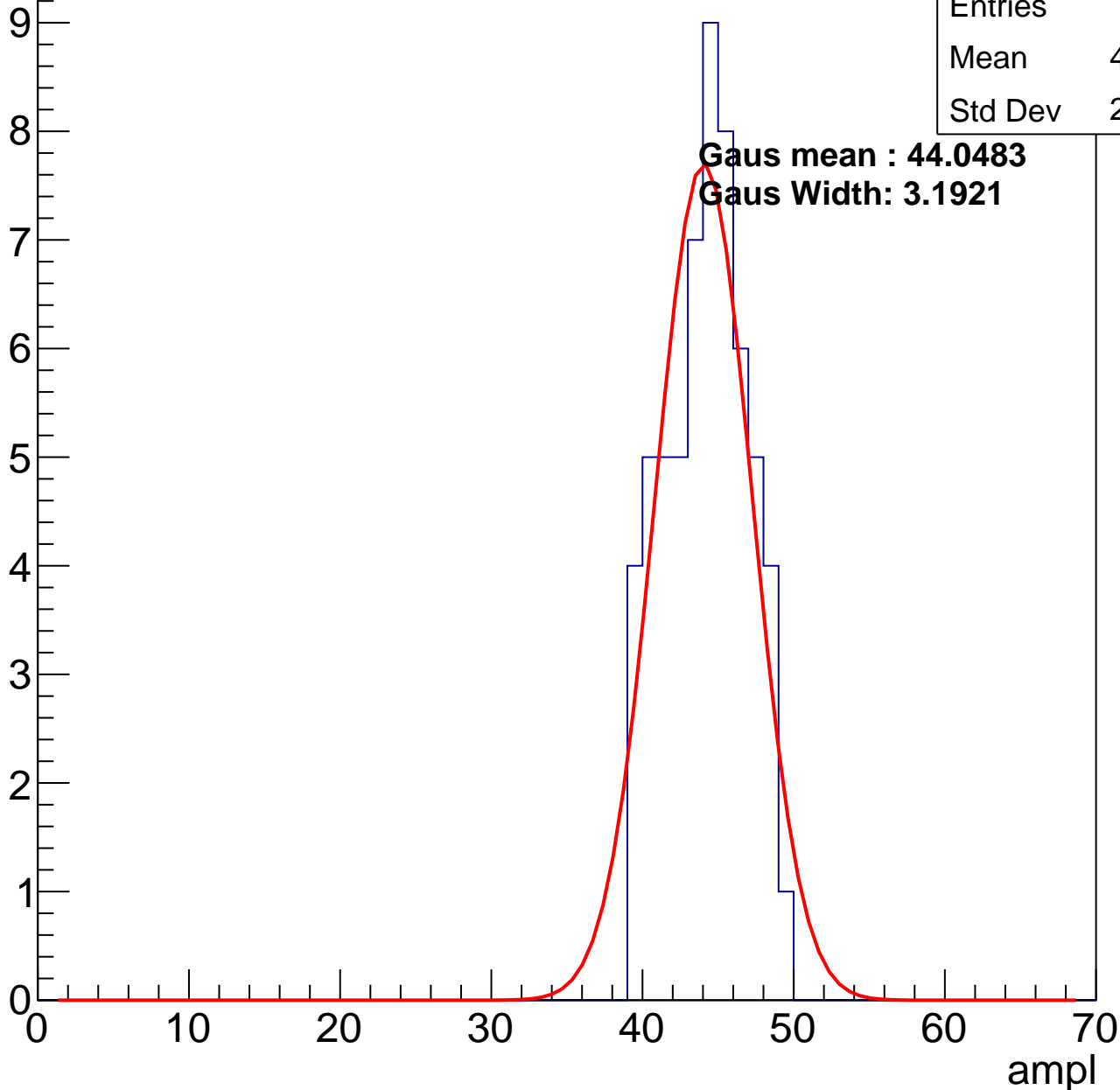
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 43.73 |
| Std Dev | 2.648 |

Gaus mean : 44.0483

Gaus Width: 3.1921

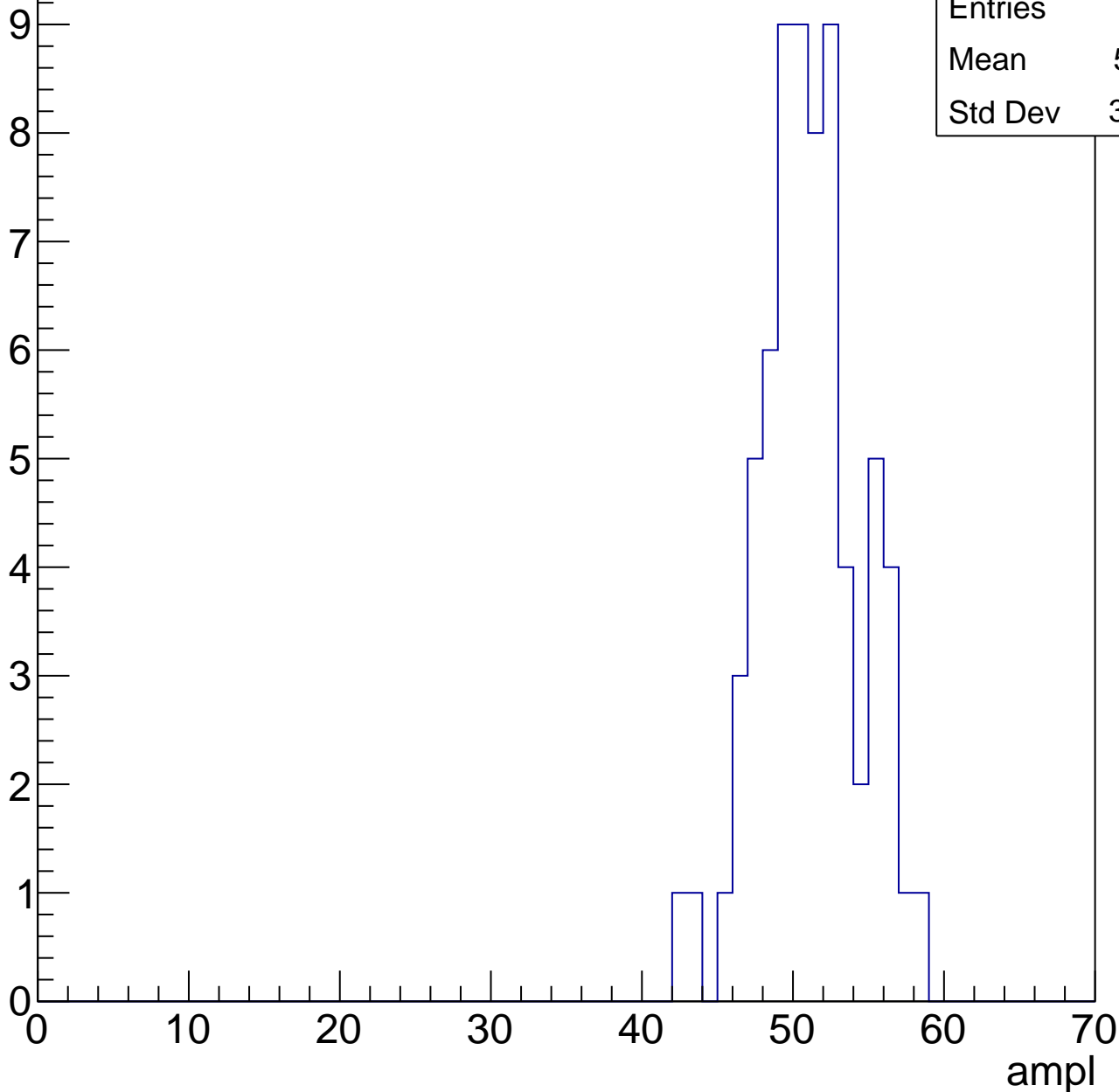


B0L001S, U7-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.61 |
| Std Dev | 3.258 |

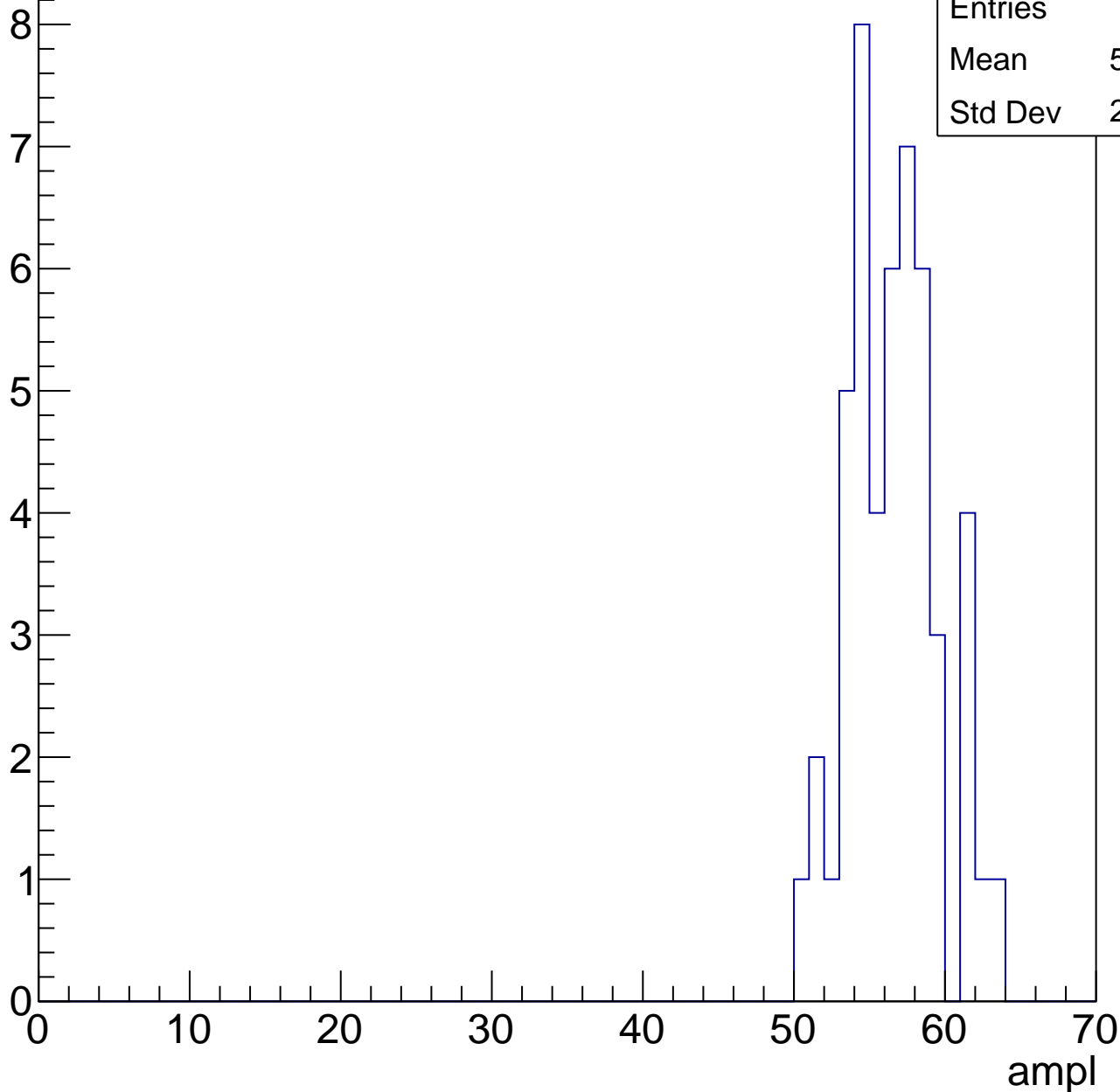


B0L001S, U7-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

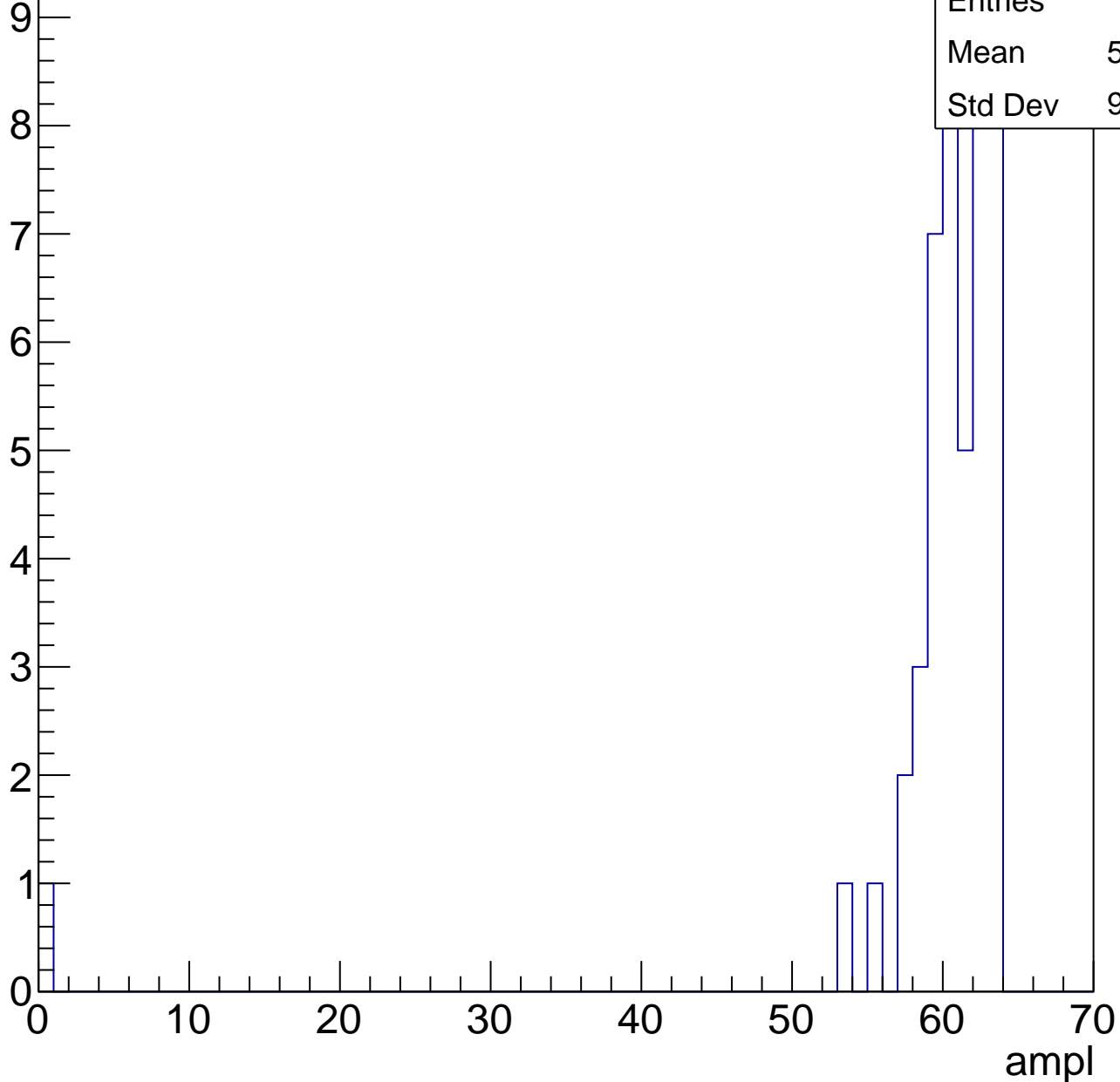
| | |
|---------|-------|
| Entries | 49 |
| Mean | 56.12 |
| Std Dev | 2.946 |



B0L001S, U7-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

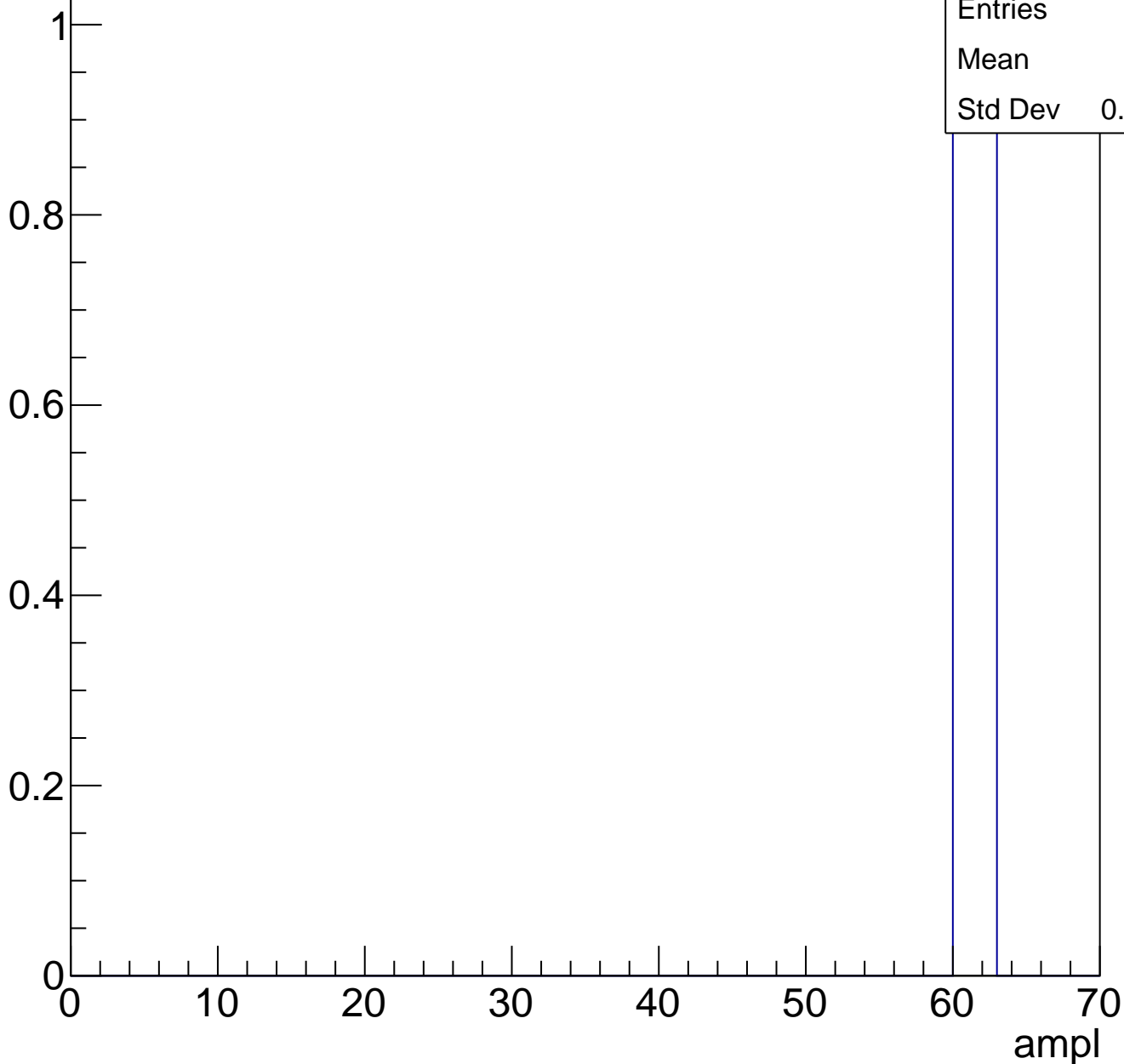
Entry



B0L001S, U7-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch89, adc0

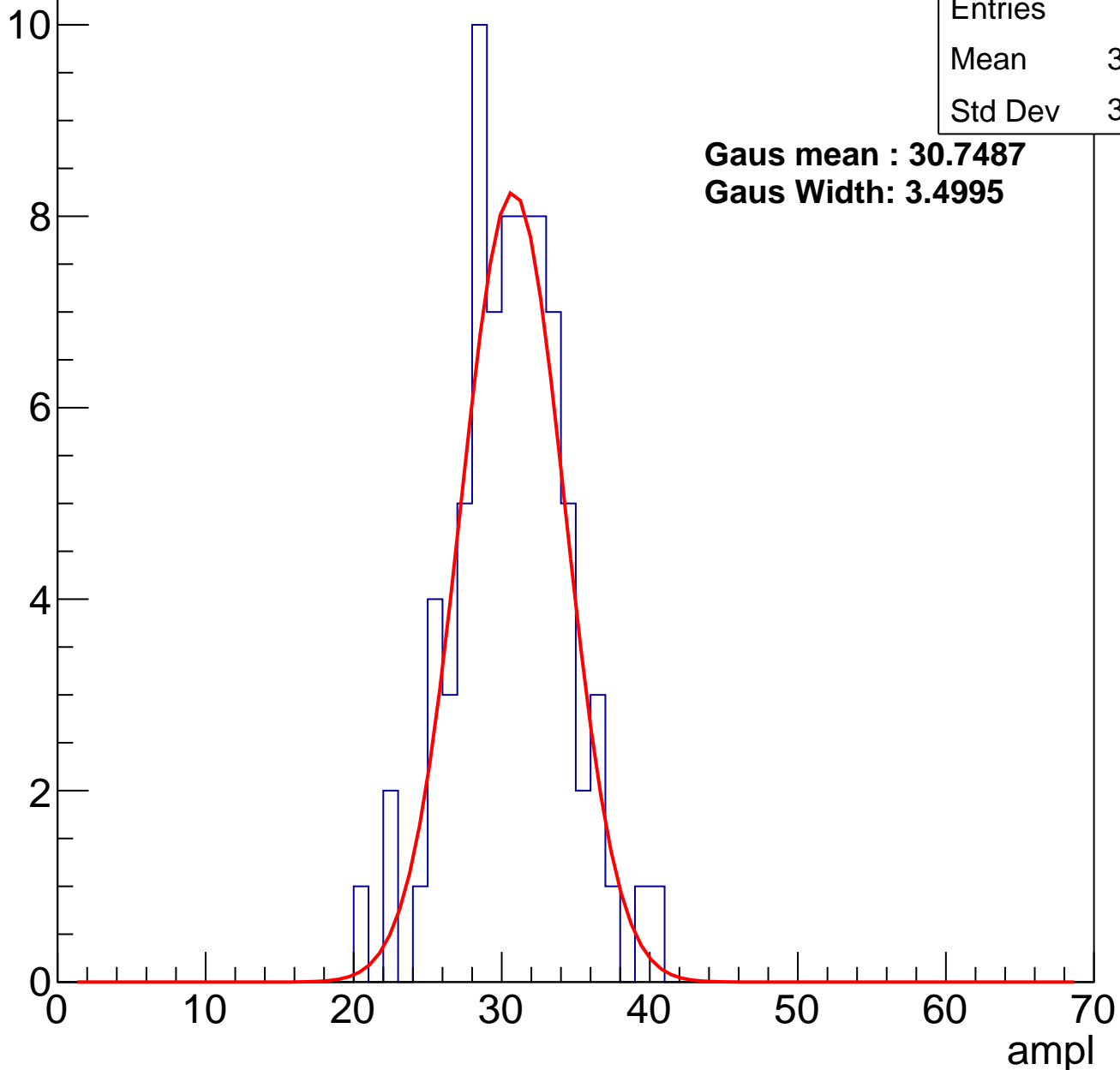
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 77 |
| Mean | 30.17 |
| Std Dev | 3.733 |

Gaus mean : 30.7487

Gaus Width: 3.4995

Entry



B0L001S, U7-ch89, adc1

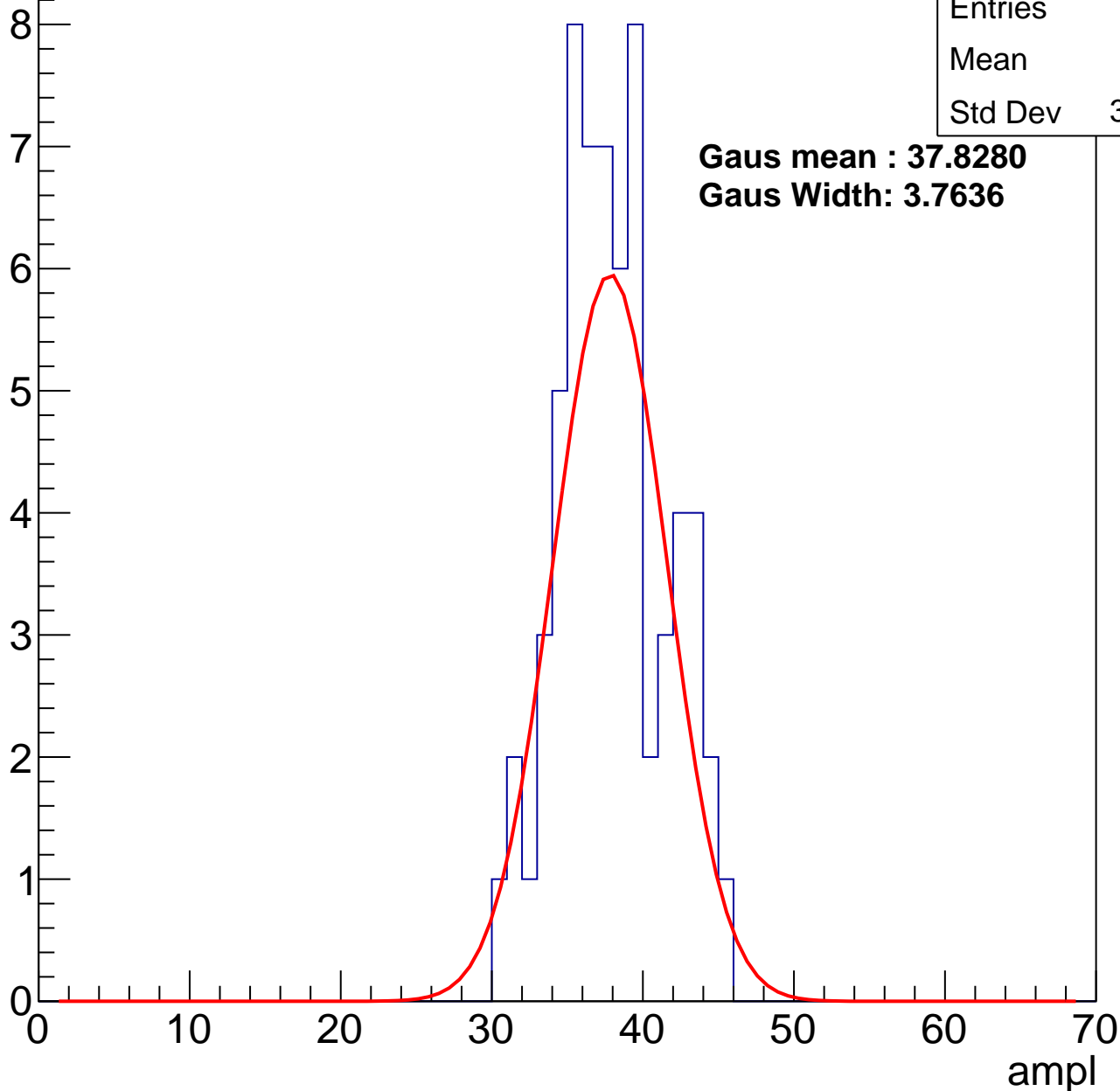
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37.5 |
| Std Dev | 3.473 |

Gaus mean : 37.8280

Gaus Width: 3.7636



B0L001S, U7-ch89, adc2

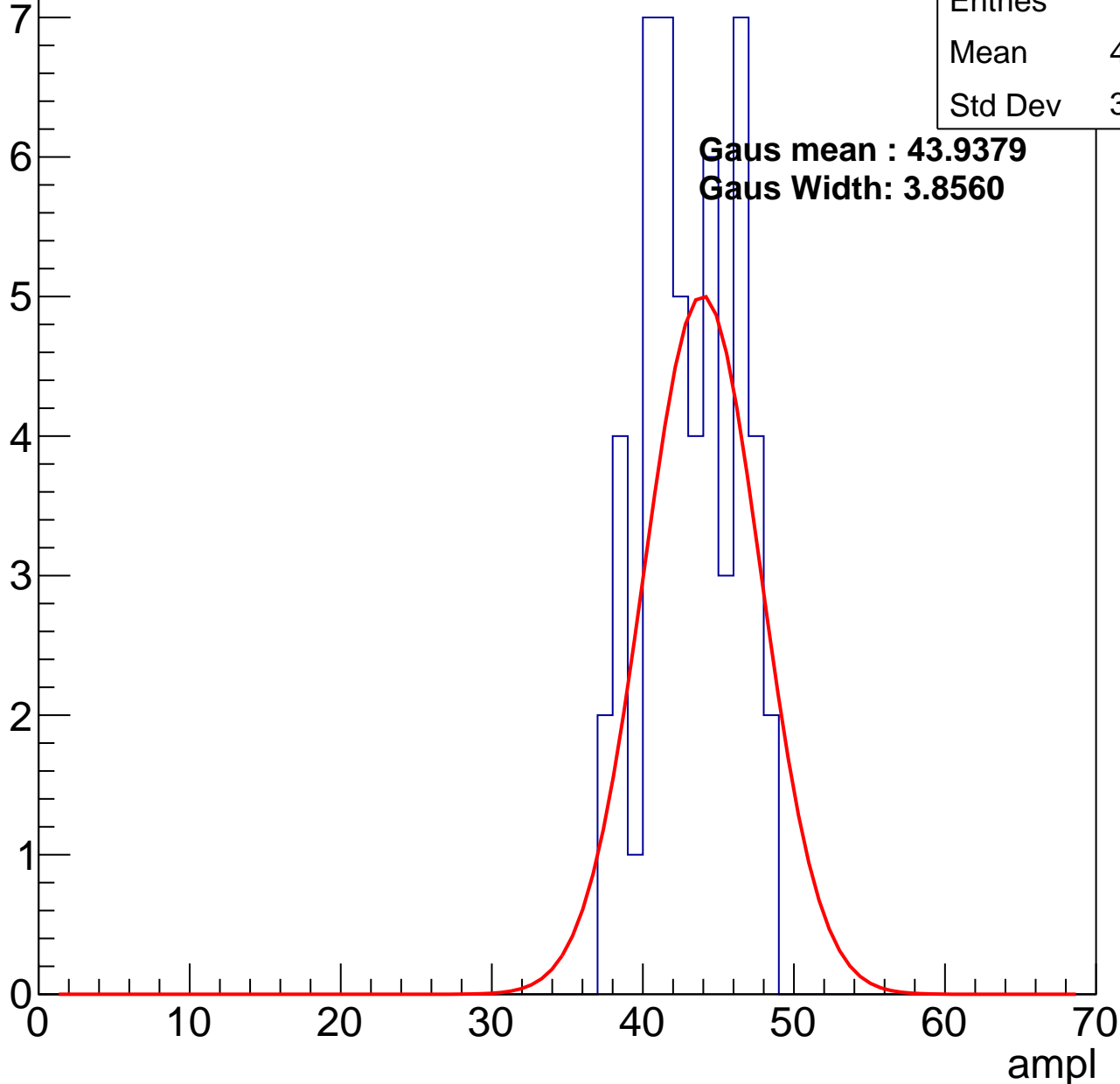
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 42.67 |
| Std Dev | 3.017 |

Gaus mean : 43.9379

Gaus Width: 3.8560

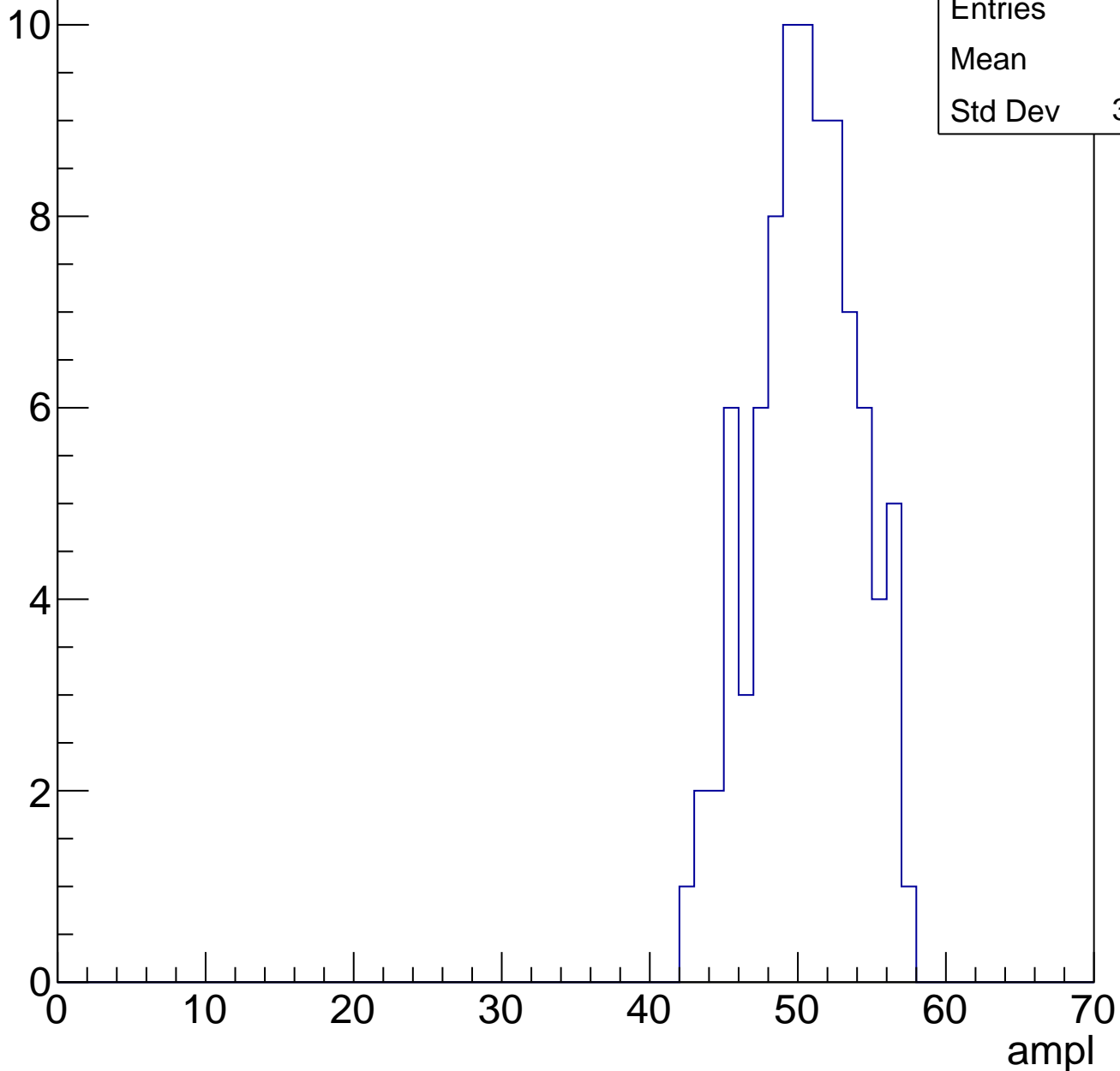


B0L001S, U7-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 50.1 |
| Std Dev | 3.451 |

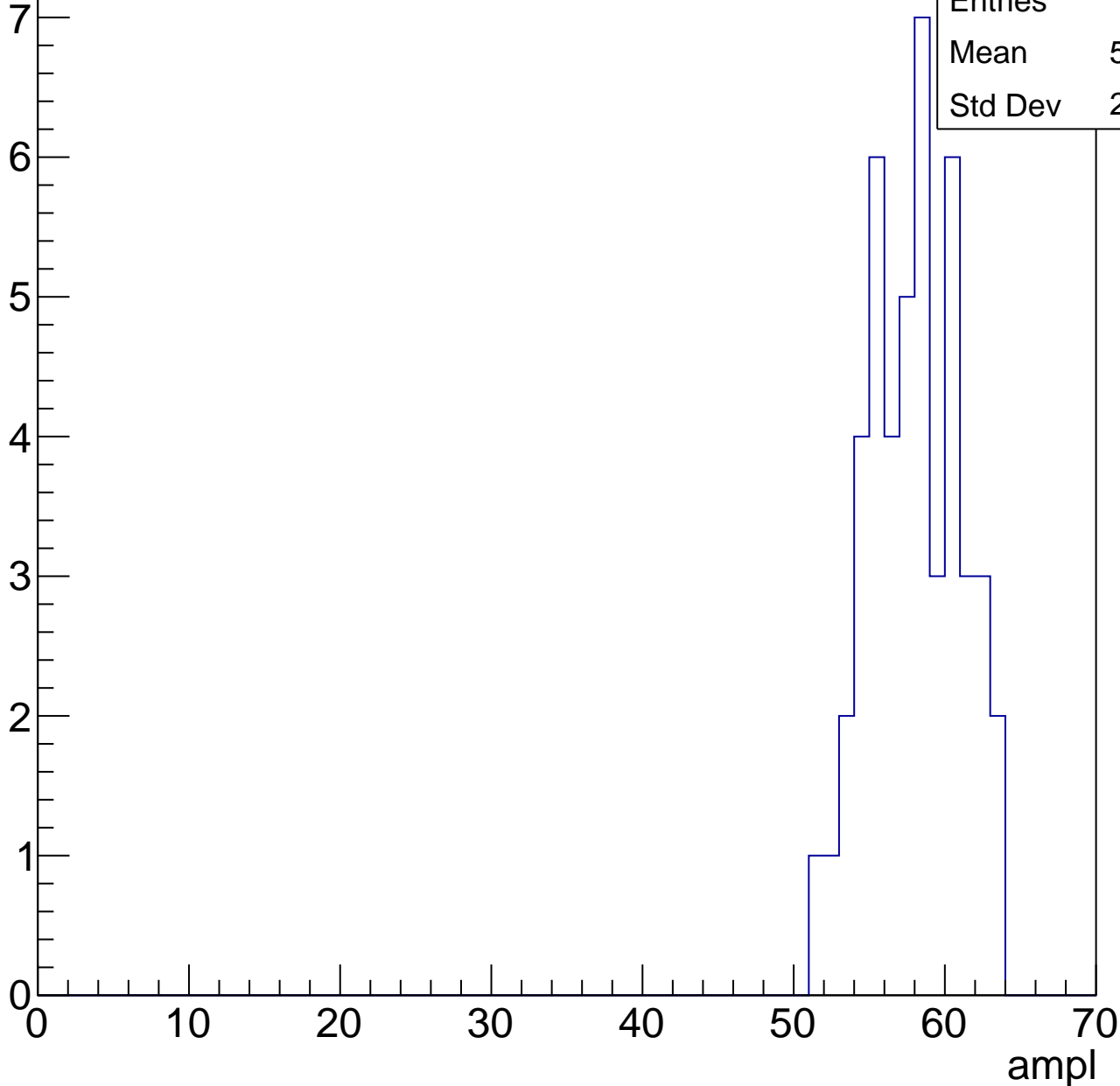


B0L001S, U7-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 57.49 |
| Std Dev | 2.967 |

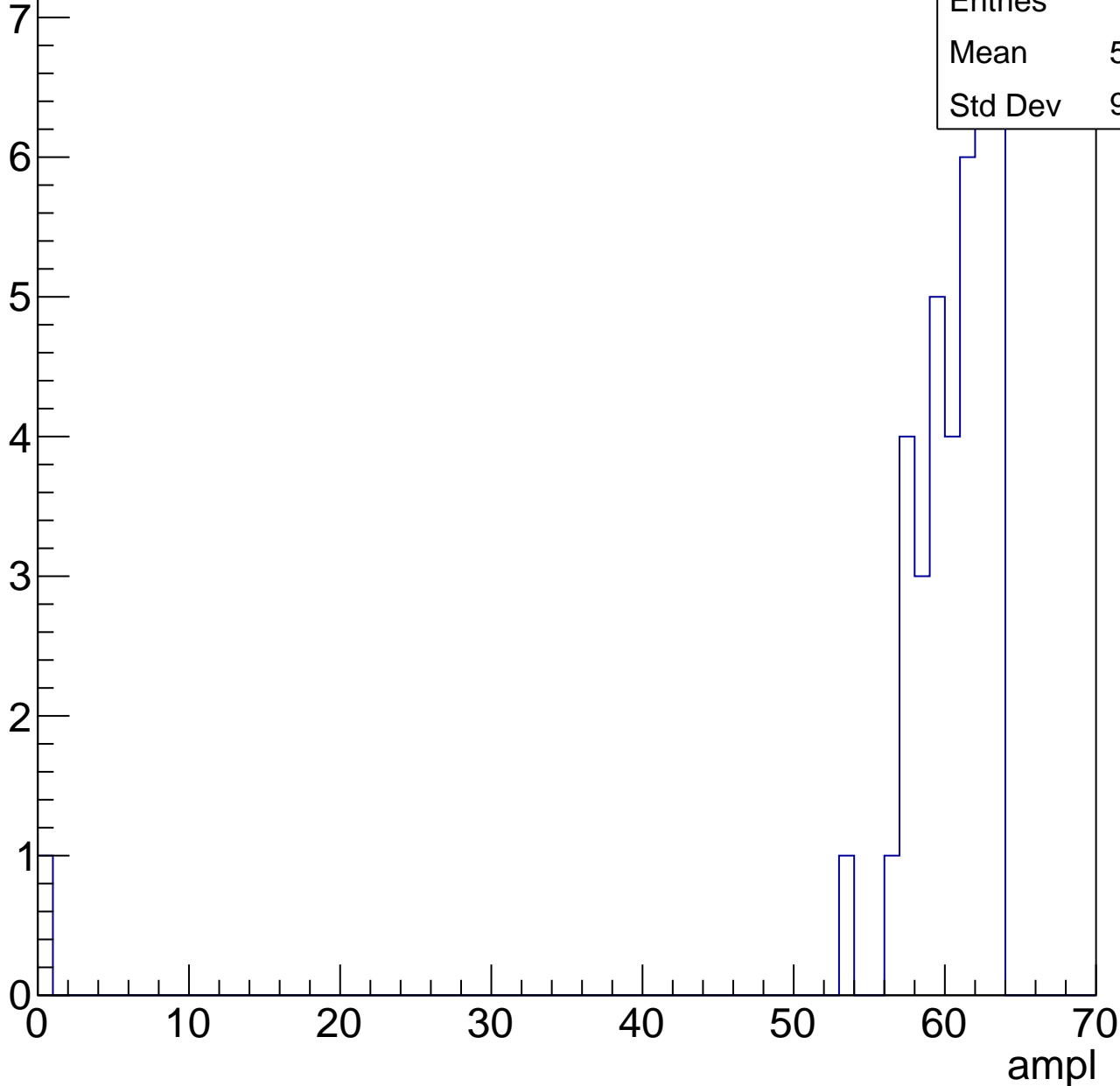


B0L001S, U7-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.64 |
| Std Dev | 9.797 |



B0L001S, U7-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

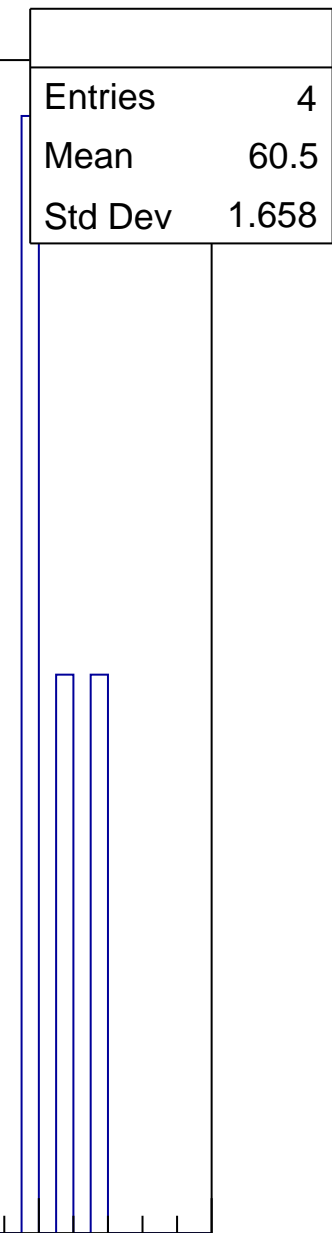
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 60.5 |
| Std Dev | 1.658 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch90, adc0

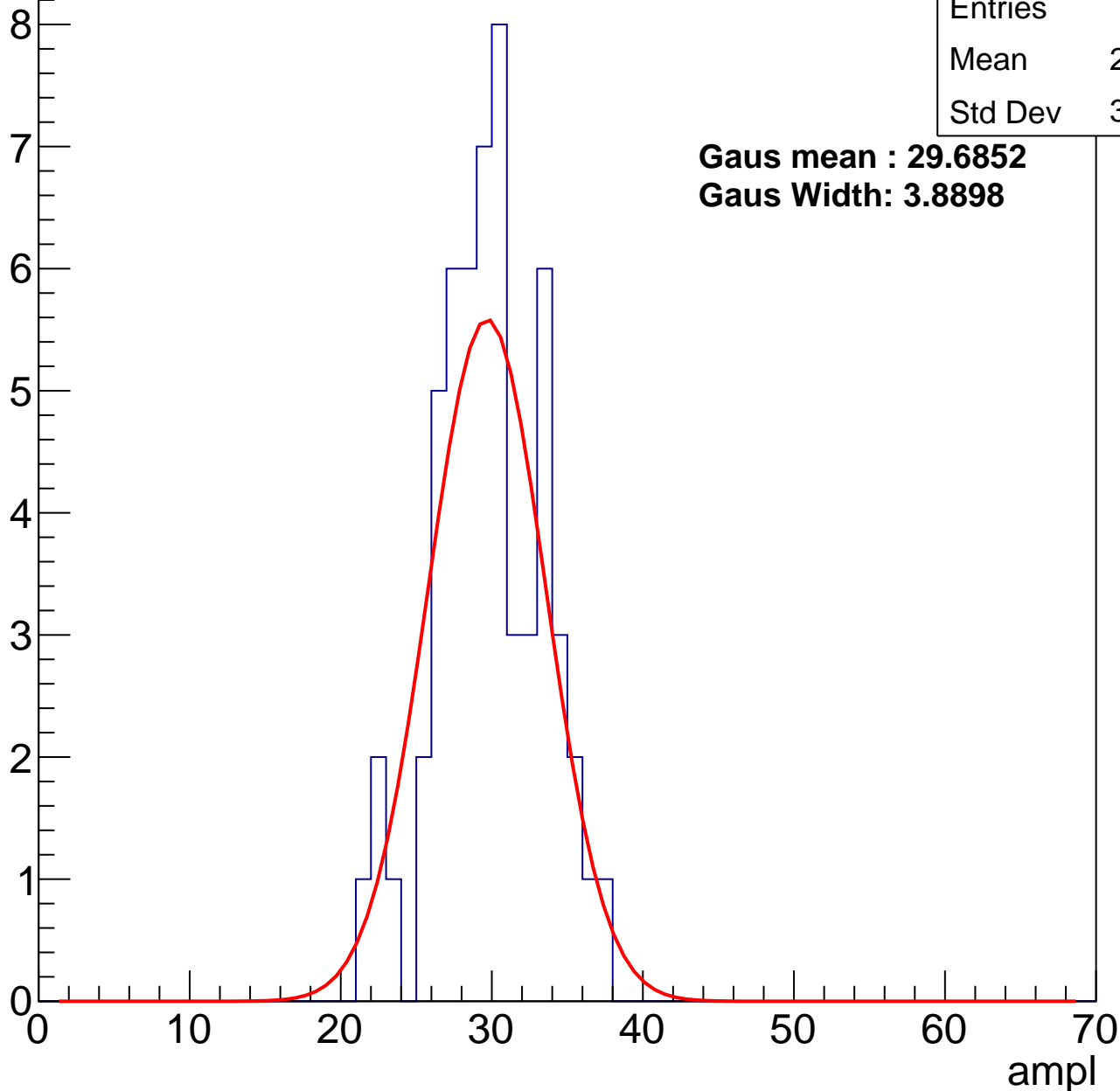
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 29.35 |
| Std Dev | 3.507 |

Gaus mean : 29.6852

Gaus Width: 3.8898



B0L001S, U7-ch90, adc1

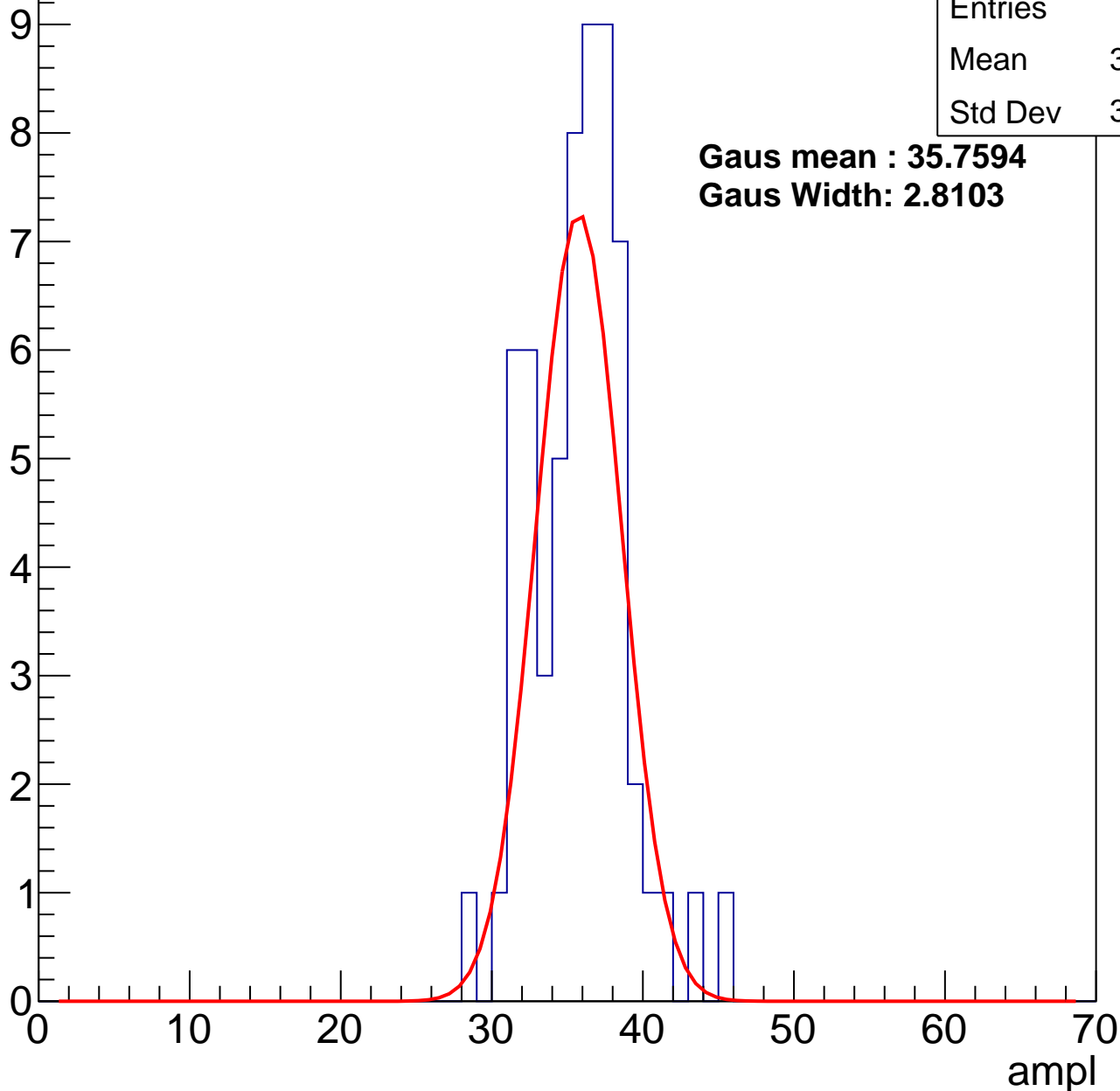
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.33 |
| Std Dev | 3.135 |

Gaus mean : 35.7594

Gaus Width: 2.8103



B0L001S, U7-ch90, adc2

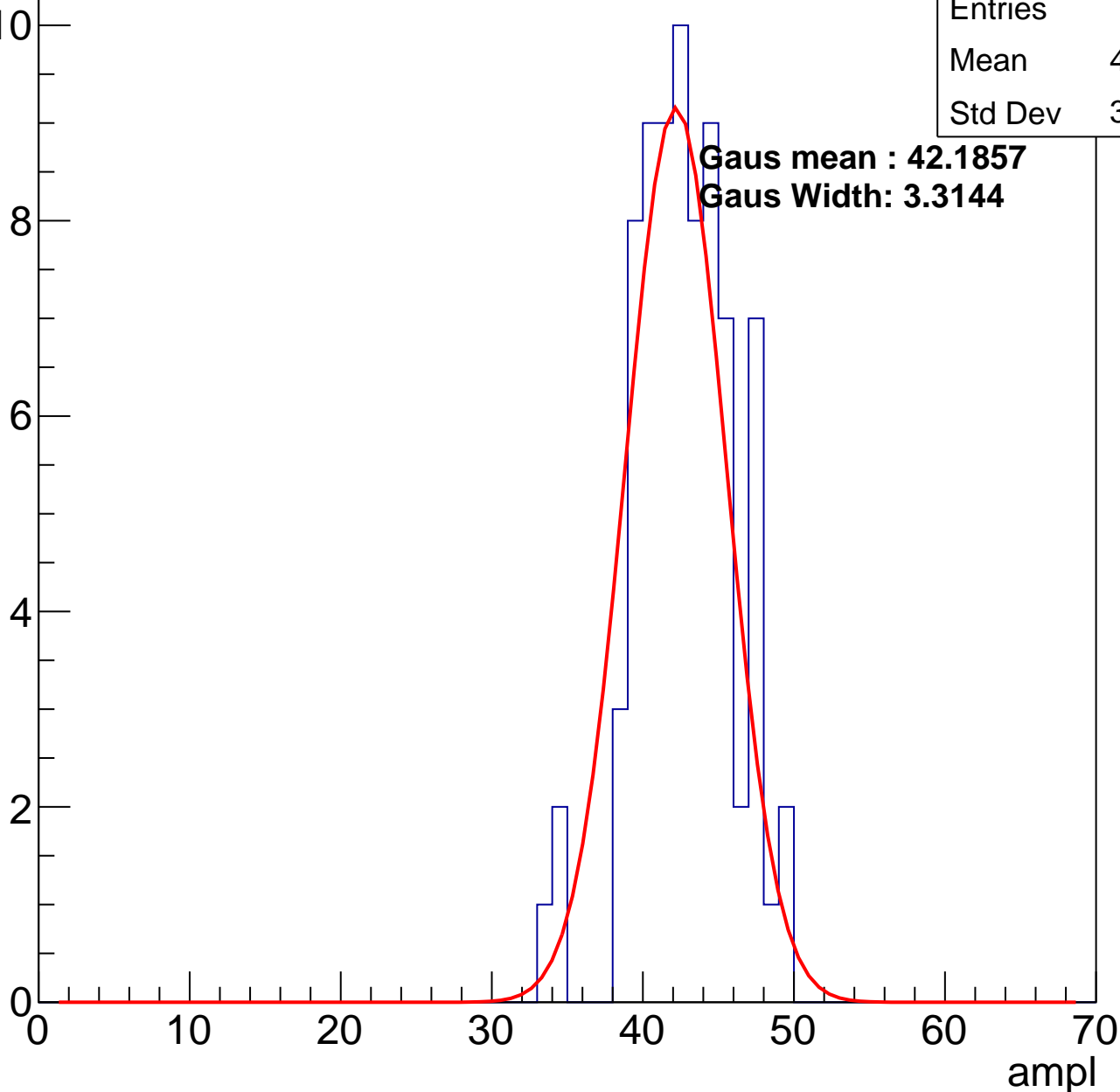
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 42.28 |
| Std Dev | 3.234 |

Gaus mean : 42.1857

Gaus Width: 3.3144

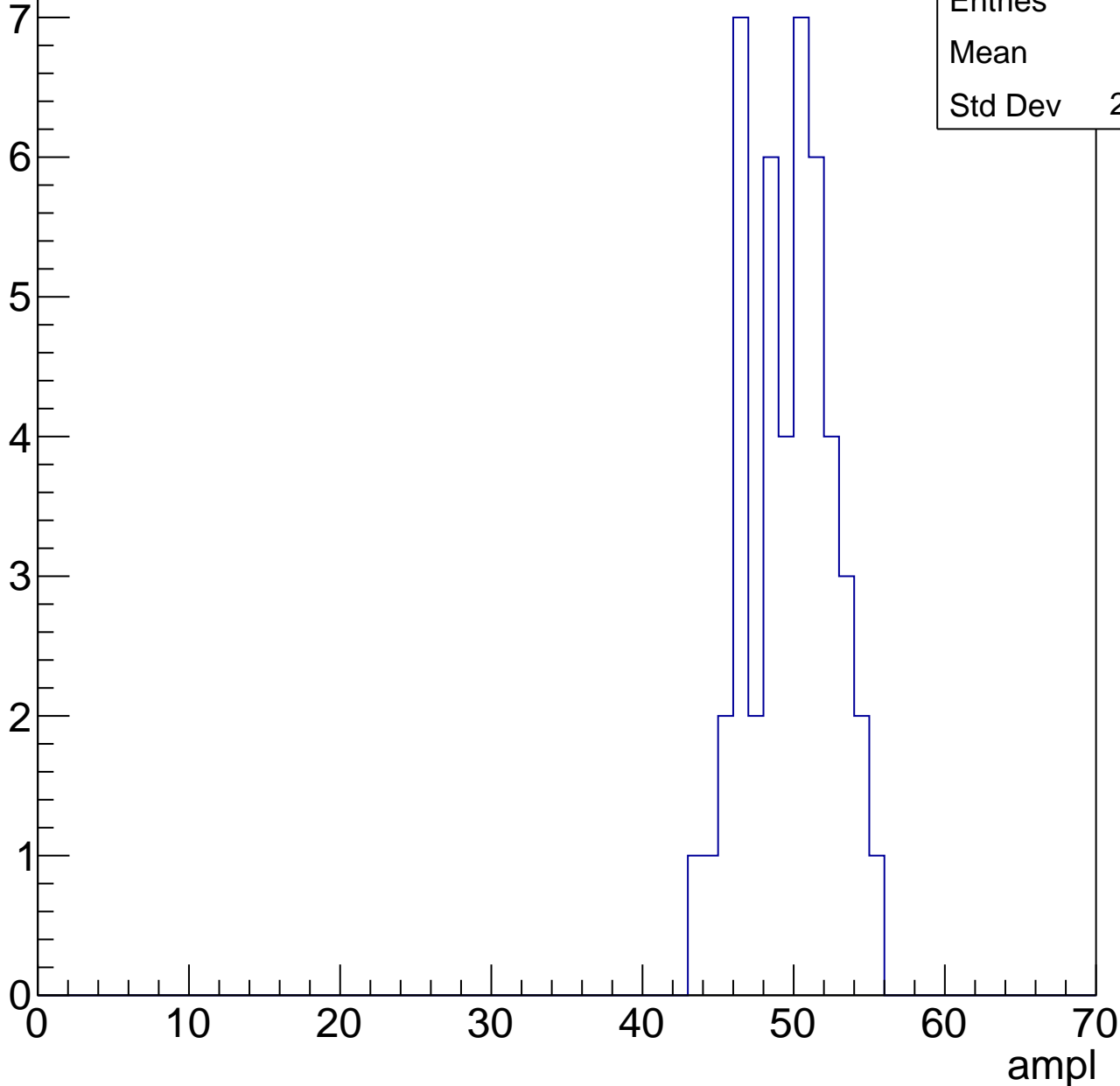


B0L001S, U7-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 49.2 |
| Std Dev | 2.833 |

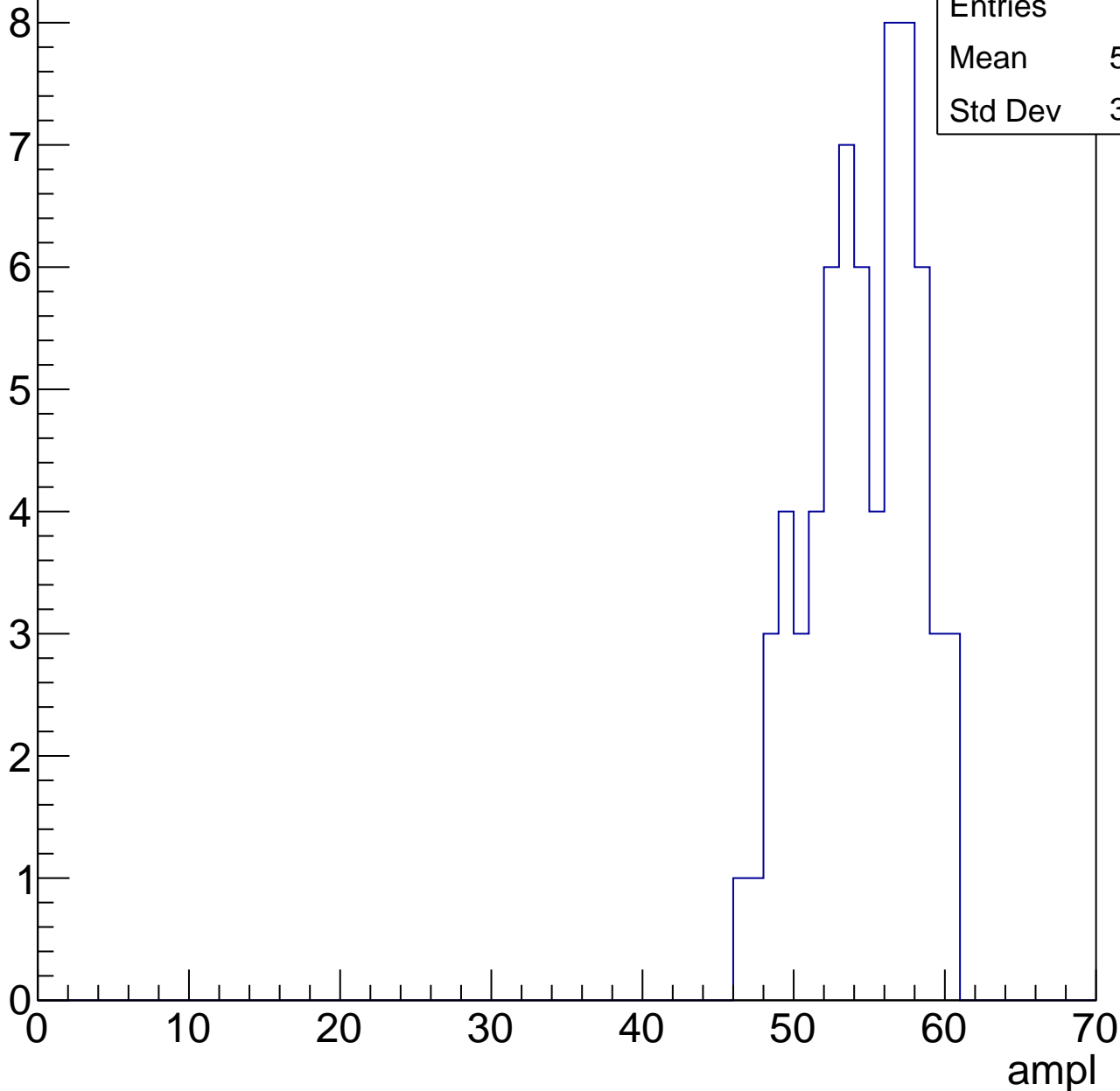


B0L001S, U7-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

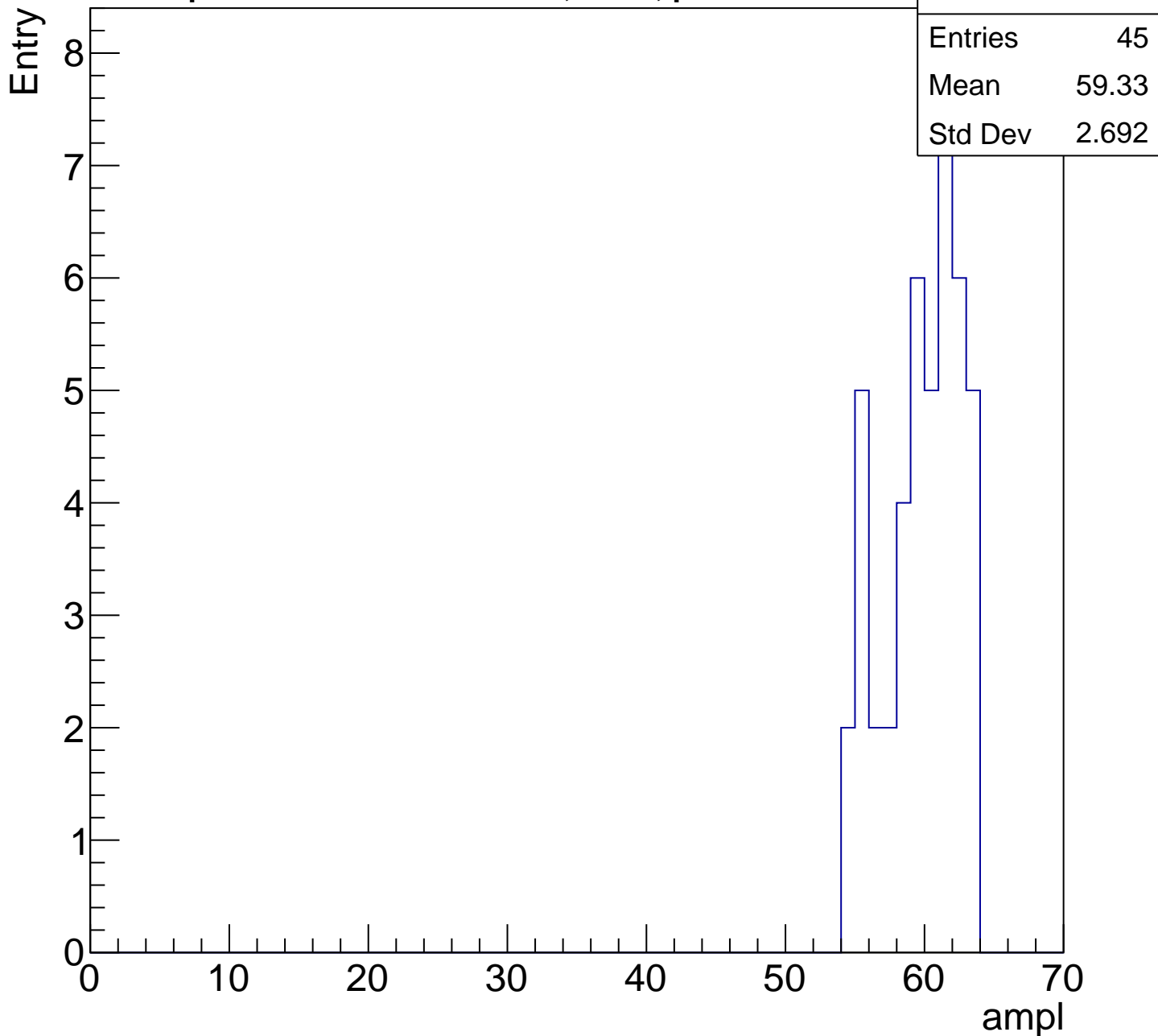
Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 54.07 |
| Std Dev | 3.504 |



B0L001S, U7-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

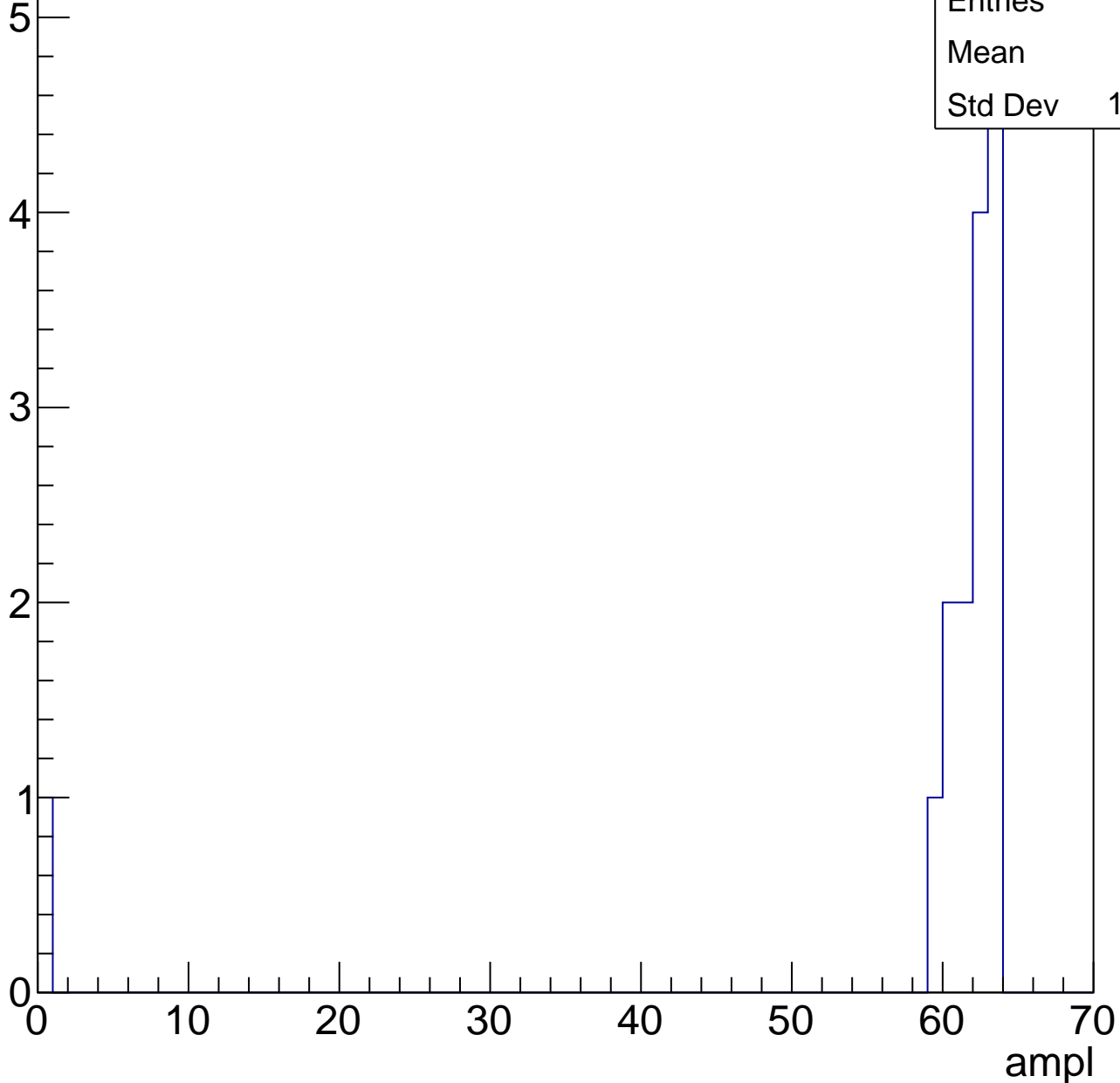


B0L001S, U7-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 57.6 |
| Std Dev | 15.44 |



B0L001S, U7-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch91, adc0

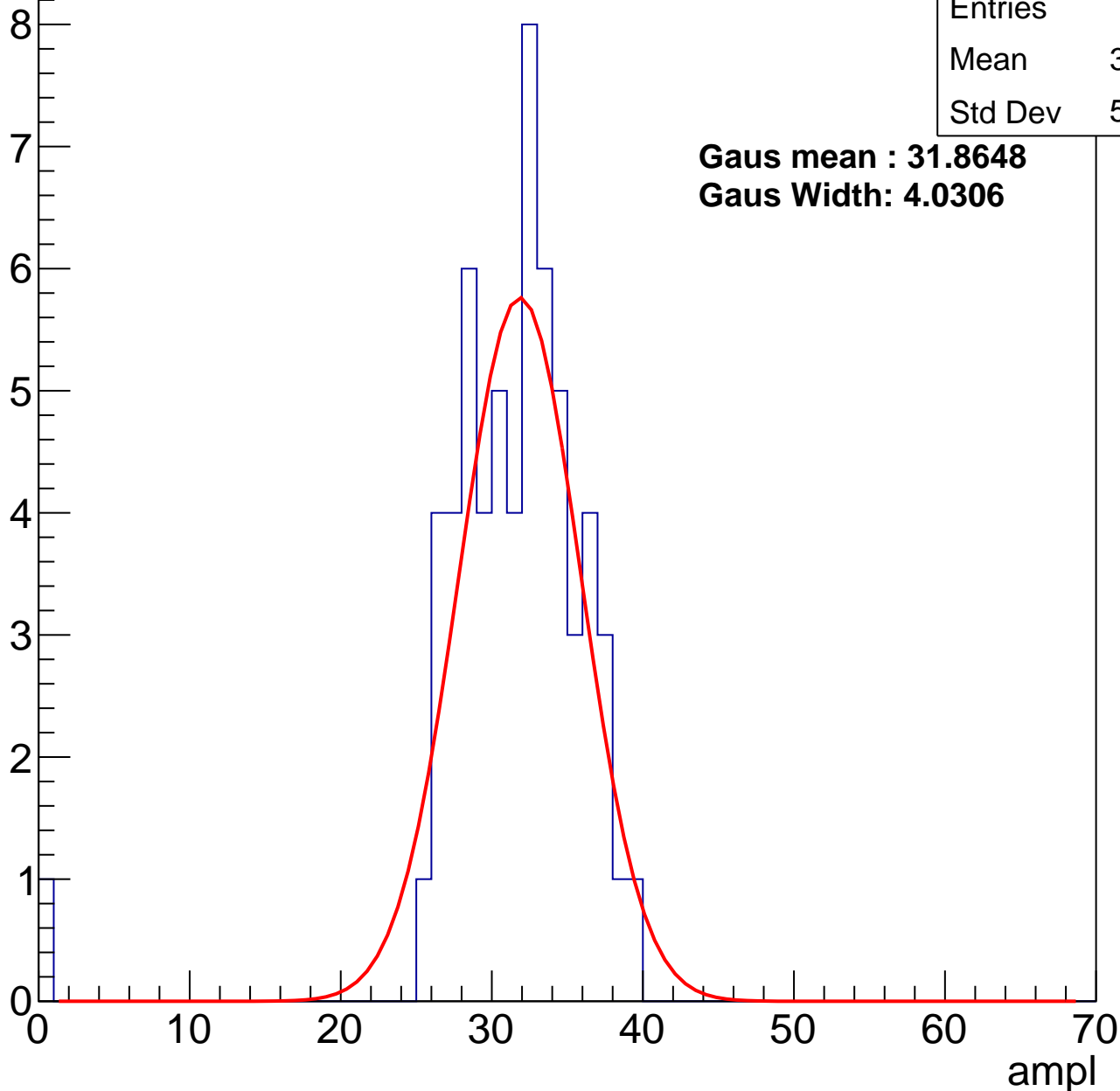
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 30.93 |
| Std Dev | 5.294 |

Gaus mean : 31.8648

Gaus Width: 4.0306



B0L001S, U7-ch91, adc1

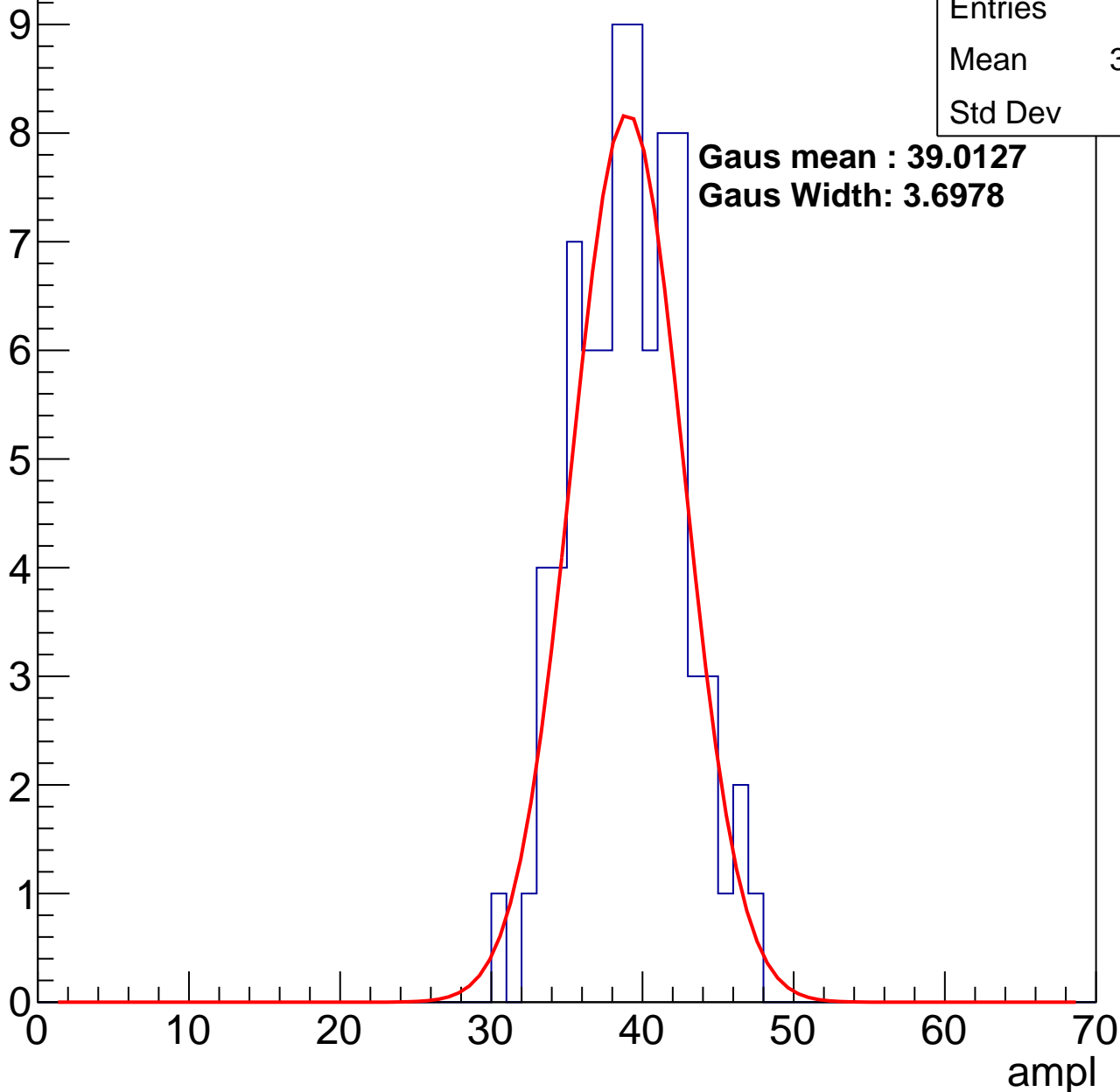
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 38.67 |
| Std Dev | 3.55 |

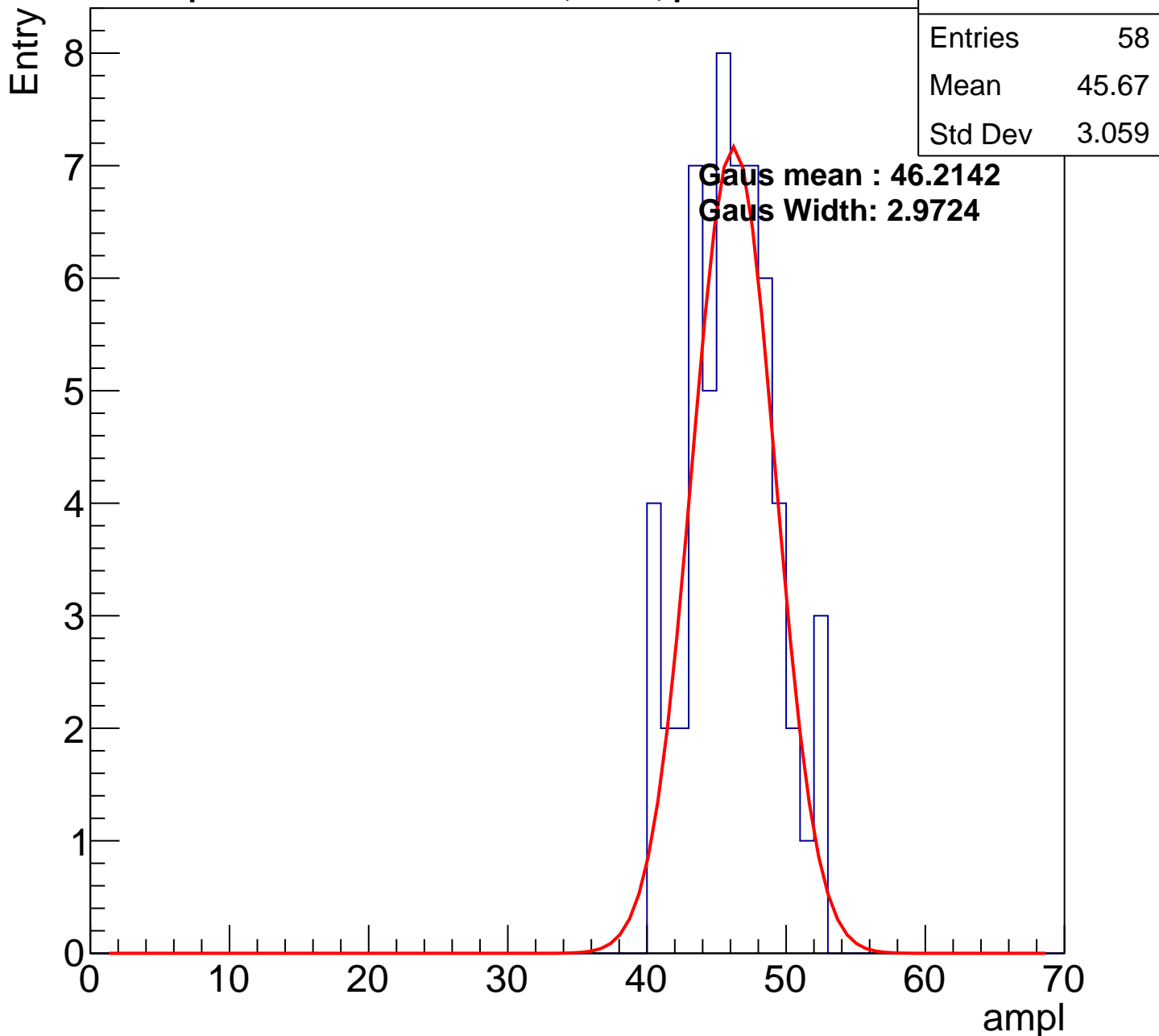
Gaus mean : 39.0127

Gaus Width: 3.6978



B0L001S, U7-ch91, adc2

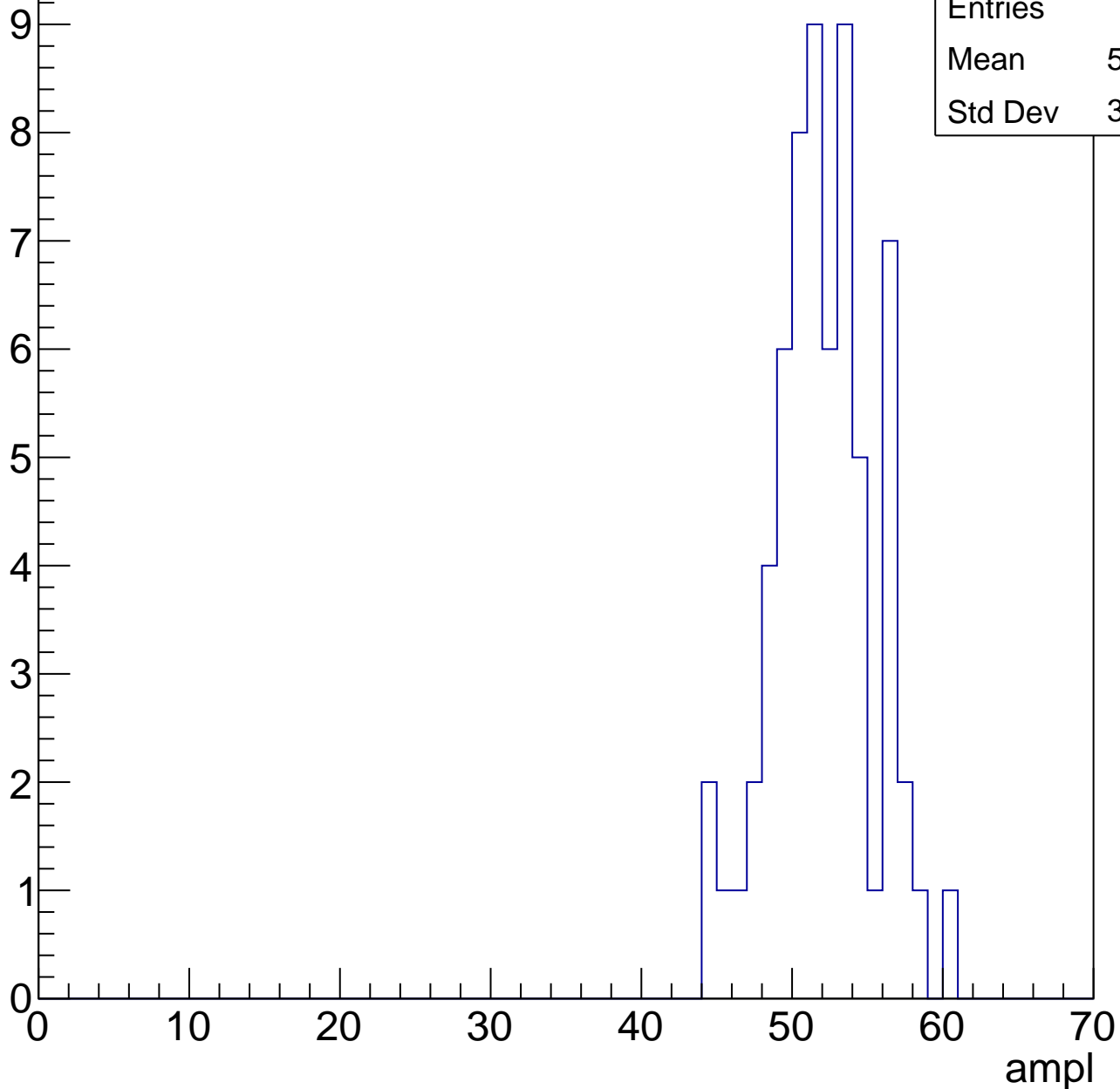
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

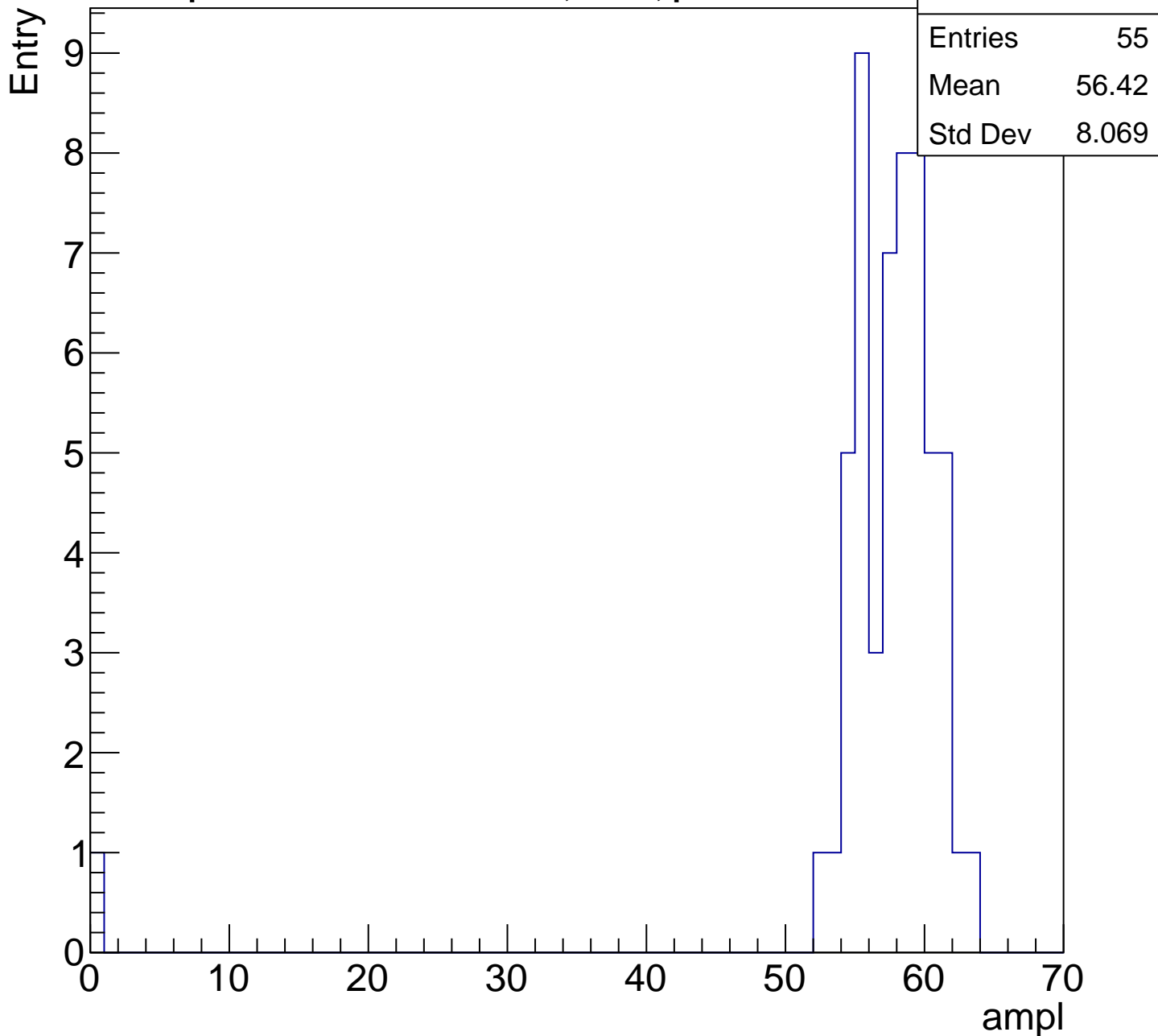
Entry



| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.63 |
| Std Dev | 3.312 |

B0L001S, U7-ch91, adc4

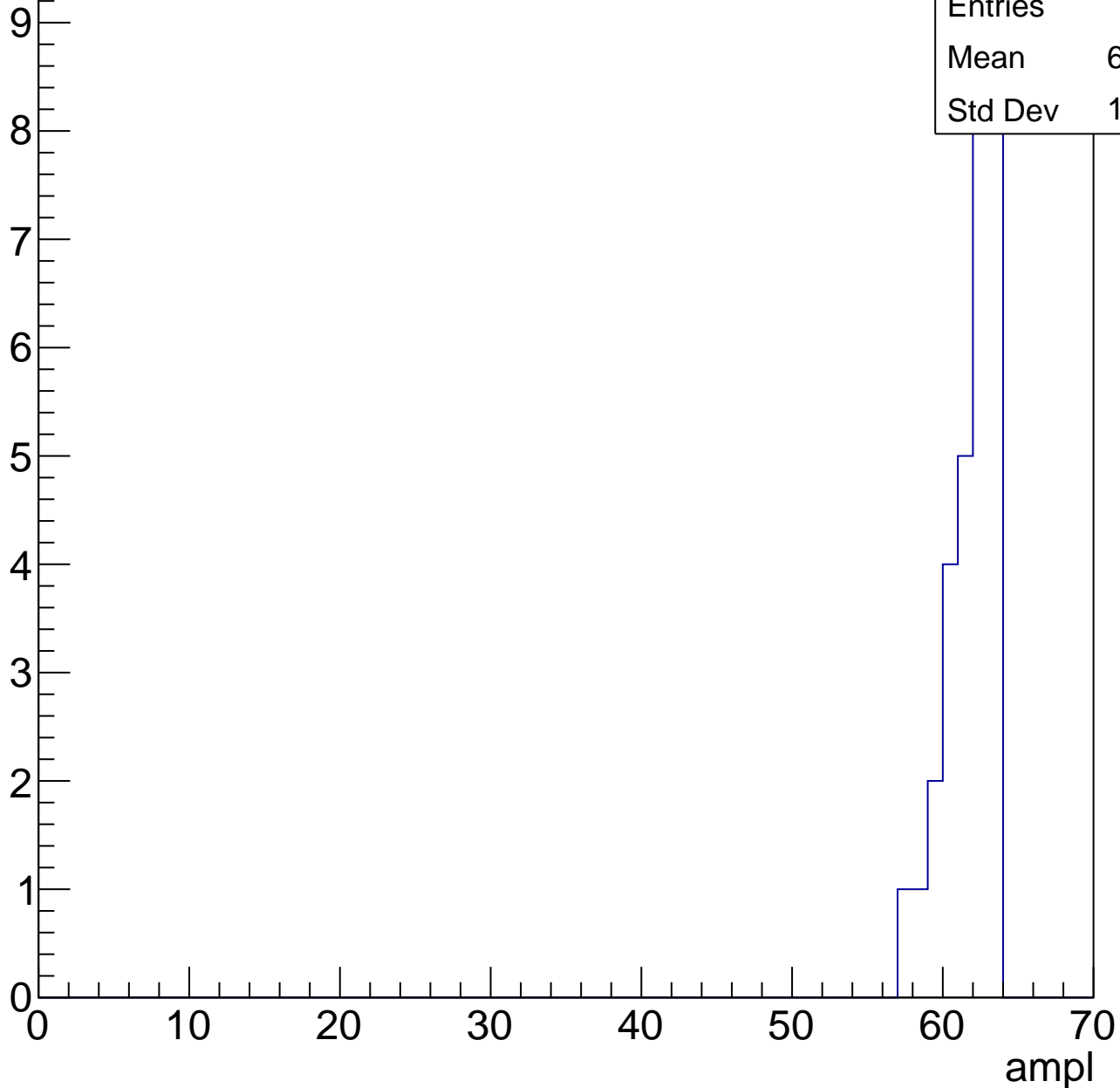
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U7-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 30 |
| Mean | 61.33 |
| Std Dev | 1.578 |

B0L001S, U7-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 63 |
| Std Dev | 0 |

ampl

B0L001S, U7-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch92, adc0

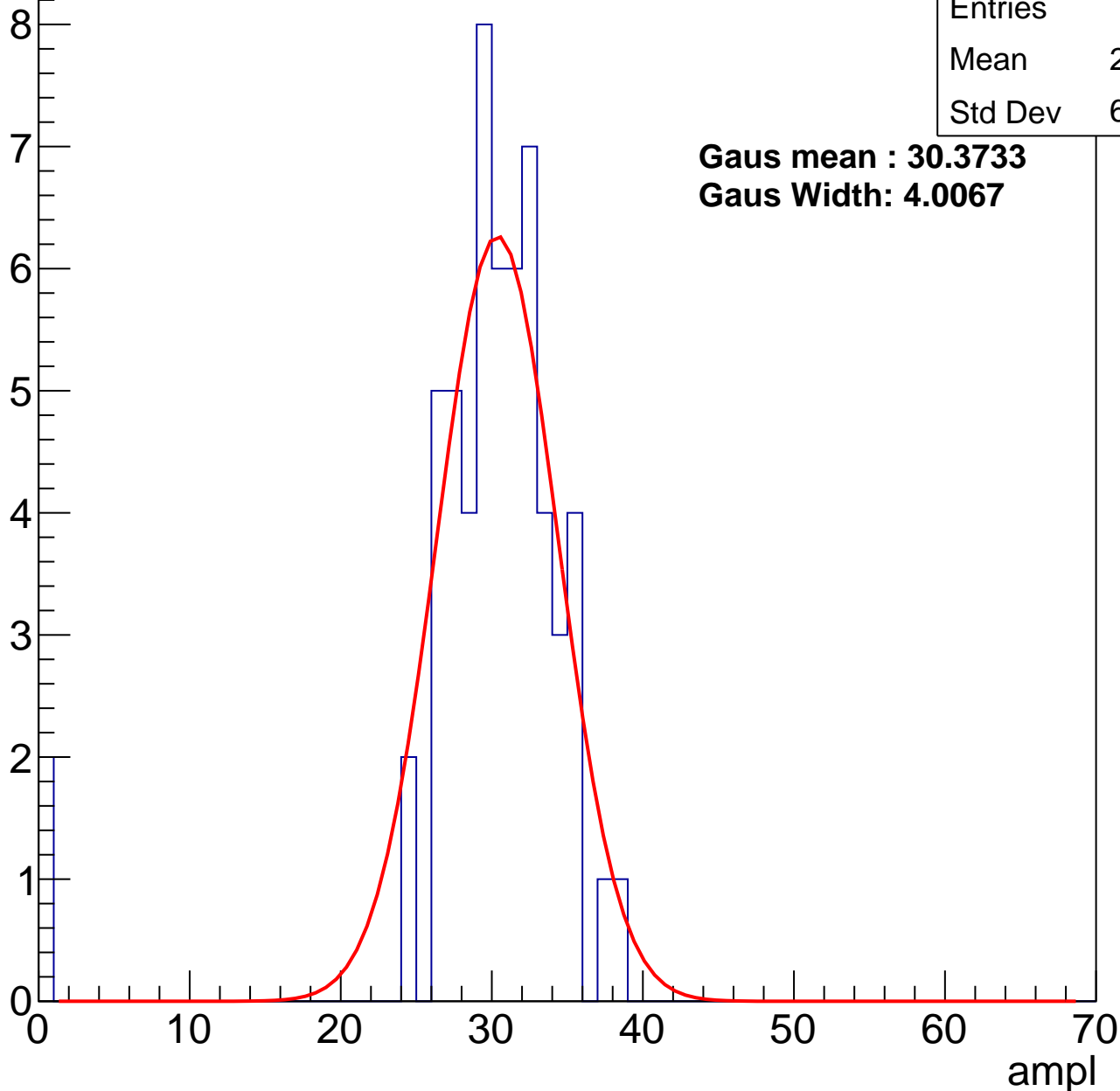
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 29.24 |
| Std Dev | 6.323 |

Gaus mean : 30.3733

Gaus Width: 4.0067



B0L001S, U7-ch92, adc1

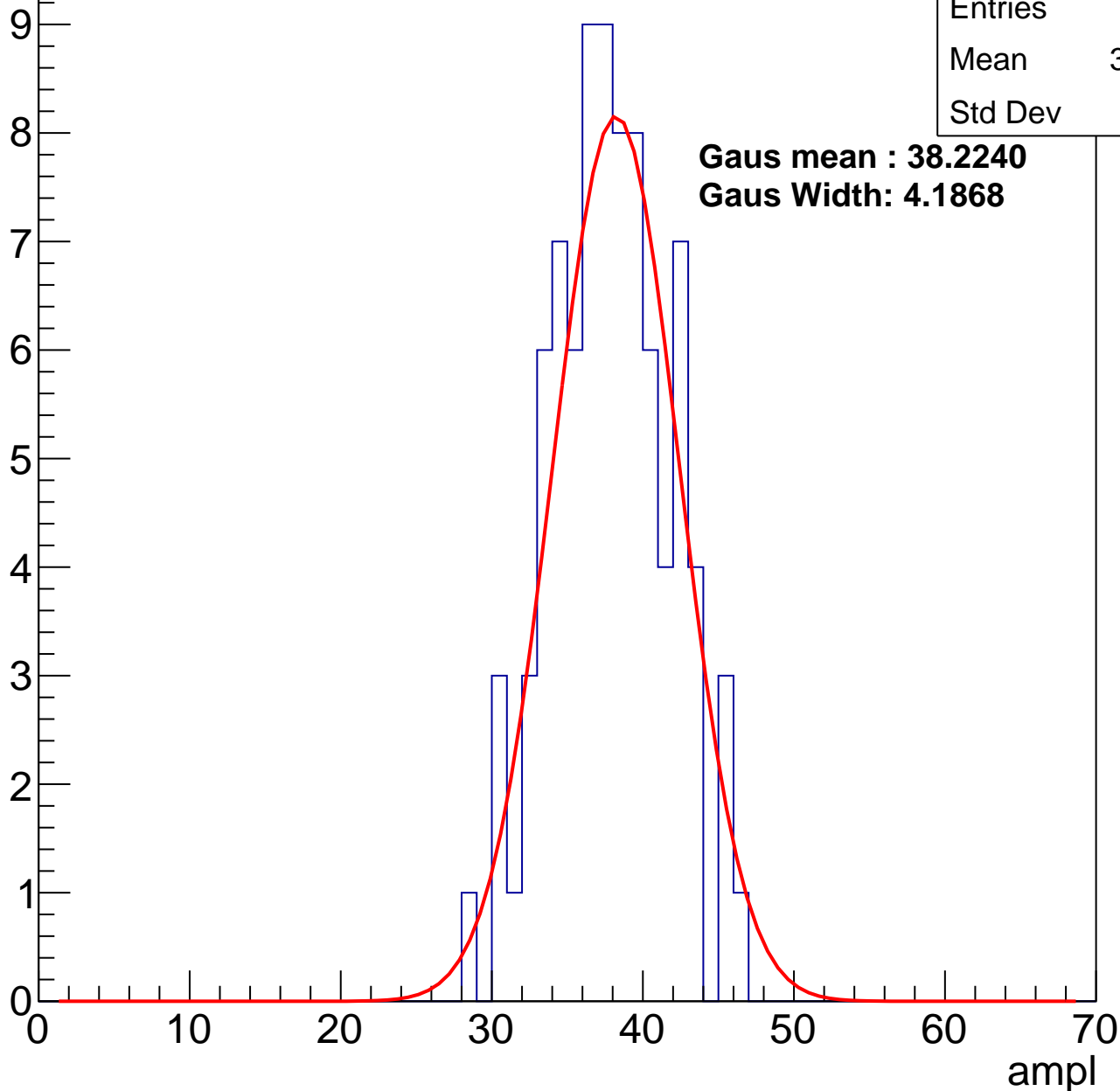
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 37.38 |
| Std Dev | 3.84 |

Gaus mean : 38.2240

Gaus Width: 4.1868



B0L001S, U7-ch92, adc2

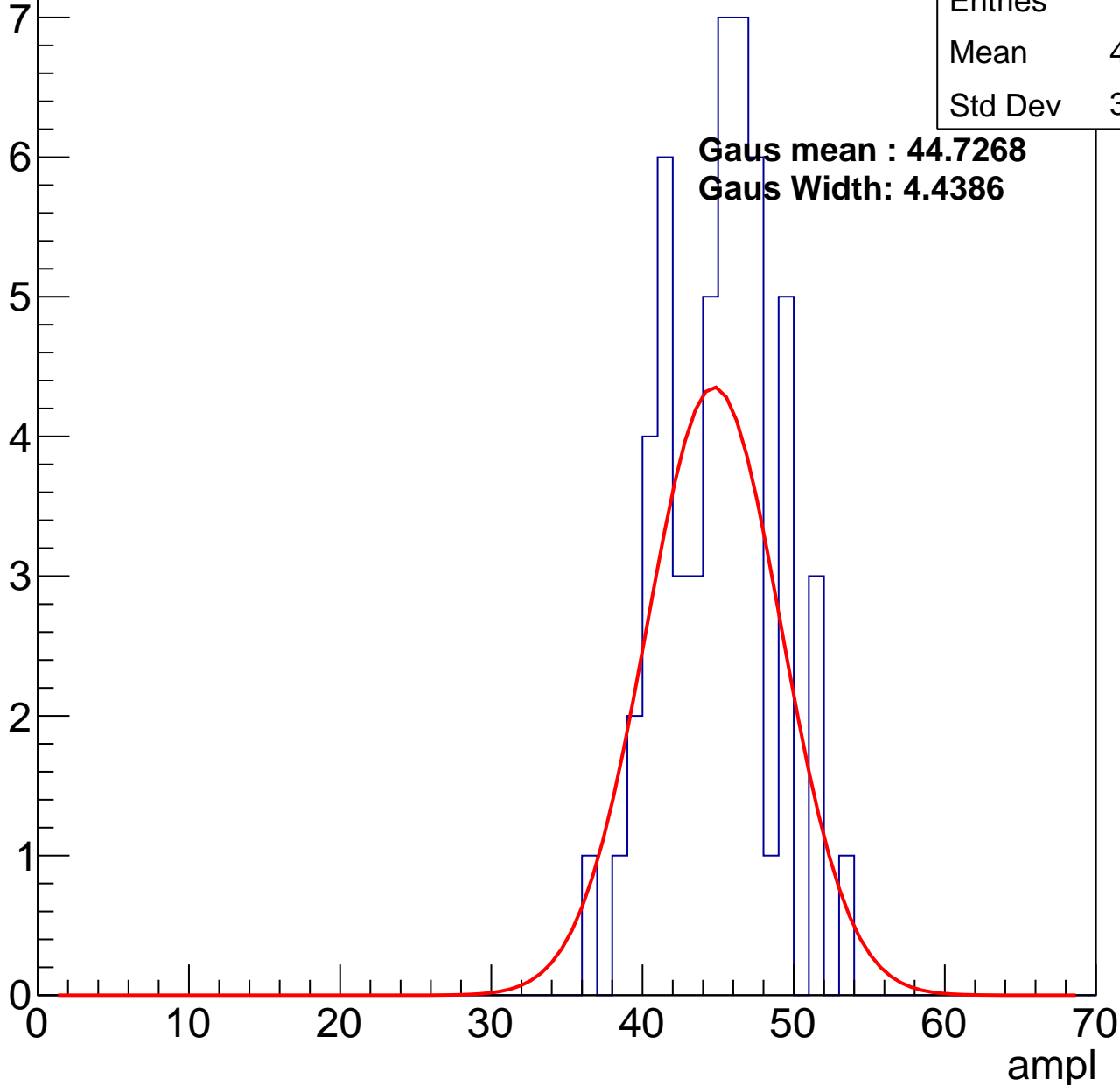
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 44.56 |
| Std Dev | 3.627 |

Gaus mean : 44.7268

Gaus Width: 4.4386

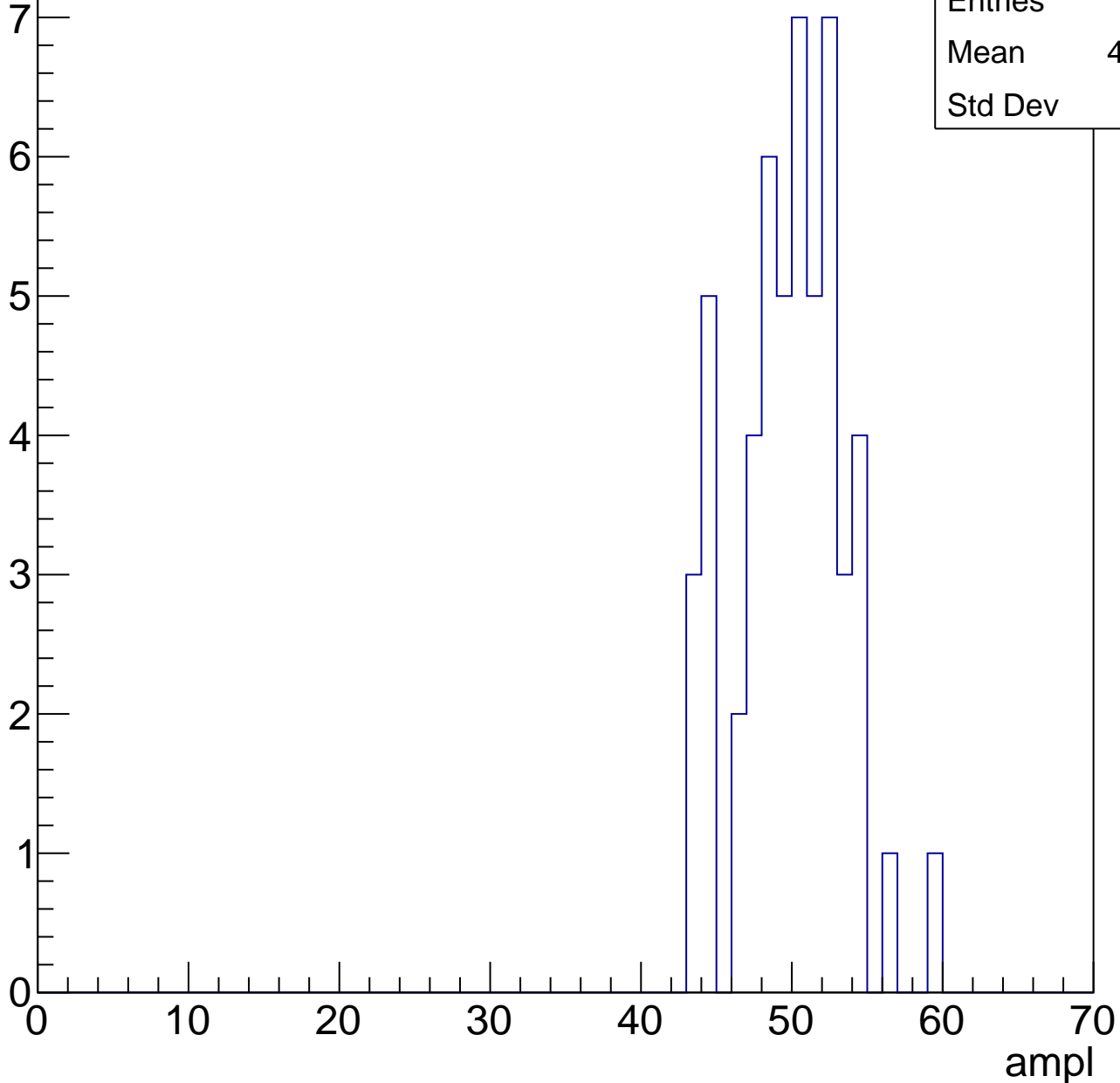


B0L001S, U7-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 49.45 |
| Std Dev | 3.5 |

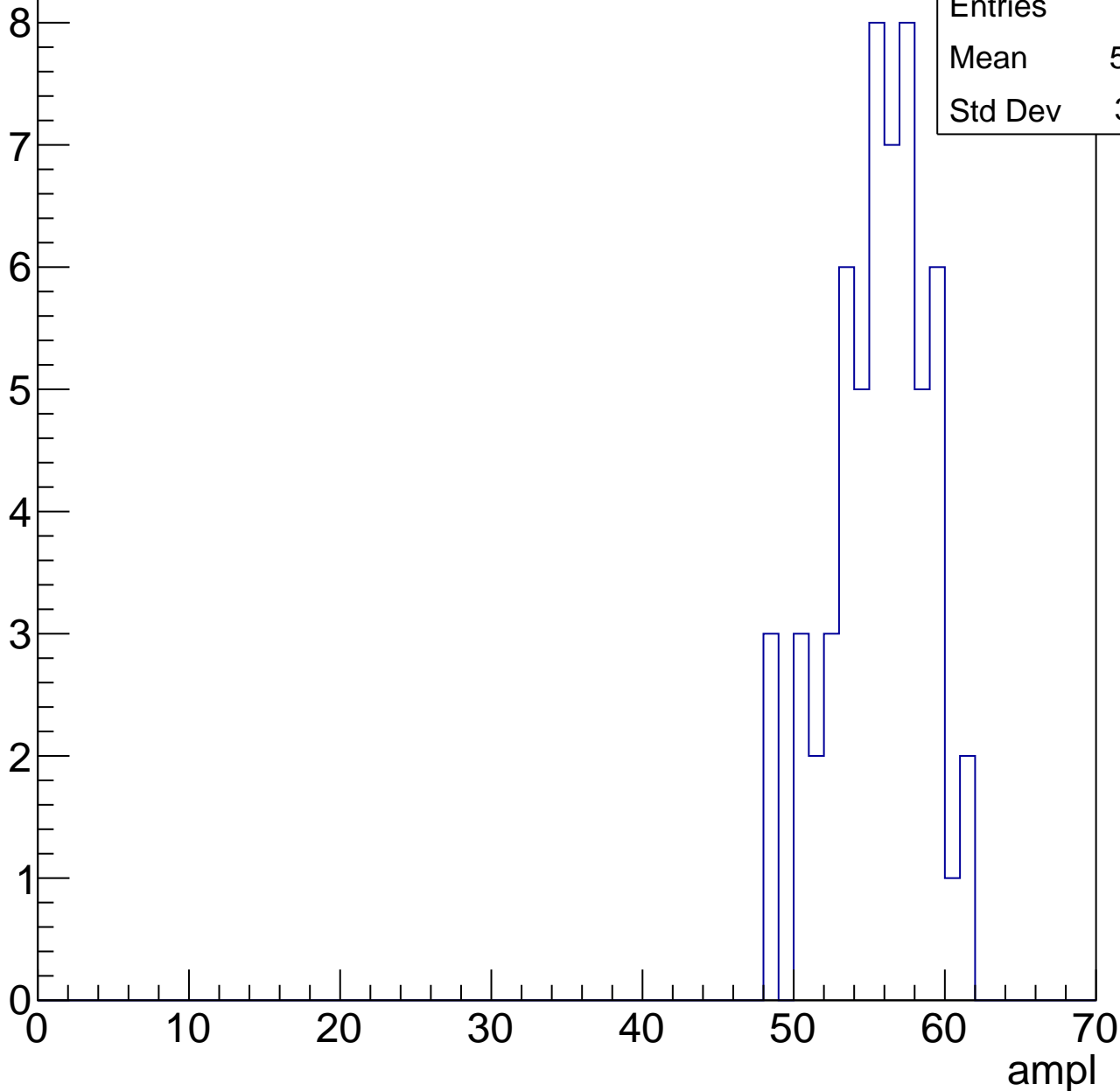


B0L001S, U7-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 55.15 |
| Std Dev | 3.151 |



B0L001S, U7-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 48 |
| Mean | 60.1 |
| Std Dev | 2.374 |

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B0L001S, U7-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch93, adc0

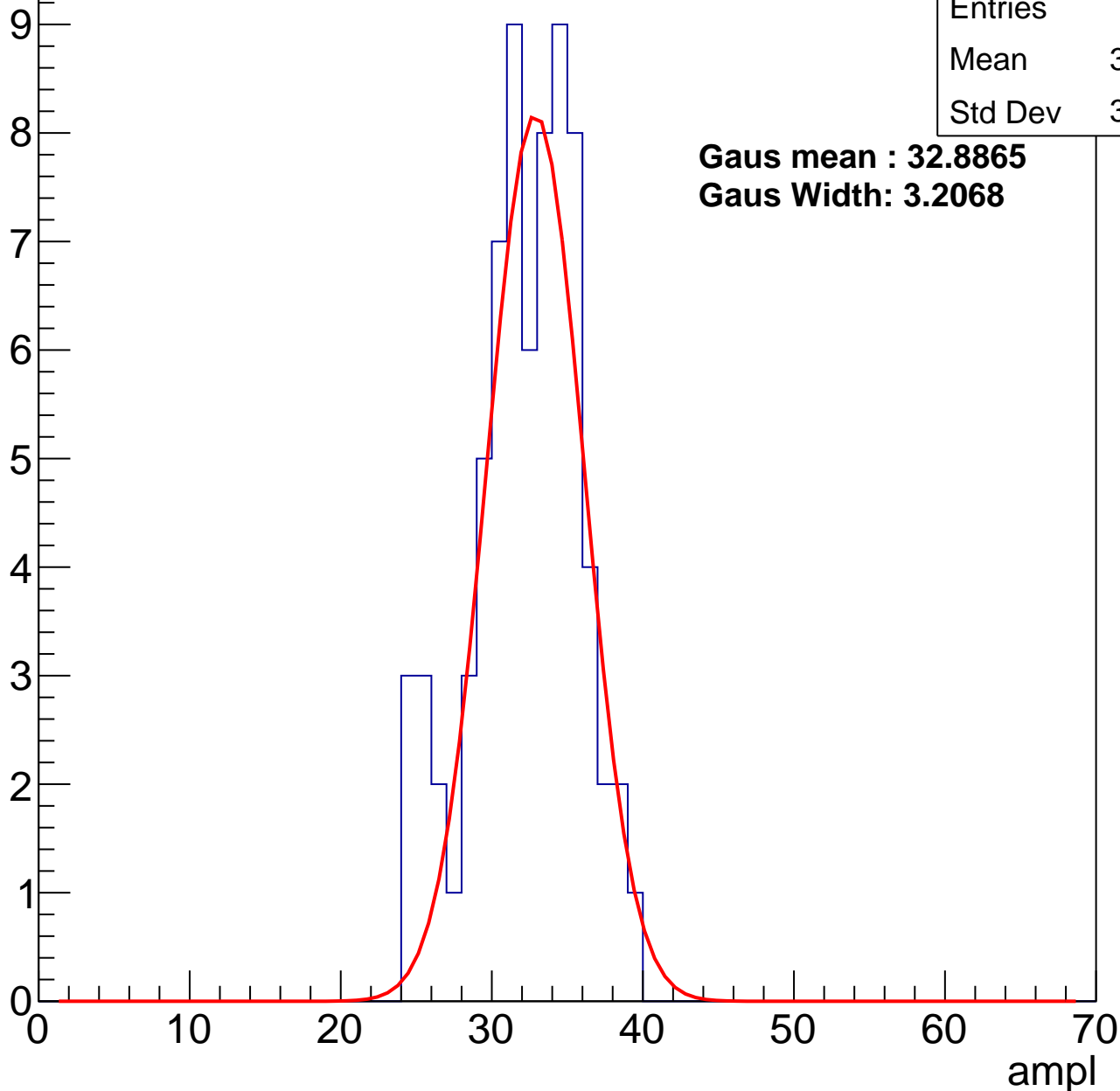
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 31.77 |
| Std Dev | 3.537 |

Gaus mean : 32.8865

Gaus Width: 3.2068



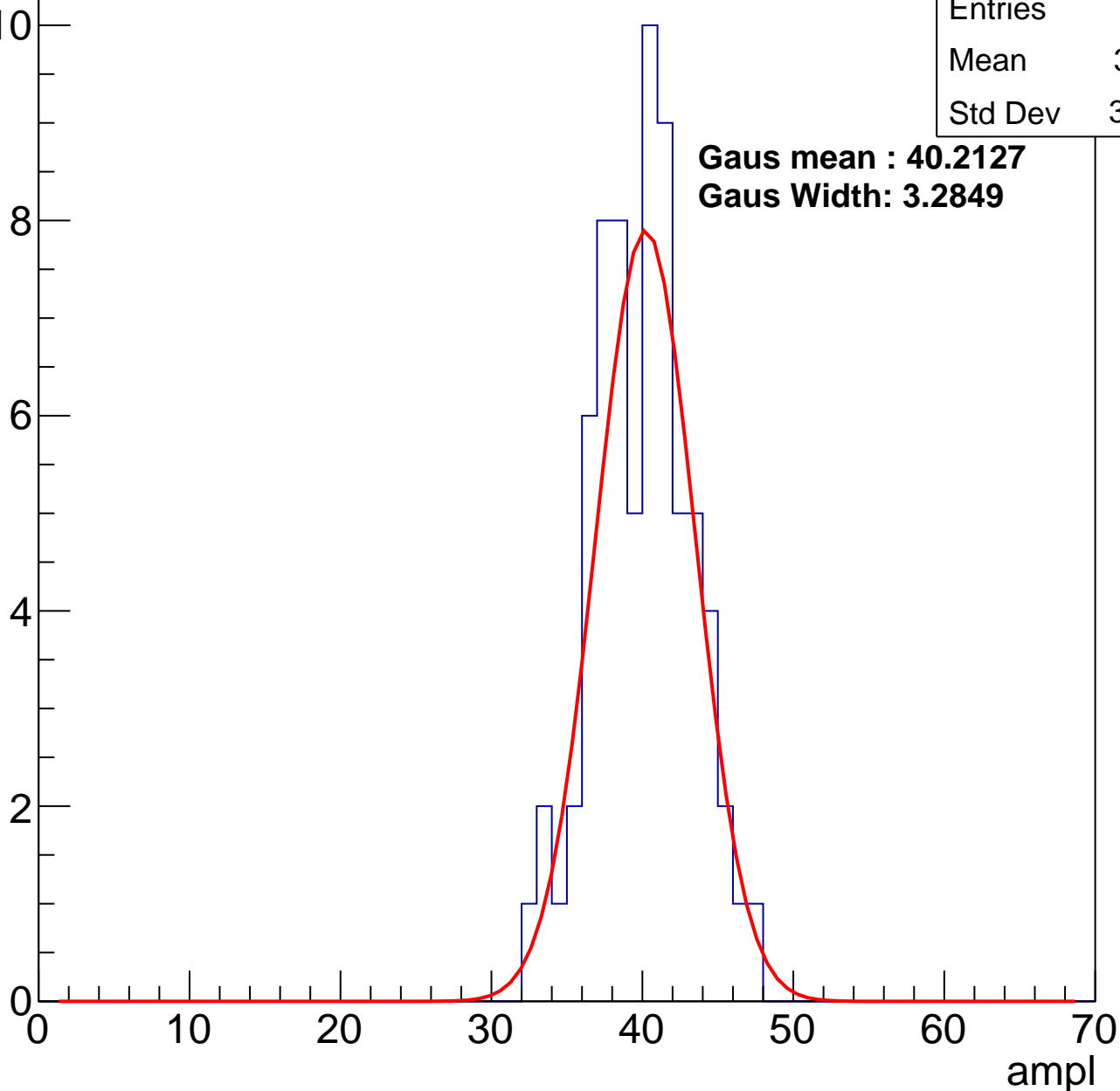
B0L001S, U7-ch93, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 39.51 |
| Std Dev | 3.184 |

Gaus mean : 40.2127
Gaus Width: 3.2849



B0L001S, U7-ch93, adc2

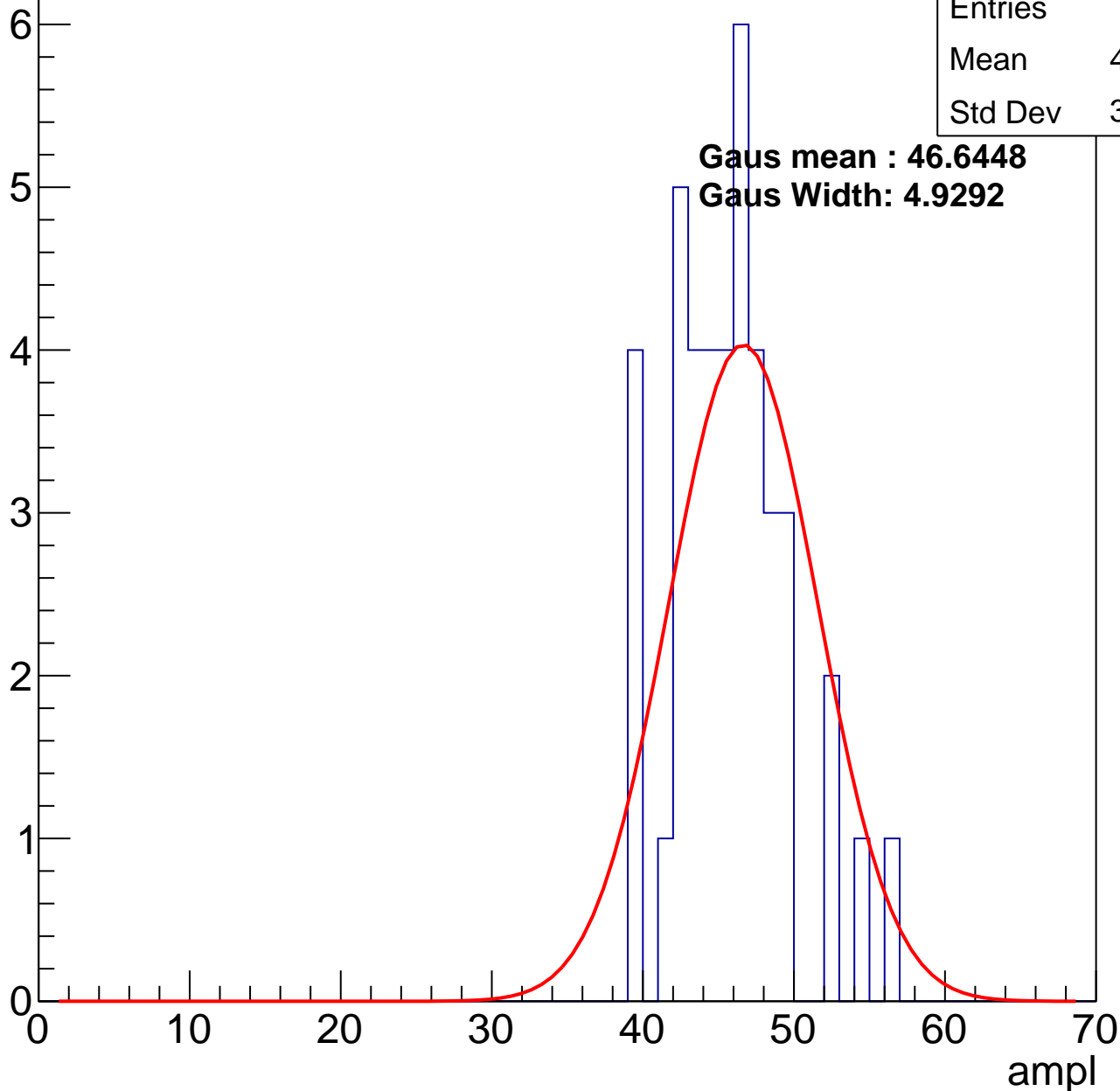
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 45.33 |
| Std Dev | 3.834 |

Gaus mean : 46.6448

Gaus Width: 4.9292

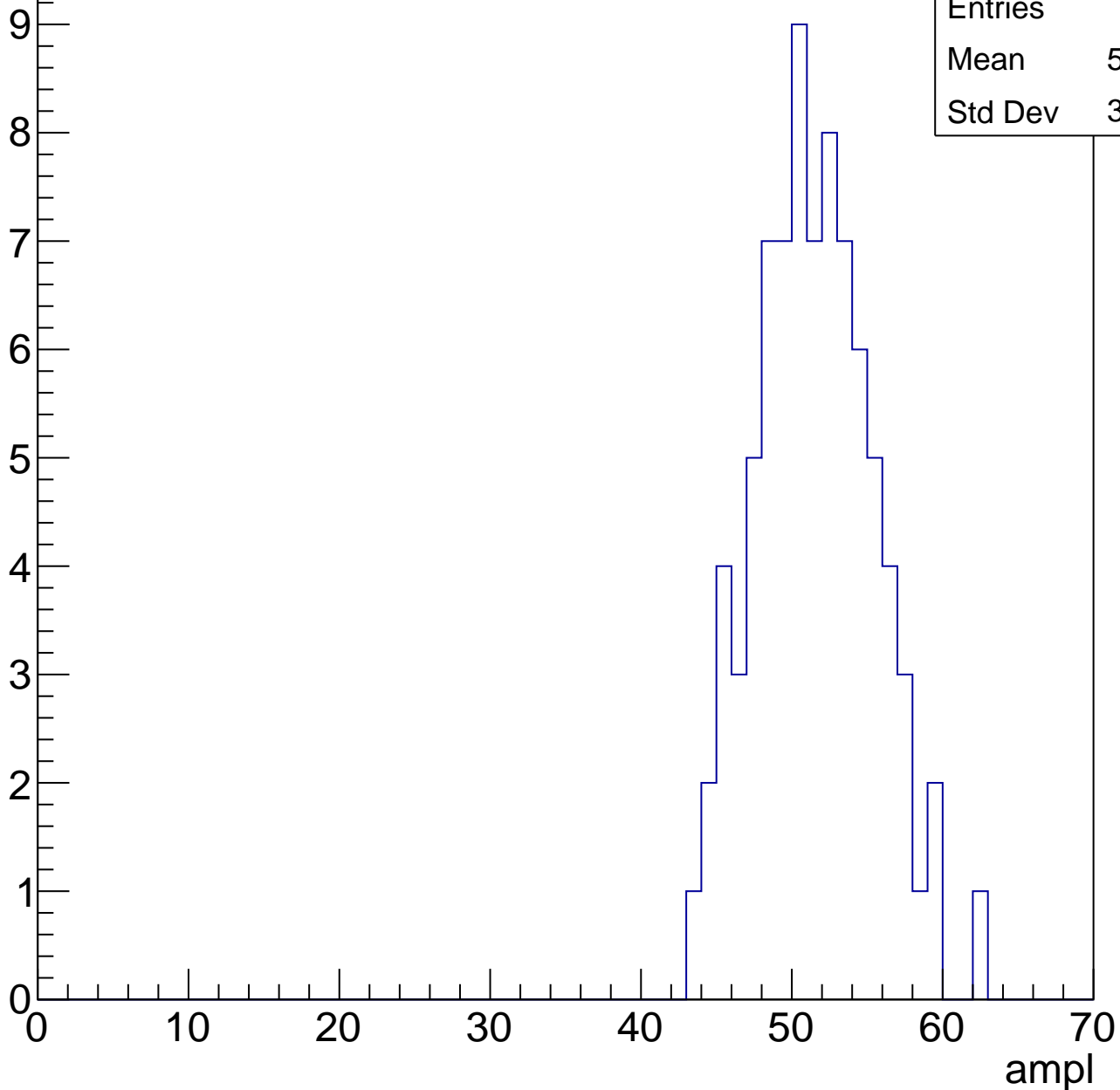


B0L001S, U7-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 51.09 |
| Std Dev | 3.883 |

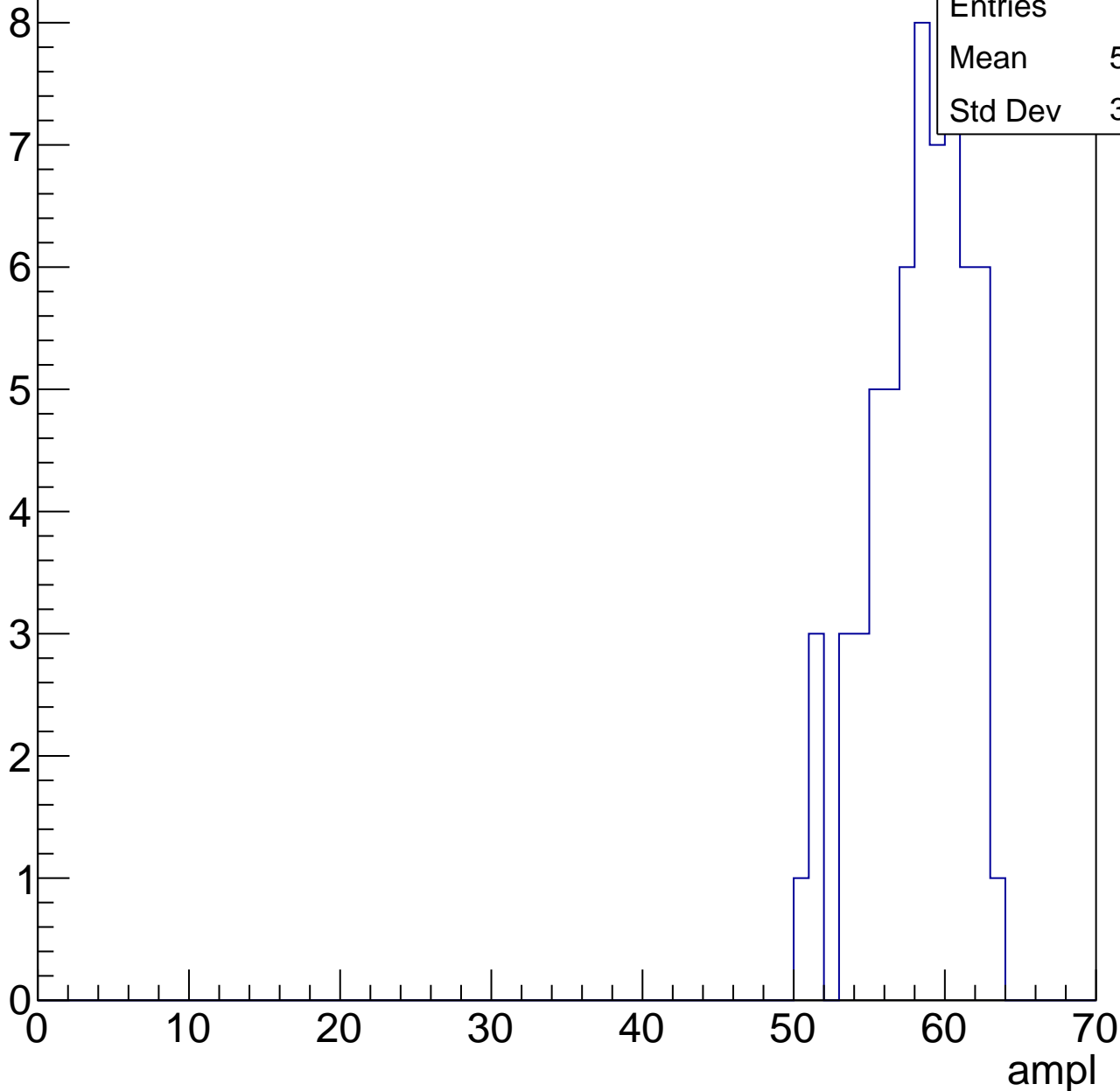


B0L001S, U7-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.73 |
| Std Dev | 3.148 |

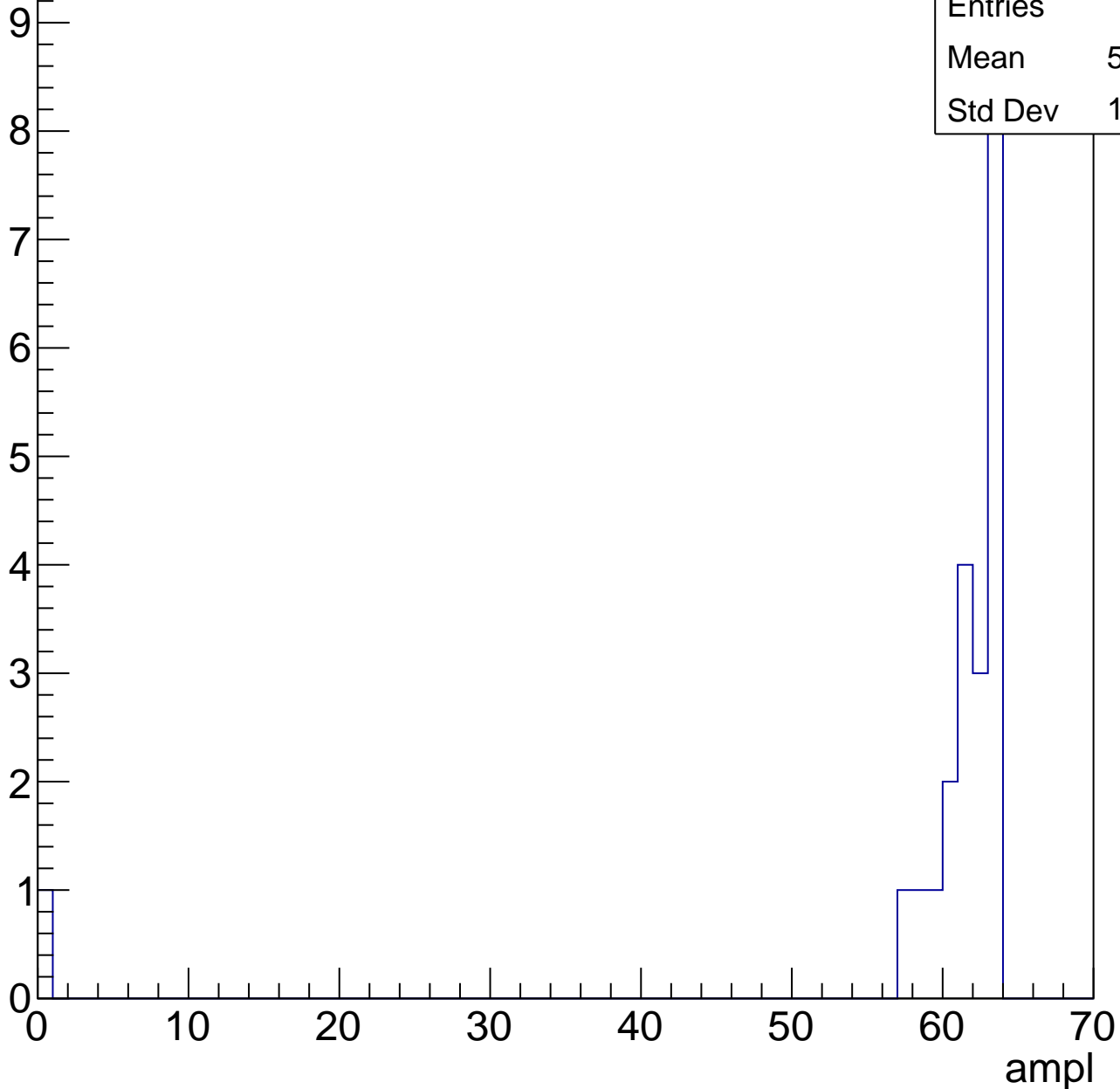


B0L001S, U7-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 22 |
| Mean | 58.68 |
| Std Dev | 12.92 |



B0L001S, U7-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch94, adc0

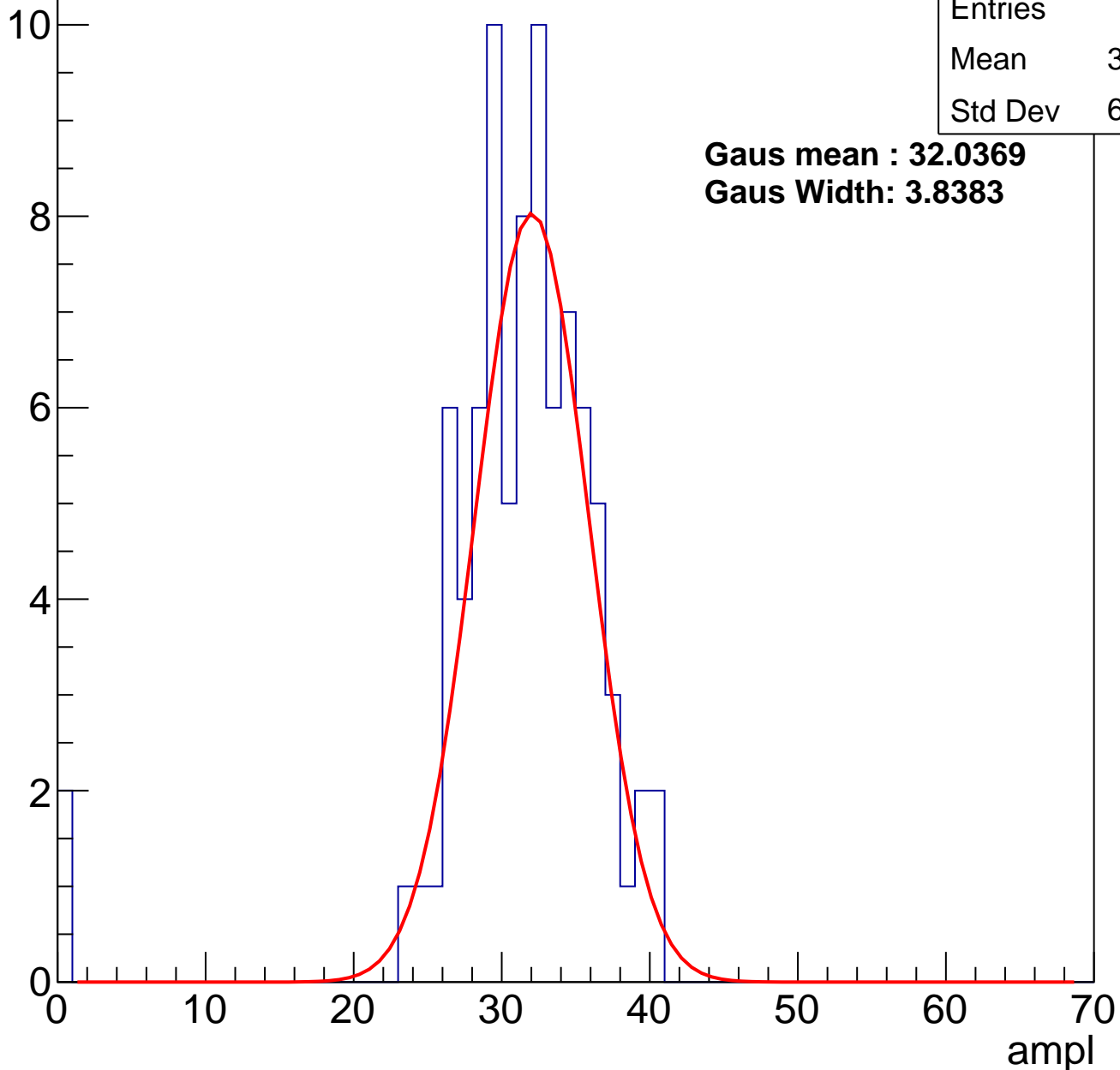
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 86 |
| Mean | 30.76 |
| Std Dev | 6.046 |

Gaus mean : 32.0369

Gaus Width: 3.8383

Entry



B0L001S, U7-ch94, adc1

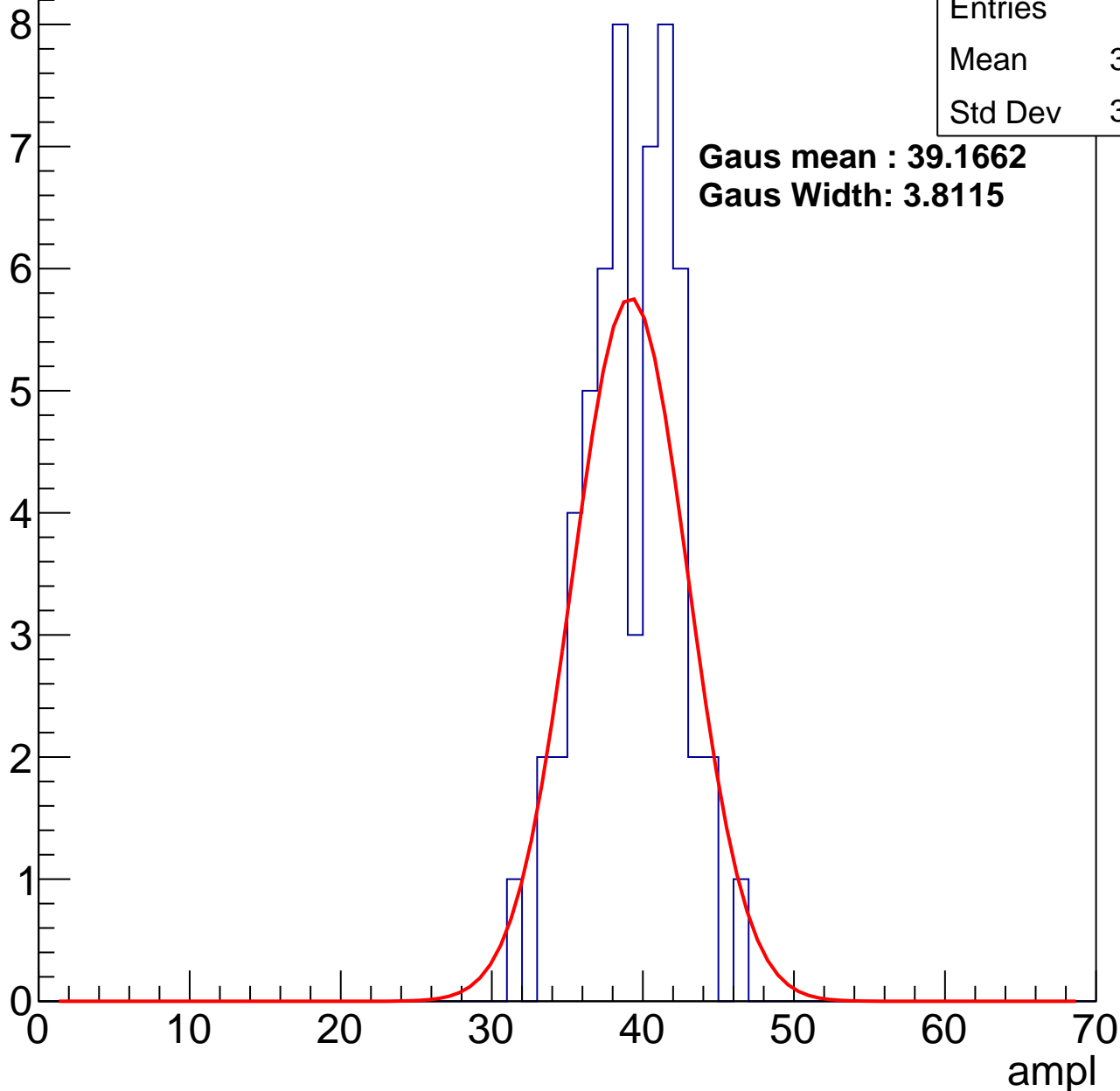
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 38.74 |
| Std Dev | 3.098 |

Gaus mean : 39.1662

Gaus Width: 3.8115



B0L001S, U7-ch94, adc2

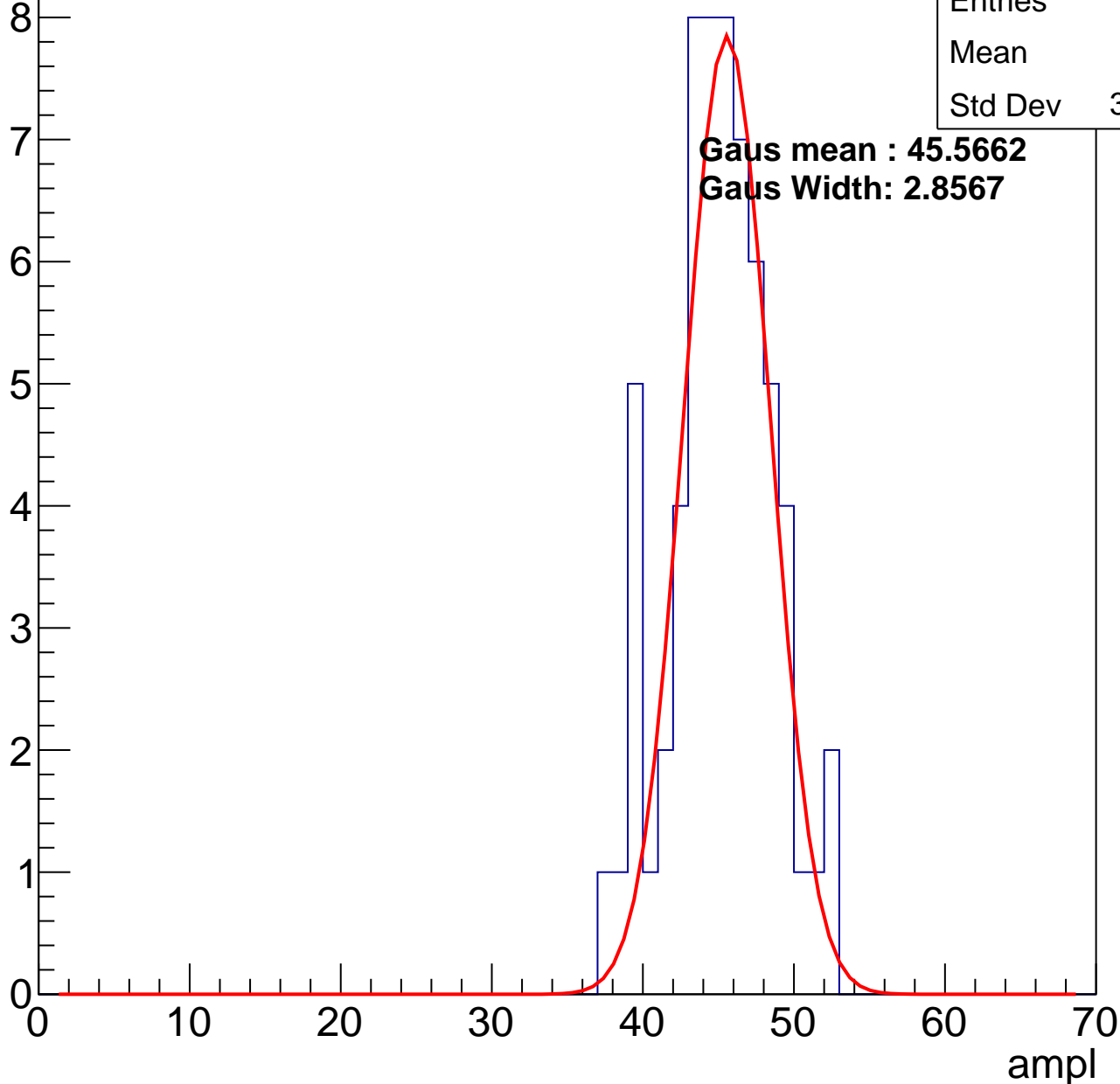
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 44.7 |
| Std Dev | 3.353 |

Gaus mean : 45.5662

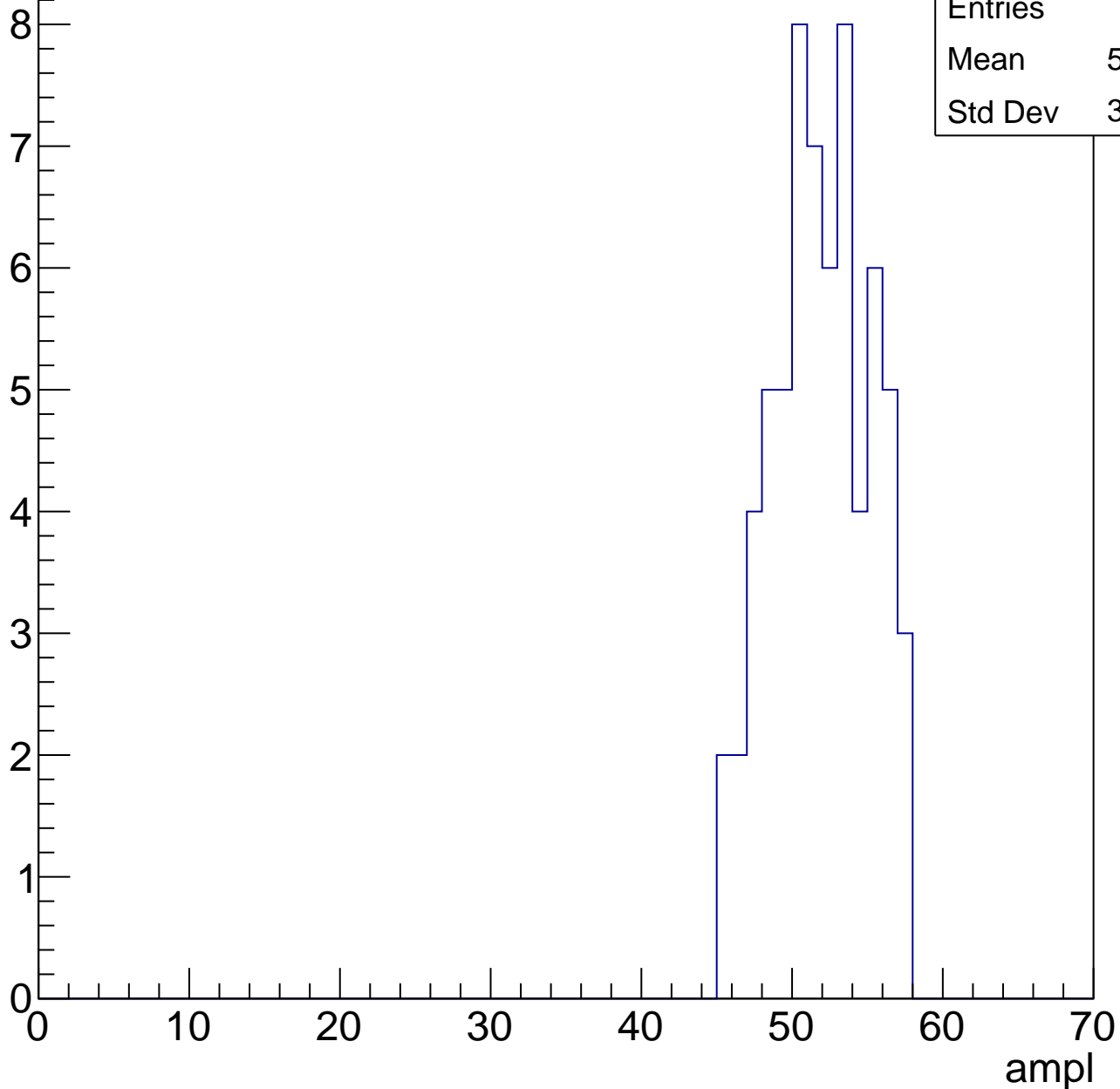
Gaus Width: 2.8567



B0L001S, U7-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



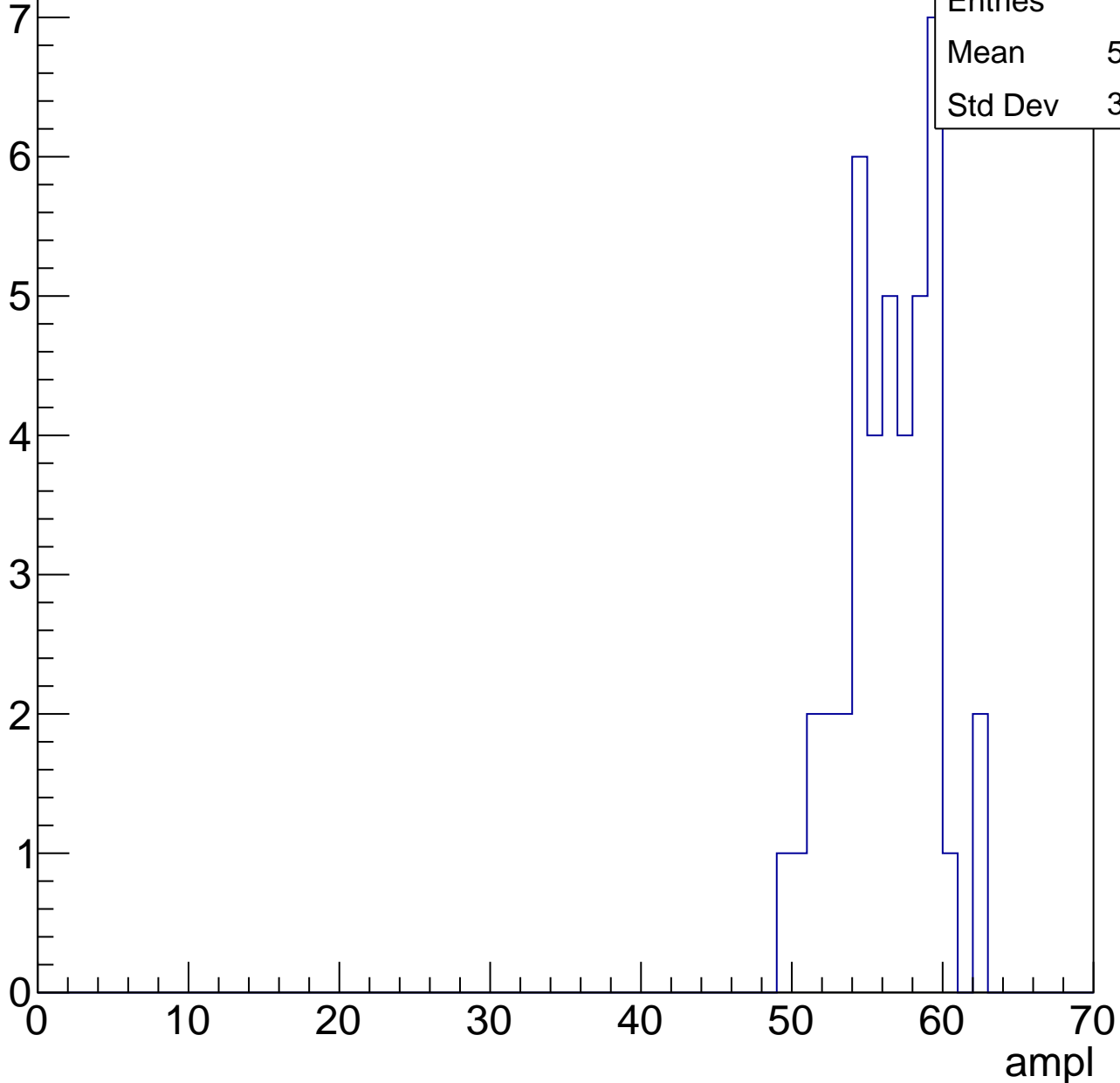
| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.46 |
| Std Dev | 3.158 |

B0L001S, U7-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

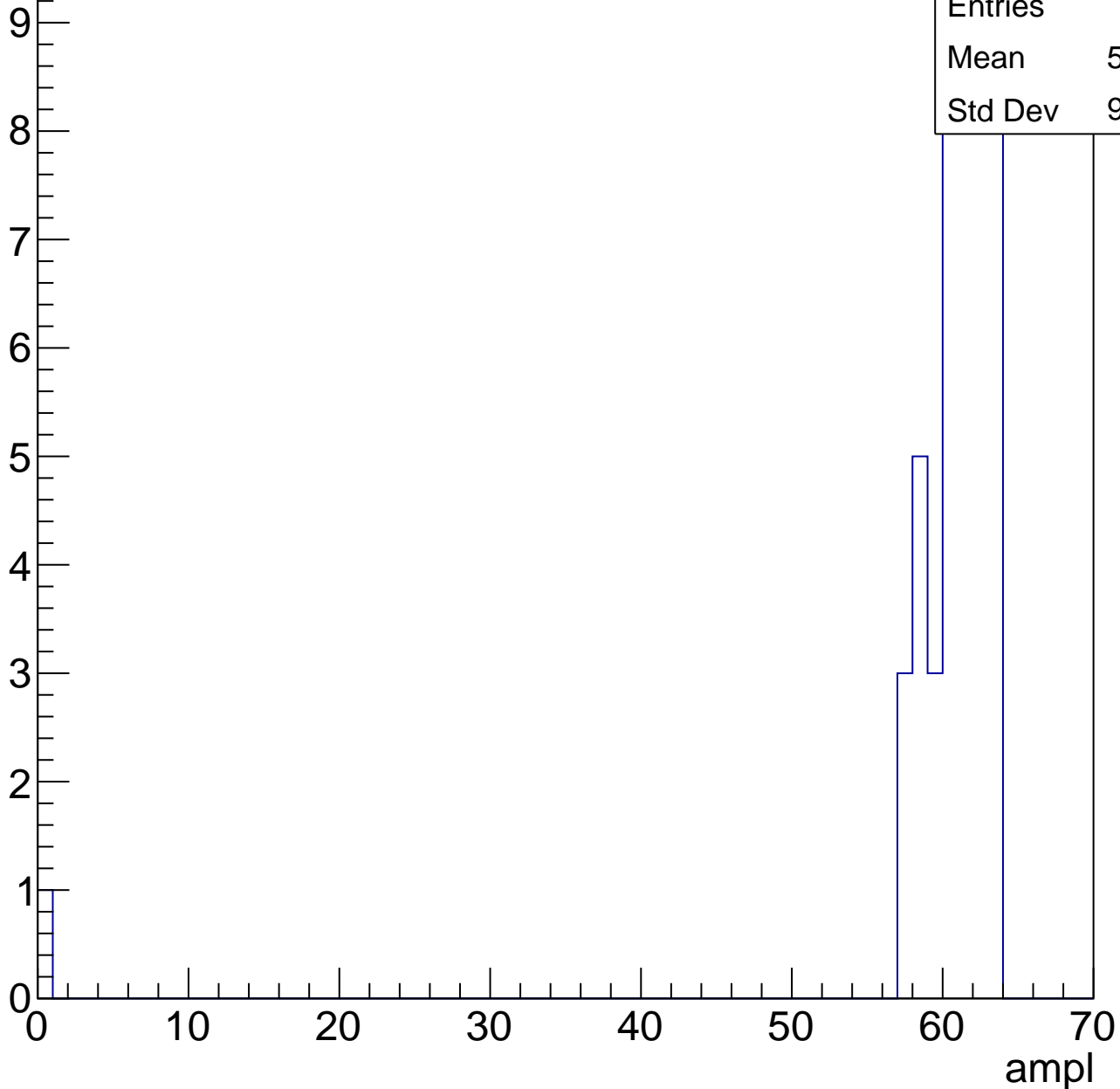
| | |
|---------|-------|
| Entries | 42 |
| Mean | 55.95 |
| Std Dev | 3.039 |



B0L001S, U7-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch95, adc0

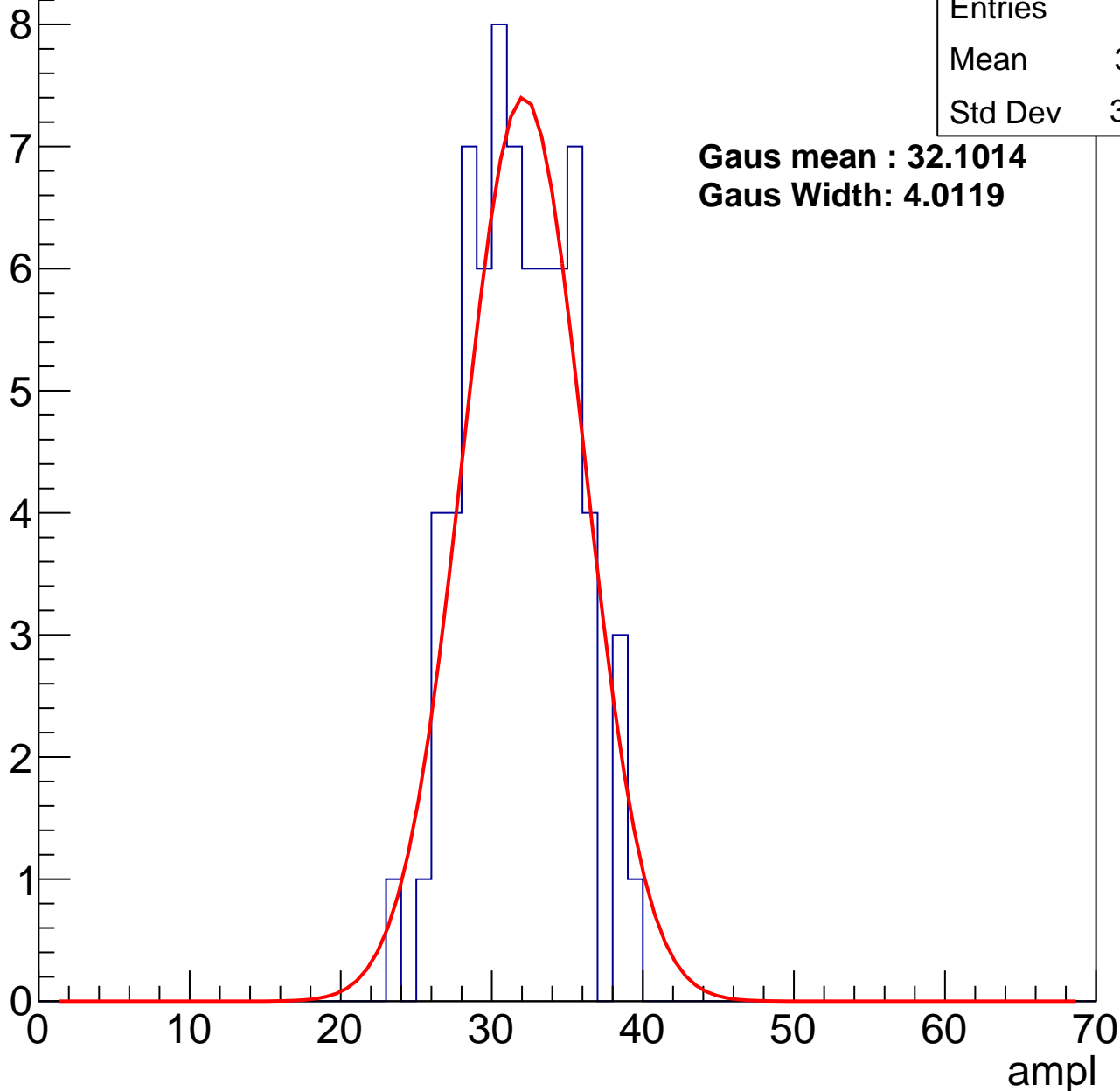
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 31.31 |
| Std Dev | 3.479 |

Gaus mean : 32.1014

Gaus Width: 4.0119



B0L001S, U7-ch95, adc1

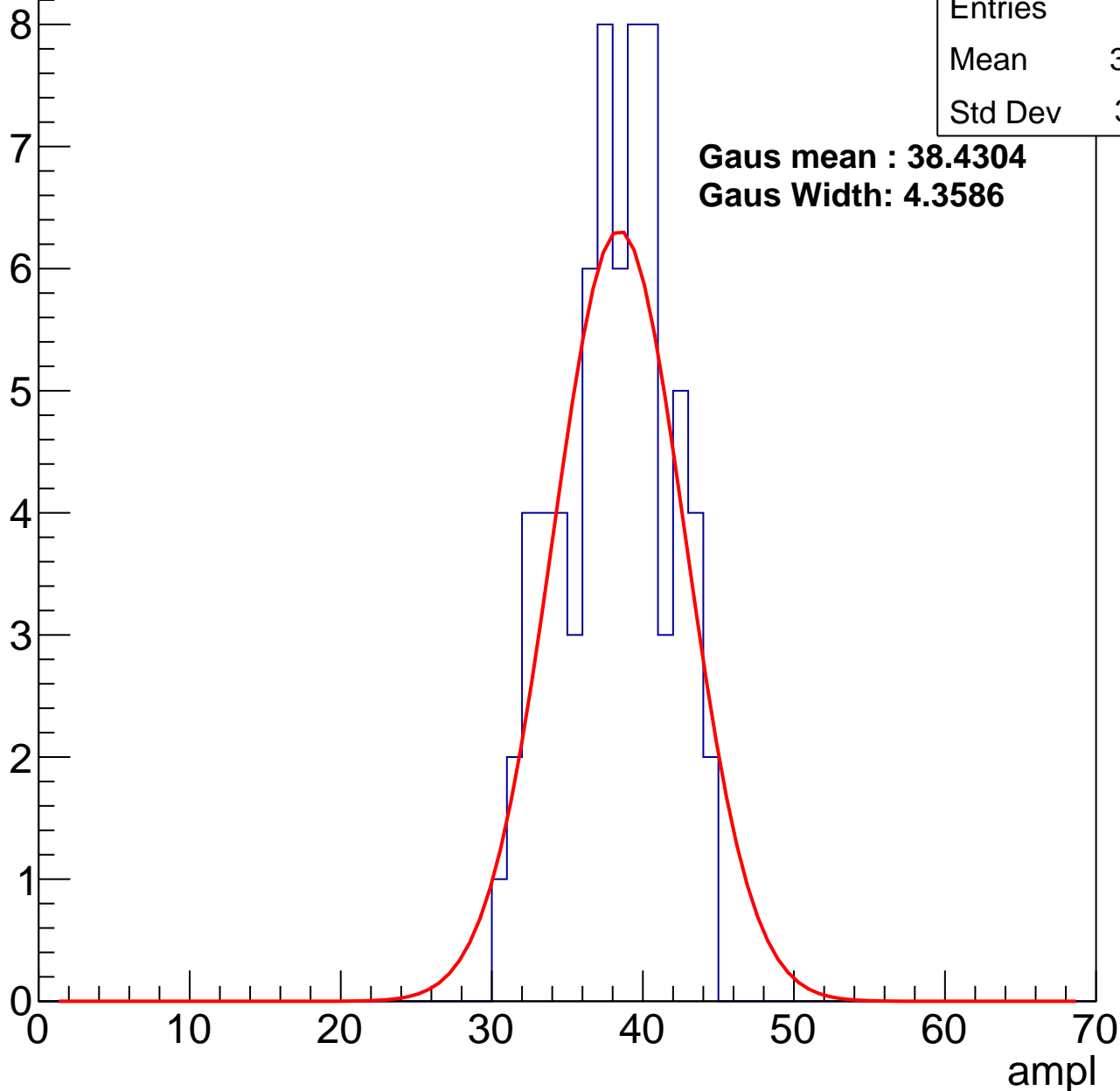
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 37.62 |
| Std Dev | 3.511 |

Gaus mean : 38.4304

Gaus Width: 4.3586



B0L001S, U7-ch95, adc2

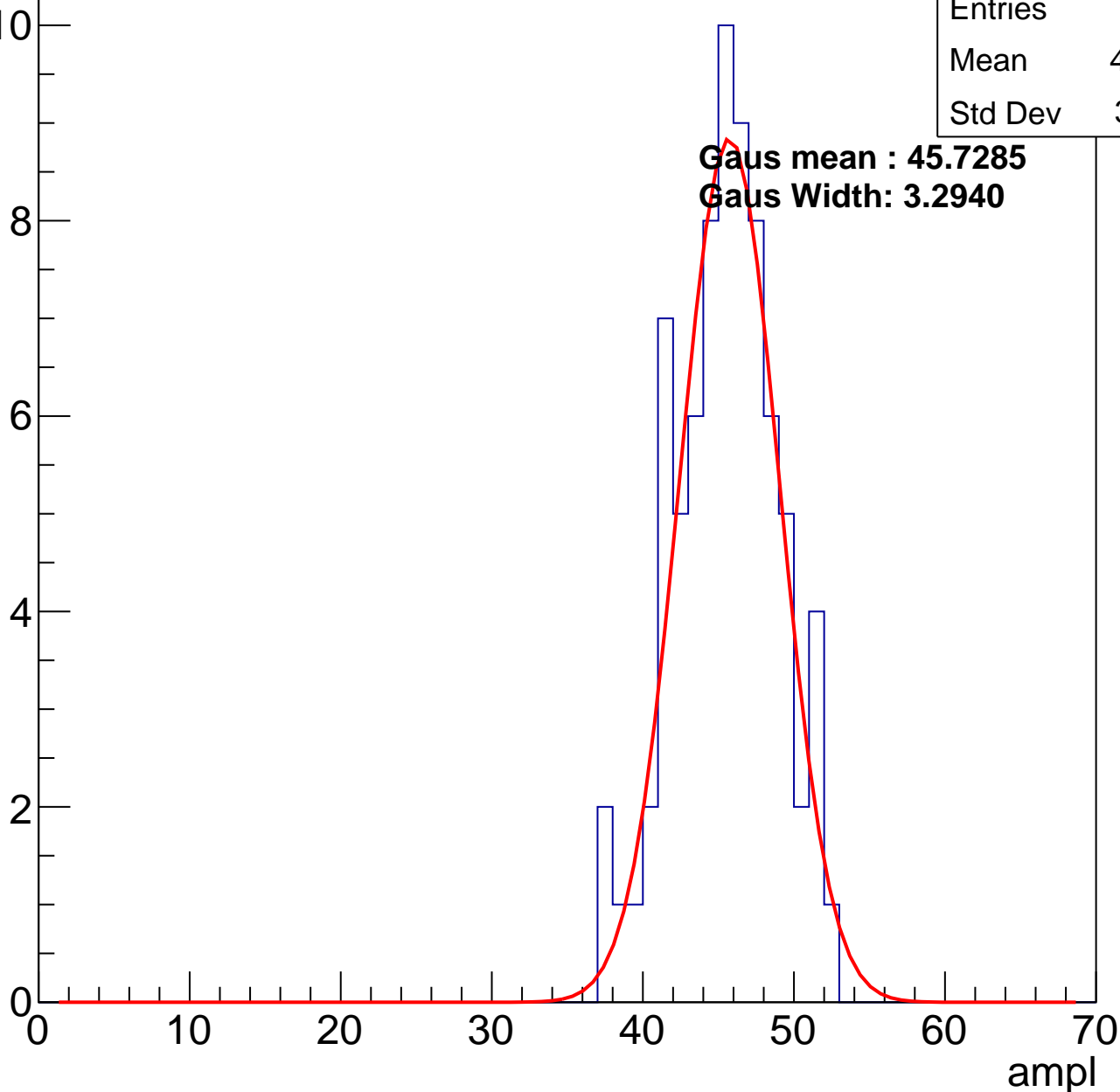
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 45.03 |
| Std Dev | 3.361 |

Gaus mean : 45.7285

Gaus Width: 3.2940

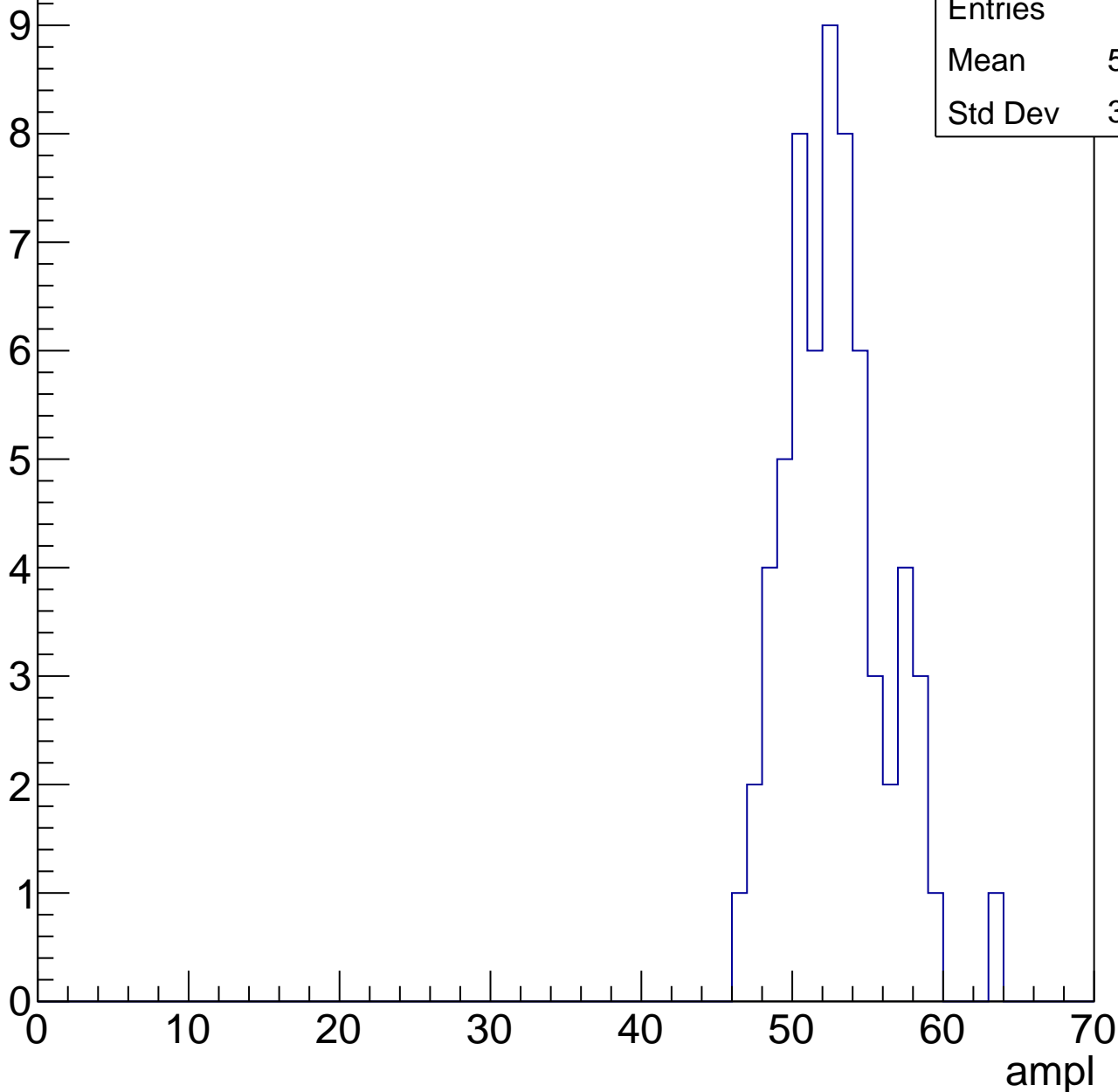


B0L001S, U7-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 52.38 |
| Std Dev | 3.316 |

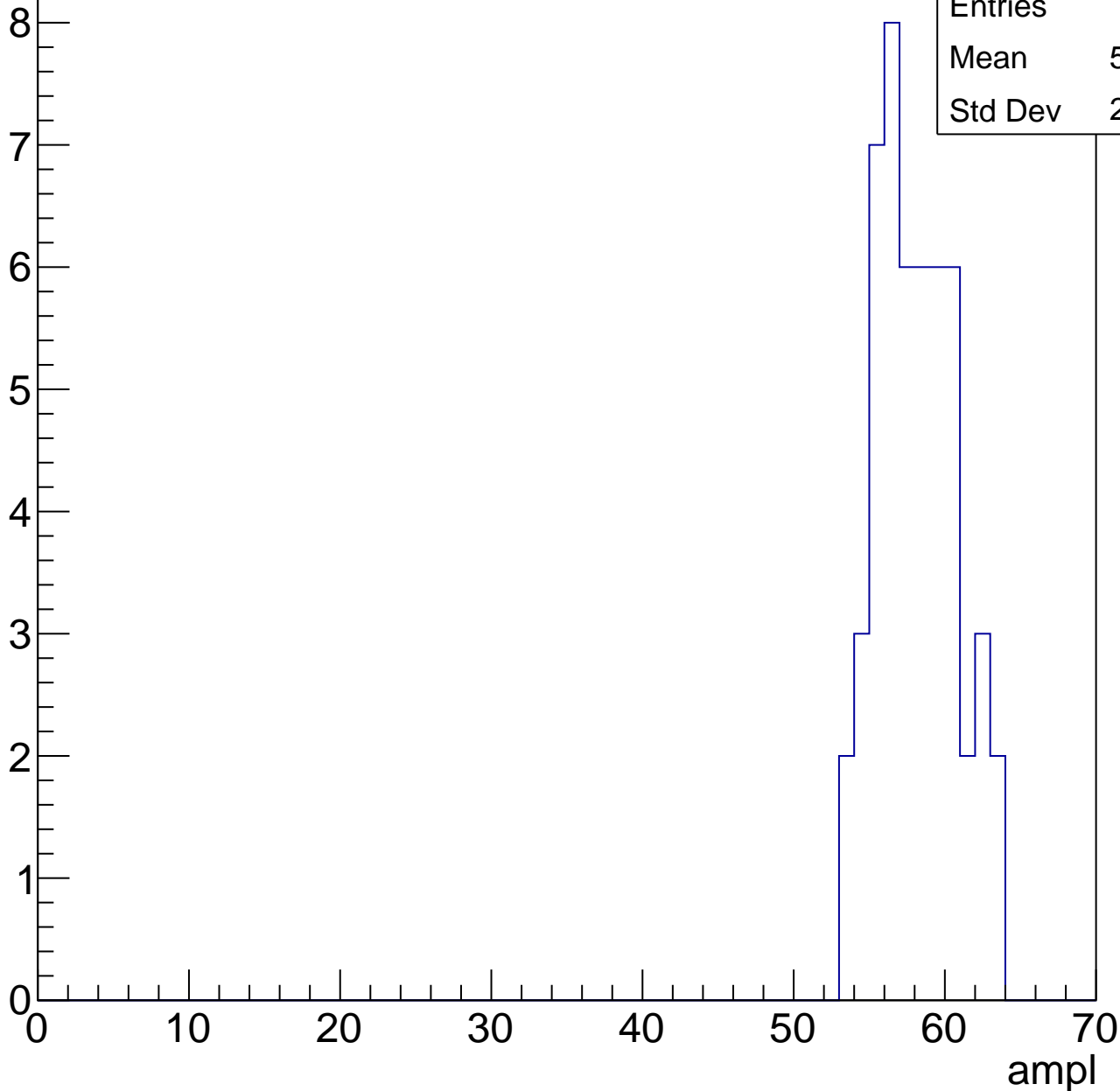


B0L001S, U7-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.63 |
| Std Dev | 2.574 |

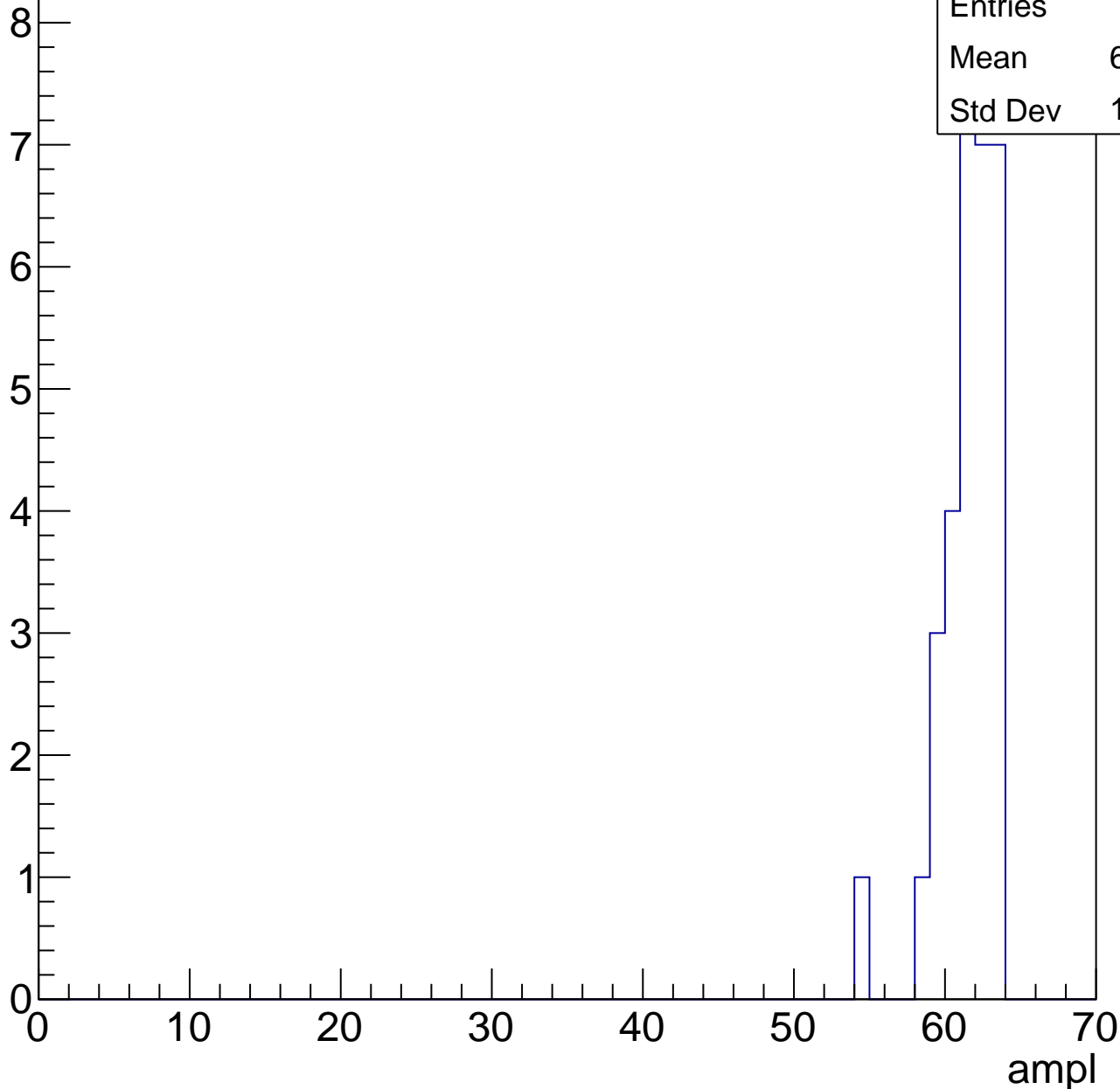


B0L001S, U7-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 31 |
| Mean | 61.03 |
| Std Dev | 1.875 |



B0L001S, U7-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch96, adc0

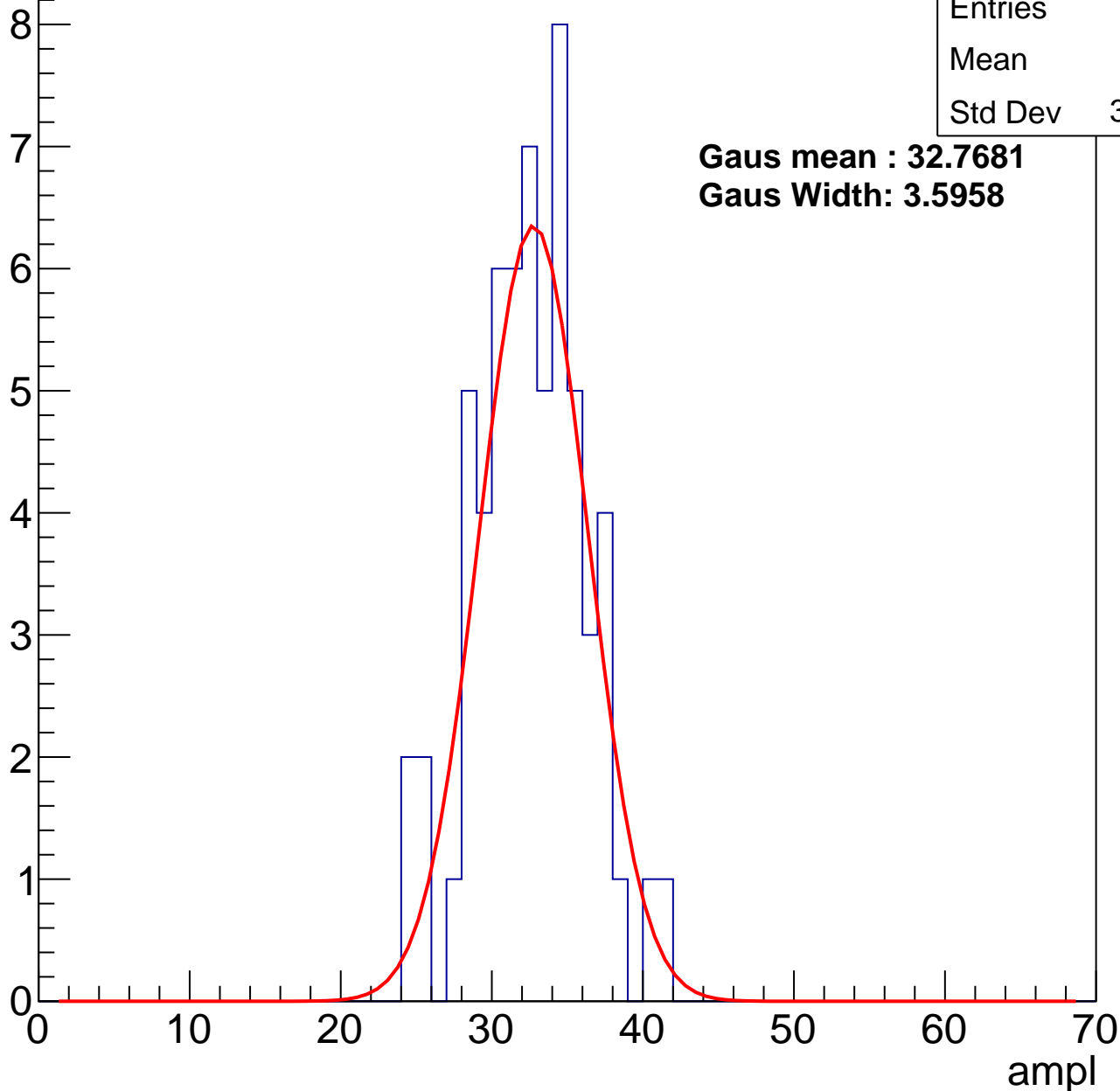
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 32.1 |
| Std Dev | 3.638 |

Gaus mean : 32.7681

Gaus Width: 3.5958



B0L001S, U7-ch96, adc1

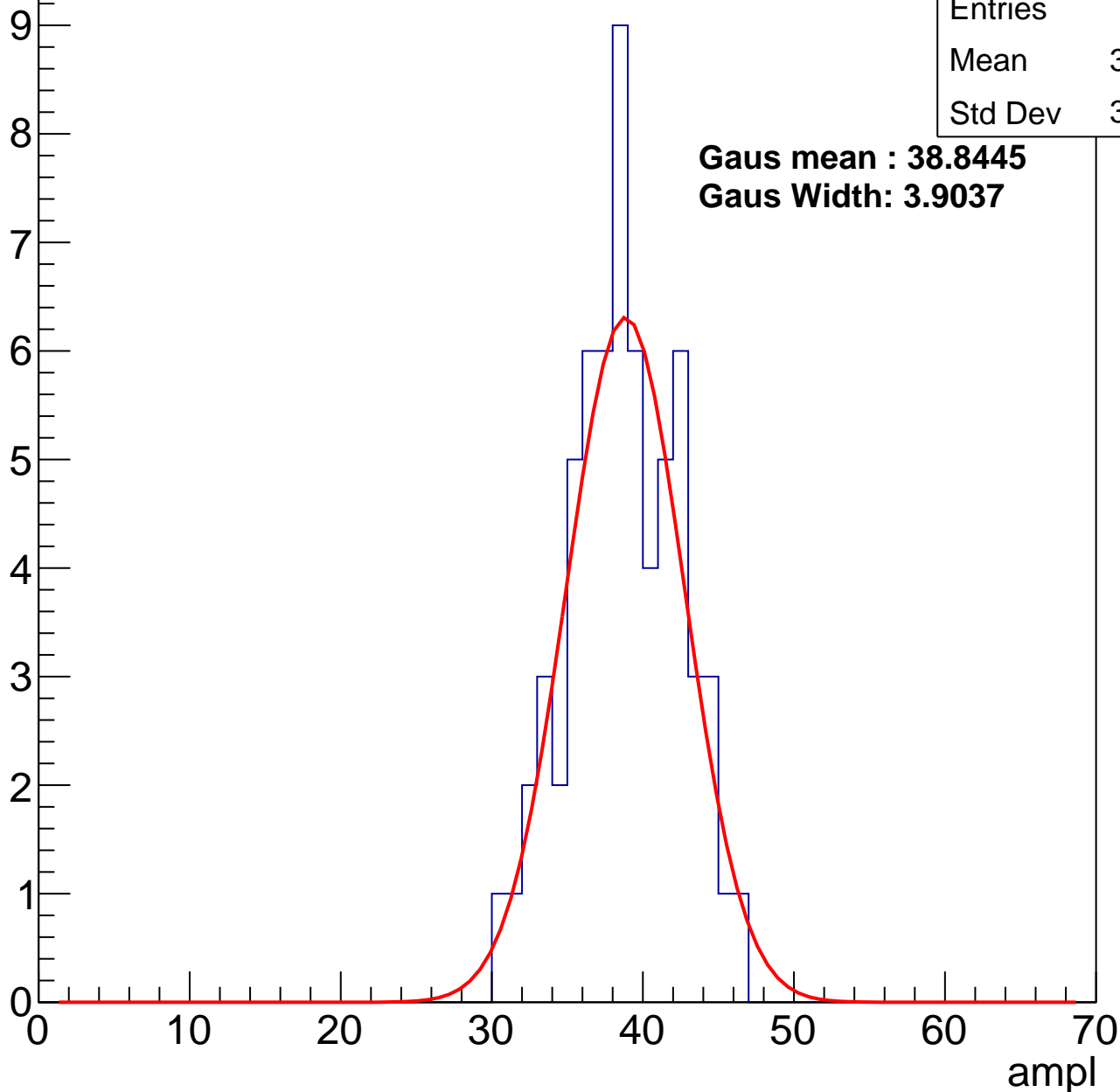
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 38.28 |
| Std Dev | 3.582 |

Gaus mean : 38.8445

Gaus Width: 3.9037



B0L001S, U7-ch96, adc2

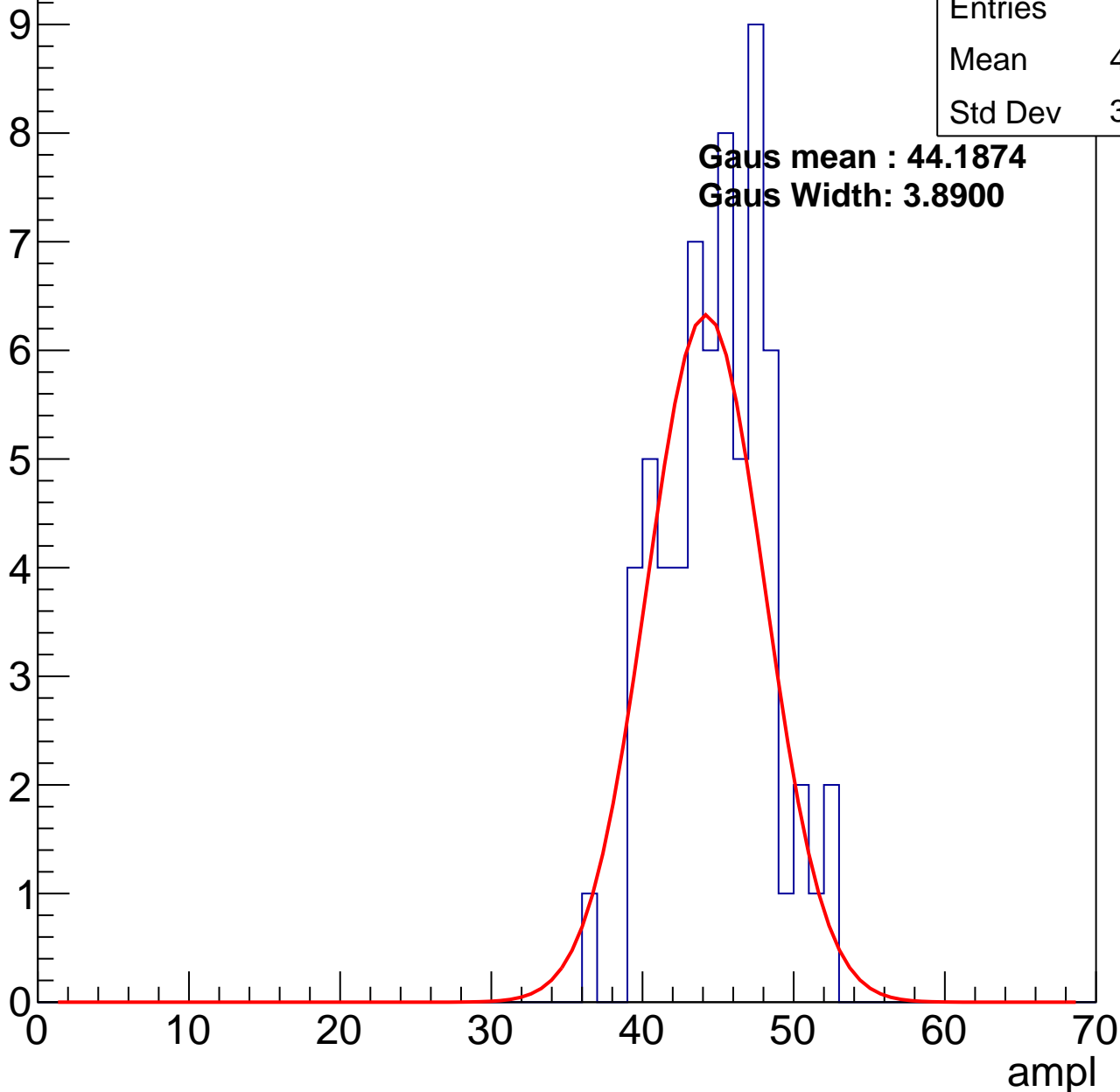
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 44.52 |
| Std Dev | 3.429 |

Gaus mean : 44.1874

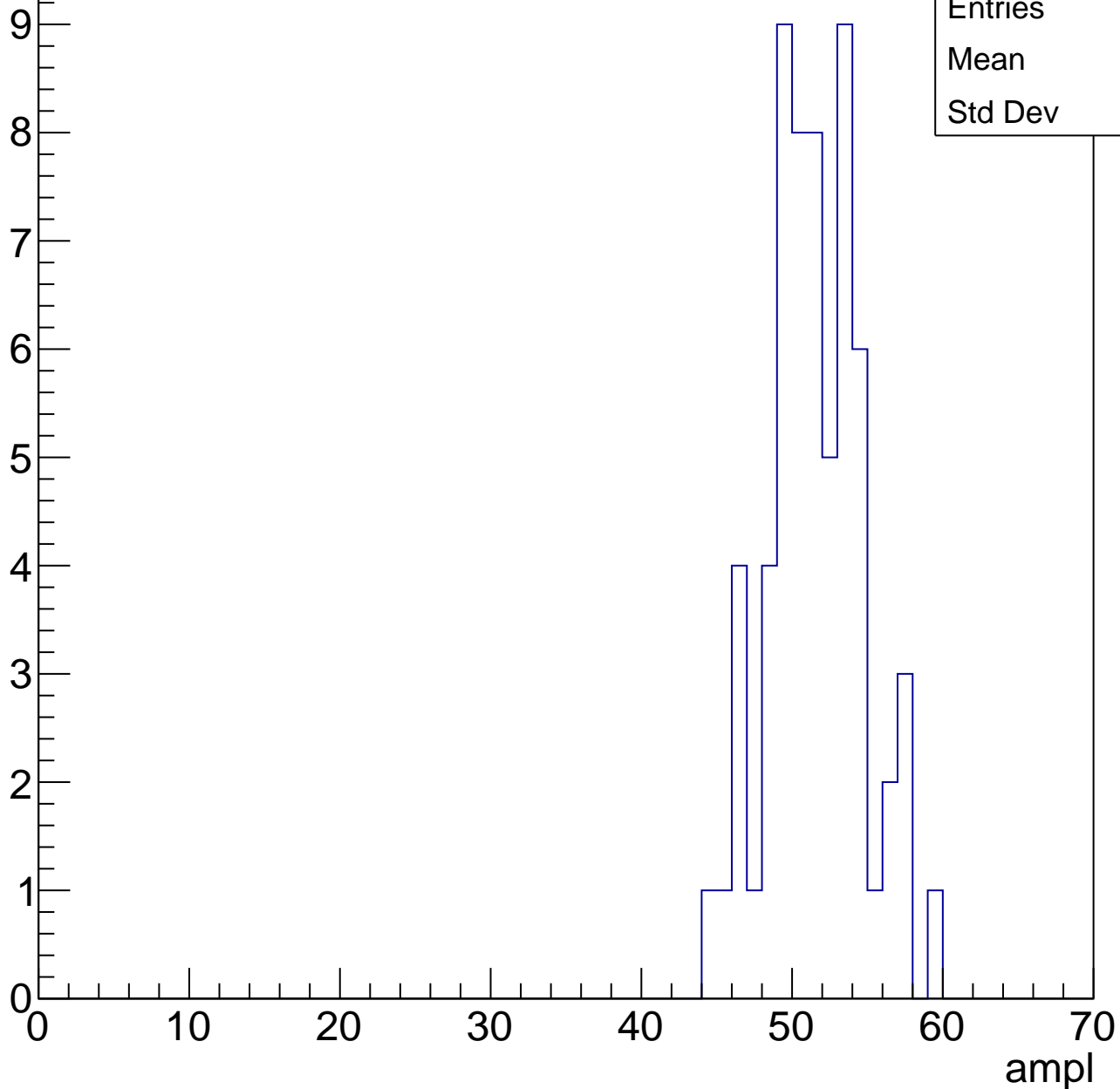
Gaus Width: 3.8900



B0L001S, U7-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

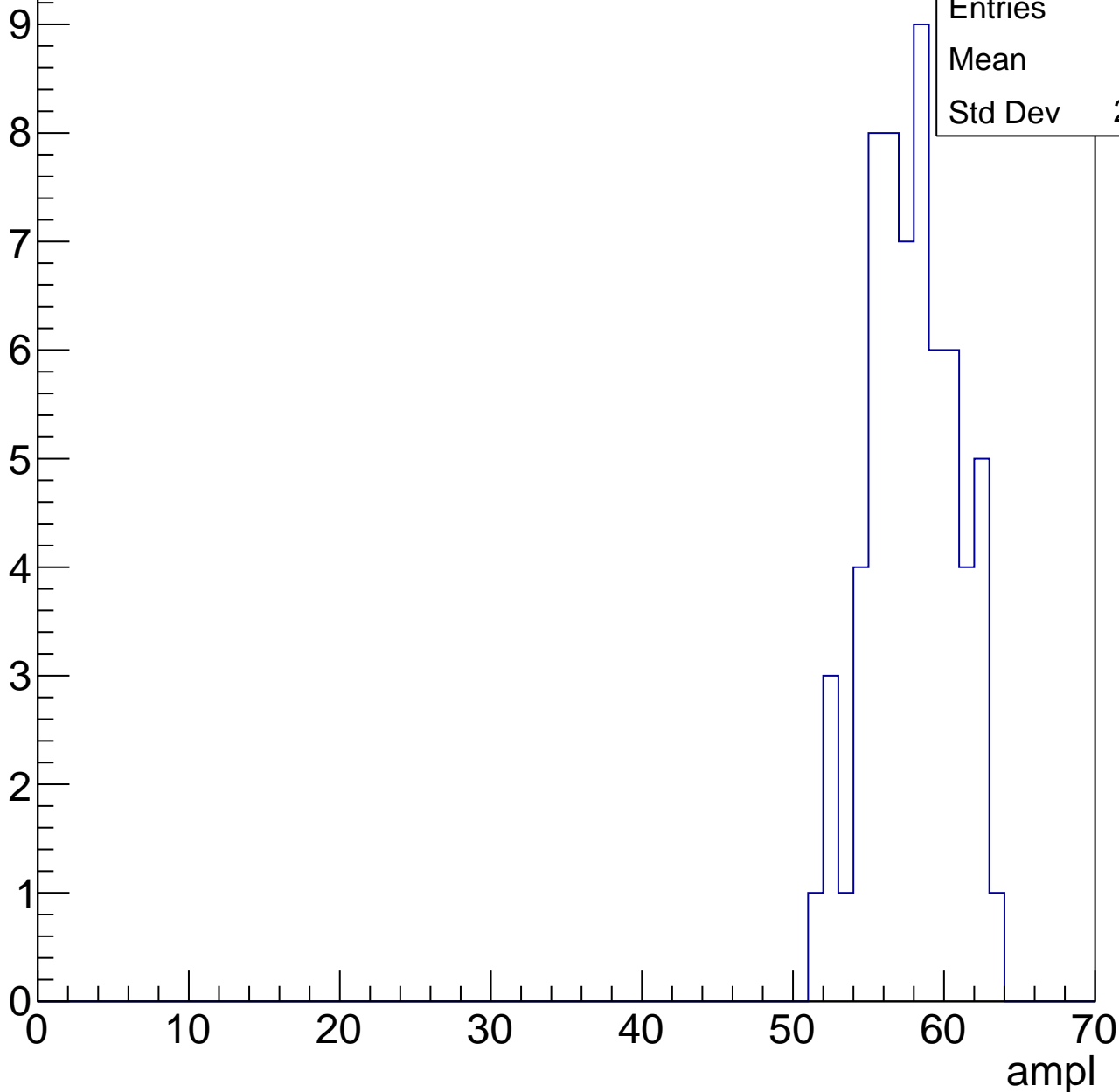


B0L001S, U7-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.4 |
| Std Dev | 2.831 |

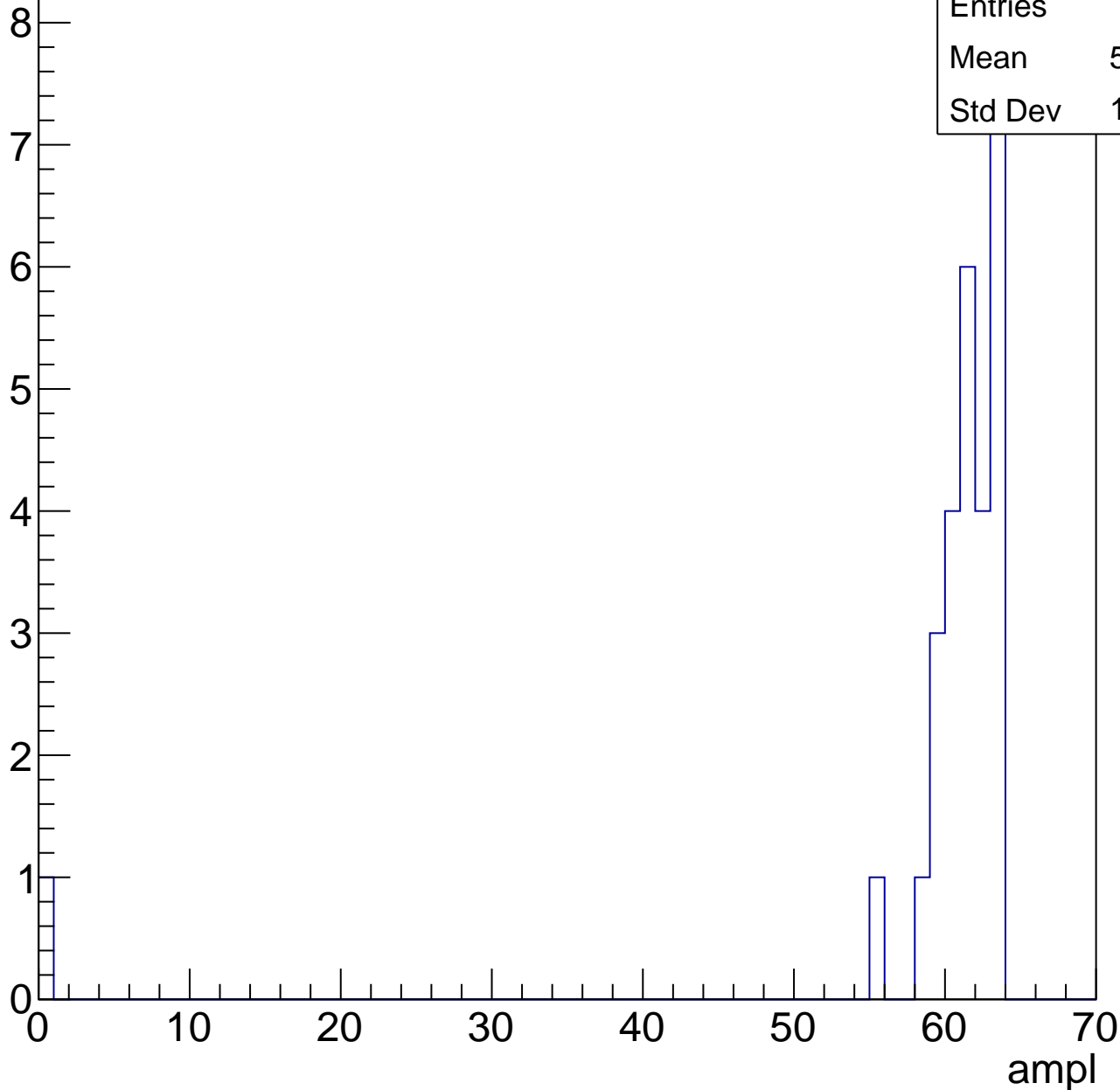


B0L001S, U7-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.86 |
| Std Dev | 11.48 |



B0L001S, U7-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch97, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 72 |
| Mean | 31 |
| Std Dev | 7.255 |

Gaus mean : 33.3707

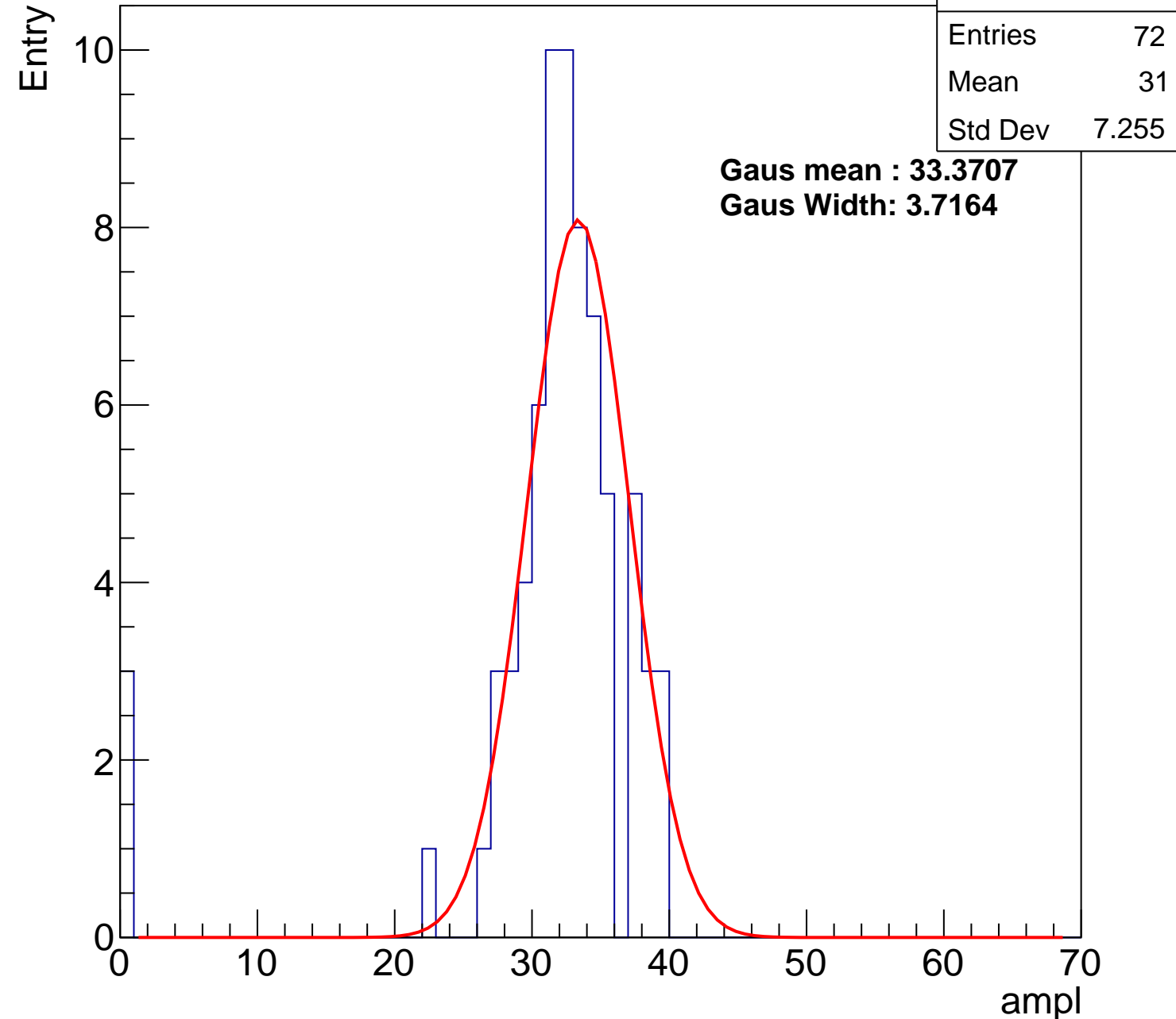
Gaus Width: 3.7164

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch97, adc1

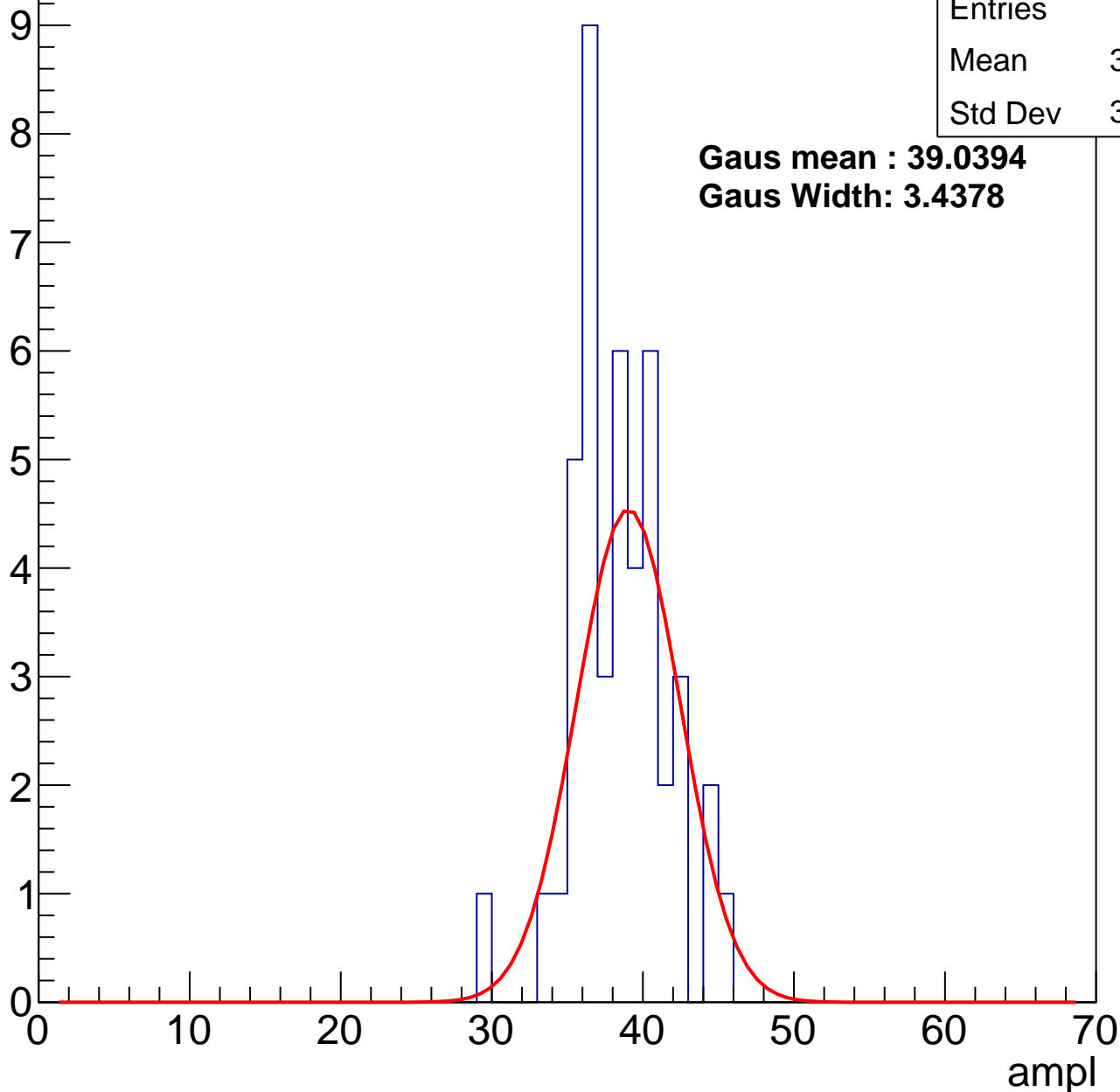
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 37.98 |
| Std Dev | 3.093 |

Gaus mean : 39.0394

Gaus Width: 3.4378



B0L001S, U7-ch97, adc2

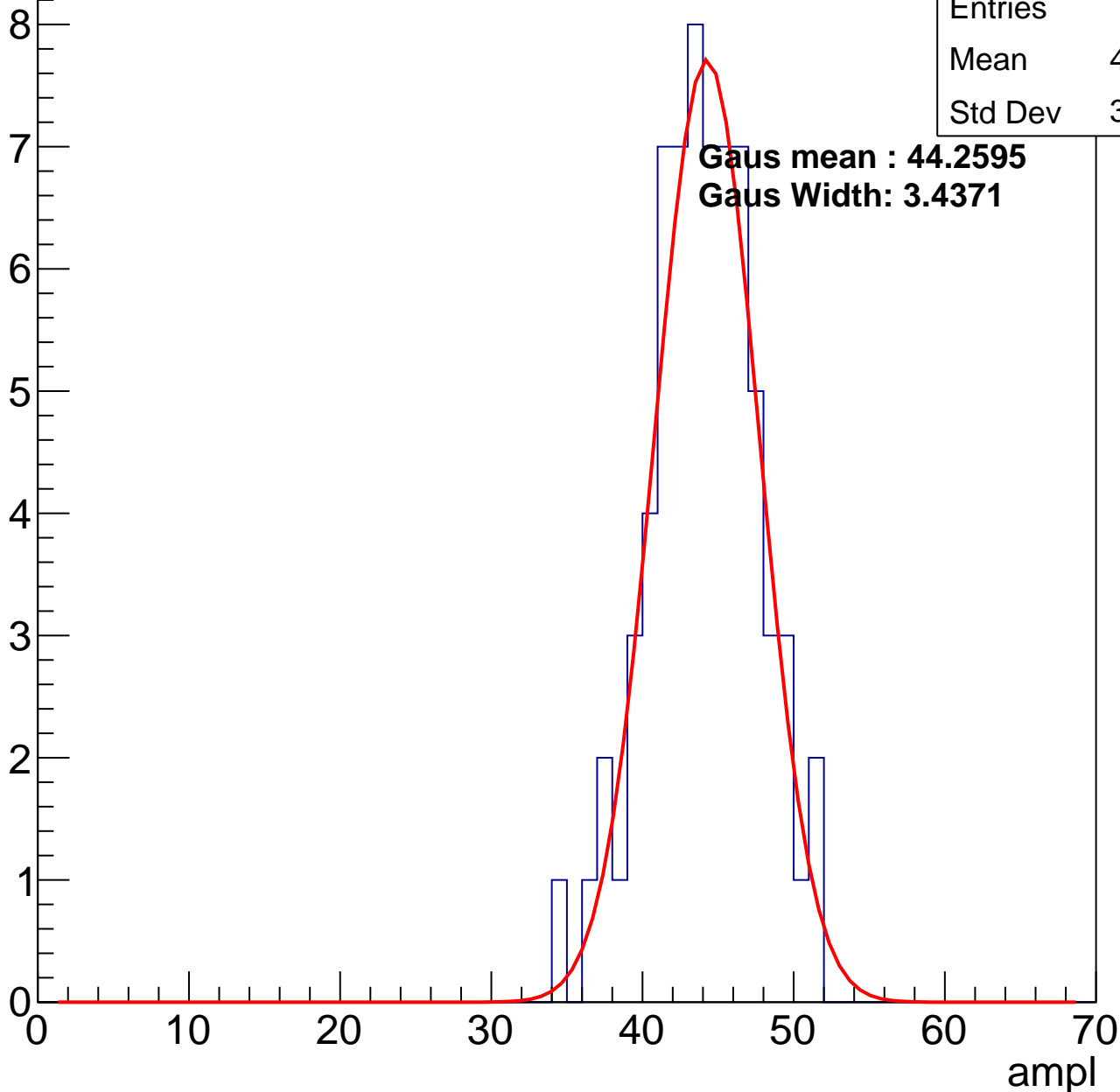
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.58 |
| Std Dev | 3.532 |

Gaus mean : 44.2595

Gaus Width: 3.4371

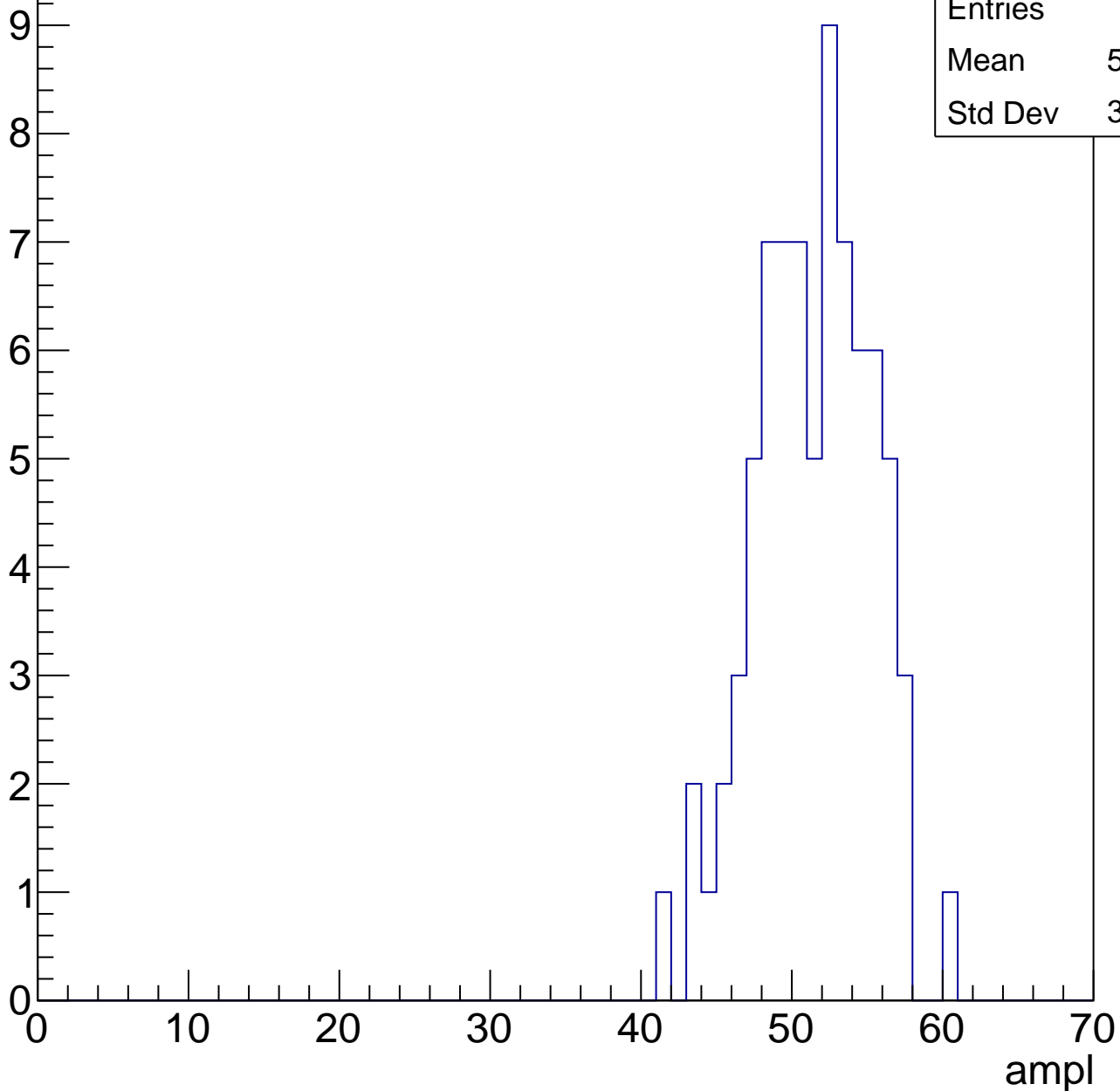


B0L001S, U7-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 50.94 |
| Std Dev | 3.784 |

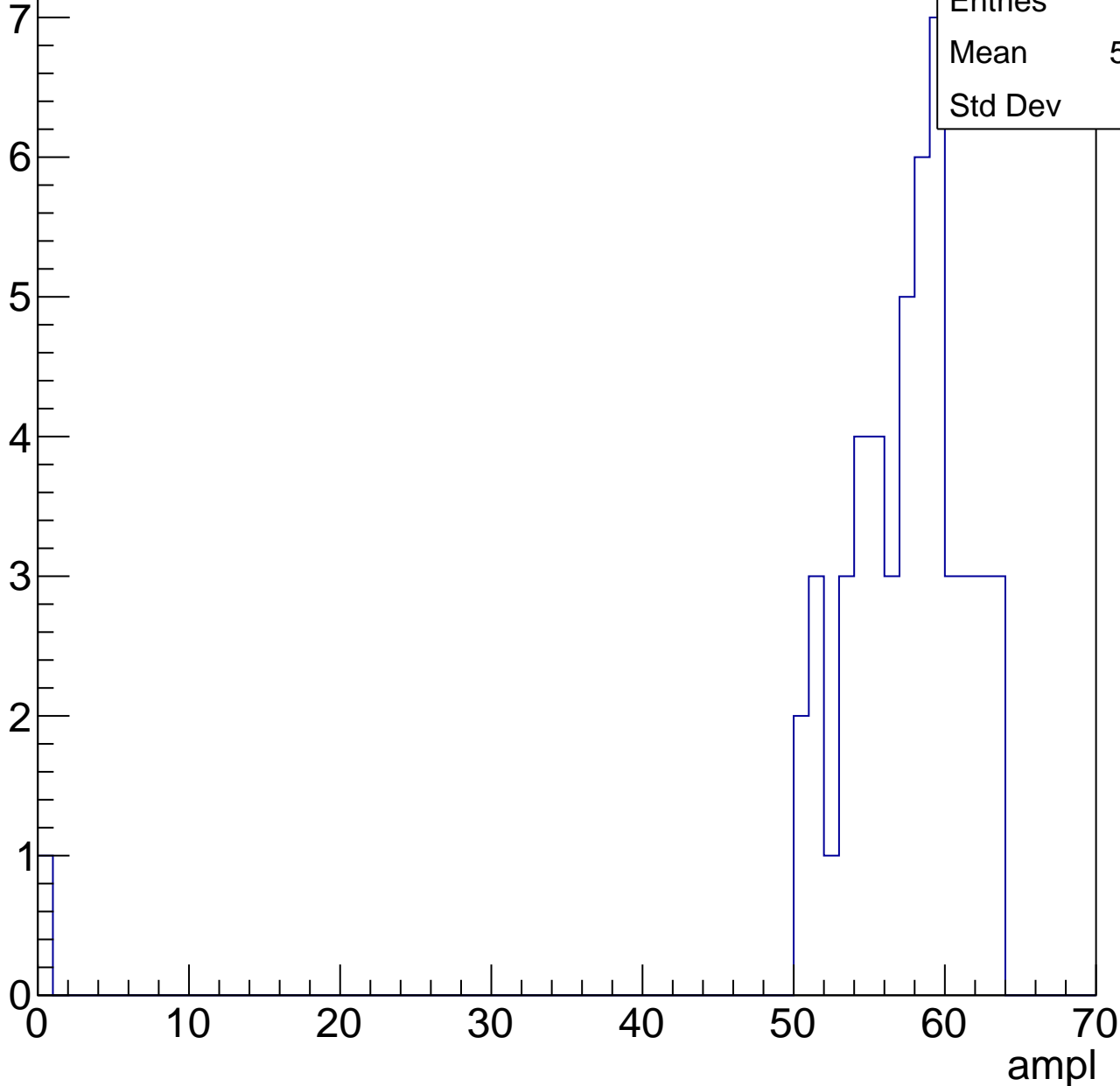


B0L001S, U7-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 55.92 |
| Std Dev | 8.65 |

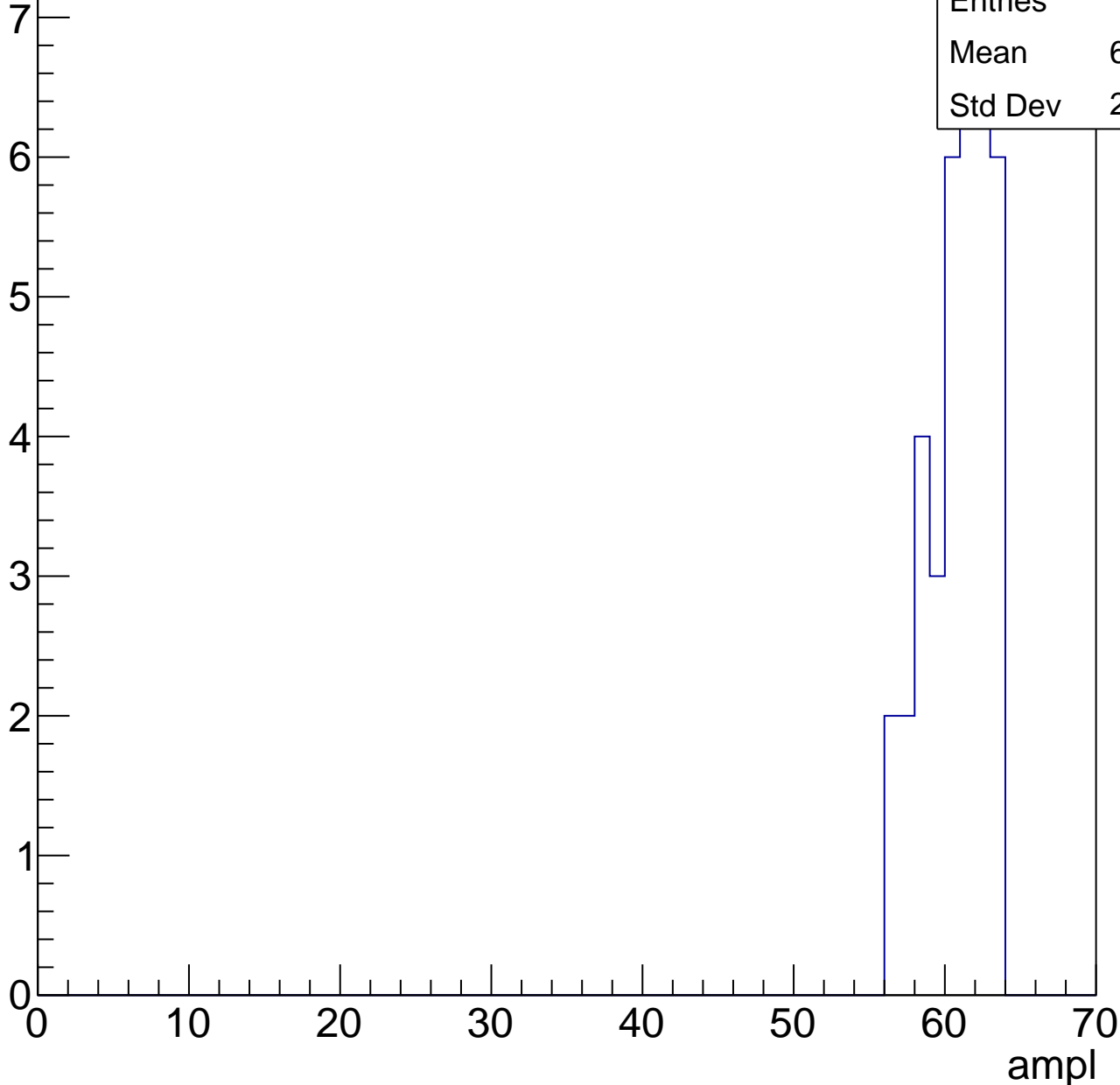


B0L001S, U7-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 60.38 |
| Std Dev | 2.032 |



B0L001S, U7-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch98, adc0

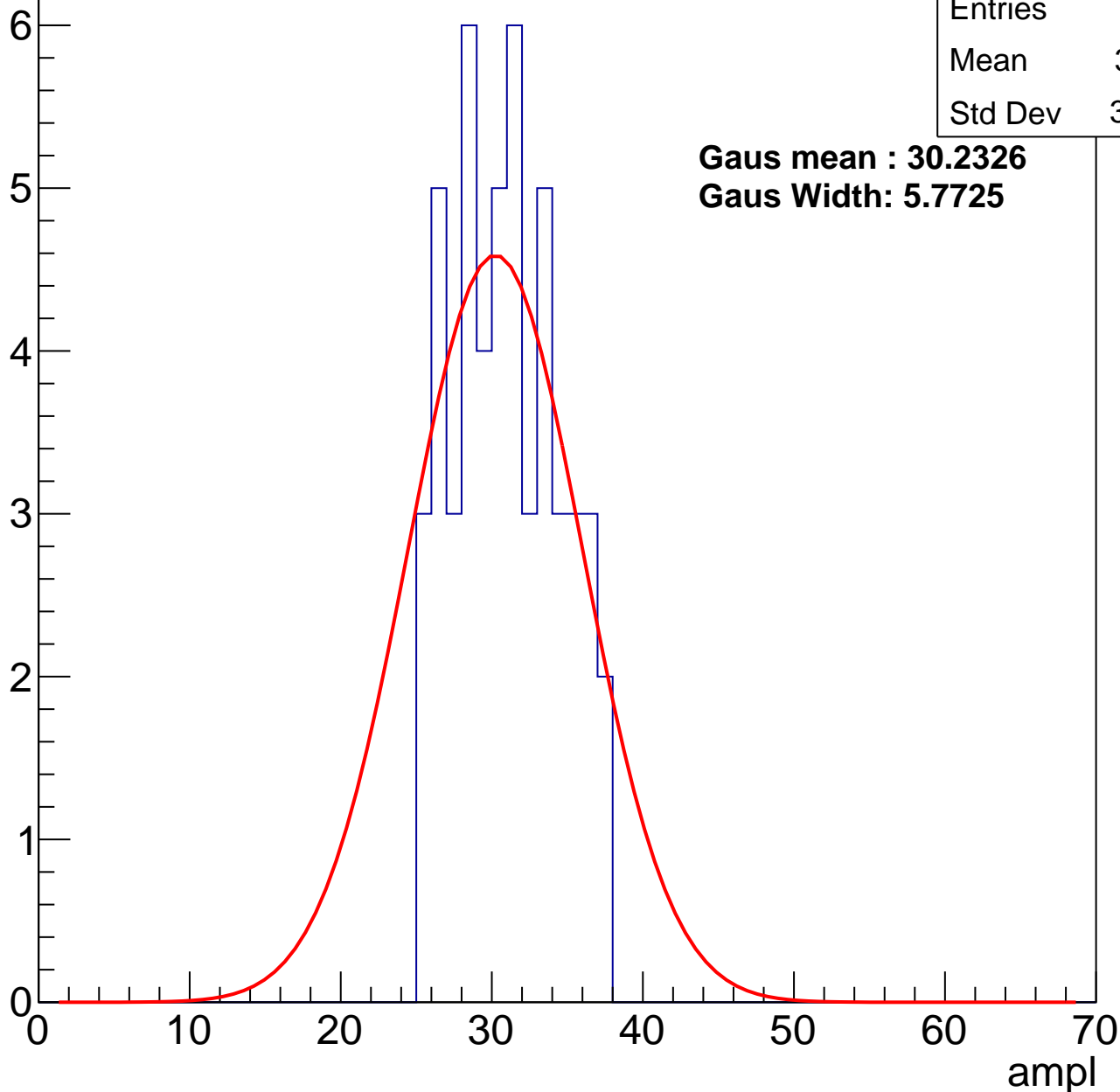
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 30.51 |
| Std Dev | 3.398 |

Gaus mean : 30.2326

Gaus Width: 5.7725



B0L001S, U7-ch98, adc1

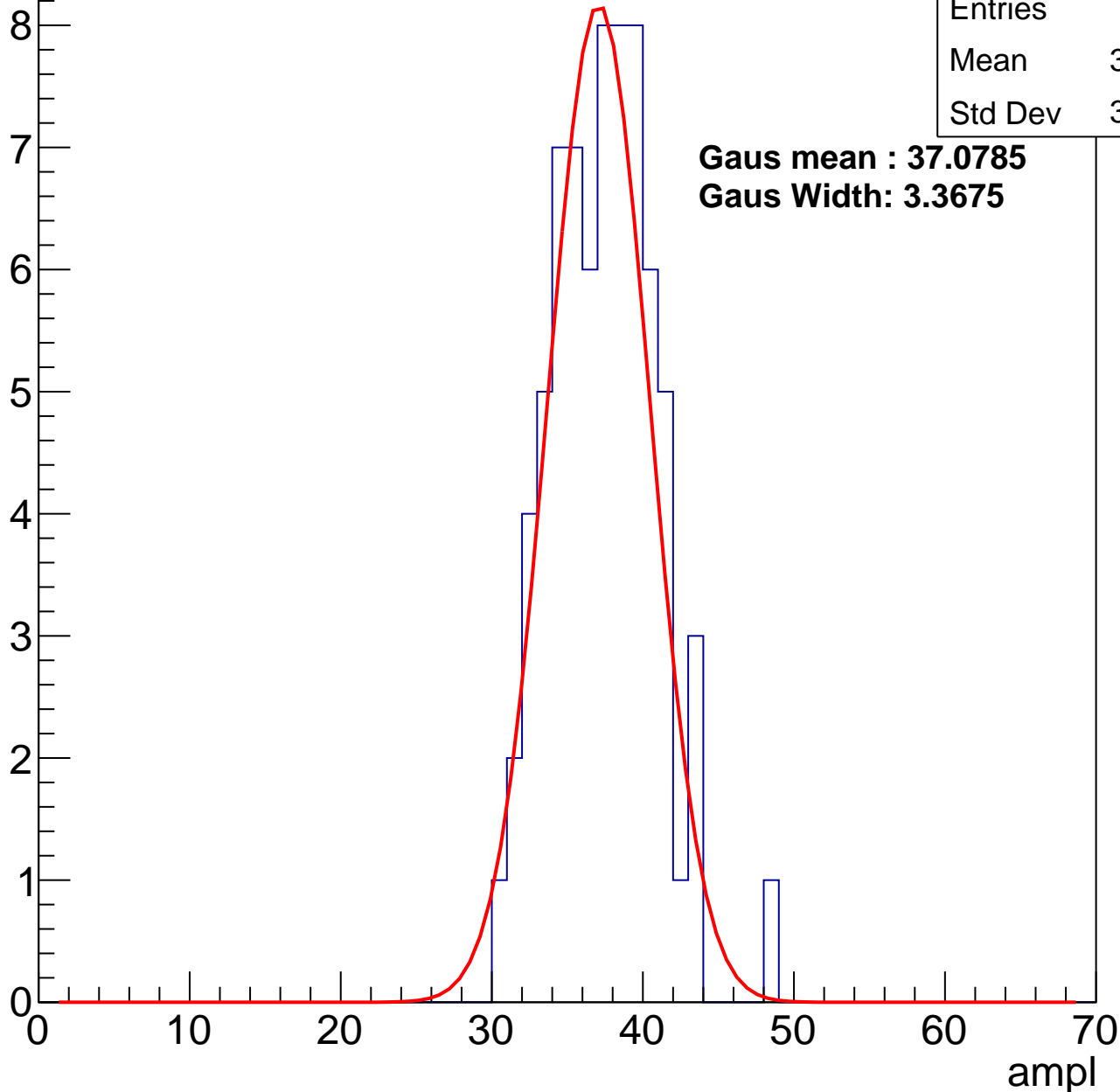
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.94 |
| Std Dev | 3.387 |

Gaus mean : 37.0785

Gaus Width: 3.3675



B0L001S, U7-ch98, adc2

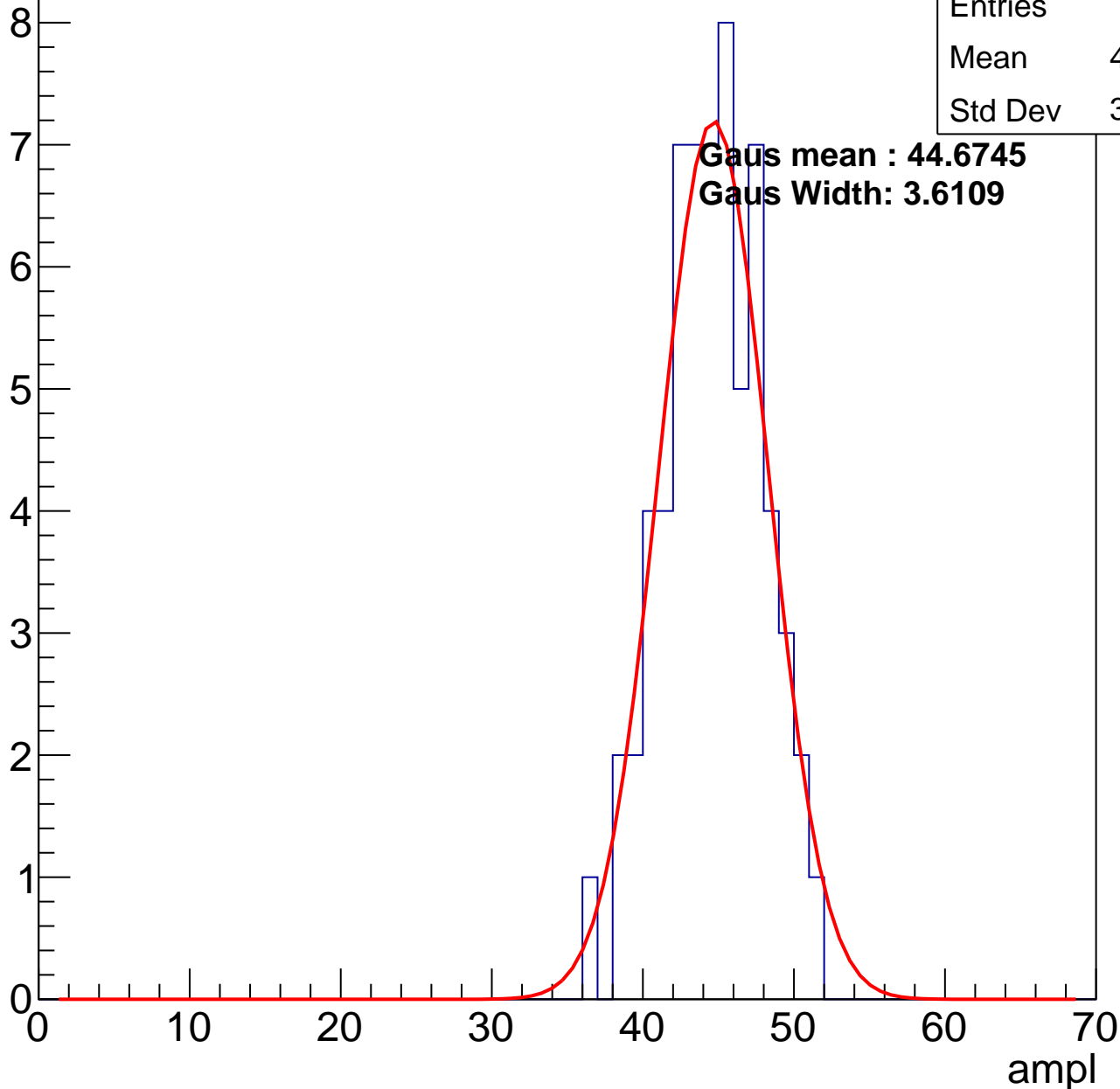
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 44.16 |
| Std Dev | 3.237 |

Gaus mean : 44.6745

Gaus Width: 3.6109

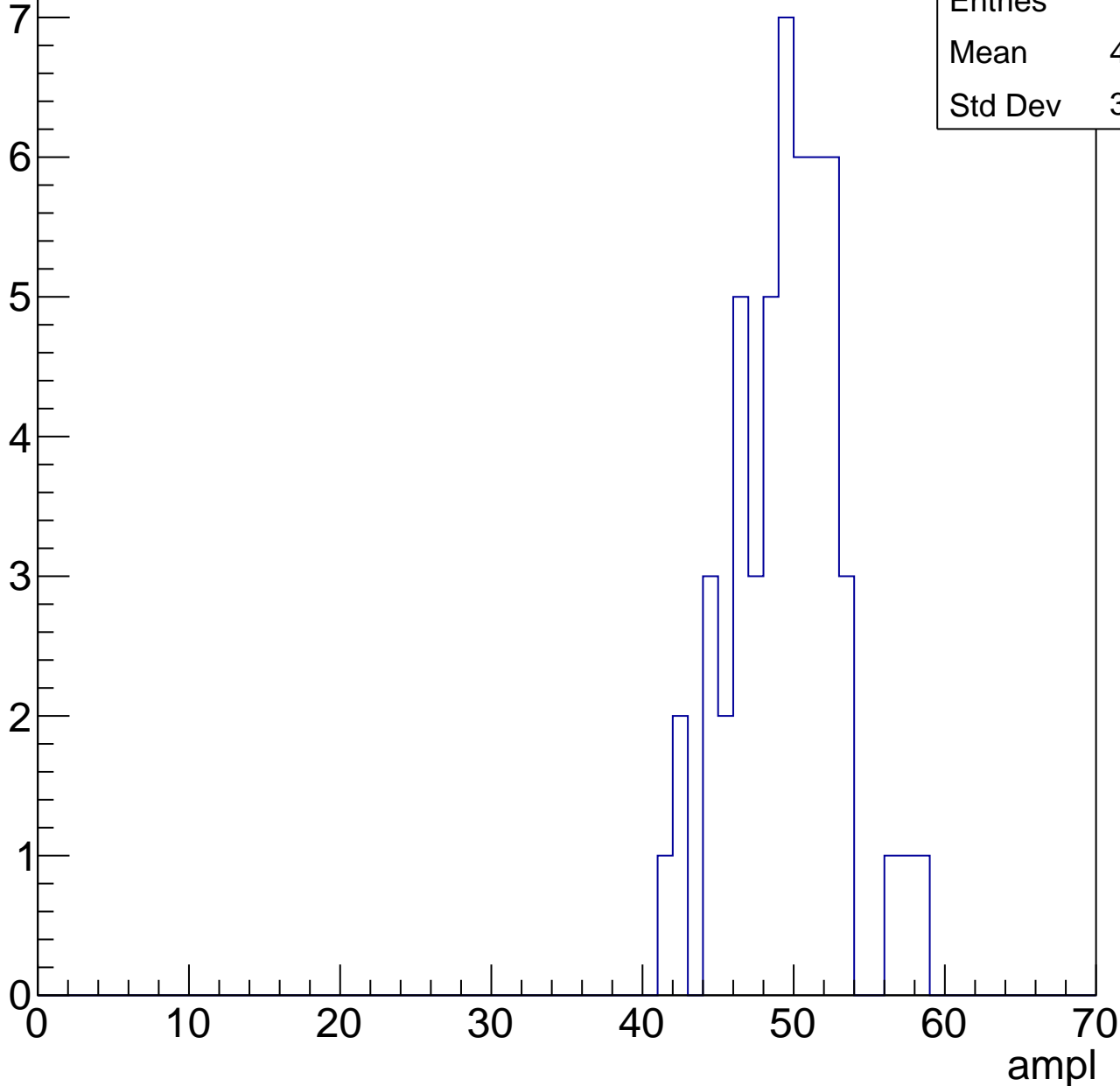


B0L001S, U7-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 49.02 |
| Std Dev | 3.554 |

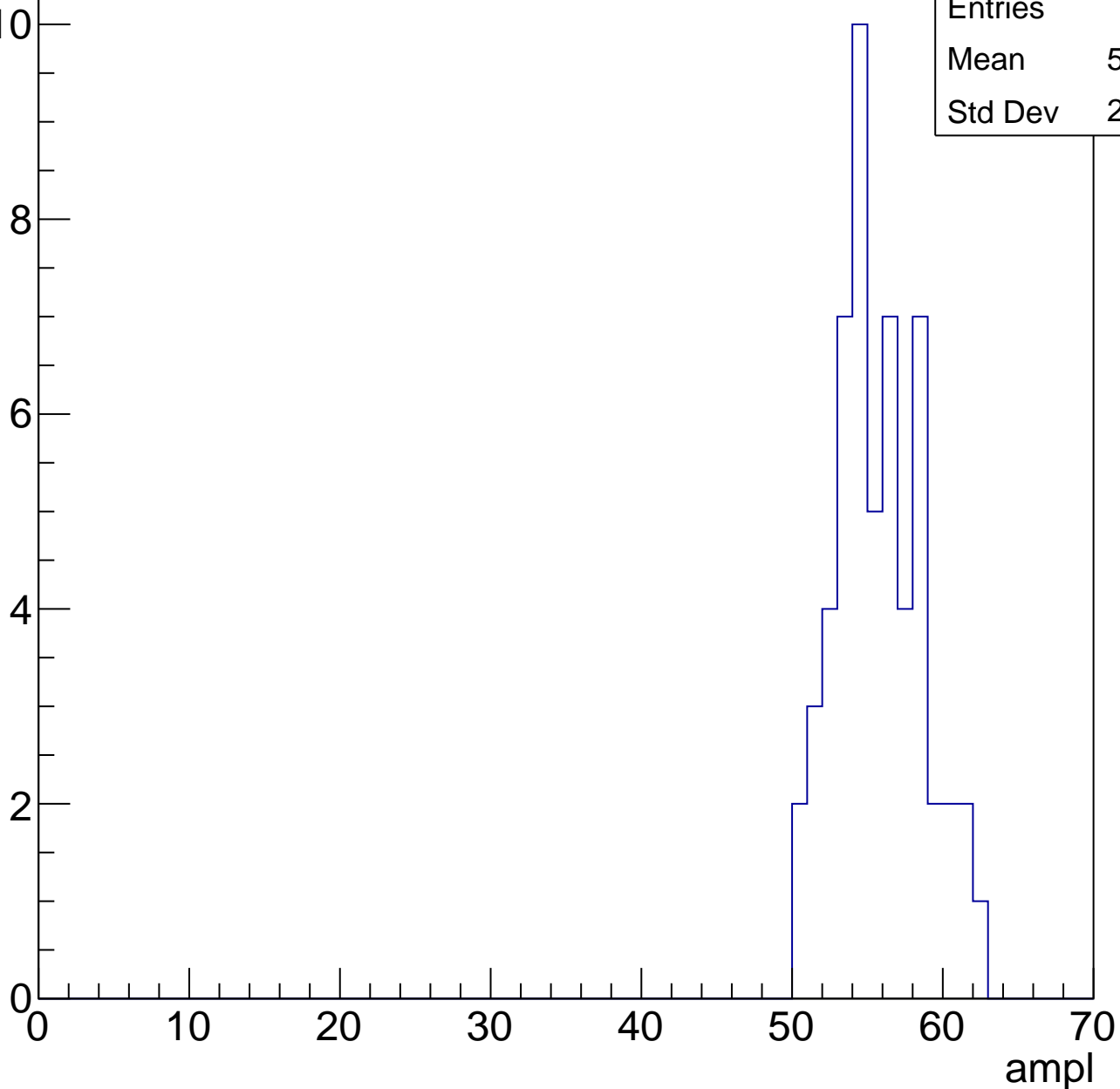


B0L001S, U7-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 55.27 |
| Std Dev | 2.857 |



B0L001S, U7-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.35 |
| Std Dev | 8.77 |

ampl

0

10

20

30

40

50

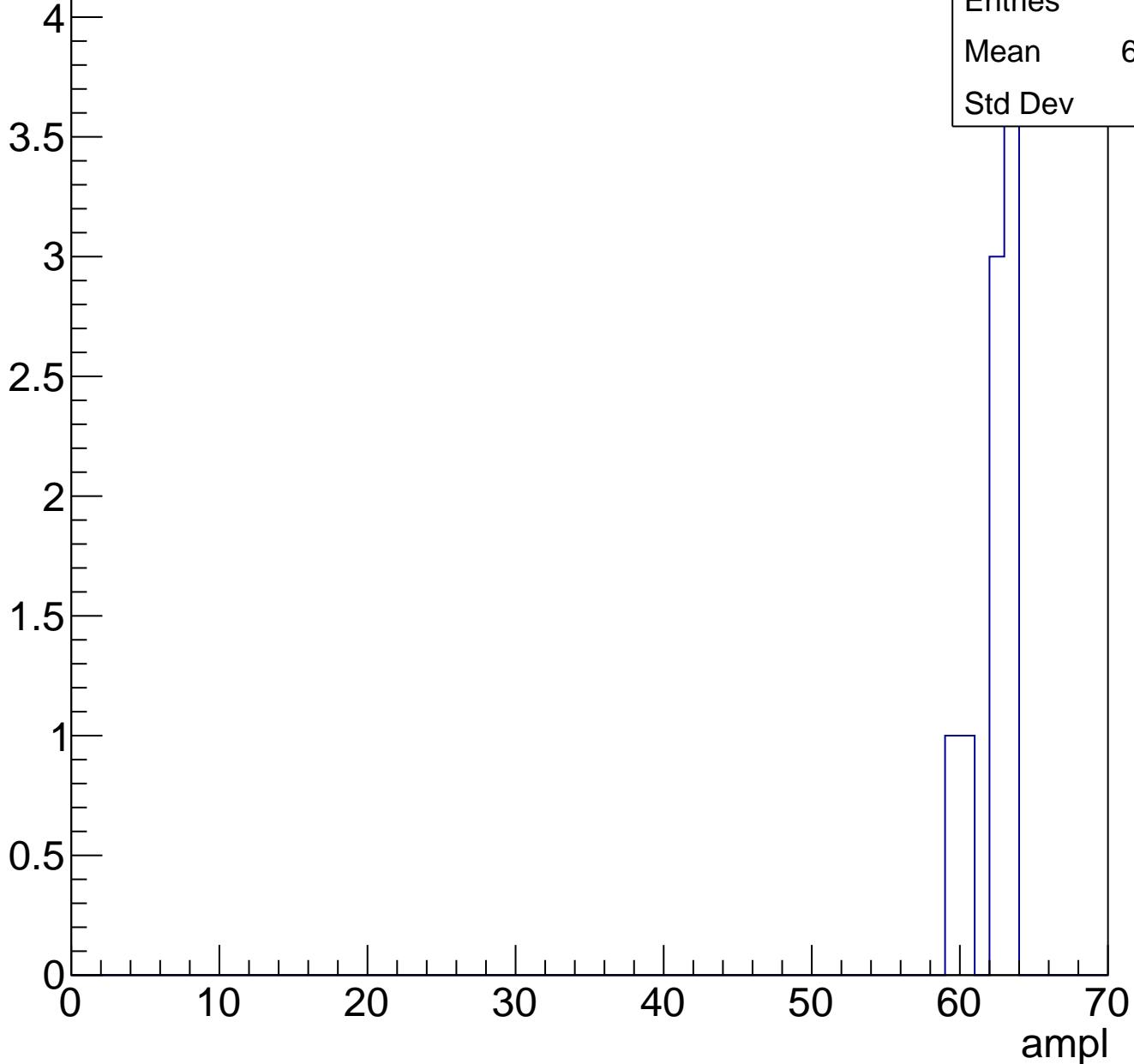
60

70

B0L001S, U7-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch99, adc0

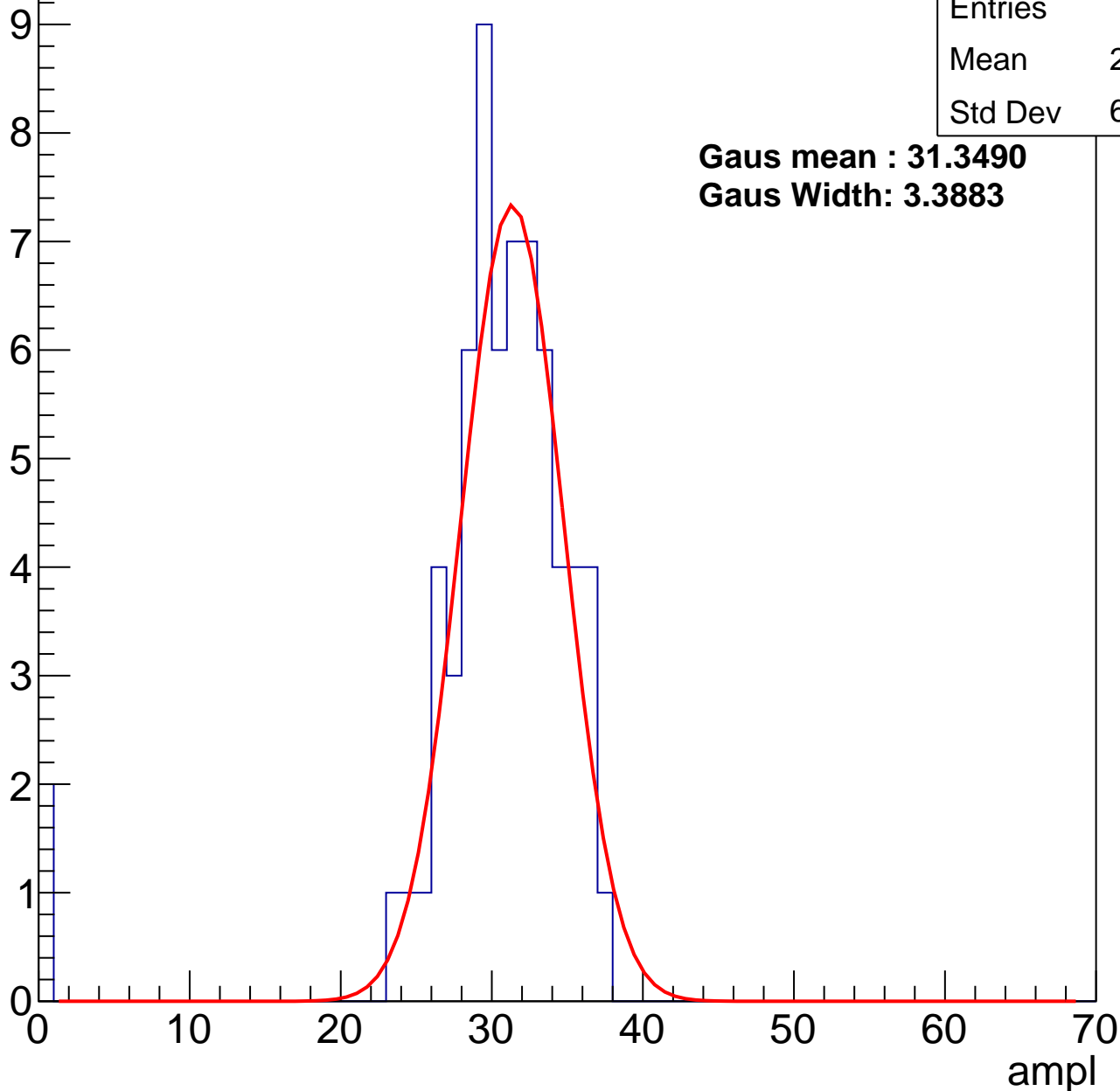
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 29.73 |
| Std Dev | 6.124 |

Gaus mean : 31.3490

Gaus Width: 3.3883



B0L001S, U7-ch99, adc1

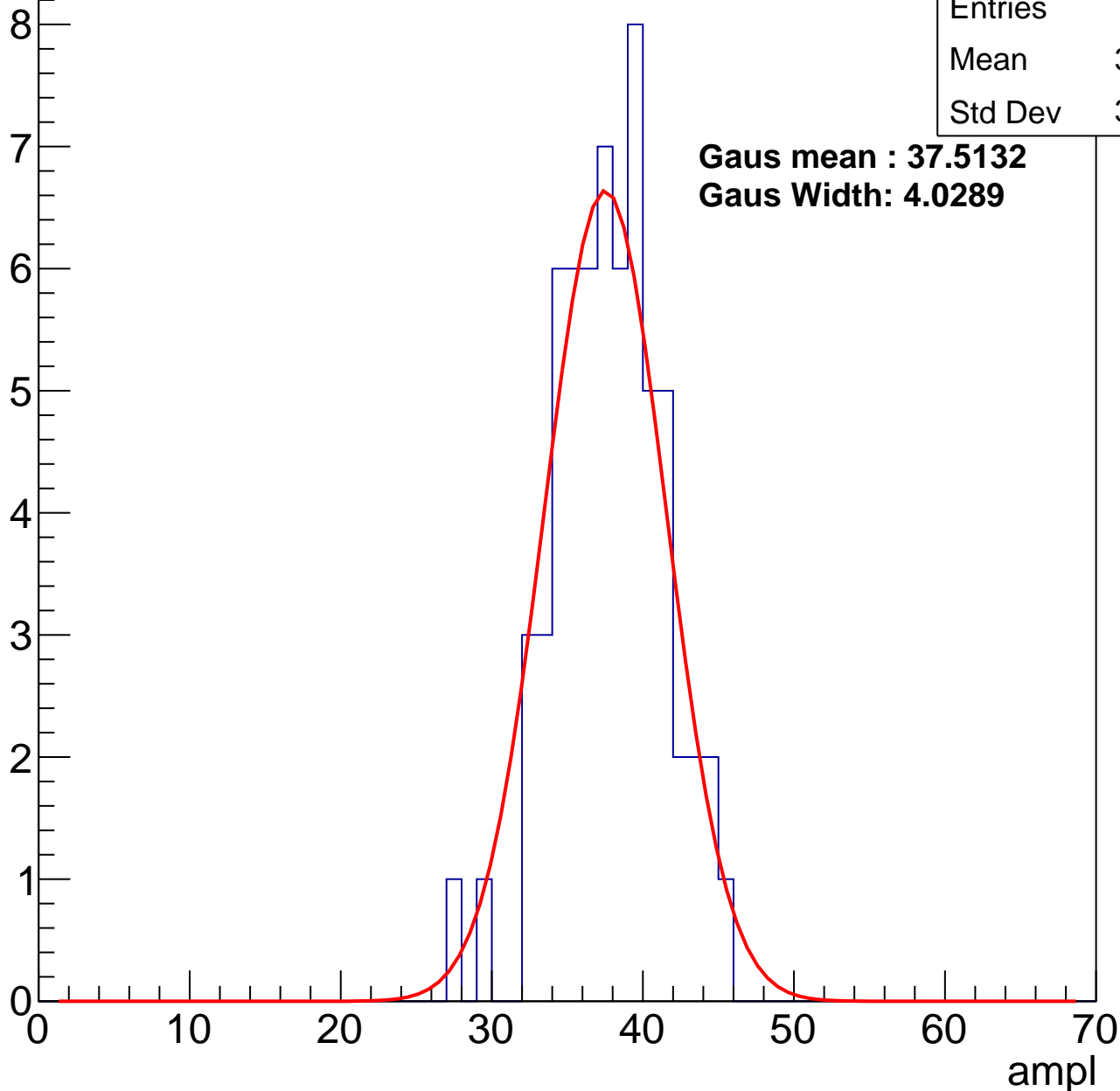
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37.31 |
| Std Dev | 3.561 |

Gaus mean : 37.5132

Gaus Width: 4.0289



B0L001S, U7-ch99, adc2

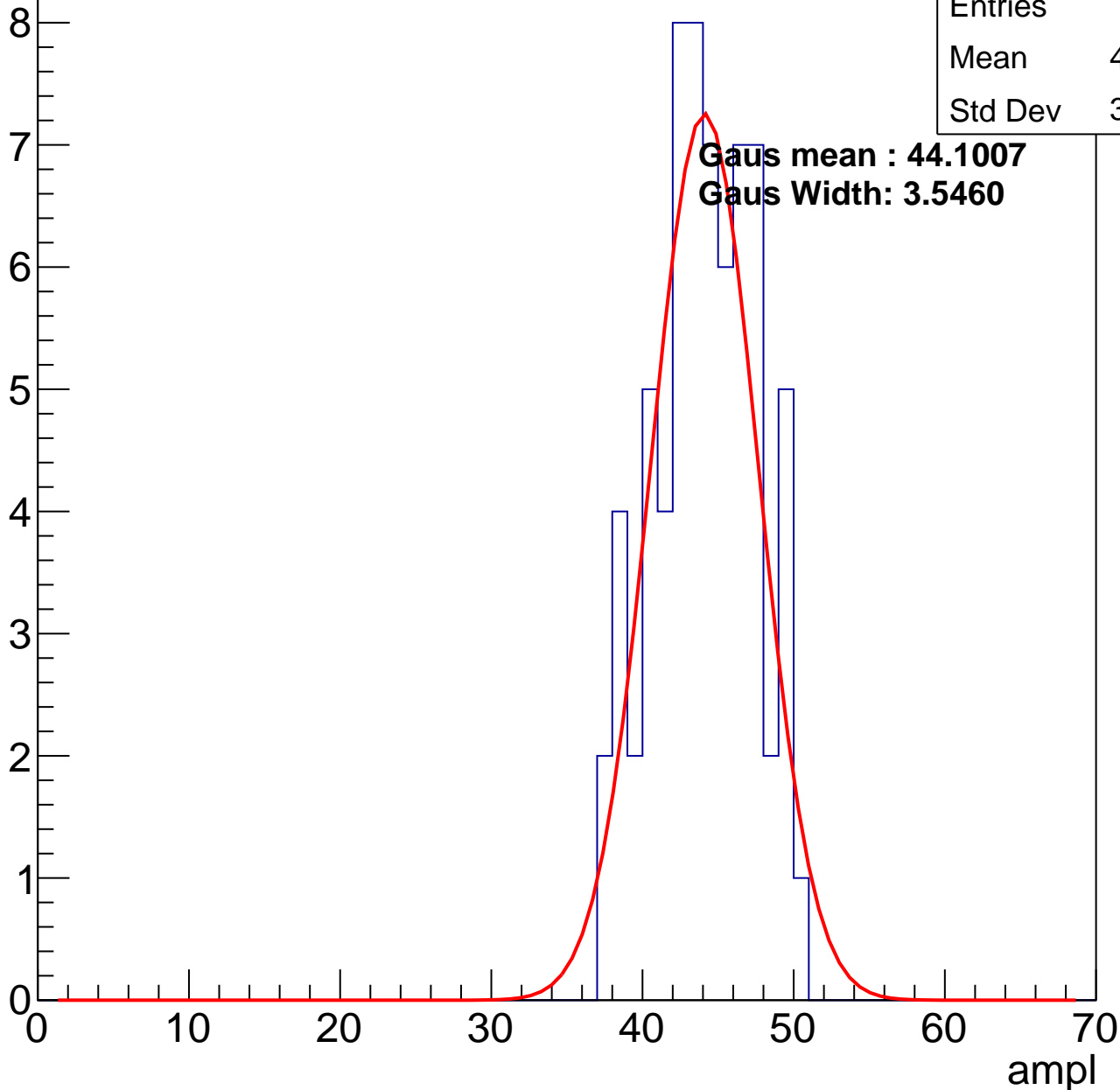
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 43.65 |
| Std Dev | 3.275 |

Gaus mean : 44.1007

Gaus Width: 3.5460

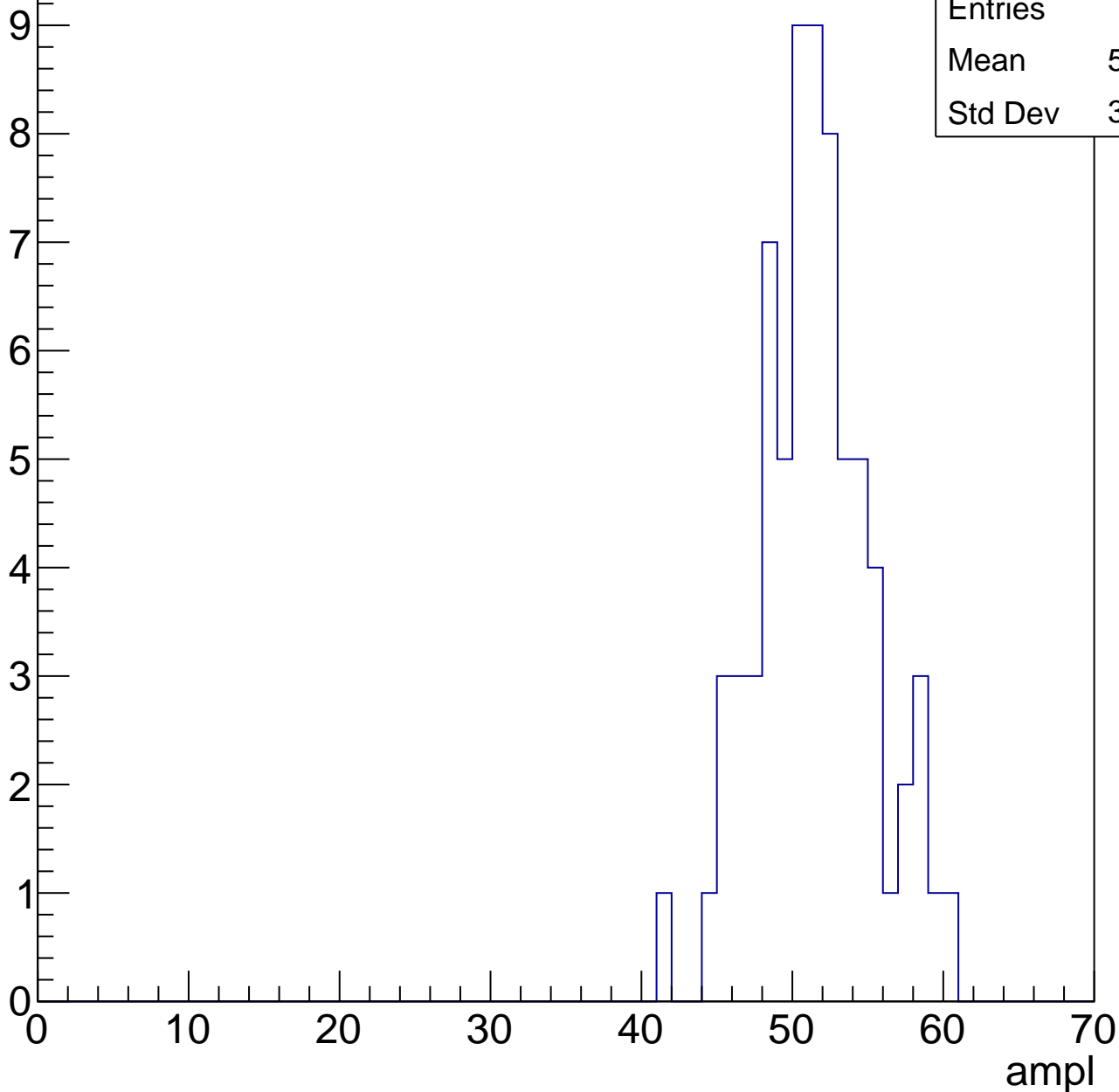


B0L001S, U7-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

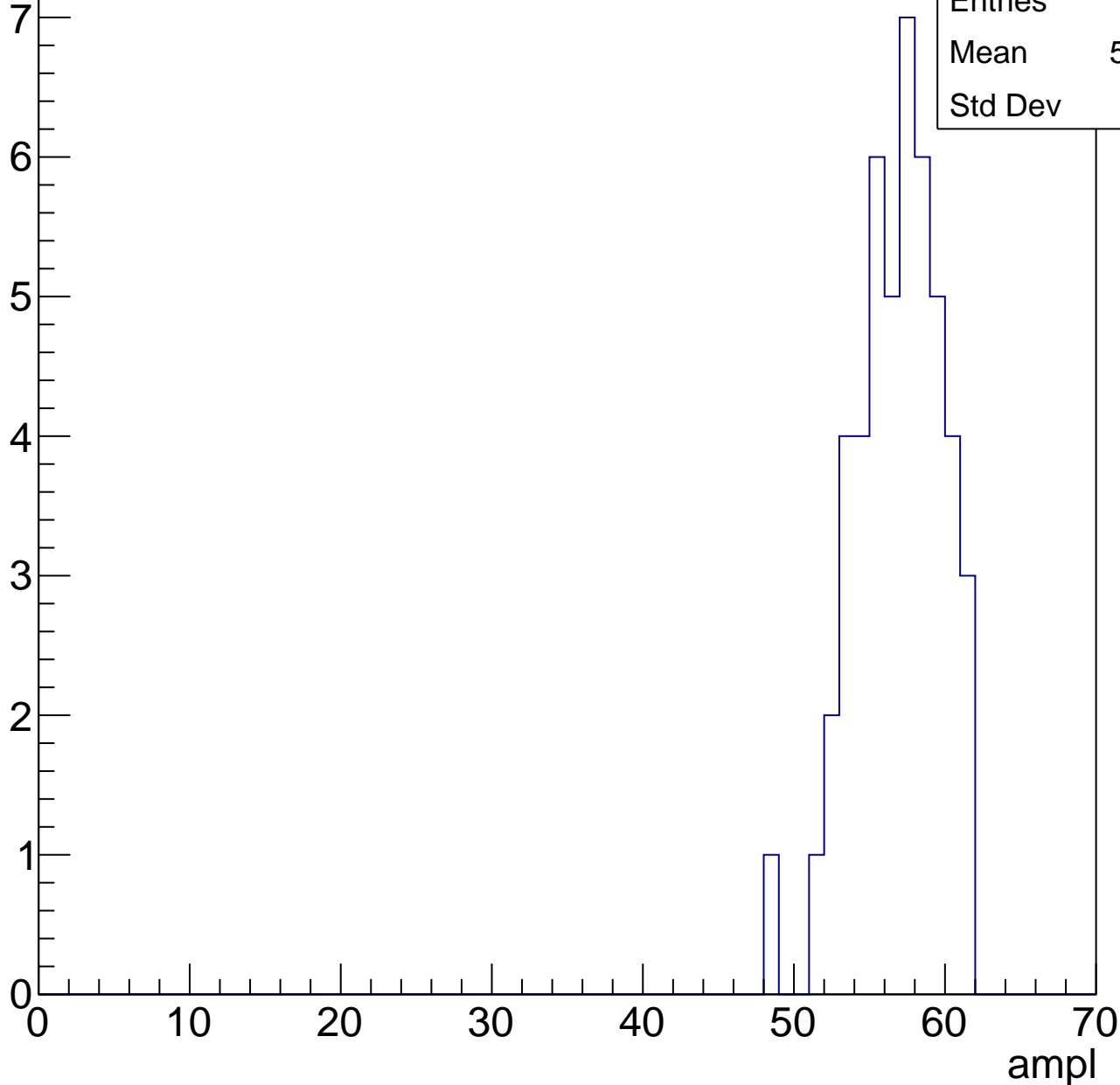
| | |
|---------|-------|
| Entries | 71 |
| Mean | 51.03 |
| Std Dev | 3.749 |



B0L001S, U7-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



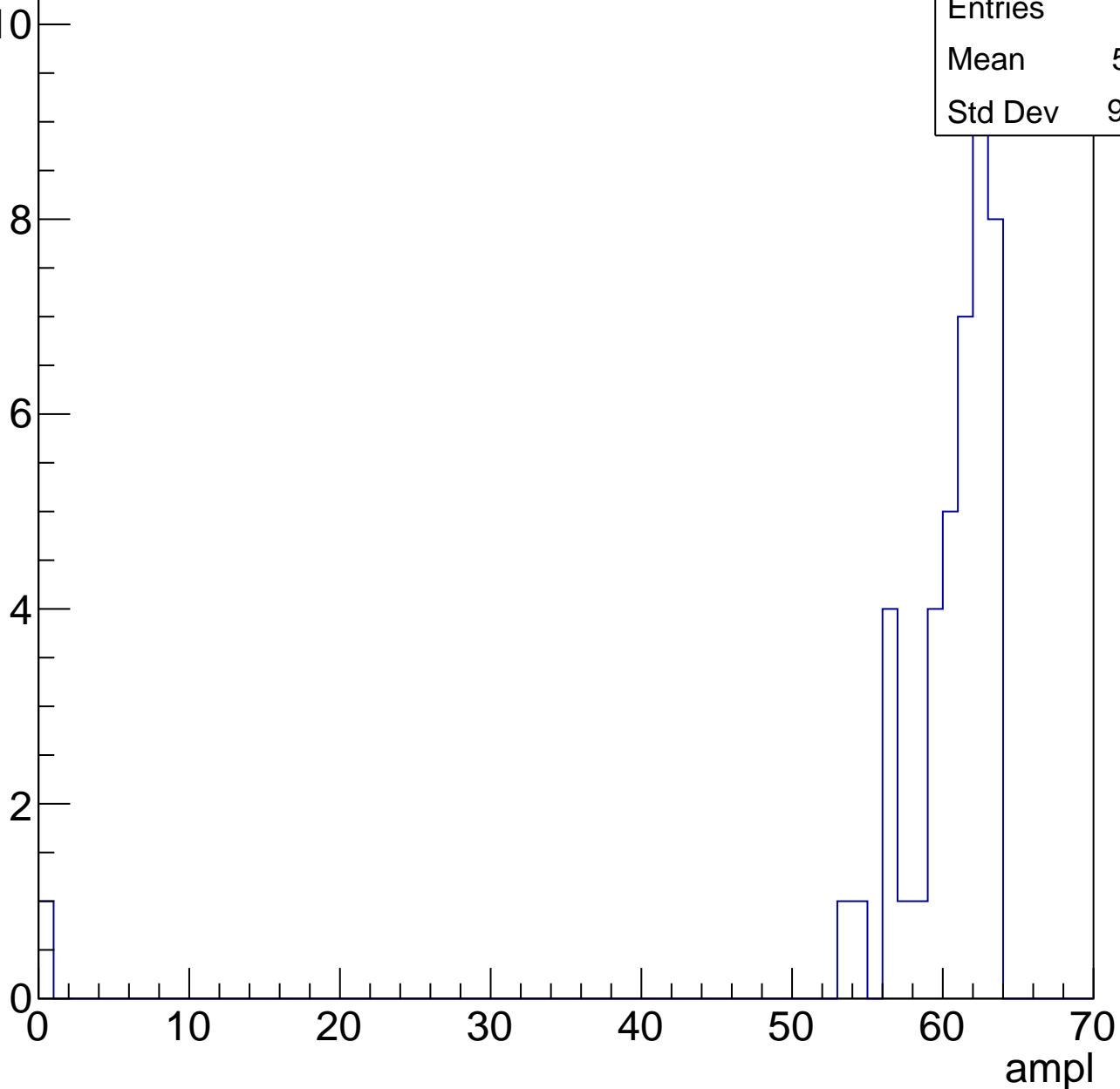
| | |
|---------|-------|
| Entries | 48 |
| Mean | 56.38 |
| Std Dev | 2.84 |

B0L001S, U7-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.91 |
| Std Dev | 9.442 |



B0L001S, U7-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|----|
| Entries | 2 |
| Mean | 63 |
| Std Dev | 0 |

B0L001S, U7-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch100, adc0

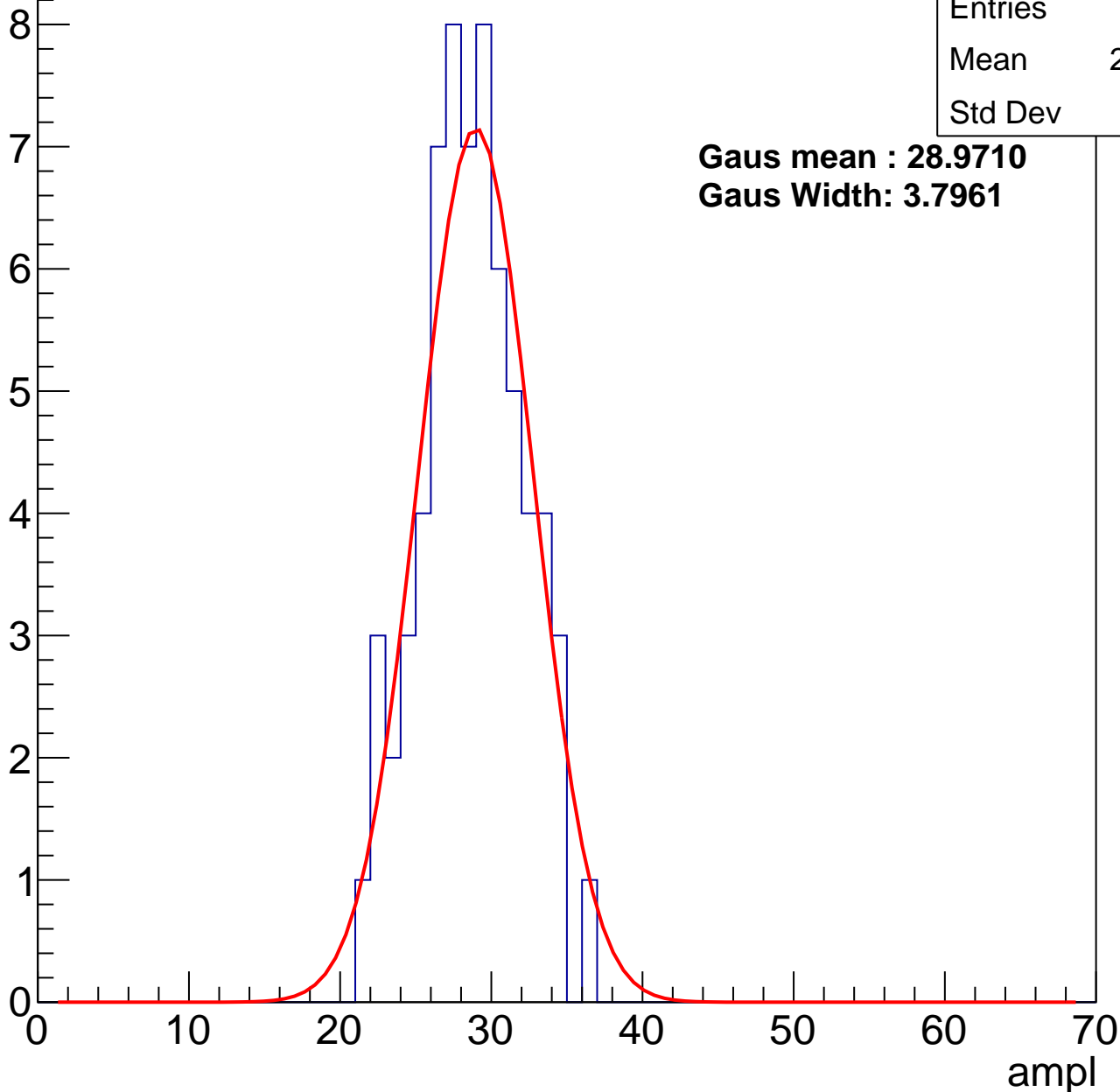
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.24 |
| Std Dev | 3.34 |

Gaus mean : 28.9710

Gaus Width: 3.7961



B0L001S, U7-ch100, adc1

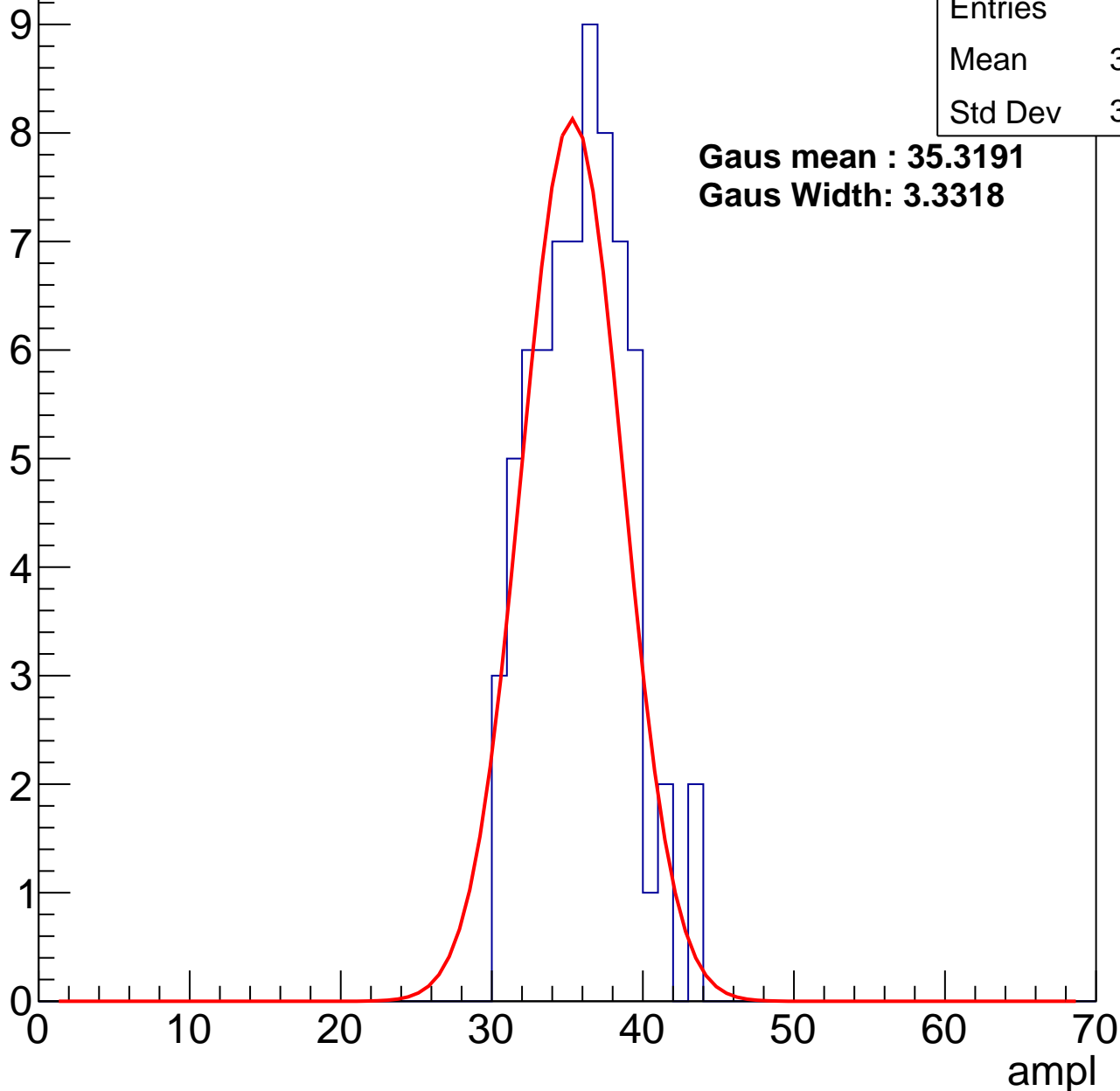
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 35.45 |
| Std Dev | 3.072 |

Gaus mean : 35.3191

Gaus Width: 3.3318



B0L001S, U7-ch100, adc2

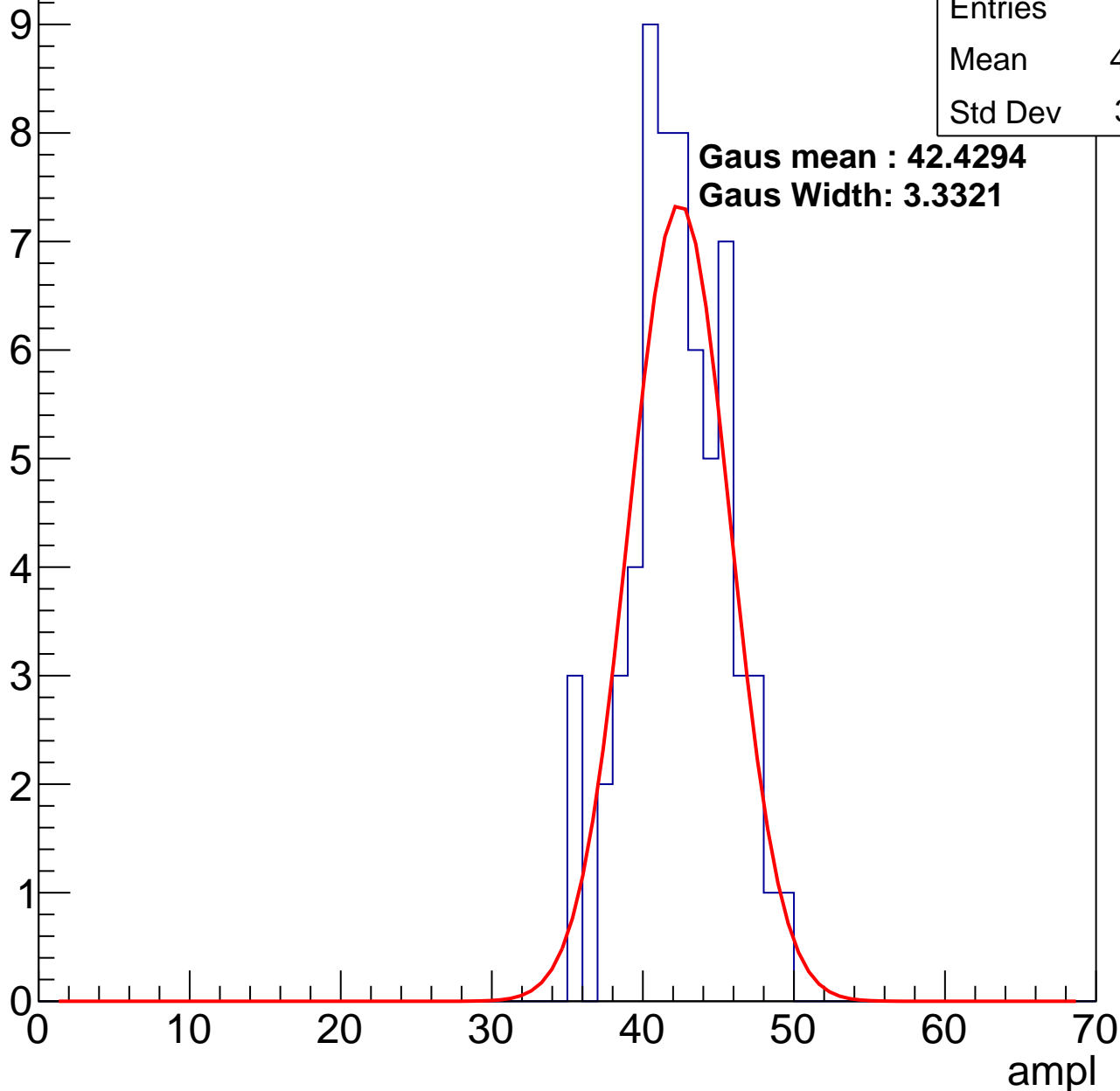
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.94 |
| Std Dev | 3.141 |

Gaus mean : 42.4294

Gaus Width: 3.3321

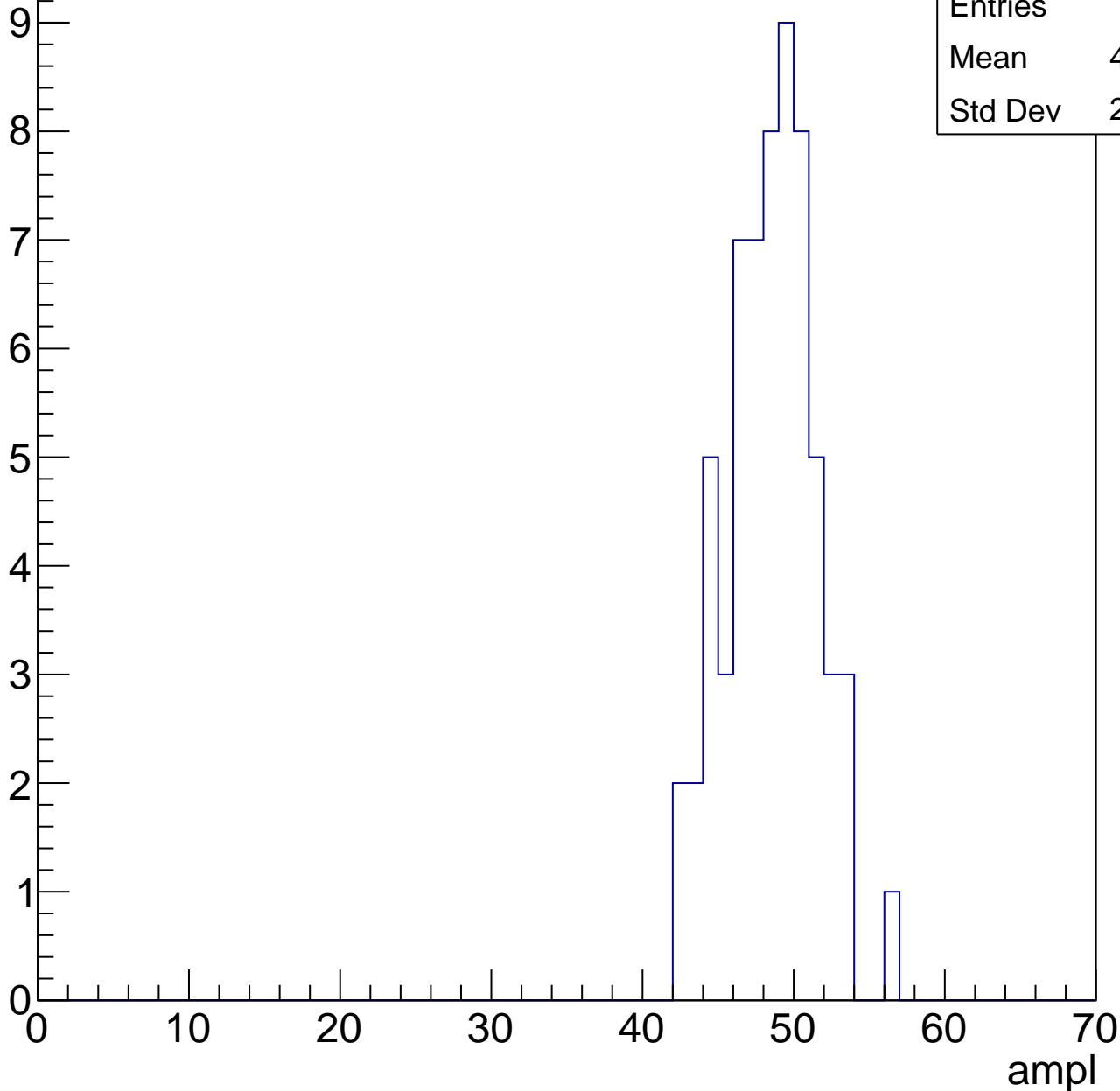


B0L001S, U7-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

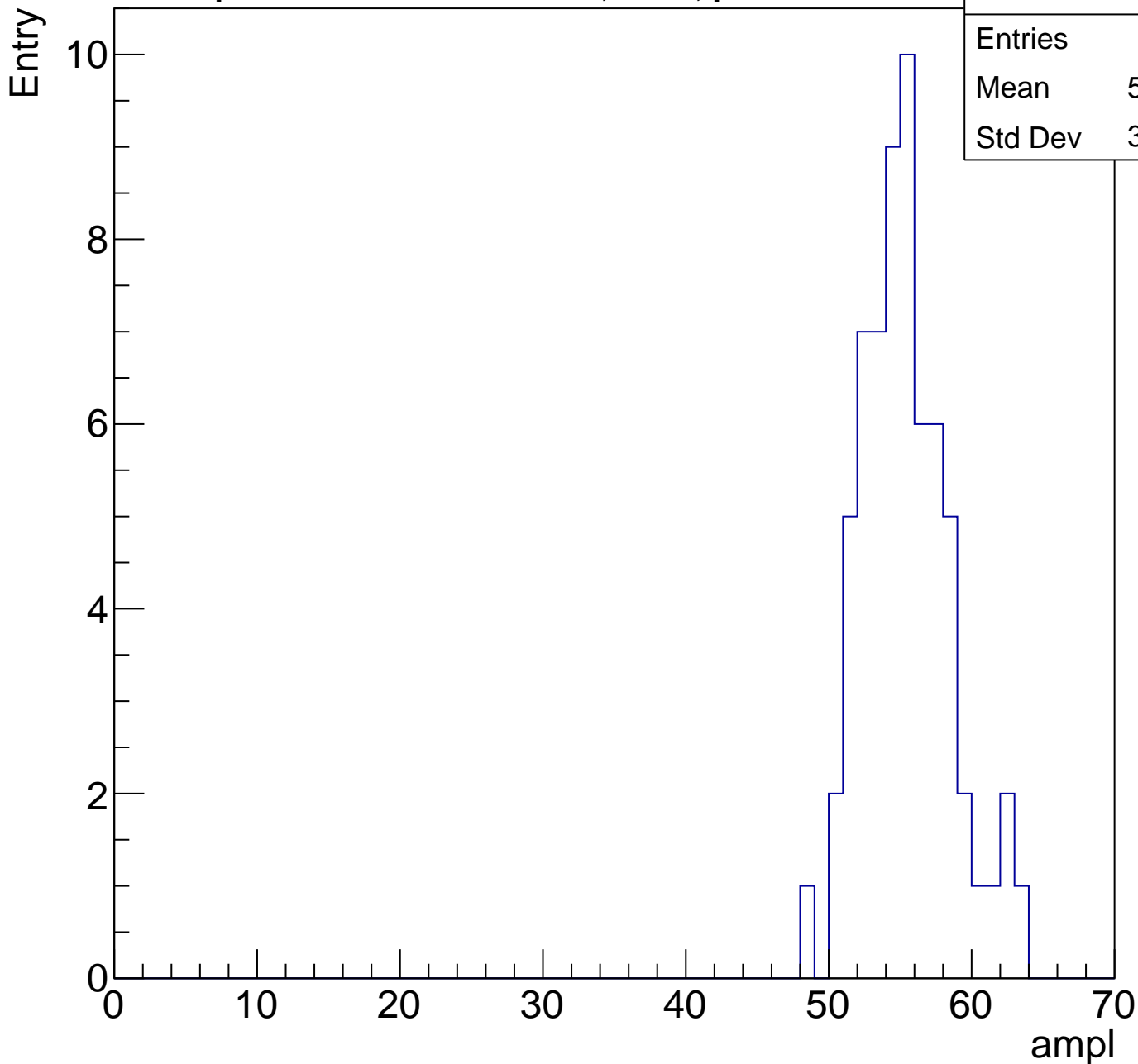
| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.05 |
| Std Dev | 2.919 |



B0L001S, U7-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.89 |
| Std Dev | 3.059 |

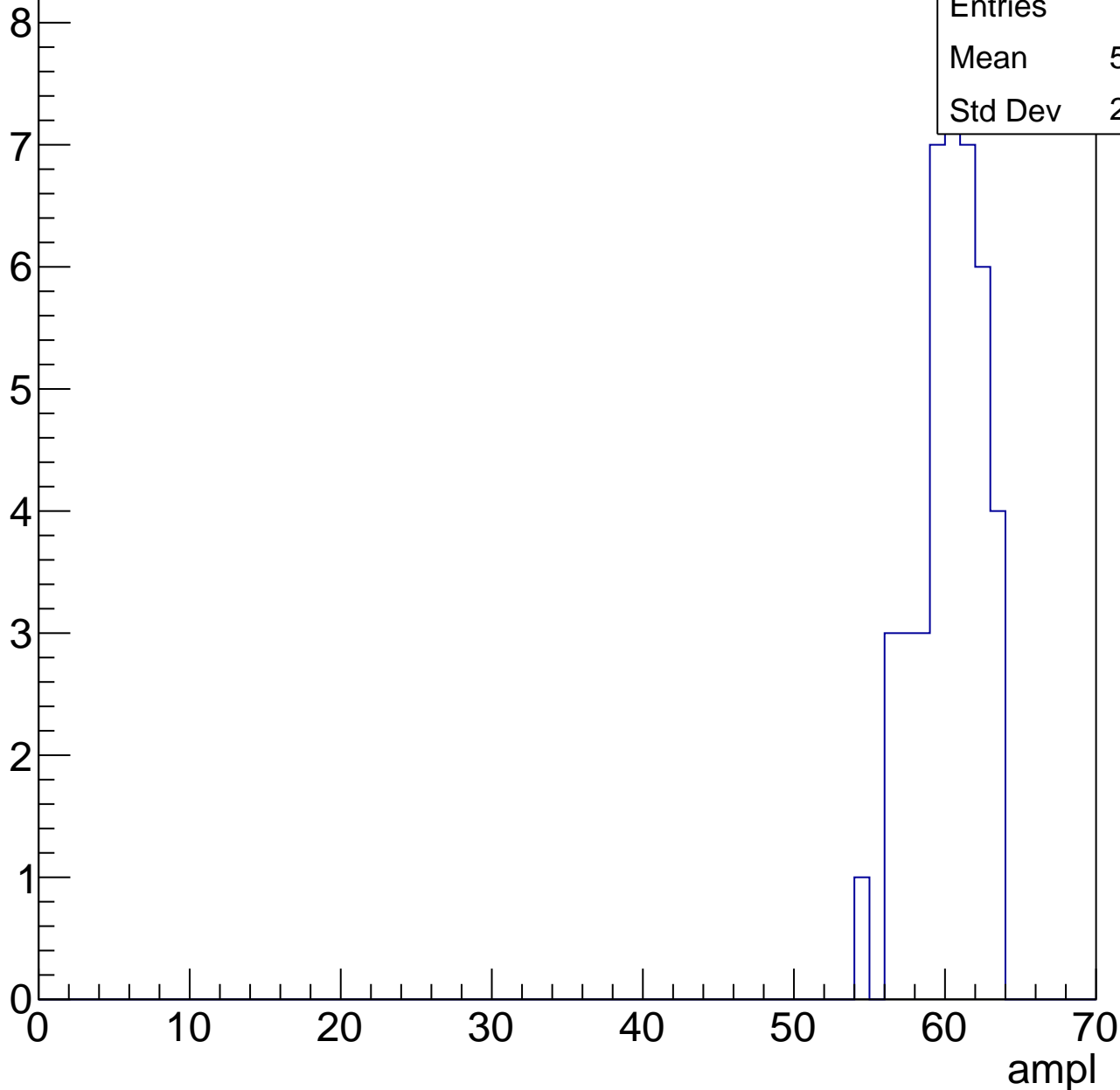


B0L001S, U7-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.79 |
| Std Dev | 2.155 |

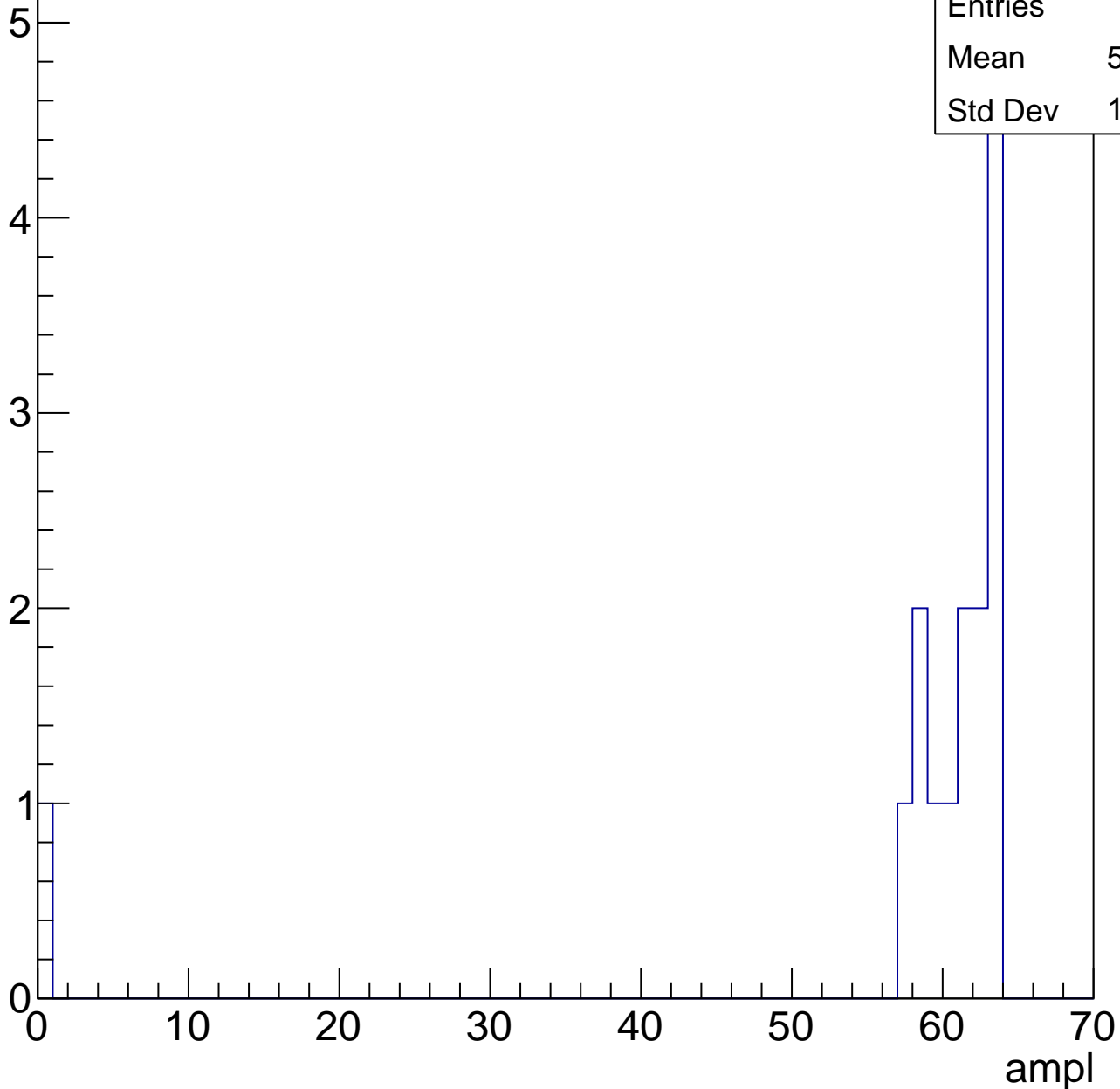


B0L001S, U7-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 56.87 |
| Std Dev | 15.33 |



B0L001S, U7-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch101, adc0

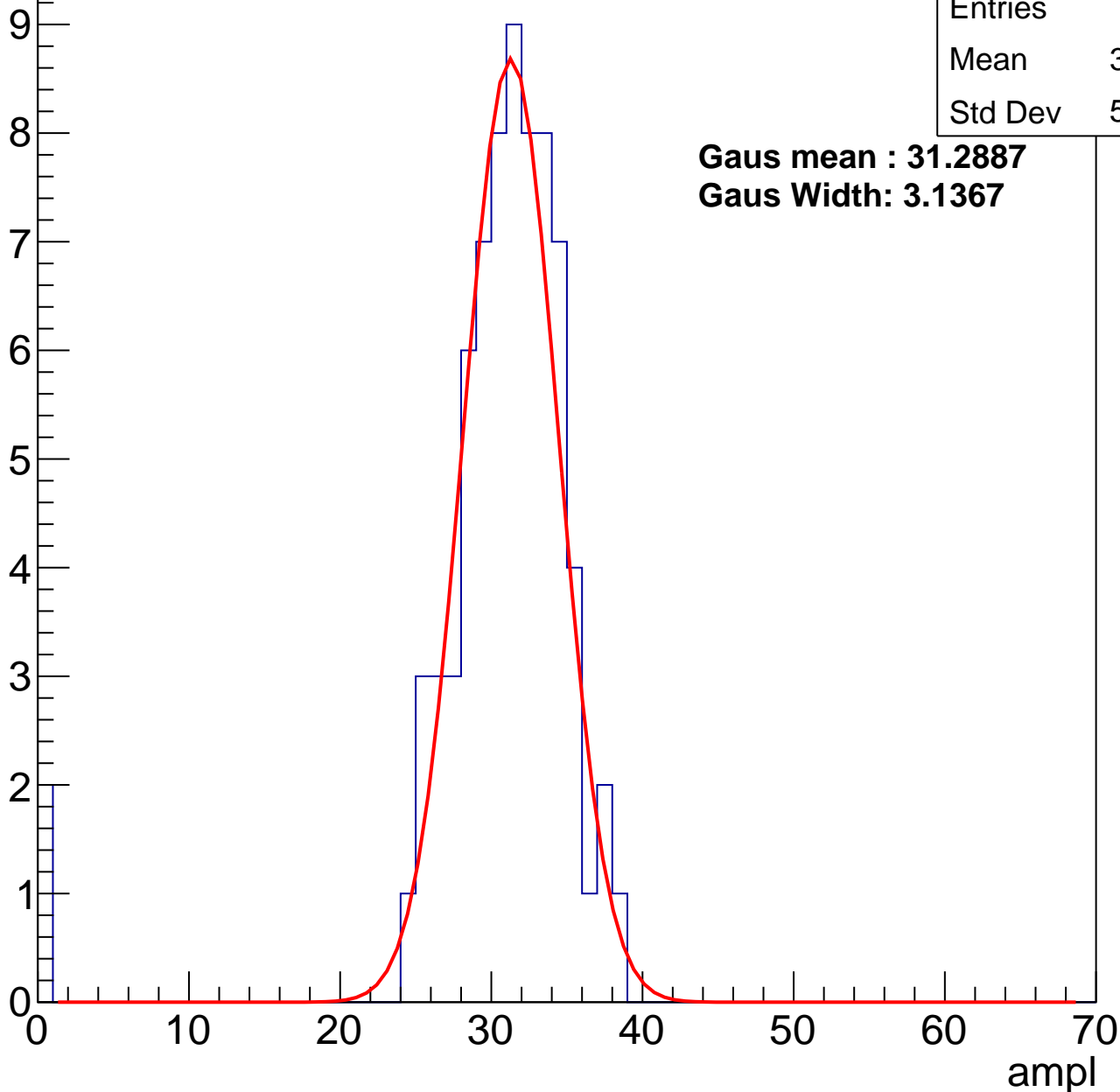
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 30.05 |
| Std Dev | 5.898 |

Gaus mean : 31.2887

Gaus Width: 3.1367



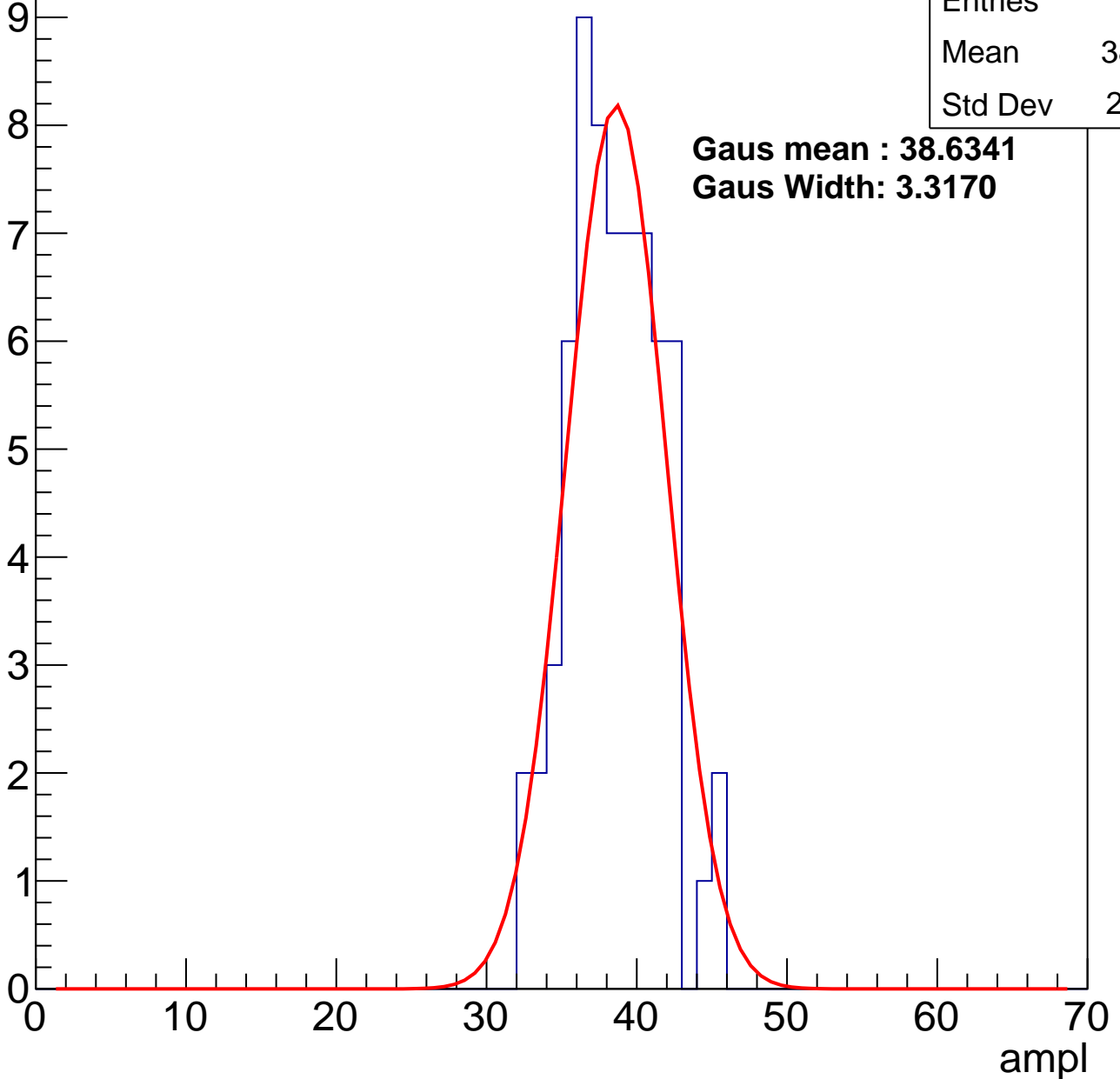
B0L001S, U7-ch101, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 38.08 |
| Std Dev | 2.981 |

Gaus mean : 38.6341
Gaus Width: 3.3170



B0L001S, U7-ch101, adc2

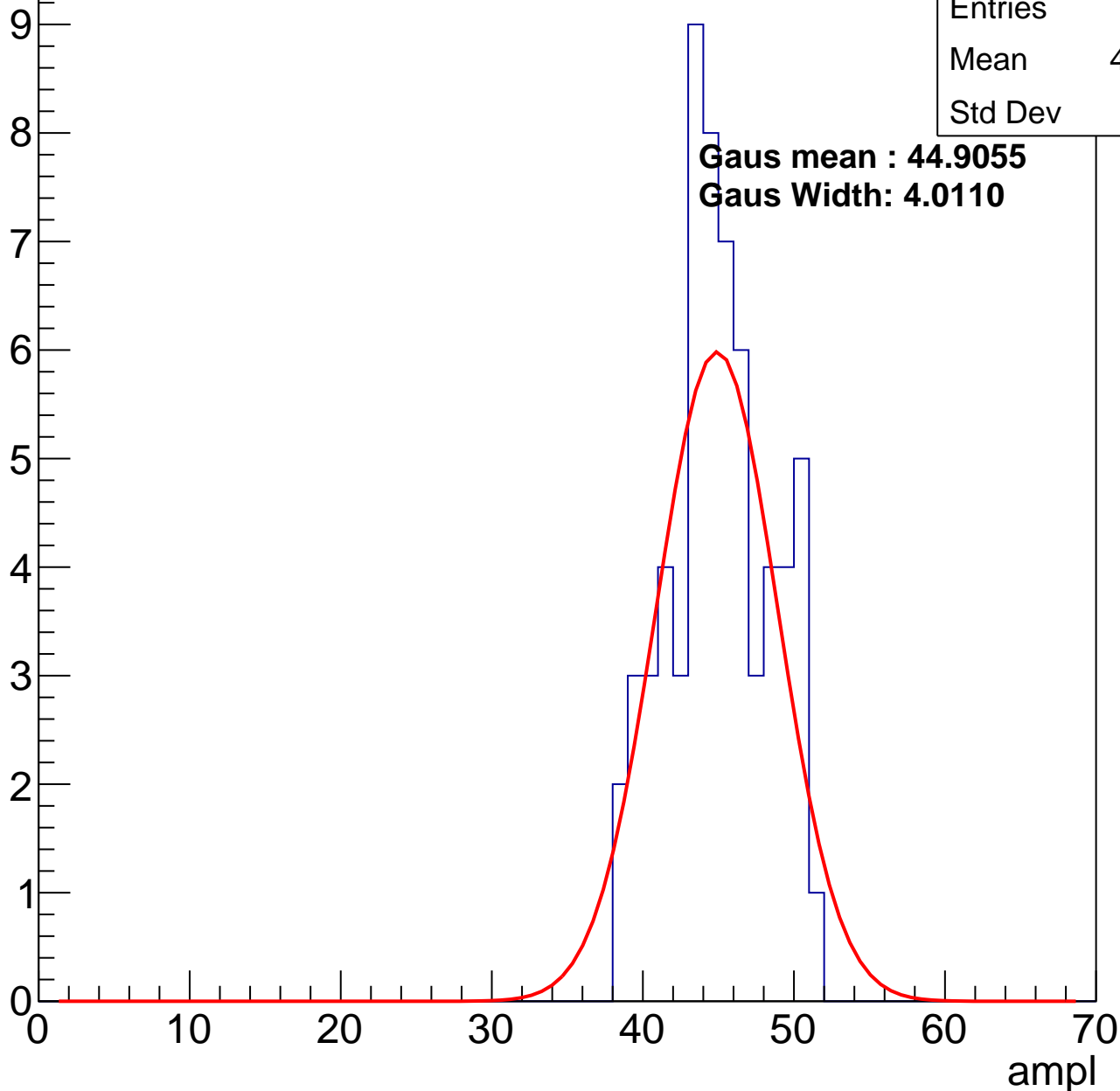
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 44.56 |
| Std Dev | 3.32 |

Gaus mean : 44.9055

Gaus Width: 4.0110

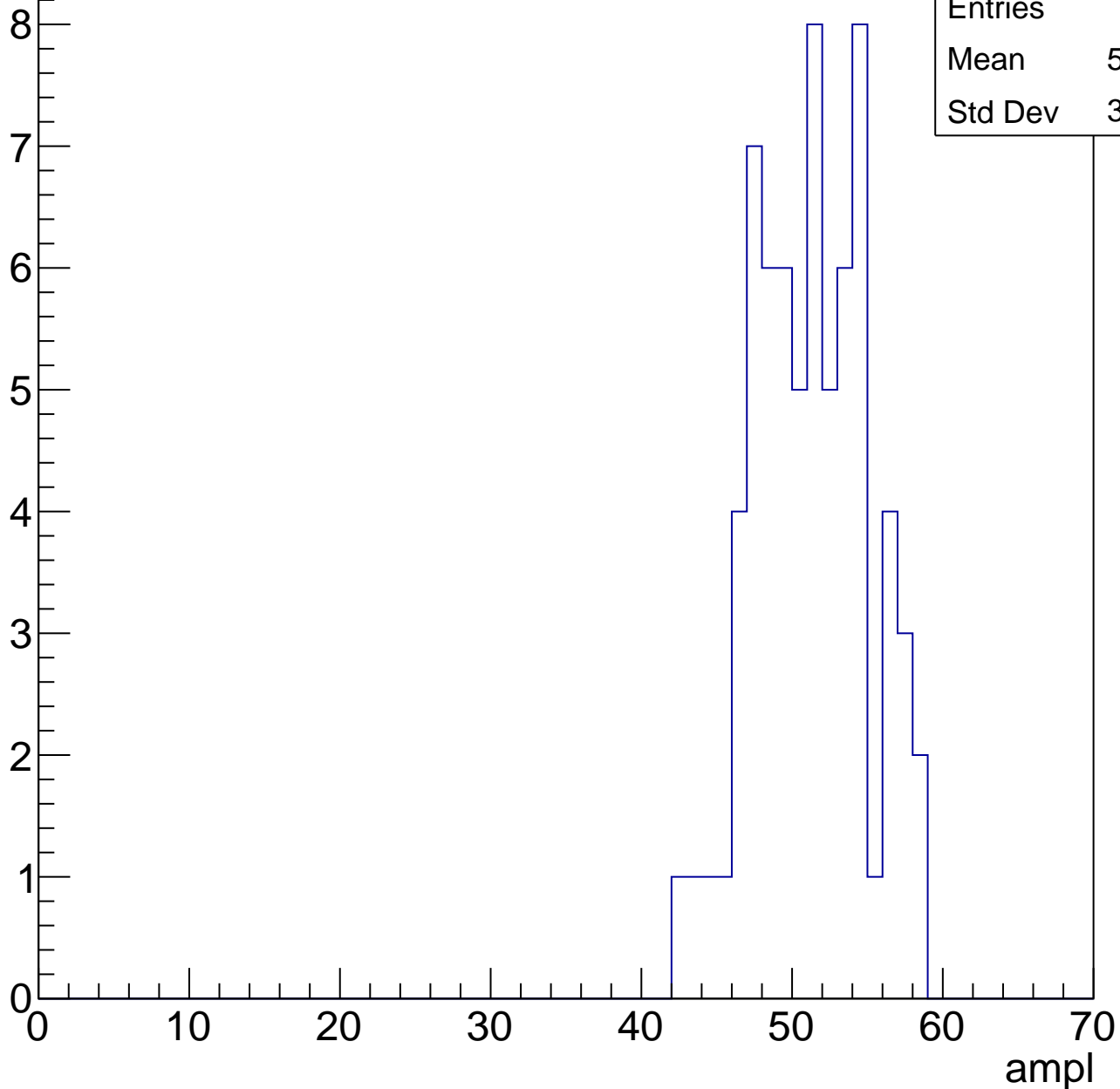


B0L001S, U7-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

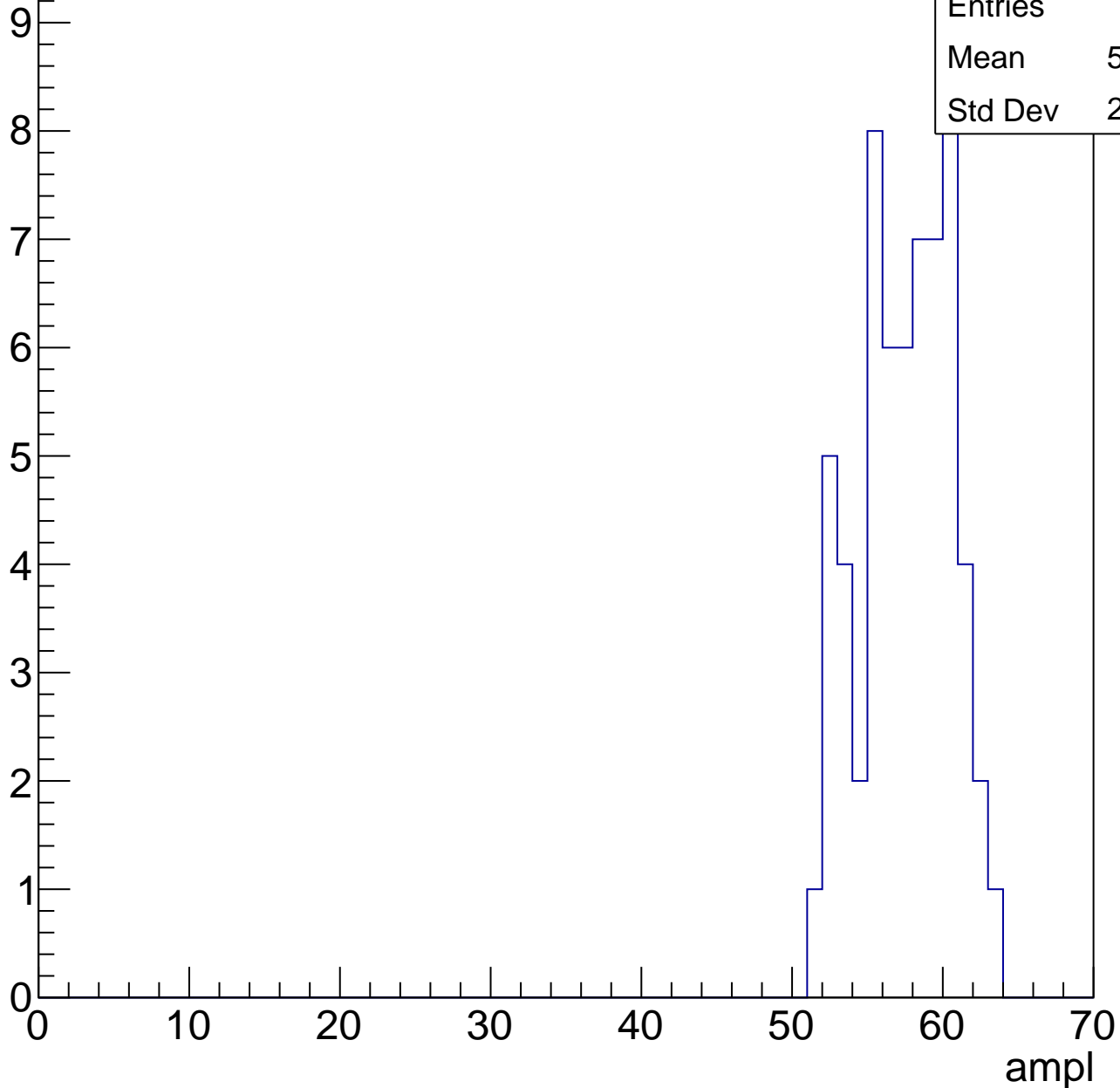
| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.77 |
| Std Dev | 3.699 |



B0L001S, U7-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



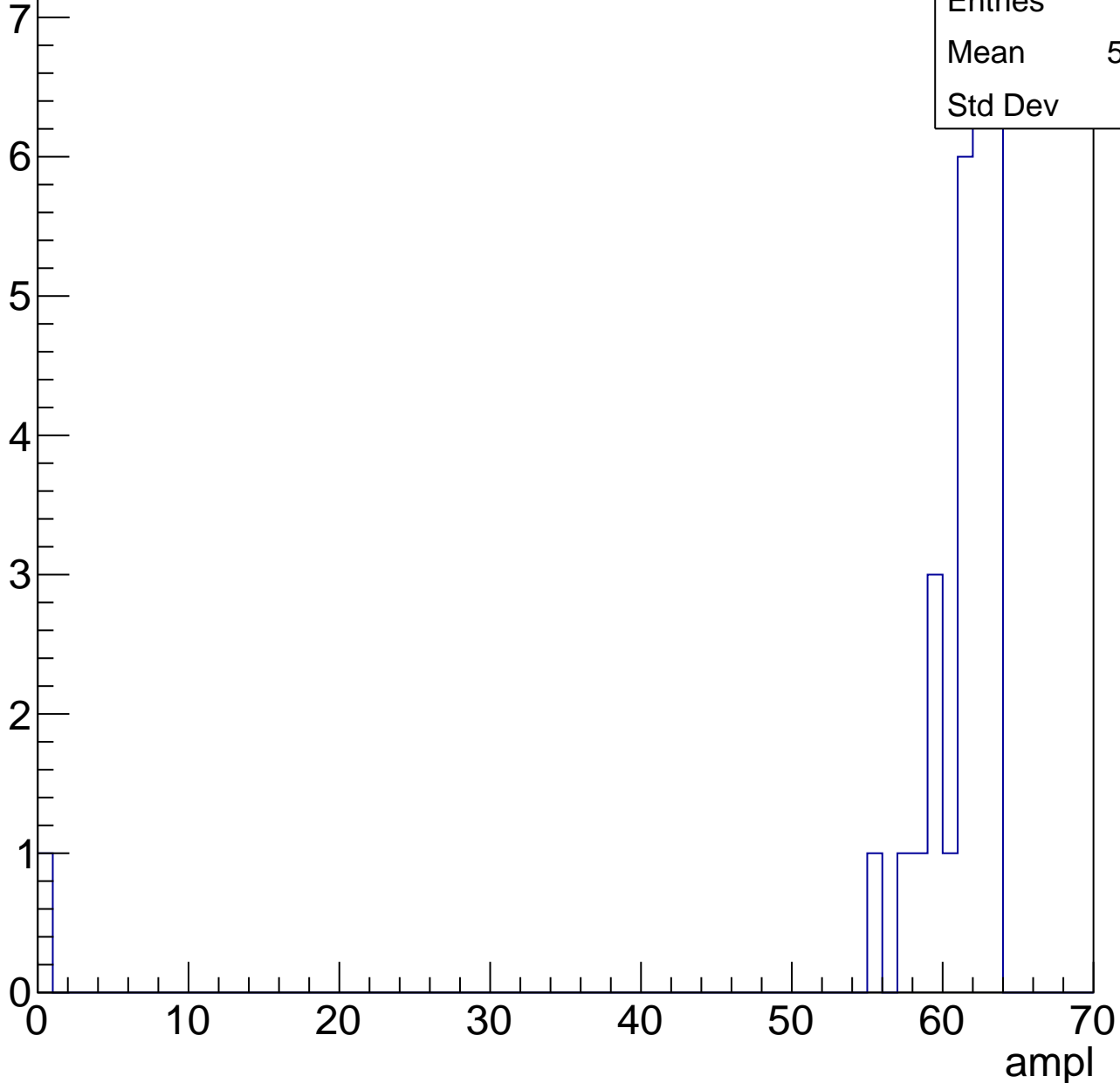
| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.08 |
| Std Dev | 2.969 |

B0L001S, U7-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.86 |
| Std Dev | 11.5 |



B0L001S, U7-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B0L001S, U7-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch102, adc0

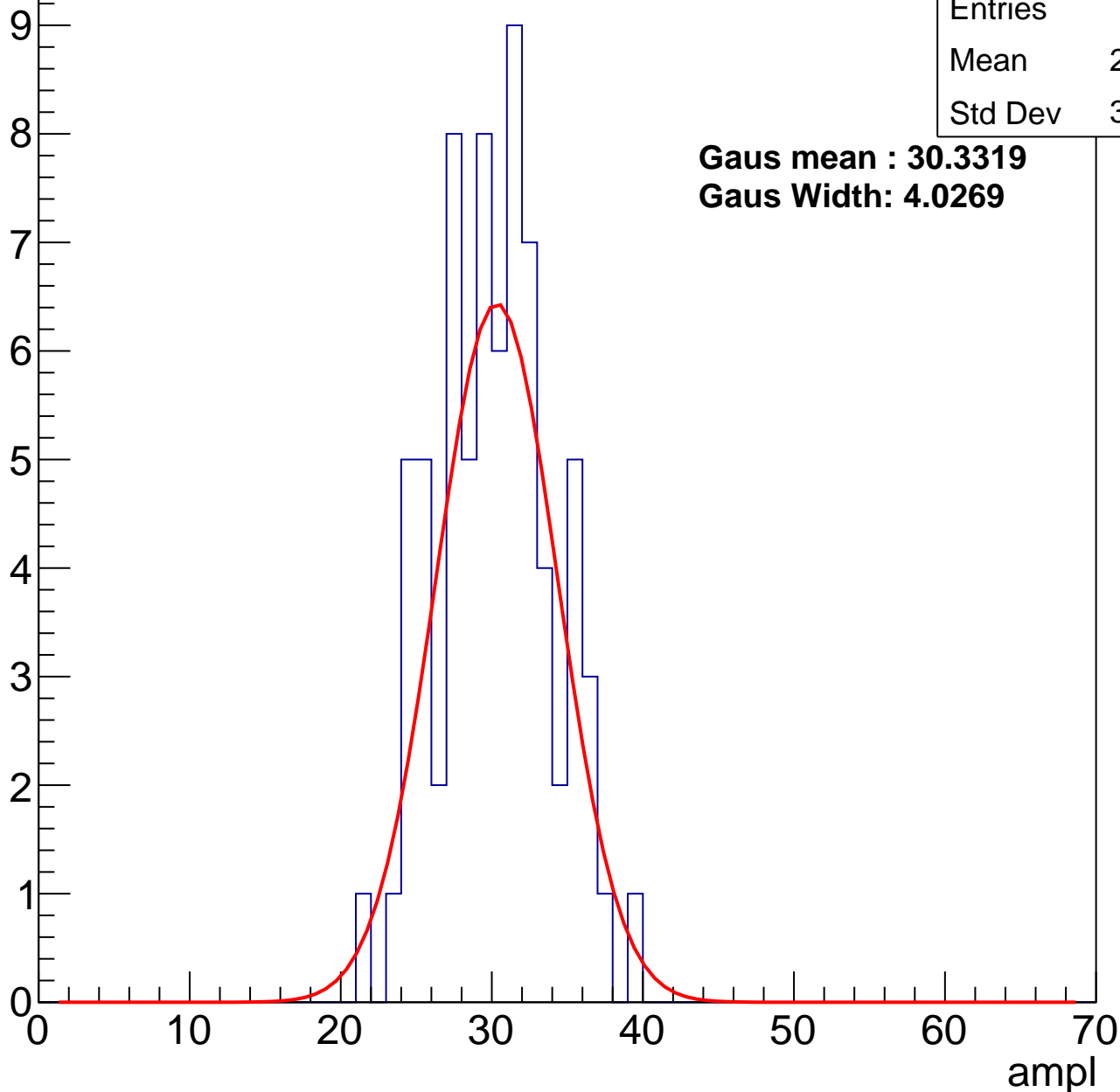
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.74 |
| Std Dev | 3.756 |

Gaus mean : 30.3319

Gaus Width: 4.0269



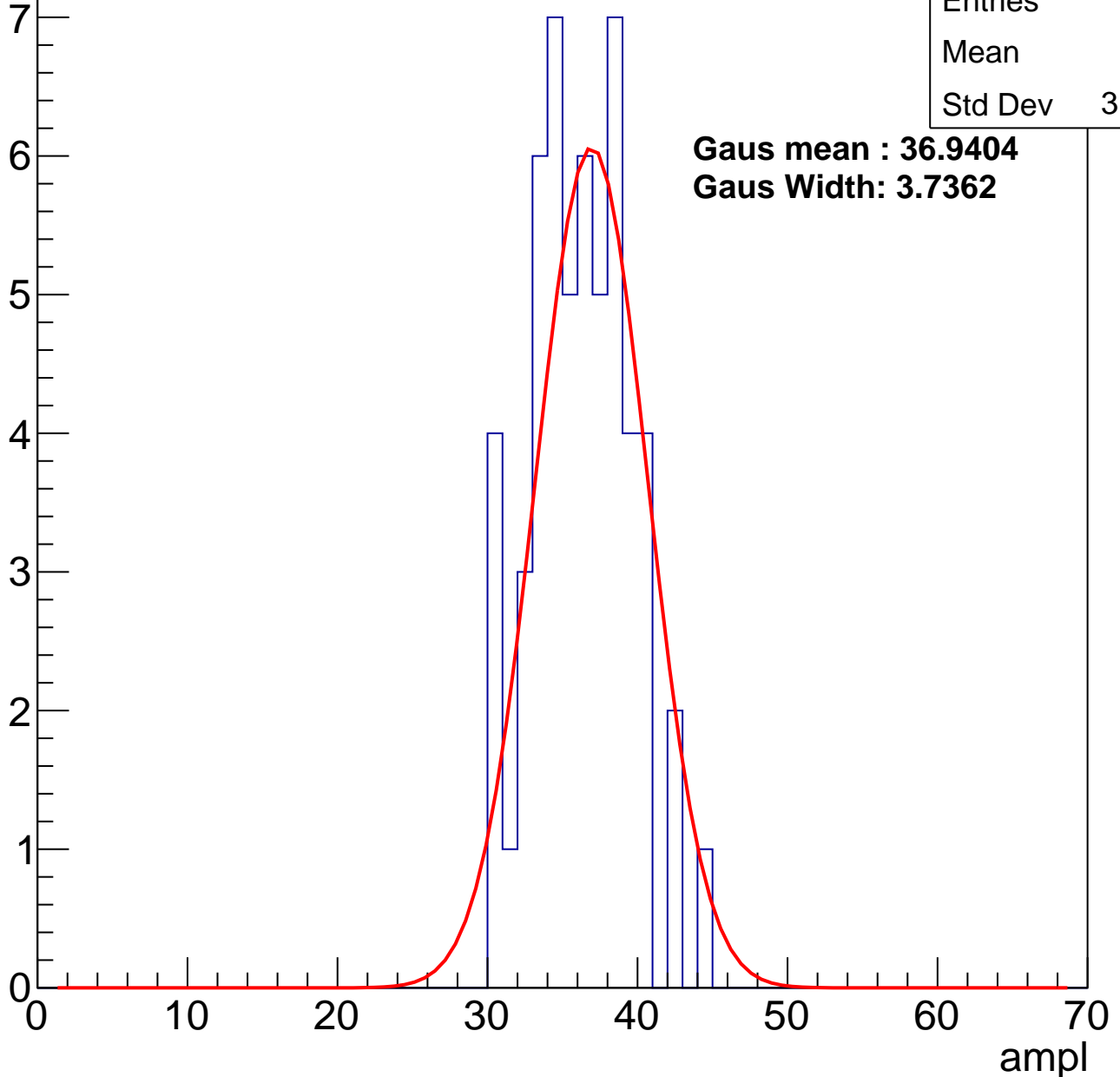
B0L001S, U7-ch102, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 35.8 |
| Std Dev | 3.222 |

Gaus mean : 36.9404
Gaus Width: 3.7362



B0L001S, U7-ch102, adc2

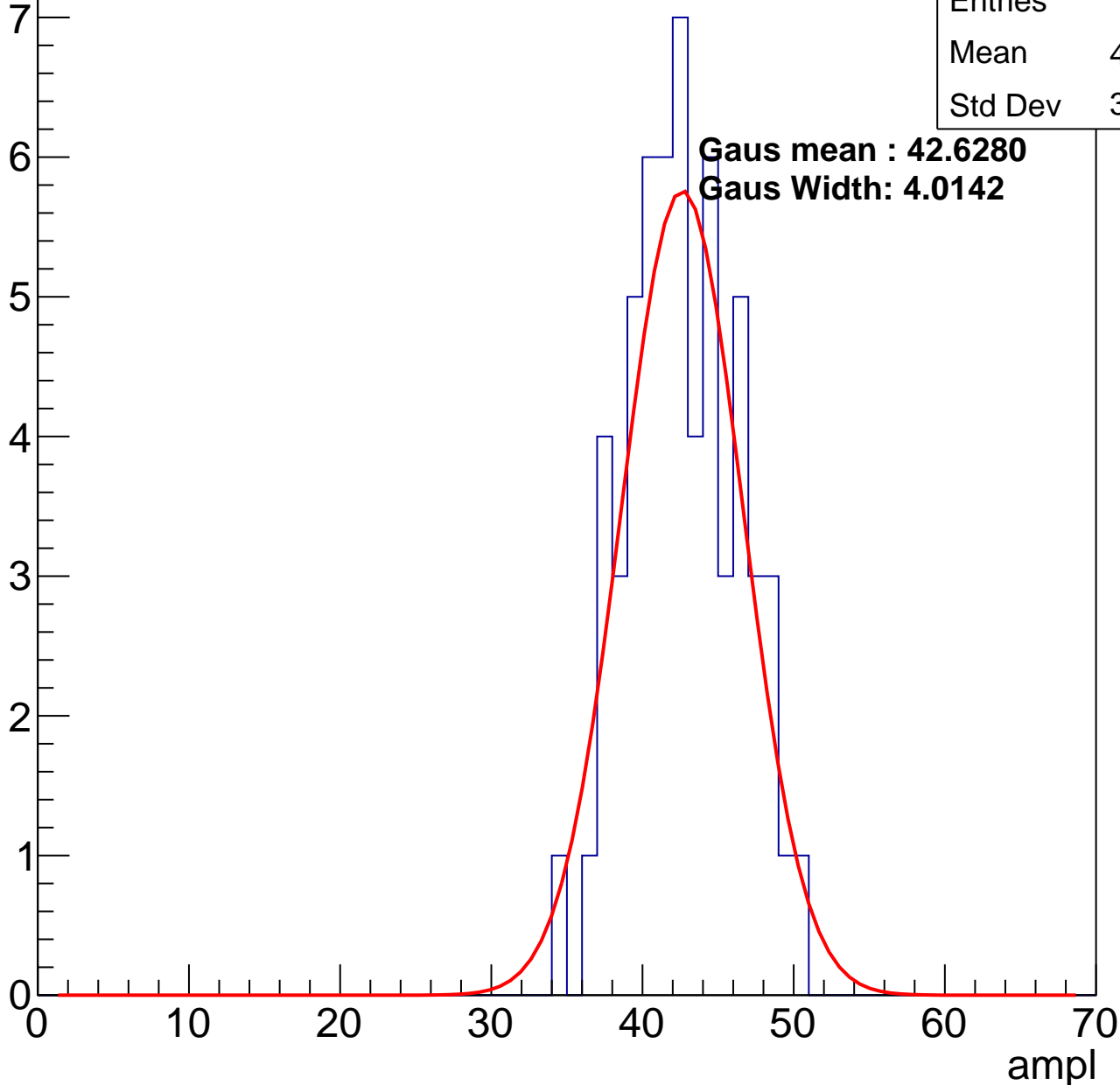
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.24 |
| Std Dev | 3.567 |

Gaus mean : 42.6280

Gaus Width: 4.0142

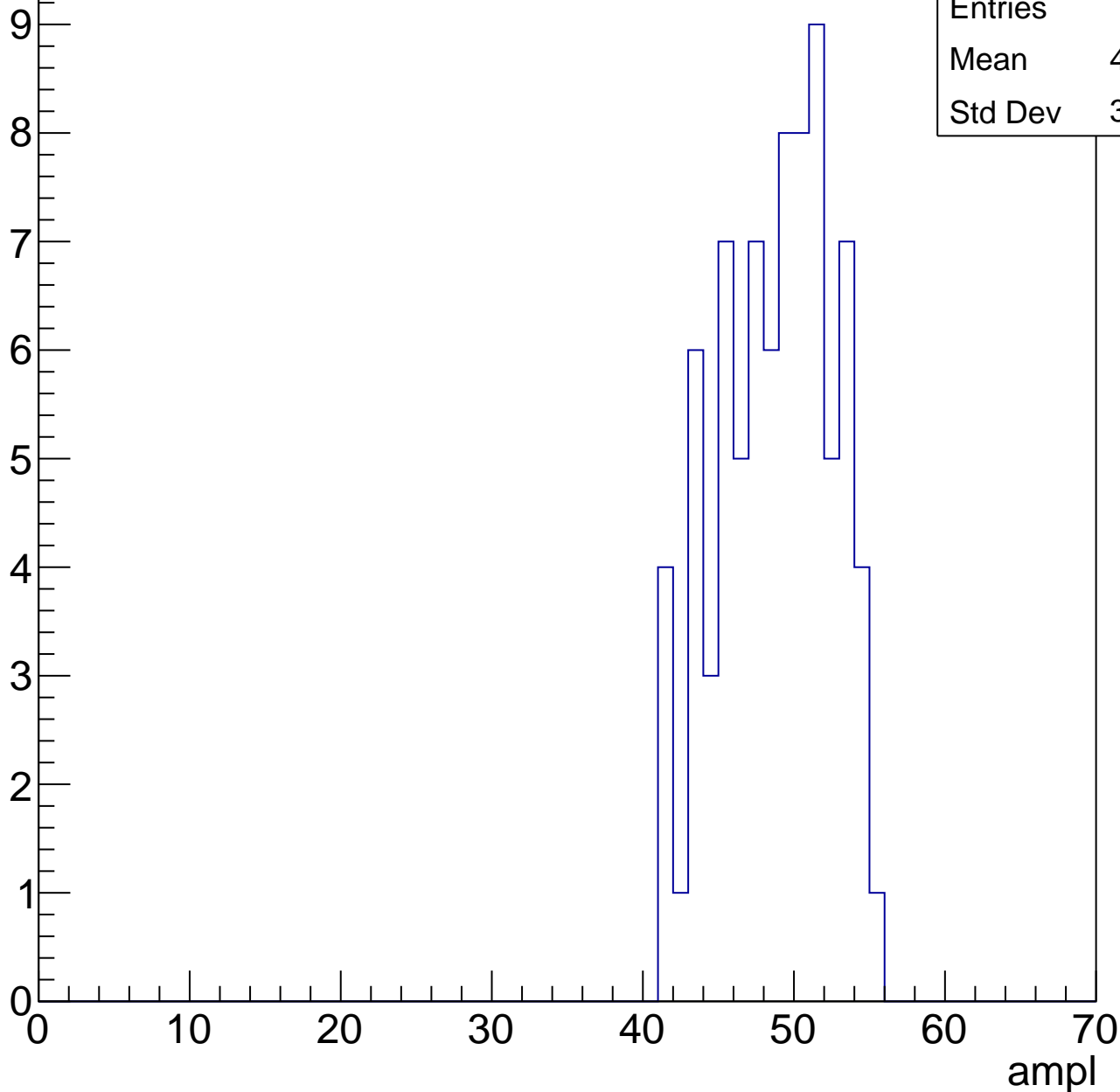


B0L001S, U7-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 48.28 |
| Std Dev | 3.656 |



B0L001S, U7-ch102, adc4

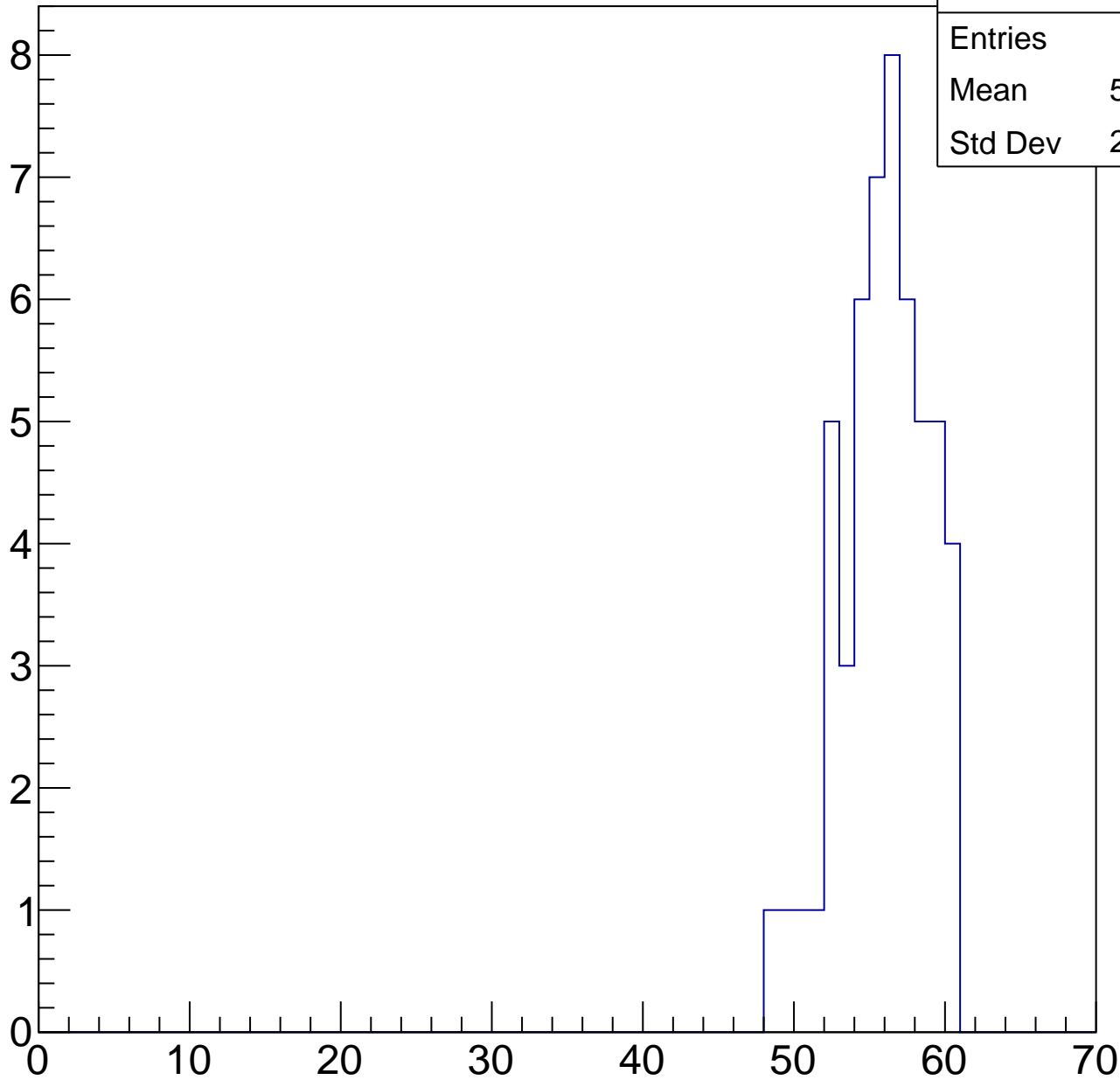
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.49 |
| Std Dev | 2.859 |

ampl



B0L001S, U7-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.71 |
| Std Dev | 2.239 |

0

10

20

30

40

50

60

70

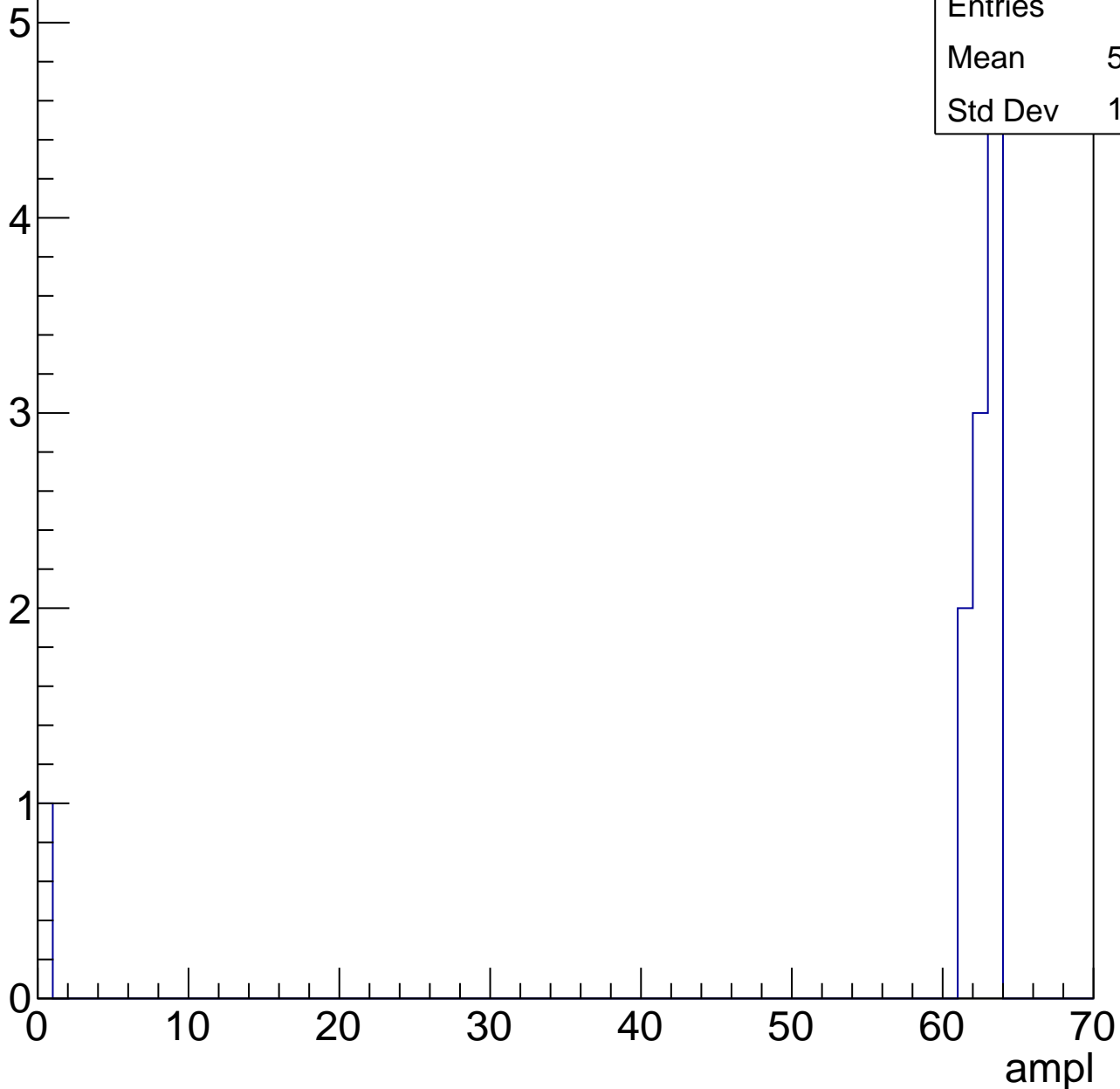
ampl

B0L001S, U7-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.64 |
| Std Dev | 17.93 |



B0L001S, U7-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch103, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 87 |
| Mean | 29.91 |
| Std Dev | 4.898 |

Gaus mean : 30.9482

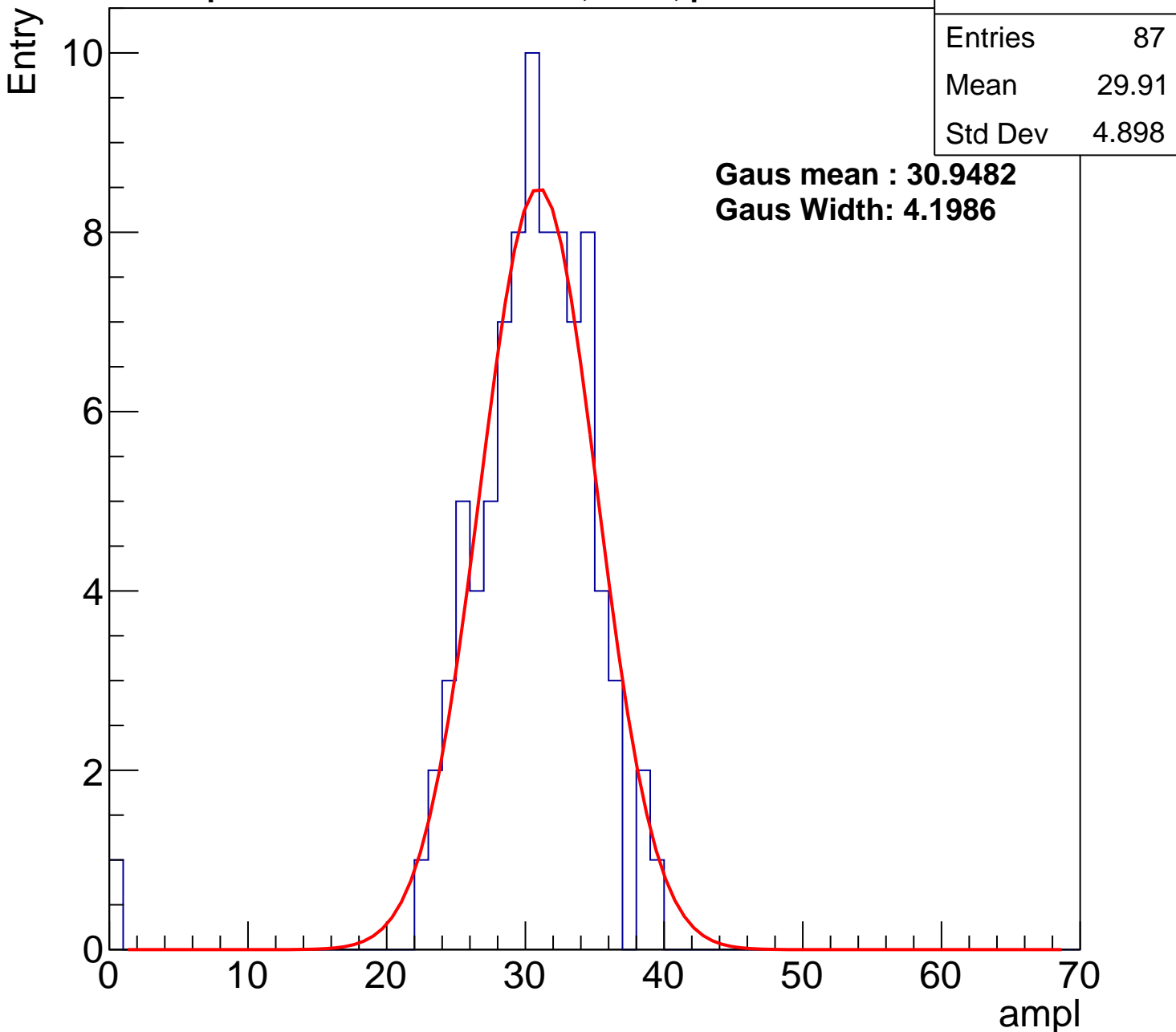
Gaus Width: 4.1986

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



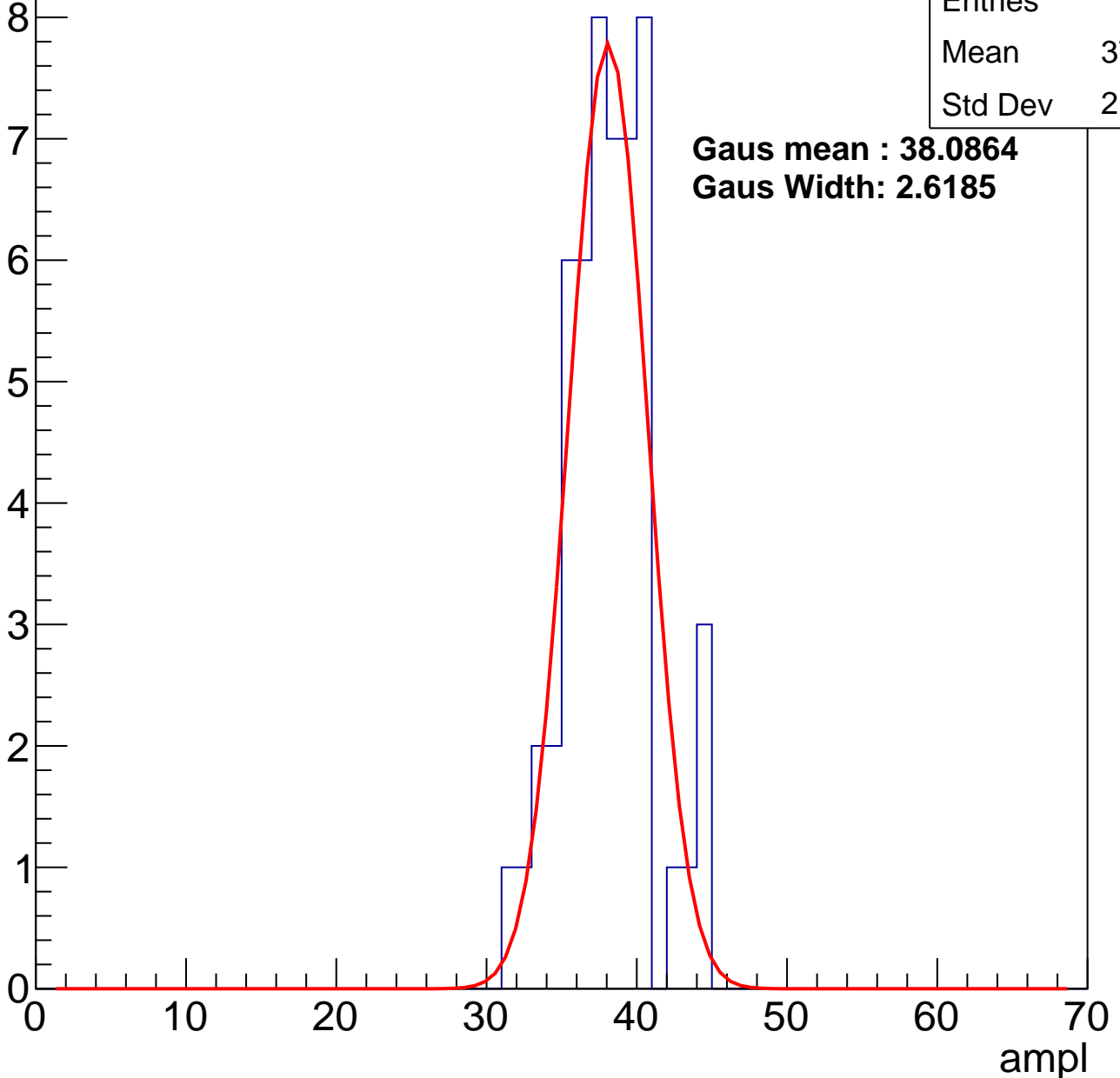
B0L001S, U7-ch103, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 37.64 |
| Std Dev | 2.862 |

Gaus mean : 38.0864
Gaus Width: 2.6185



B0L001S, U7-ch103, adc2

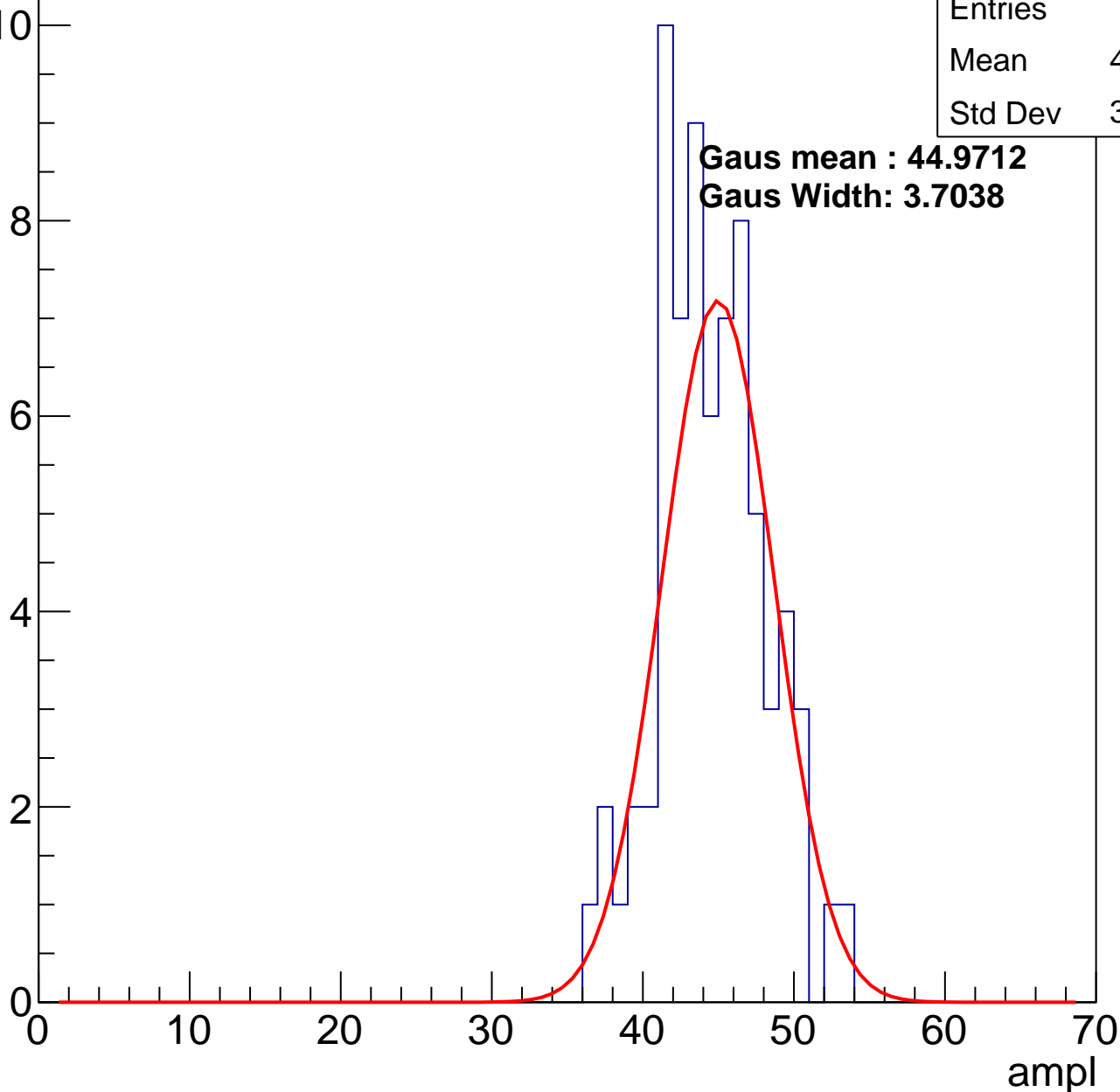
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 44.08 |
| Std Dev | 3.519 |

Gaus mean : 44.9712

Gaus Width: 3.7038

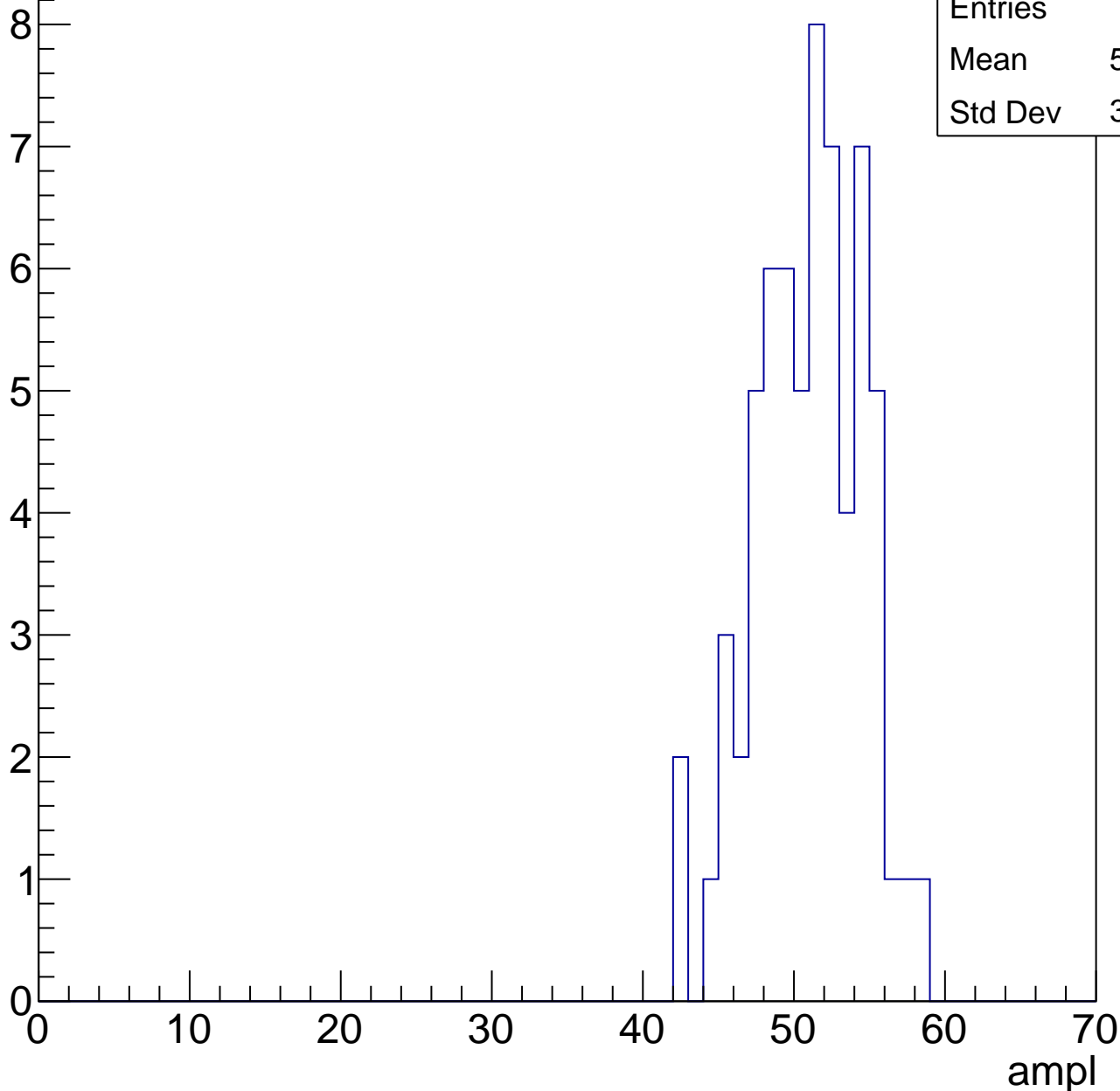


B0L001S, U7-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.47 |
| Std Dev | 3.513 |

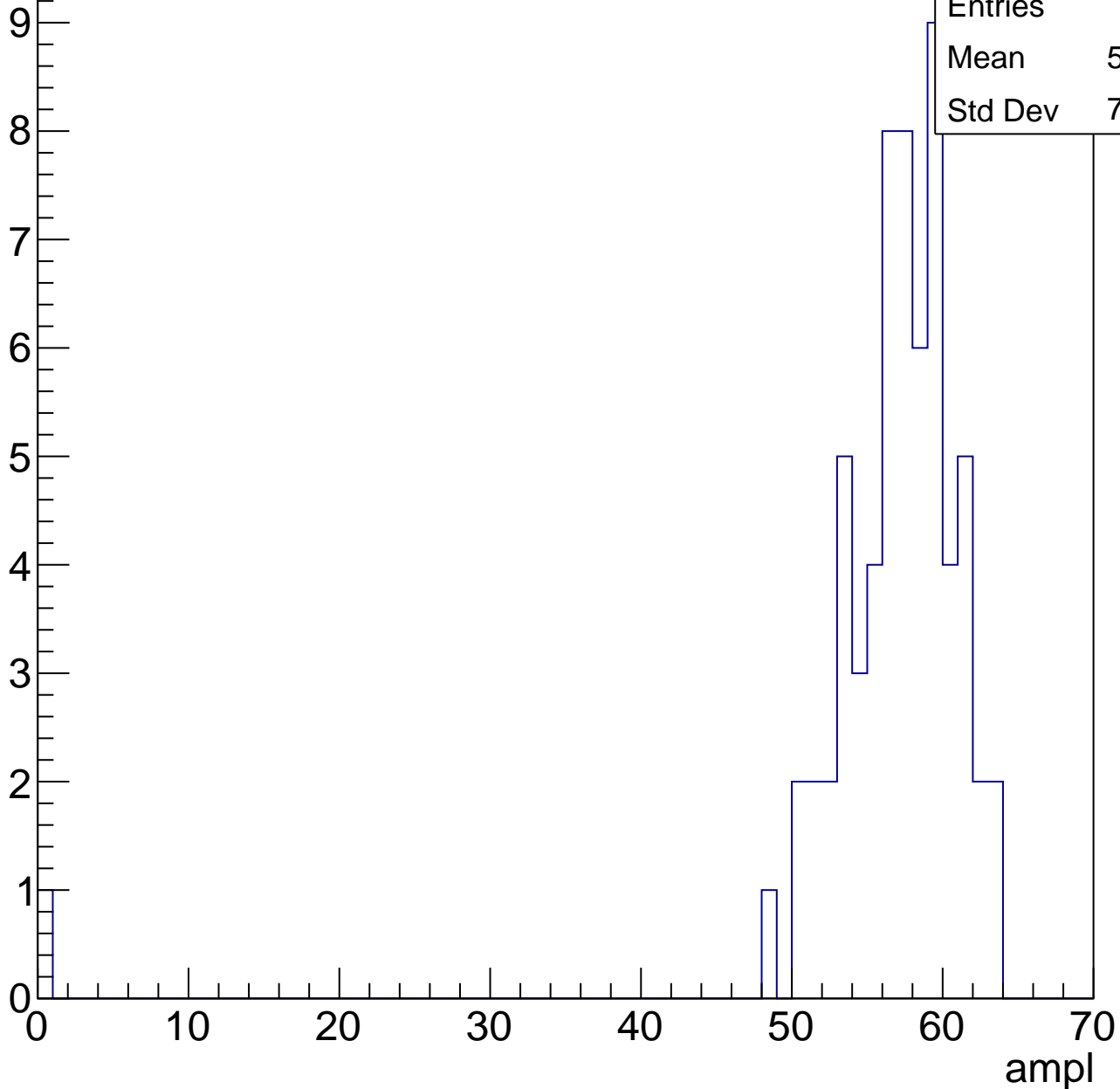


B0L001S, U7-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 55.92 |
| Std Dev | 7.793 |

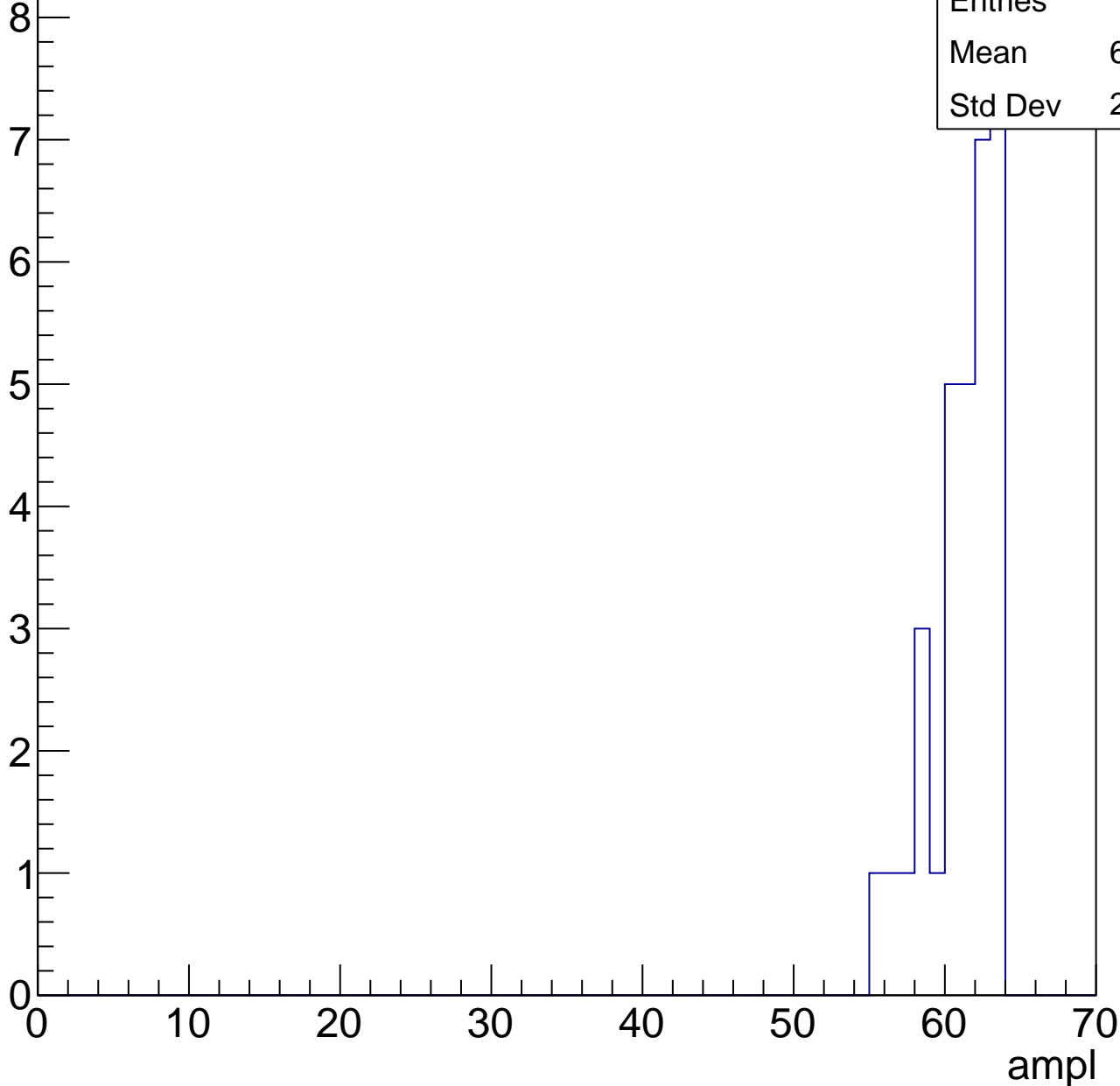


B0L001S, U7-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 60.75 |
| Std Dev | 2.165 |



B0L001S, U7-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch104, adc0

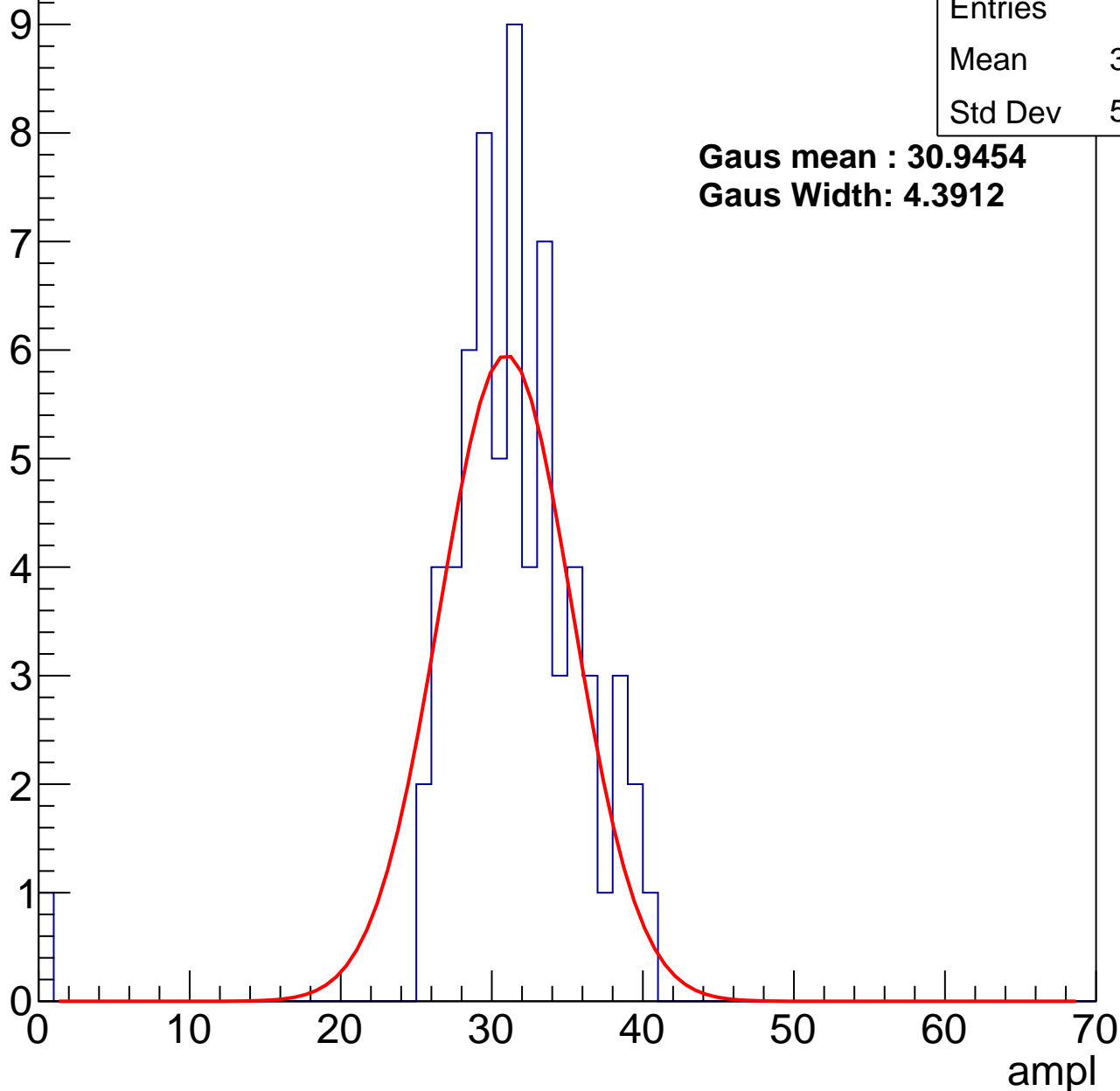
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 30.88 |
| Std Dev | 5.293 |

Gaus mean : 30.9454

Gaus Width: 4.3912



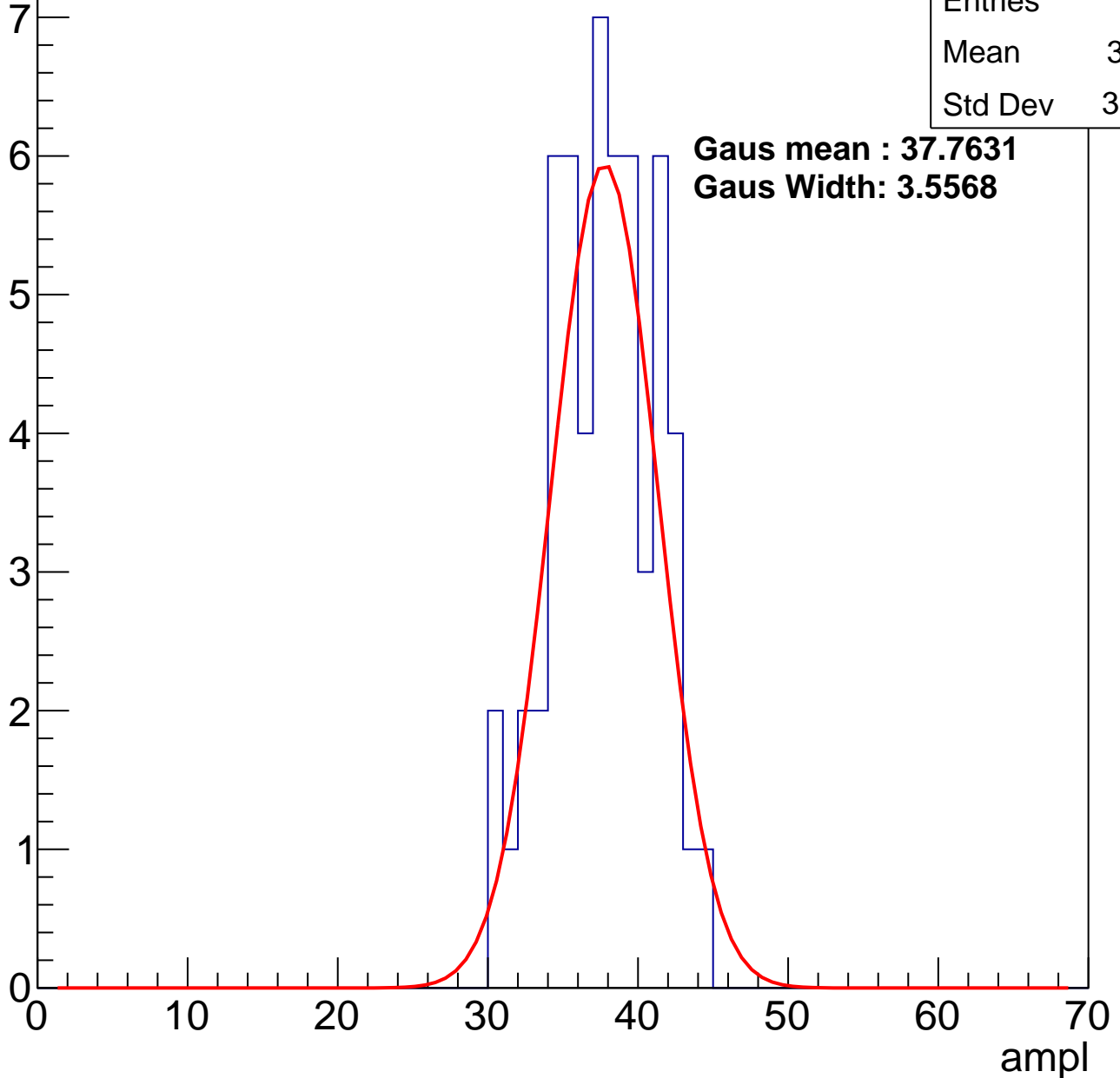
B0L001S, U7-ch104, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 37.21 |
| Std Dev | 3.334 |

Gaus mean : 37.7631
Gaus Width: 3.5568



B0L001S, U7-ch104, adc2

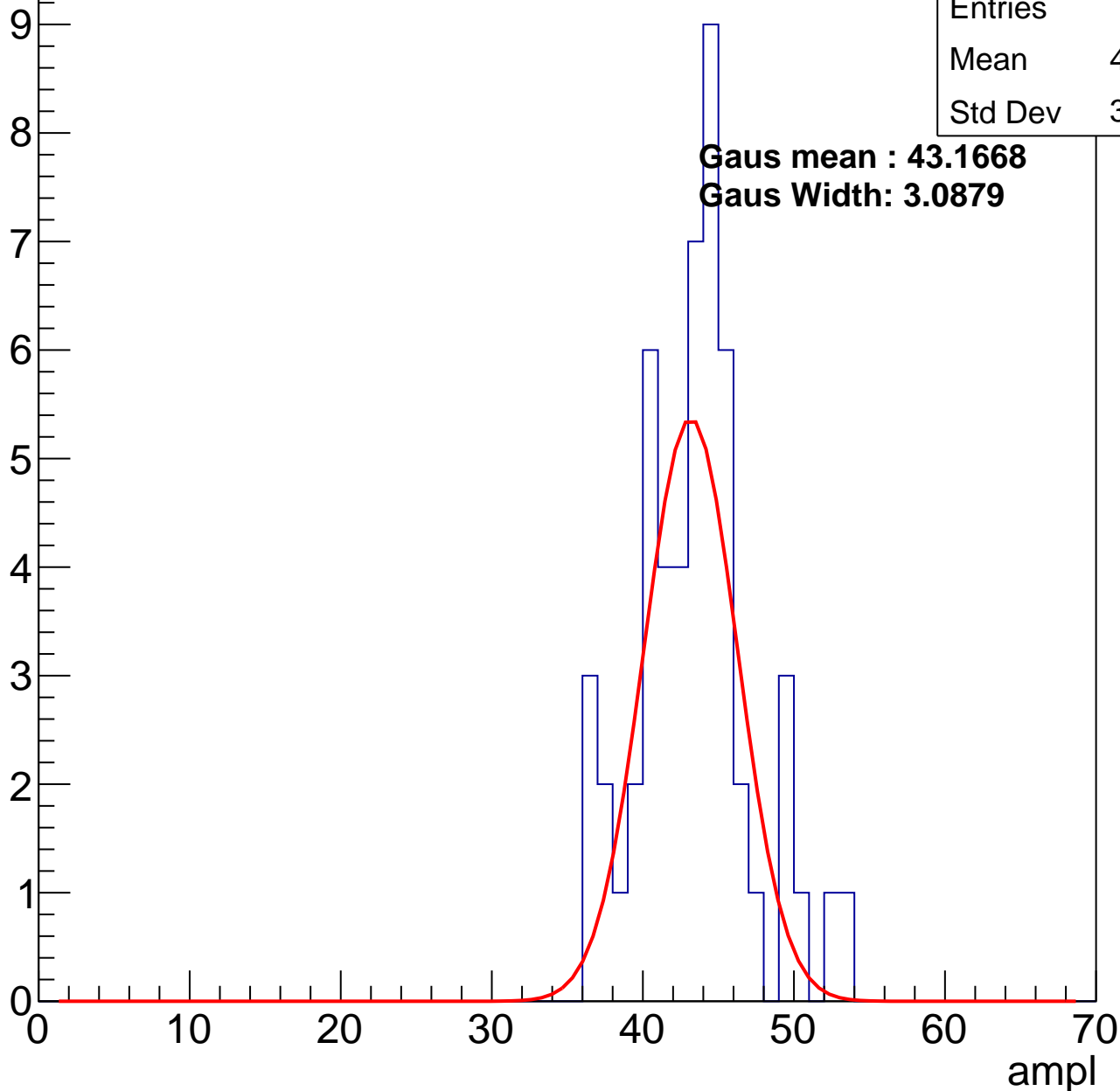
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 42.98 |
| Std Dev | 3.774 |

Gaus mean : 43.1668

Gaus Width: 3.0879

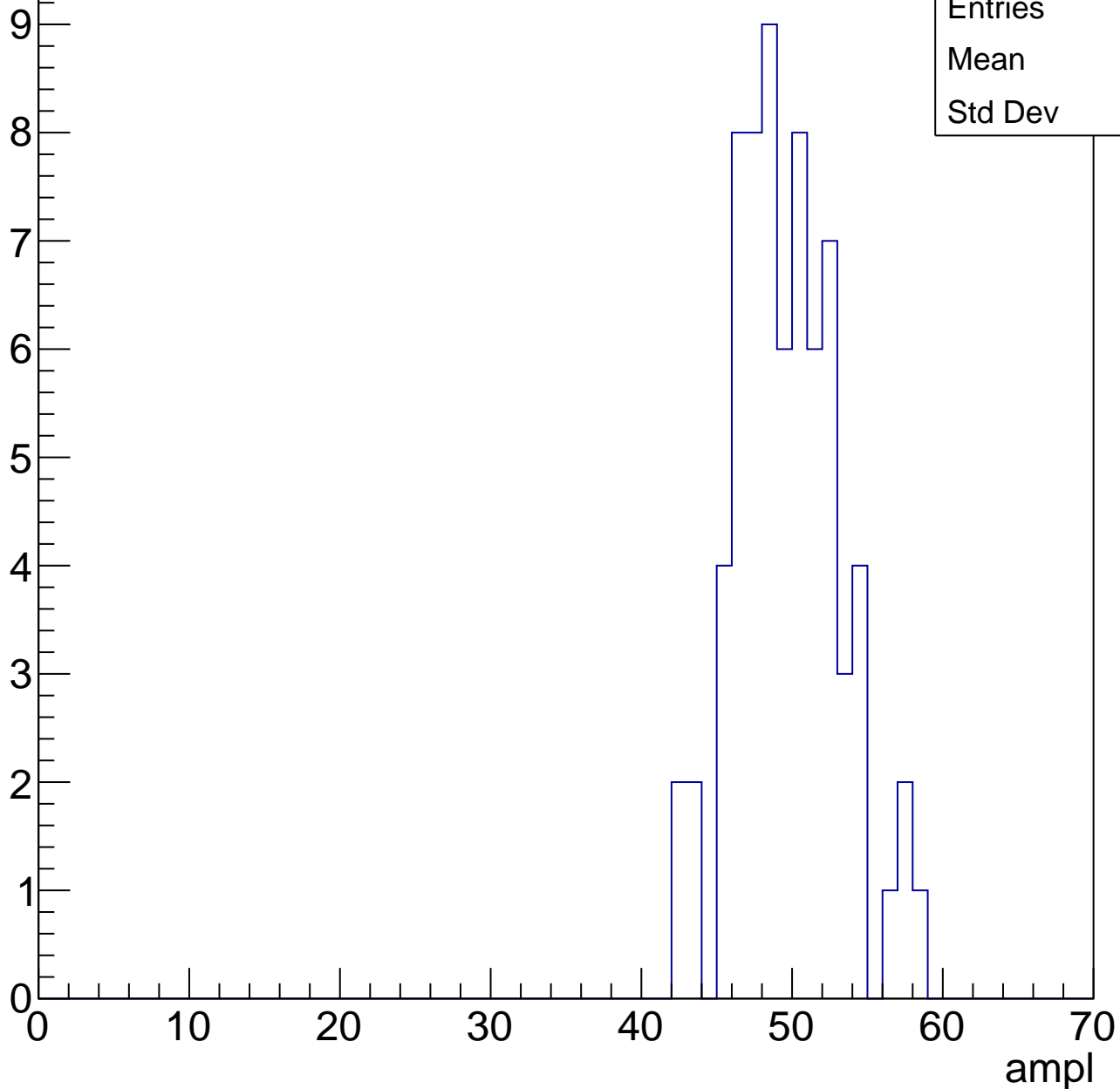


B0L001S, U7-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 71 |
| Mean | 49.2 |
| Std Dev | 3.45 |

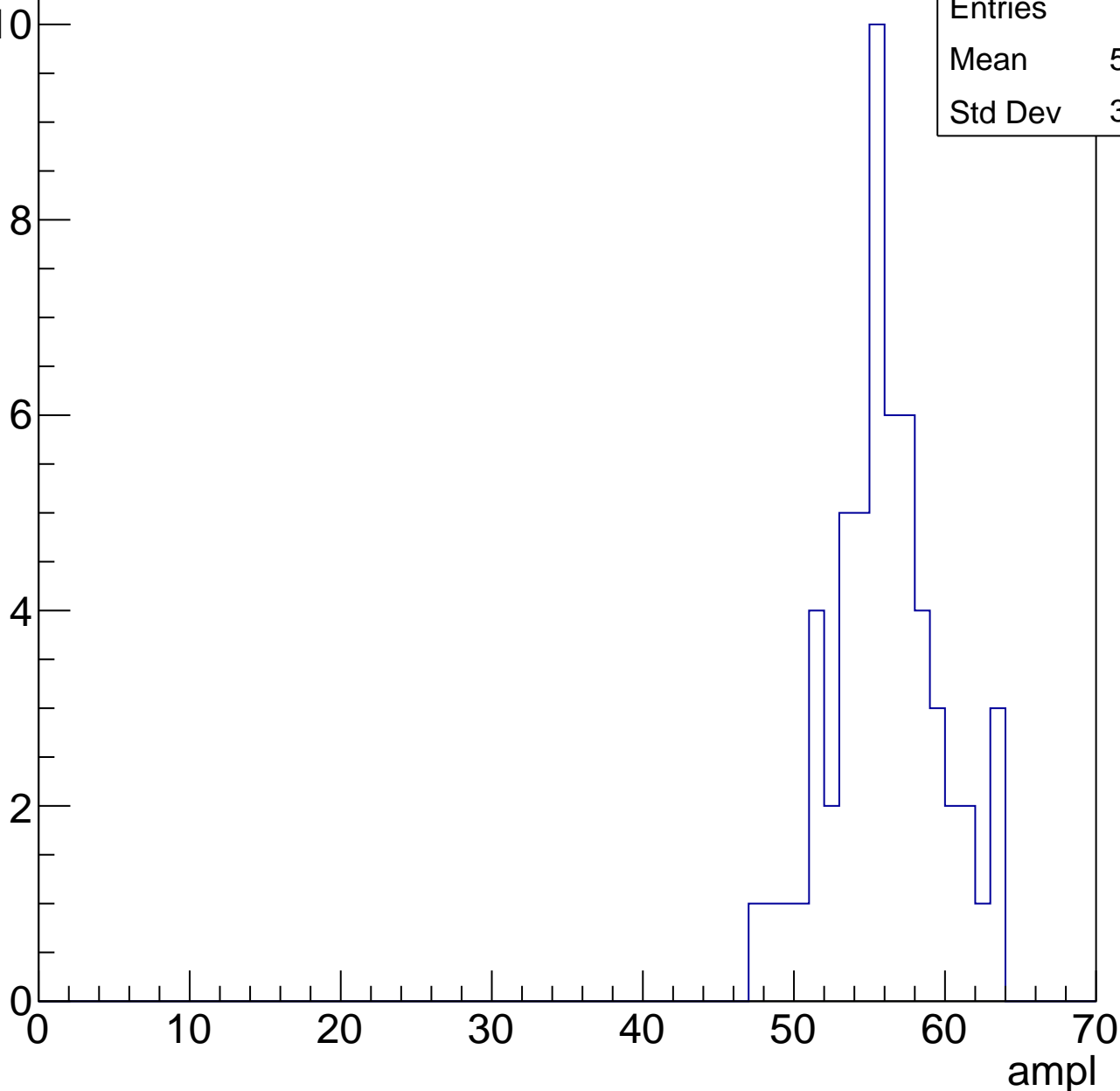


B0L001S, U7-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.56 |
| Std Dev | 3.603 |



B0L001S, U7-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 59.8

Std Dev 2.401

ampl

0

10

20

30

40

50

60

70

1

2

3

4

5

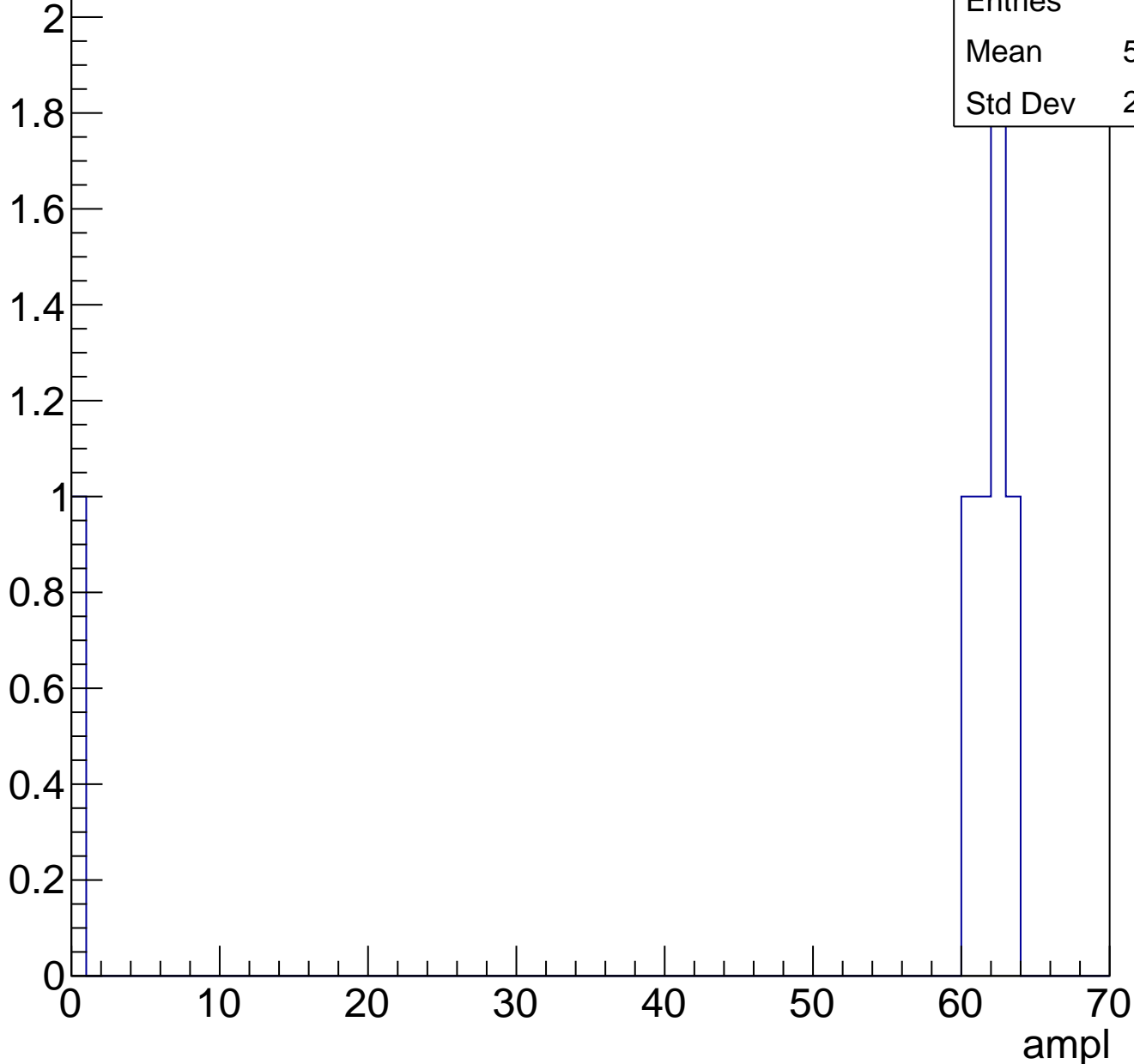
6

7

B0L001S, U7-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch105, adc0

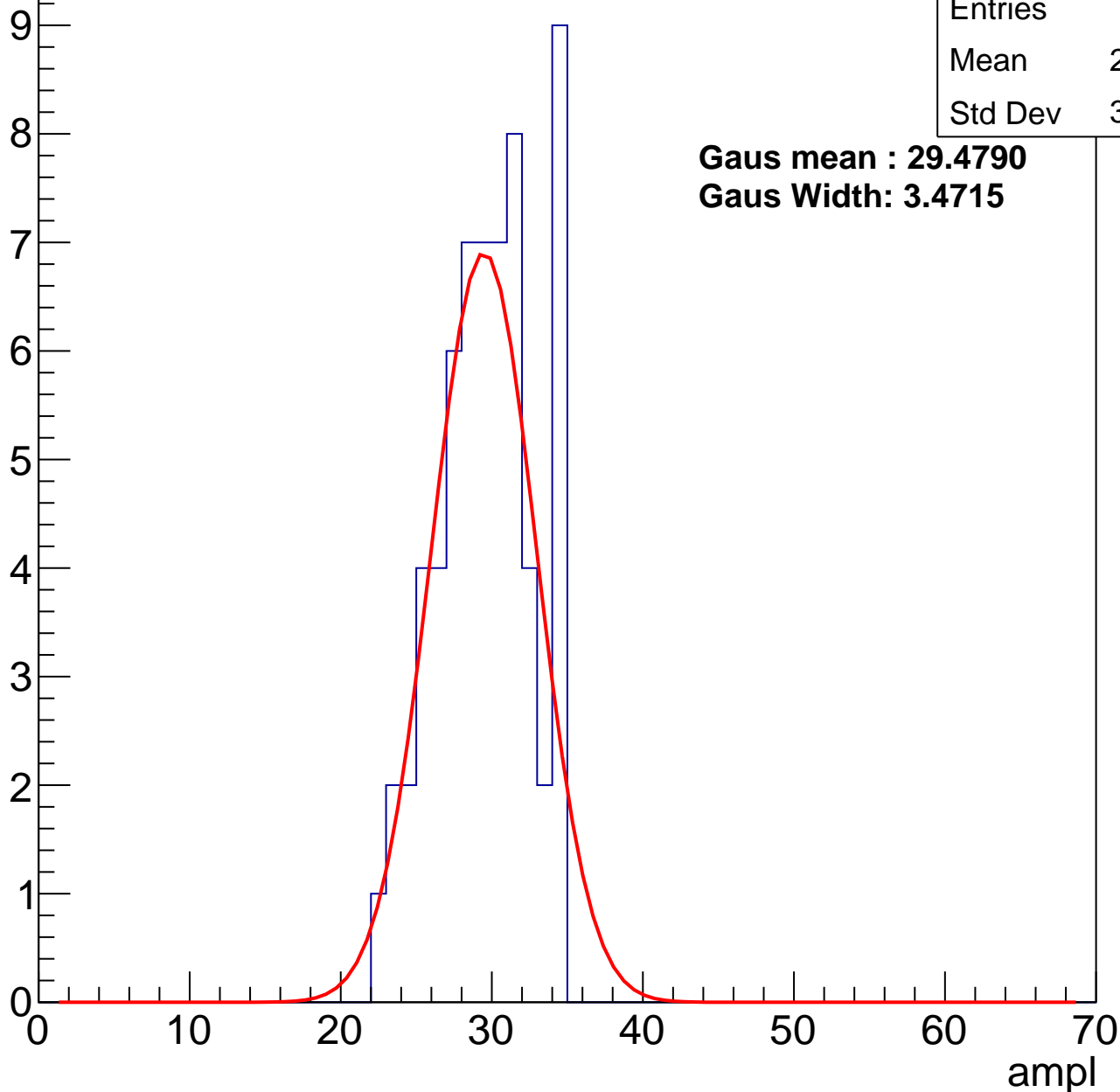
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 29.19 |
| Std Dev | 3.167 |

Gaus mean : 29.4790

Gaus Width: 3.4715



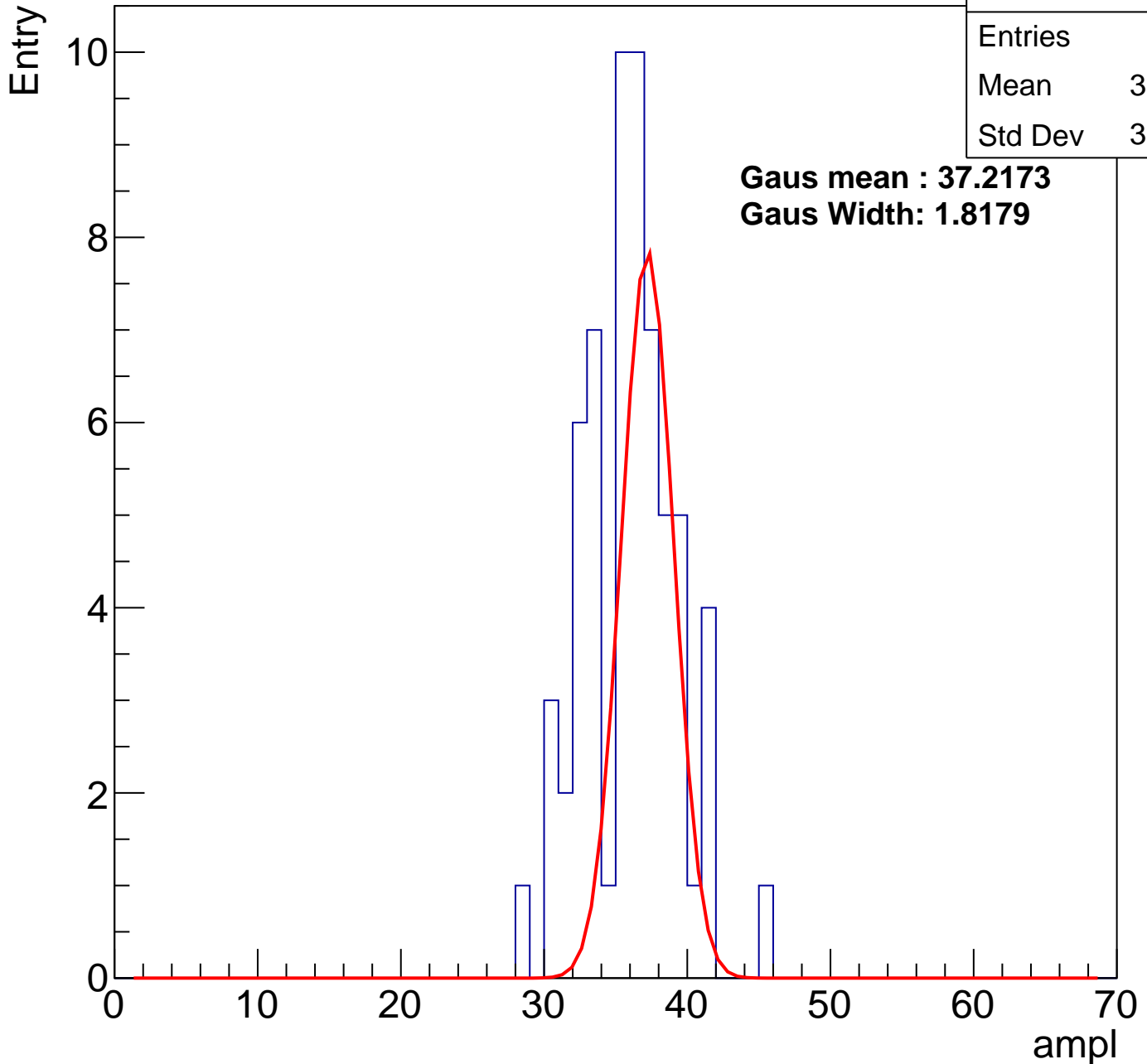
B0L001S, U7-ch105, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.56 |
| Std Dev | 3.216 |

Gaus mean : 37.2173

Gaus Width: 1.8179



B0L001S, U7-ch105, adc2

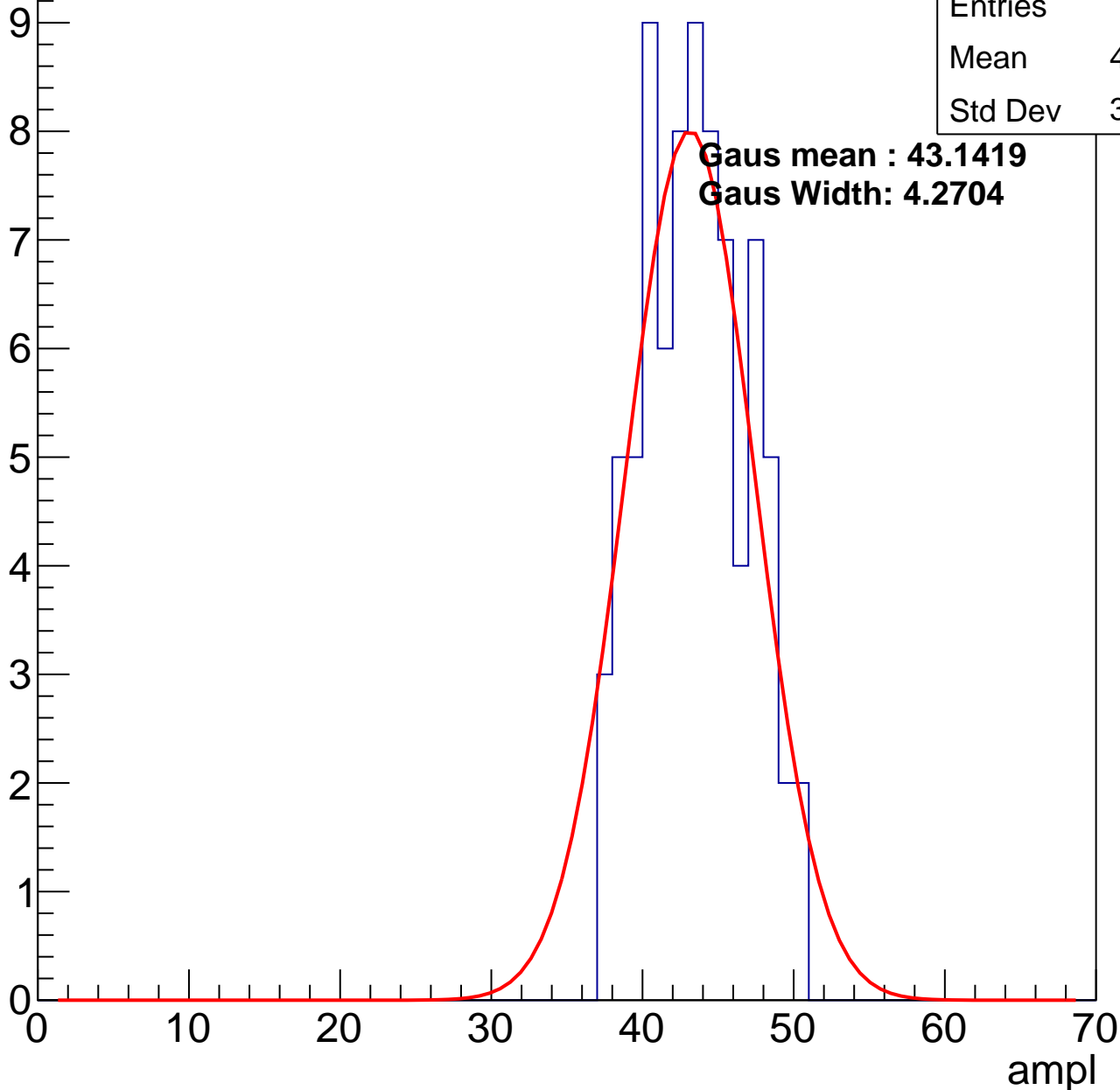
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 43.04 |
| Std Dev | 3.363 |

Gaus mean : 43.1419

Gaus Width: 4.2704

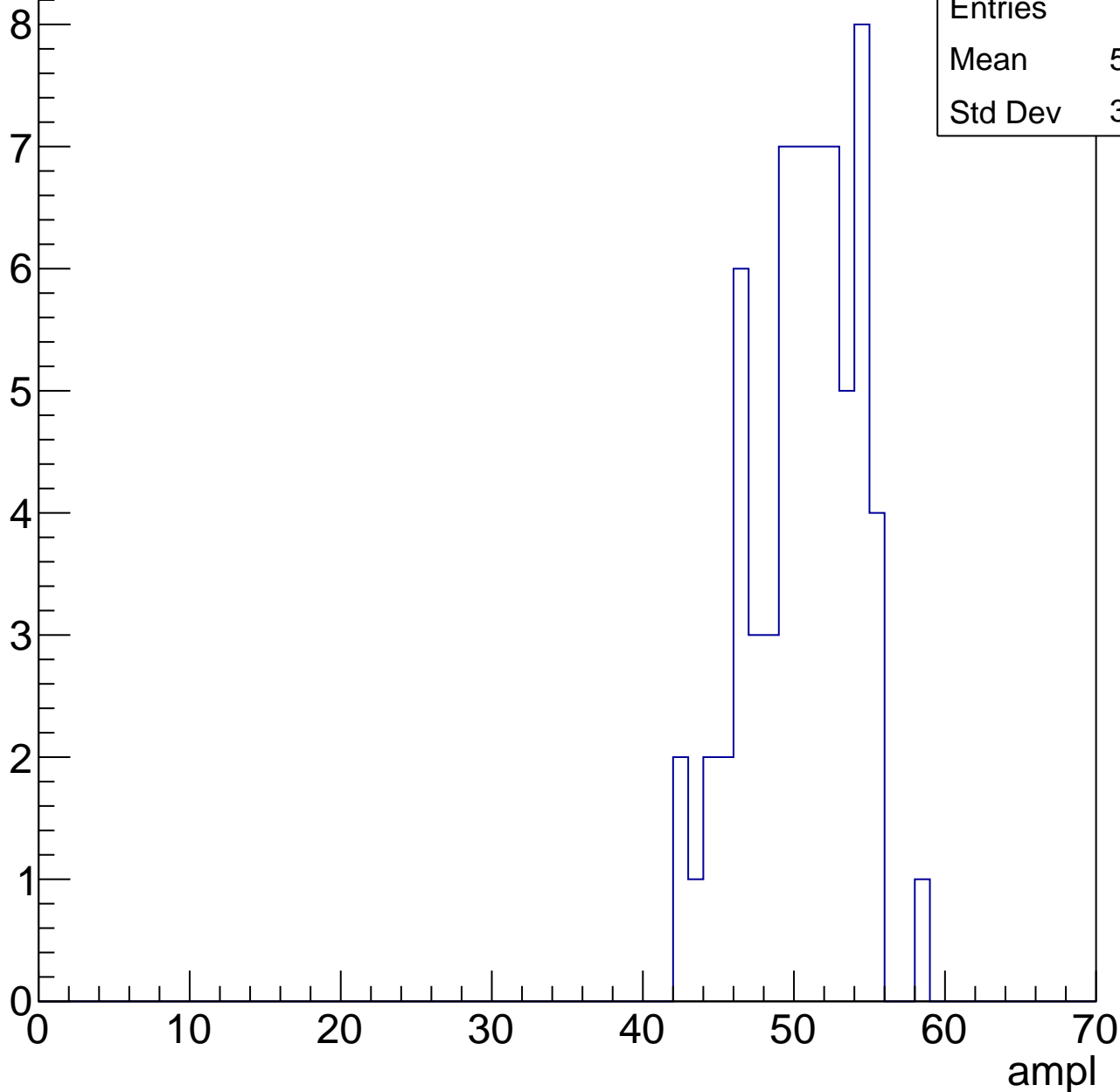


B0L001S, U7-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 50.08 |
| Std Dev | 3.536 |



B0L001S, U7-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.9 |
| Std Dev | 3.212 |

Entry

10

8

6

4

2

0

0

10

20

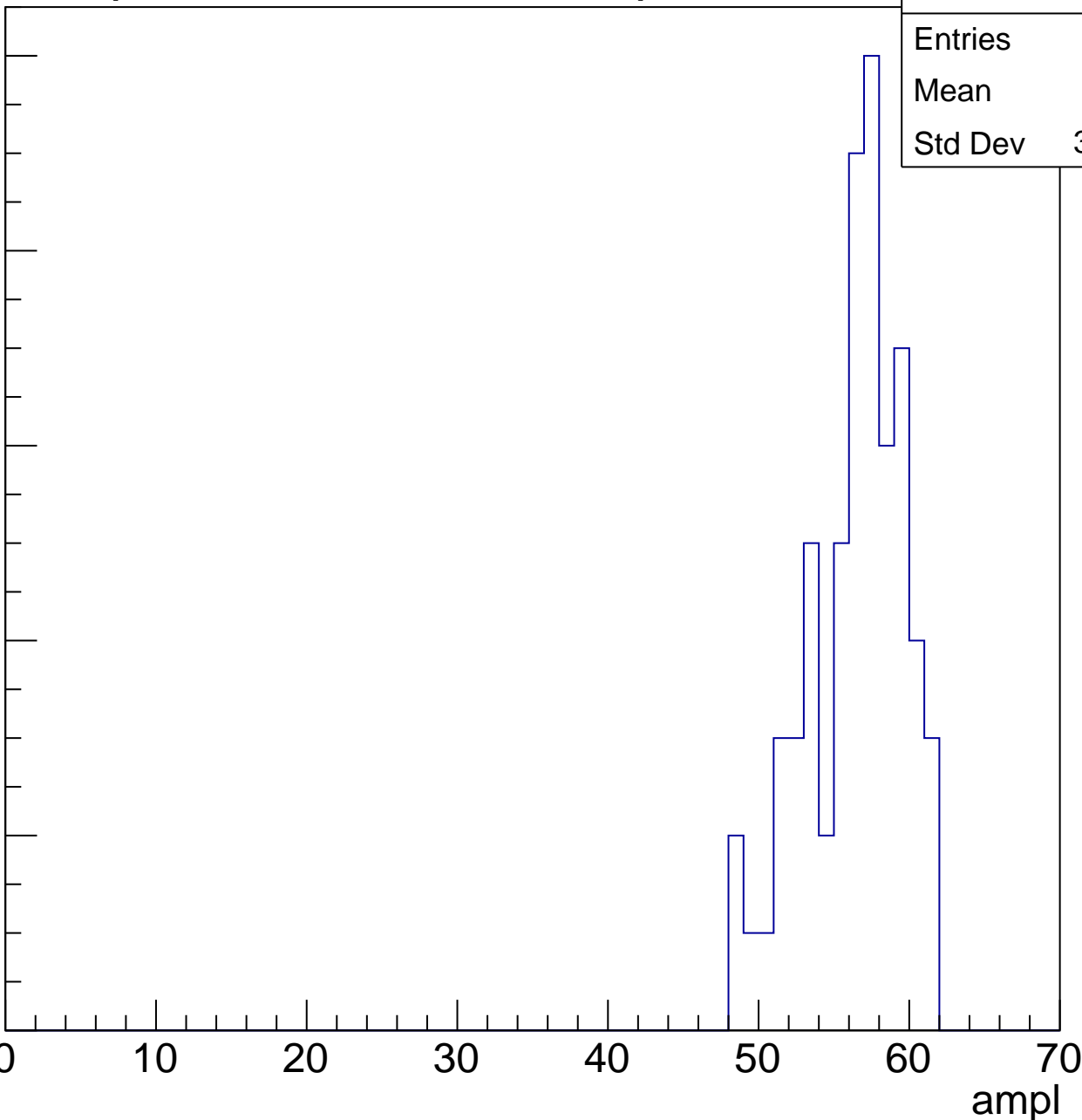
30

40

50

60

ampl

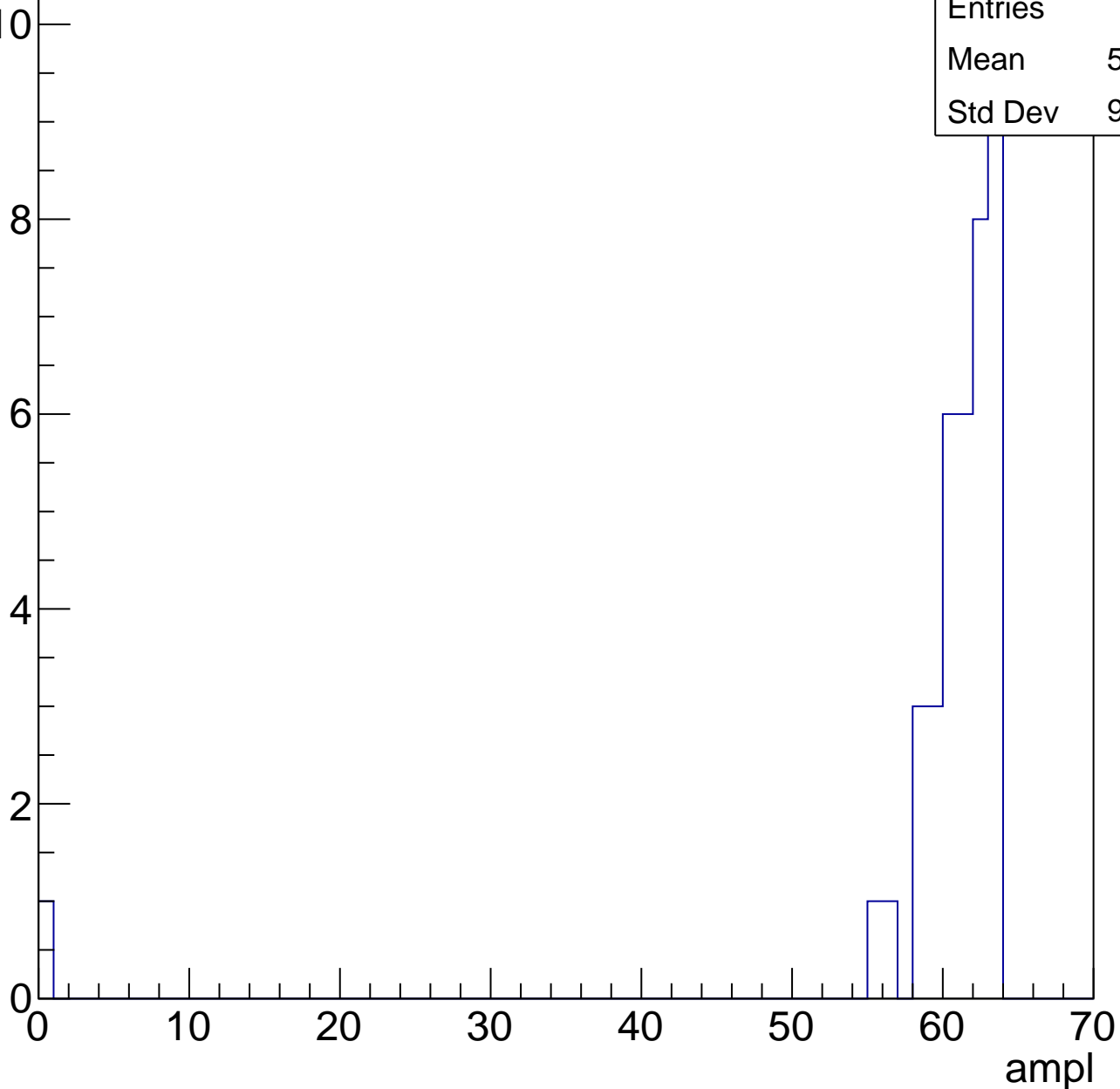


B0L001S, U7-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 59.33 |
| Std Dev | 9.828 |



B0L001S, U7-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 61.67 |
| Std Dev | 0.4714 |

ampl

0 10 20 30 40 50 60 70

B0L001S, U7-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch106, adc0

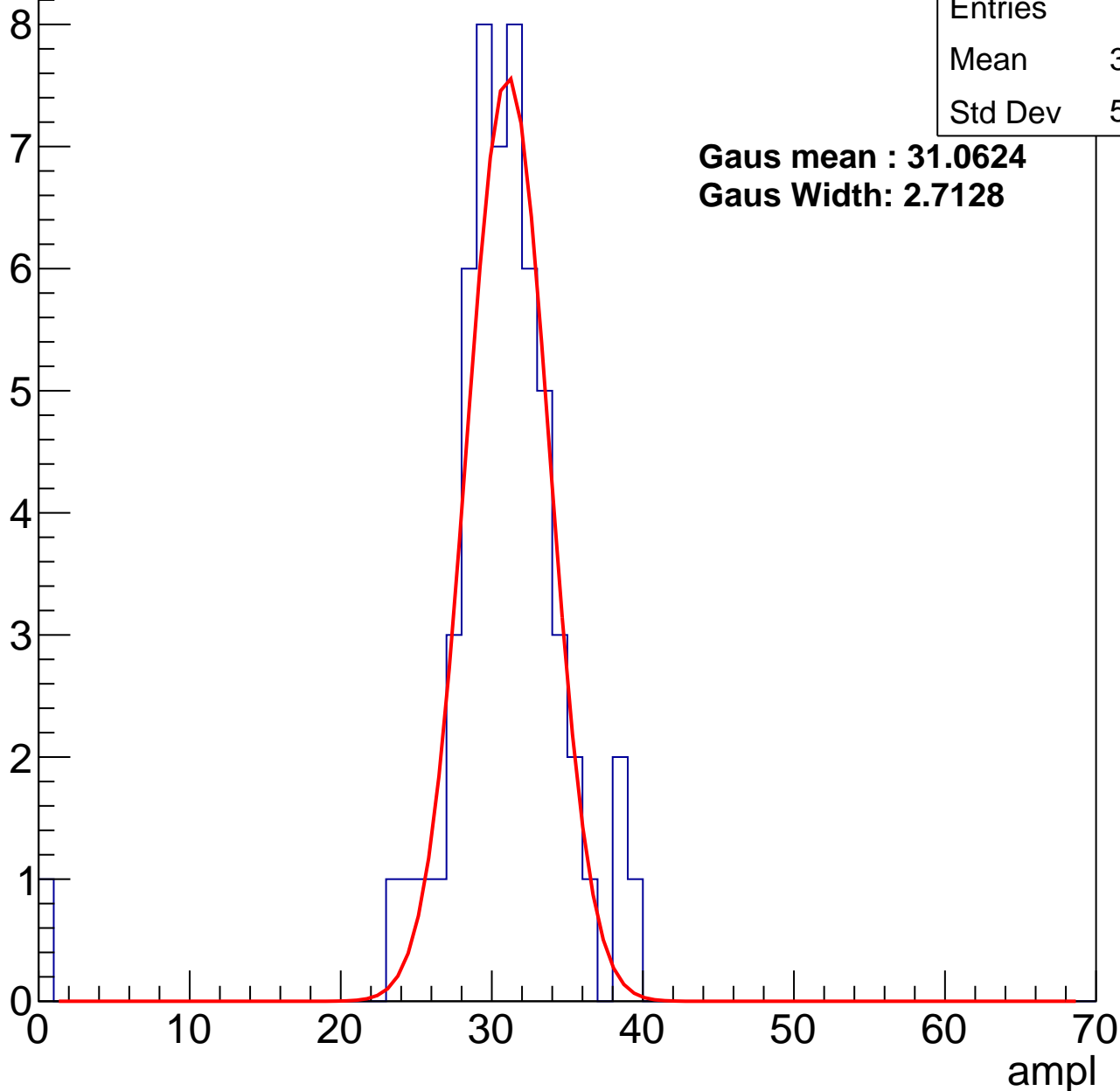
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 30.12 |
| Std Dev | 5.137 |

Gaus mean : 31.0624

Gaus Width: 2.7128



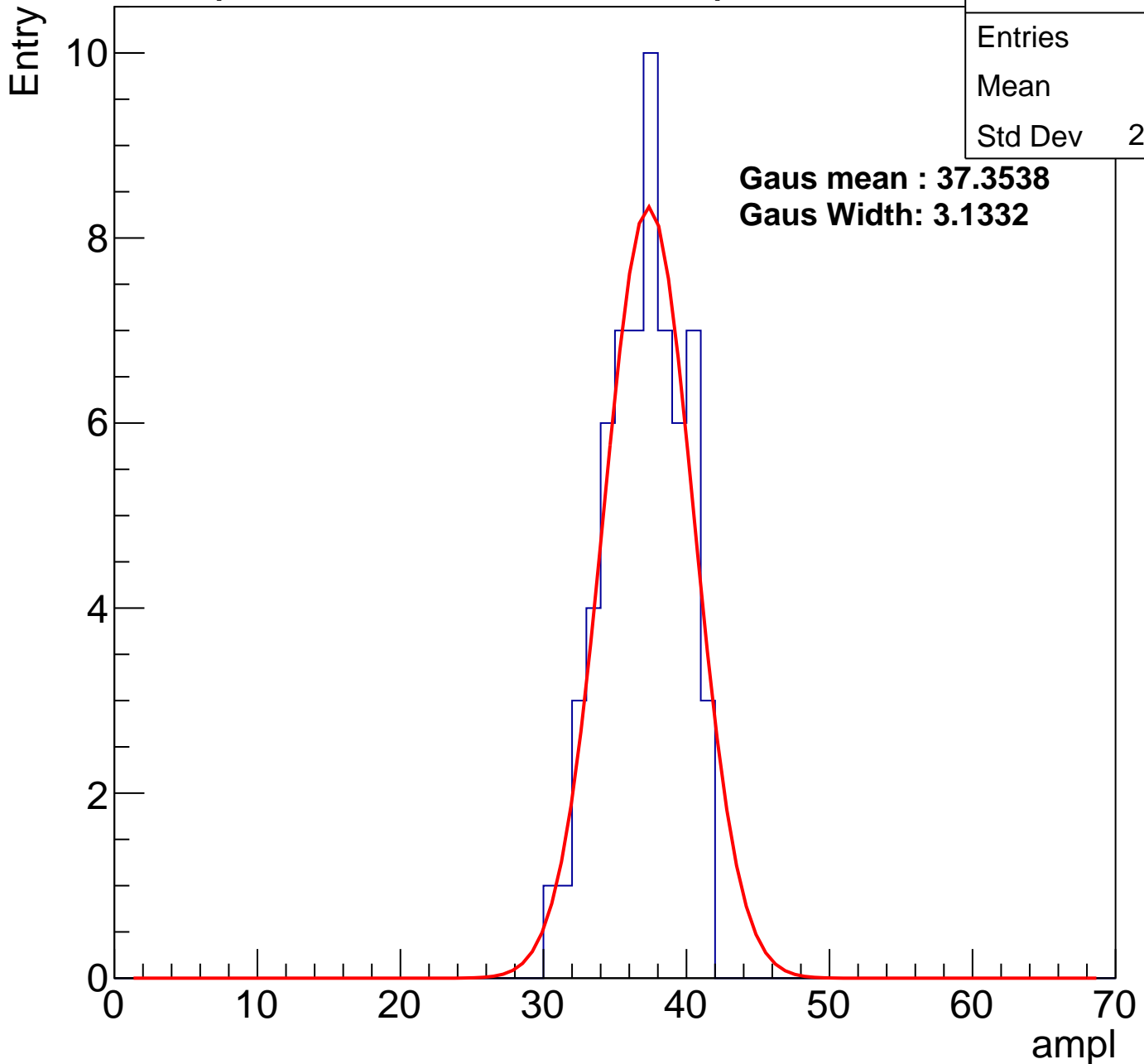
B0L001S, U7-ch106, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 62 |
| Mean | 36.5 |
| Std Dev | 2.662 |

Gaus mean : 37.3538

Gaus Width: 3.1332



B0L001S, U7-ch106, adc2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 66 |
| Mean | 43.23 |
| Std Dev | 3.157 |

Gaus mean : 43.7527

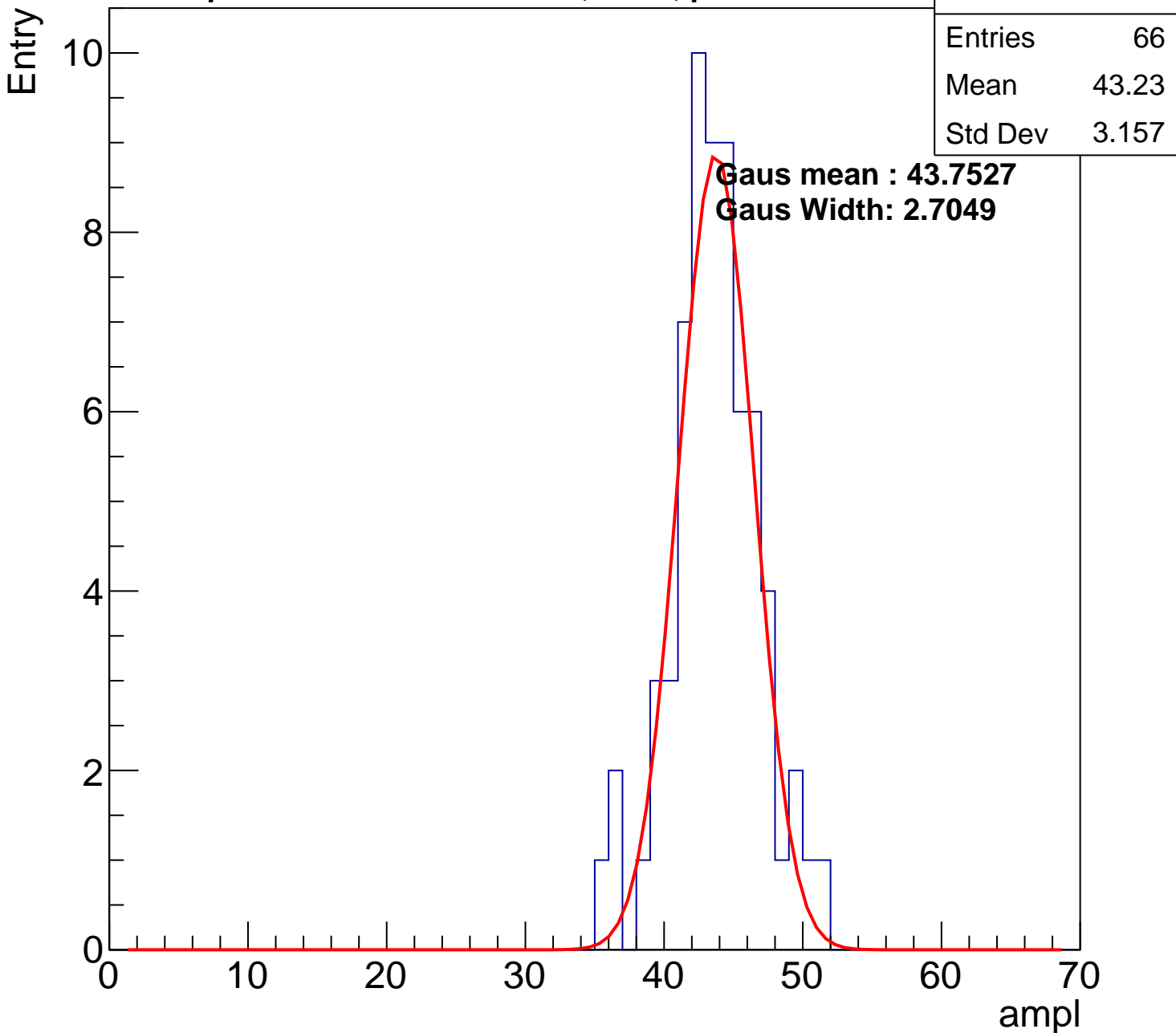
Gaus Width: 2.7049

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

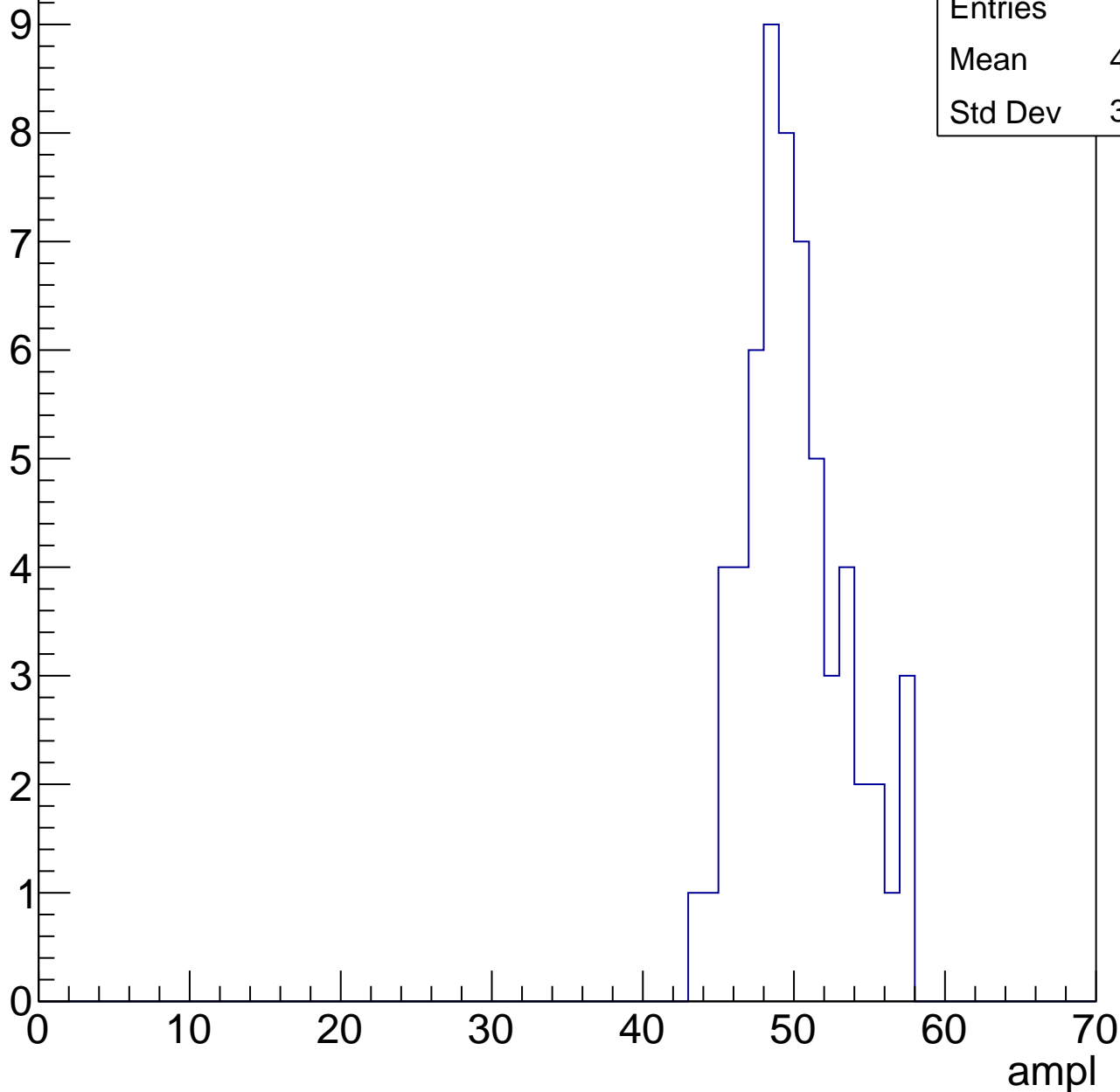


B0L001S, U7-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

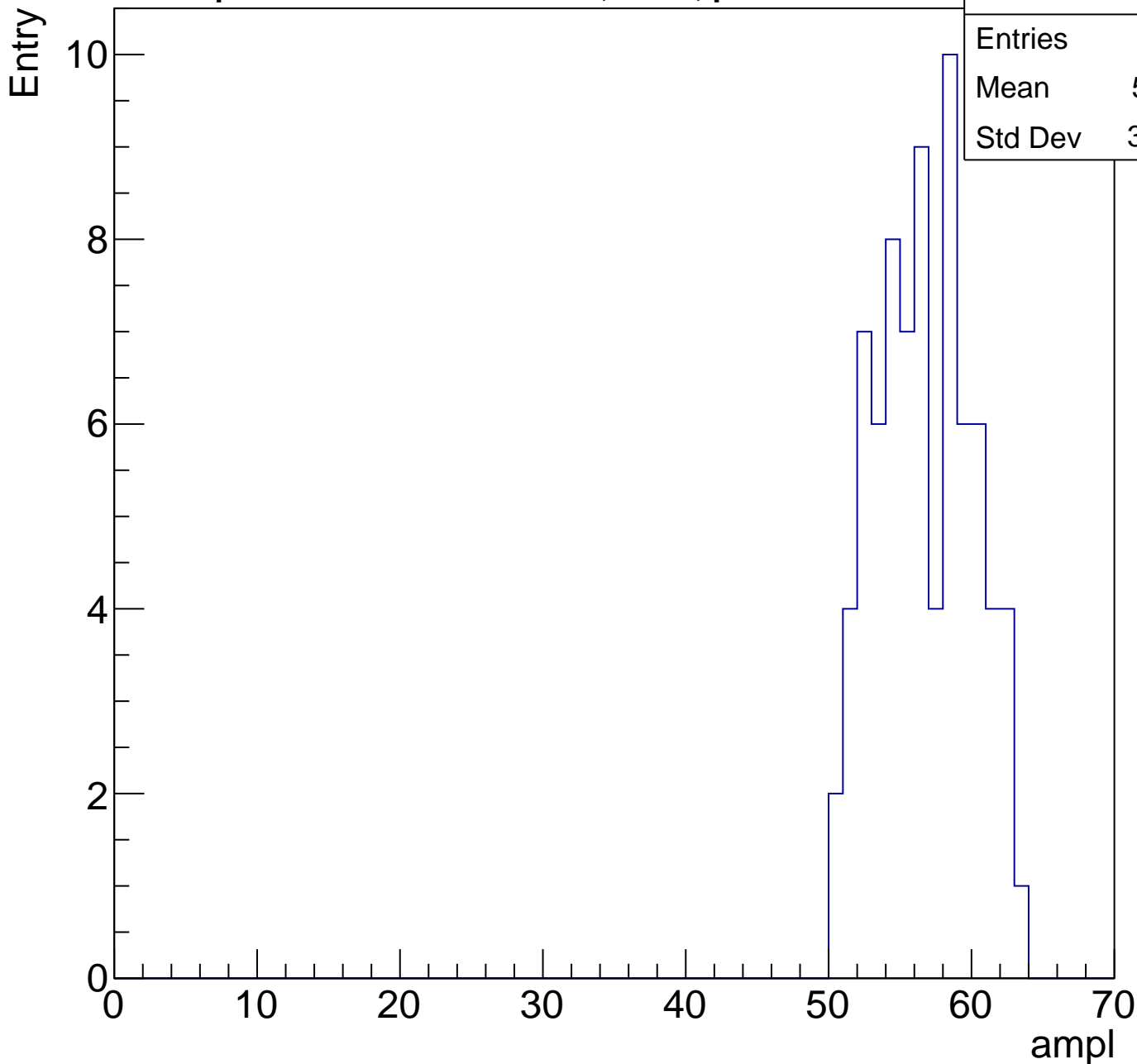
| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.58 |
| Std Dev | 3.303 |



B0L001S, U7-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 78 |
| Mean | 56.21 |
| Std Dev | 3.322 |

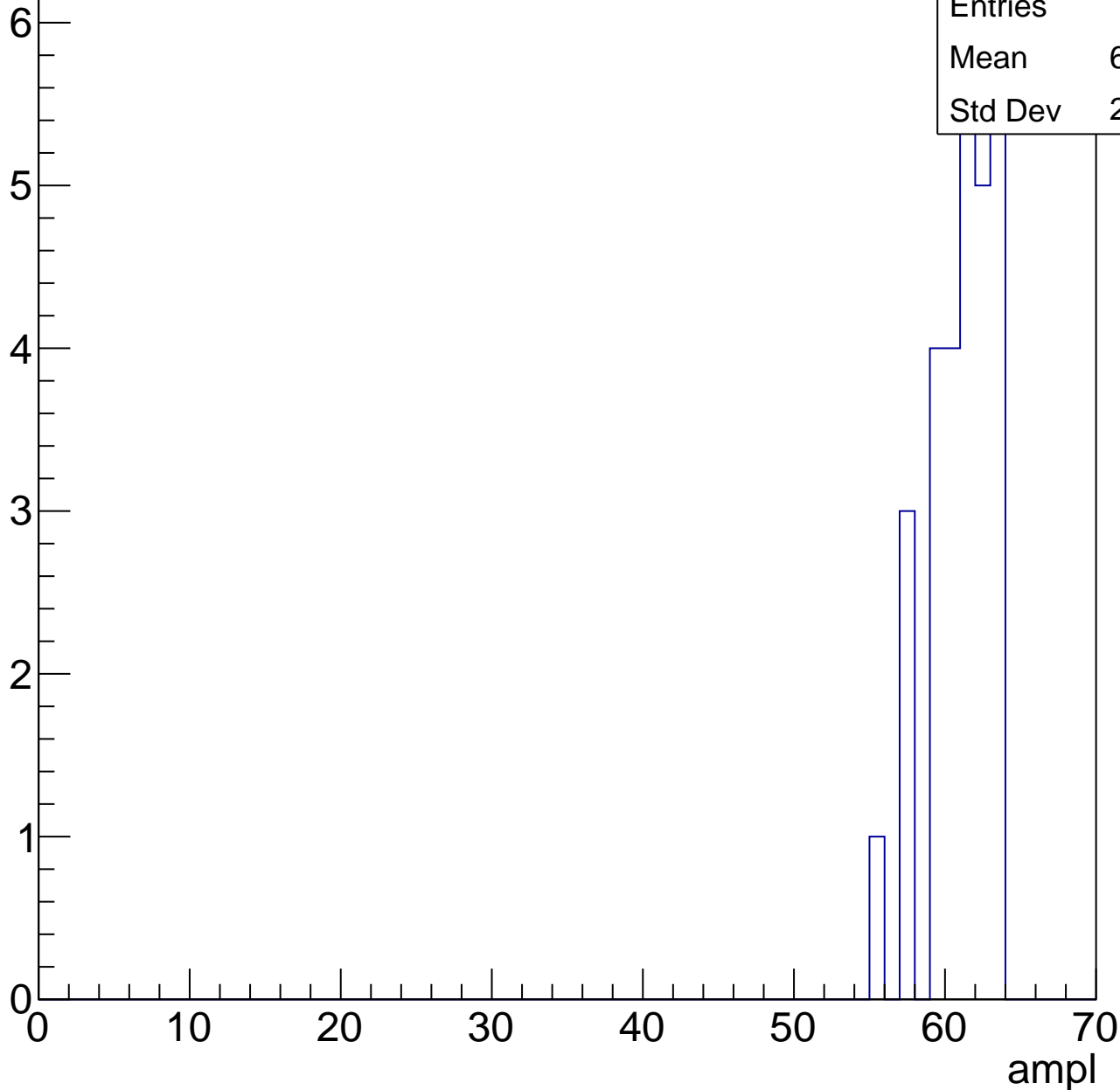


B0L001S, U7-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 60.55 |
| Std Dev | 2.094 |



B0L001S, U7-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch107, adc0

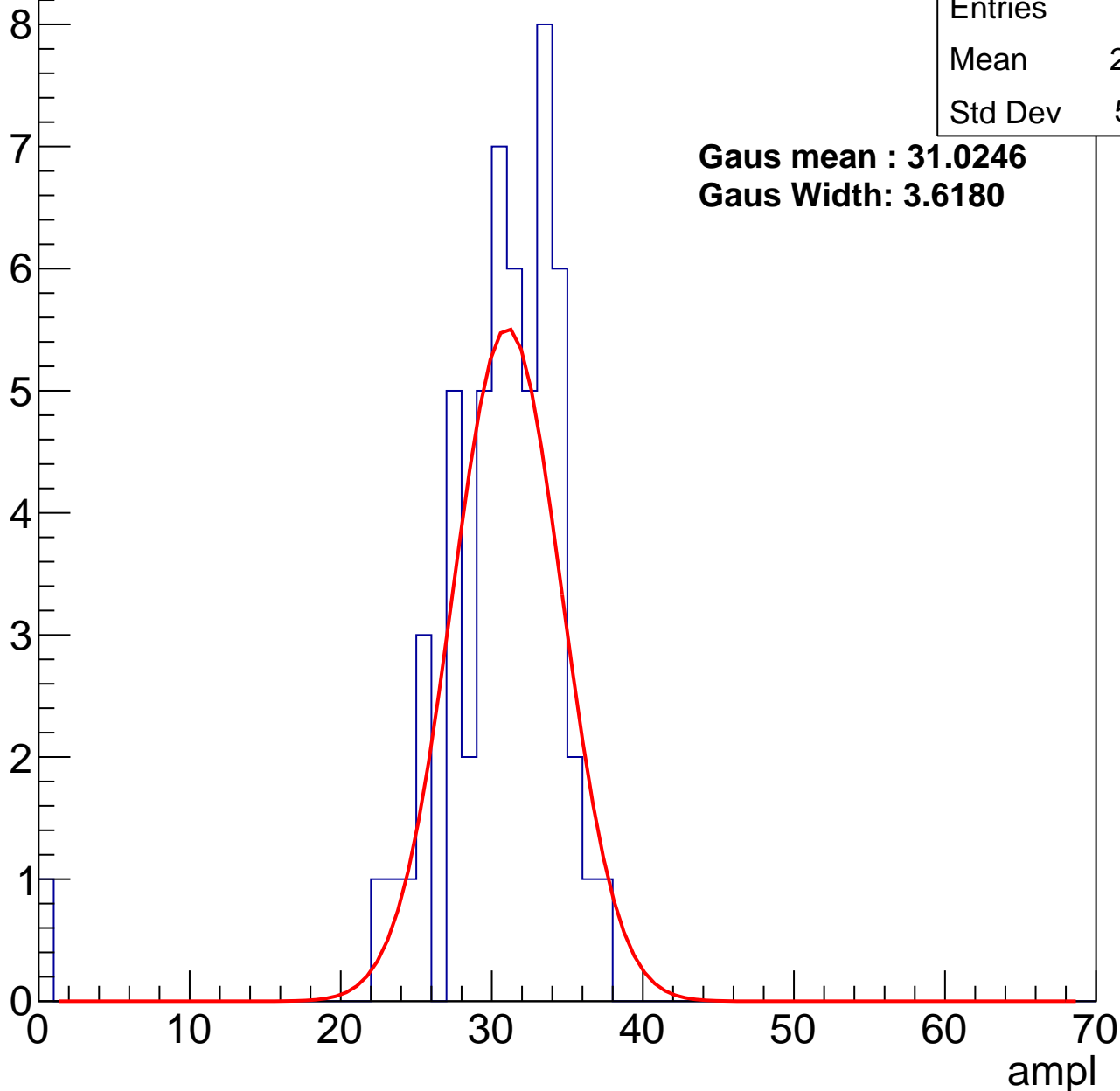
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 29.95 |
| Std Dev | 5.241 |

Gaus mean : 31.0246

Gaus Width: 3.6180



B0L001S, U7-ch107, adc1

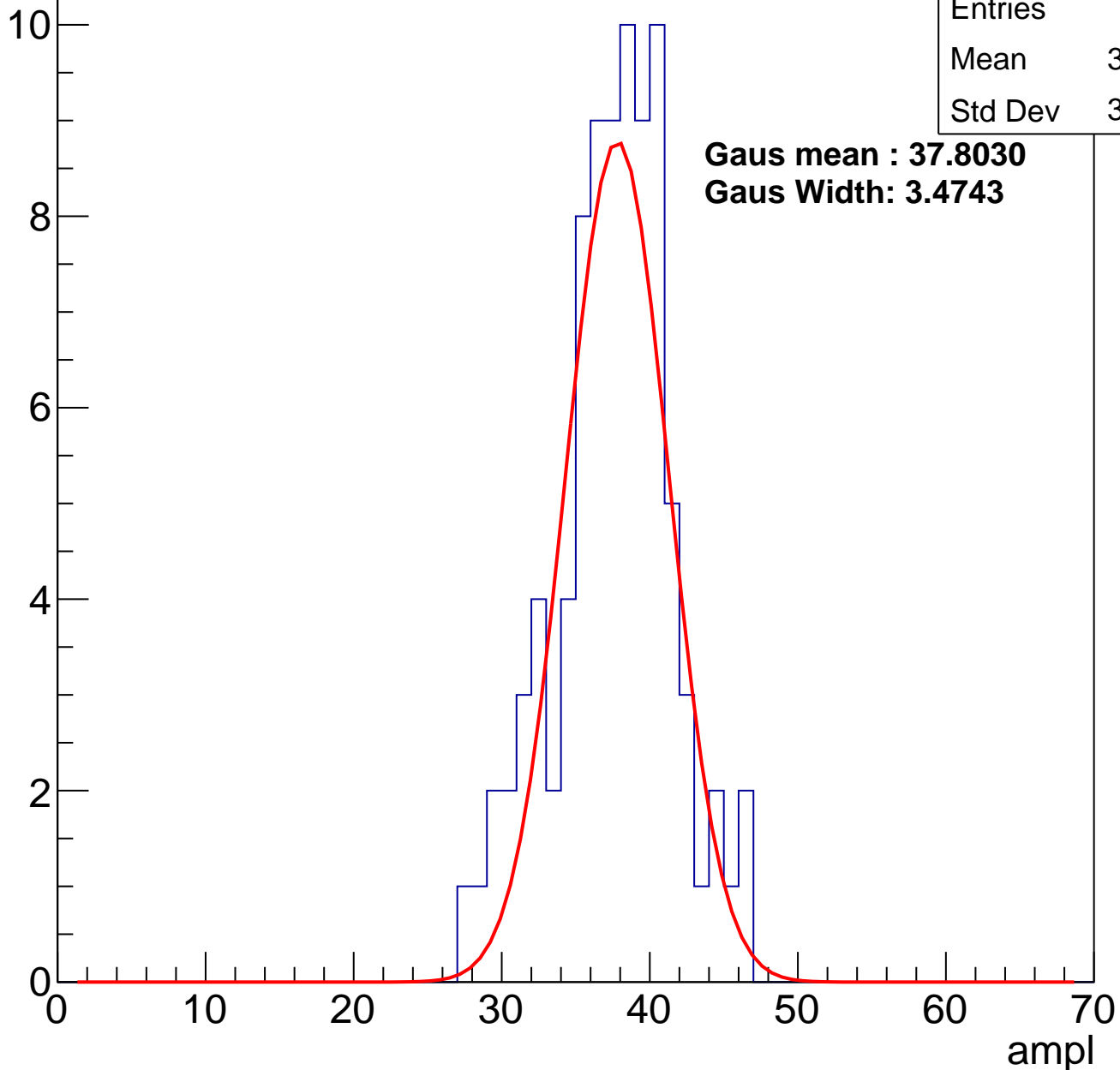
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 88 |
| Mean | 37.08 |
| Std Dev | 3.952 |

Gaus mean : 37.8030

Gaus Width: 3.4743

Entry



B0L001S, U7-ch107, adc2

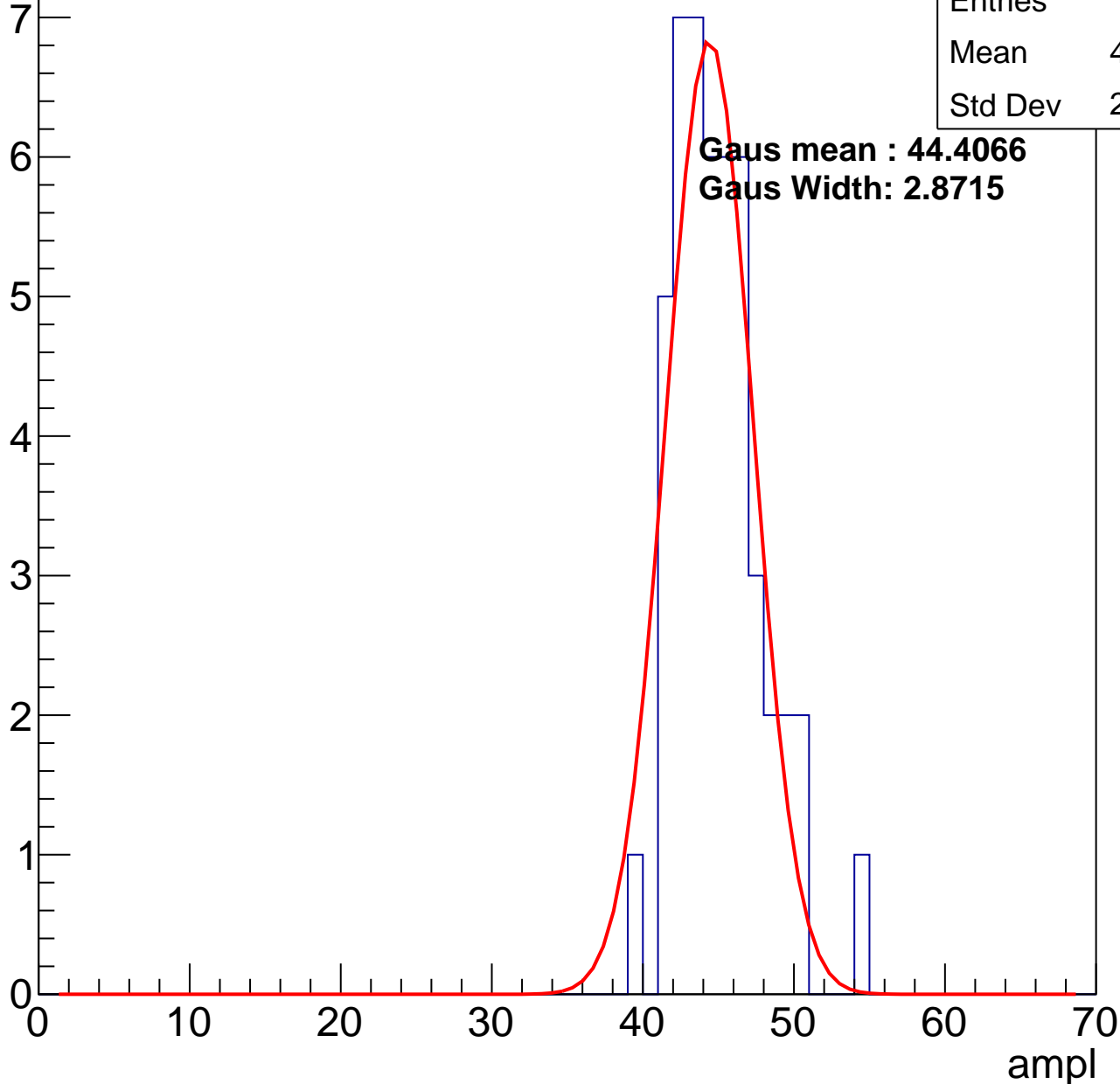
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 44.54 |
| Std Dev | 2.894 |

Gaus mean : 44.4066

Gaus Width: 2.8715

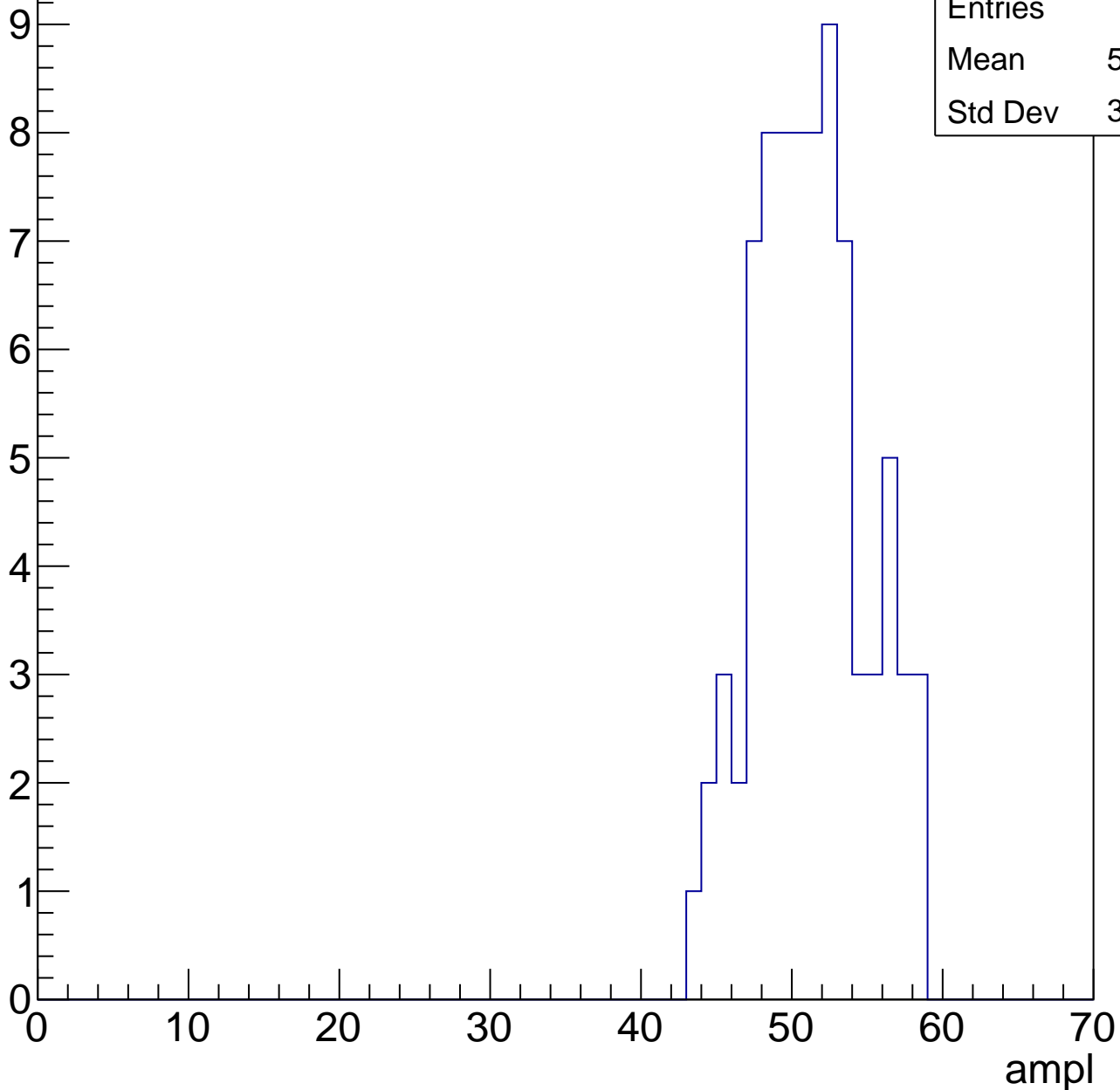


B0L001S, U7-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

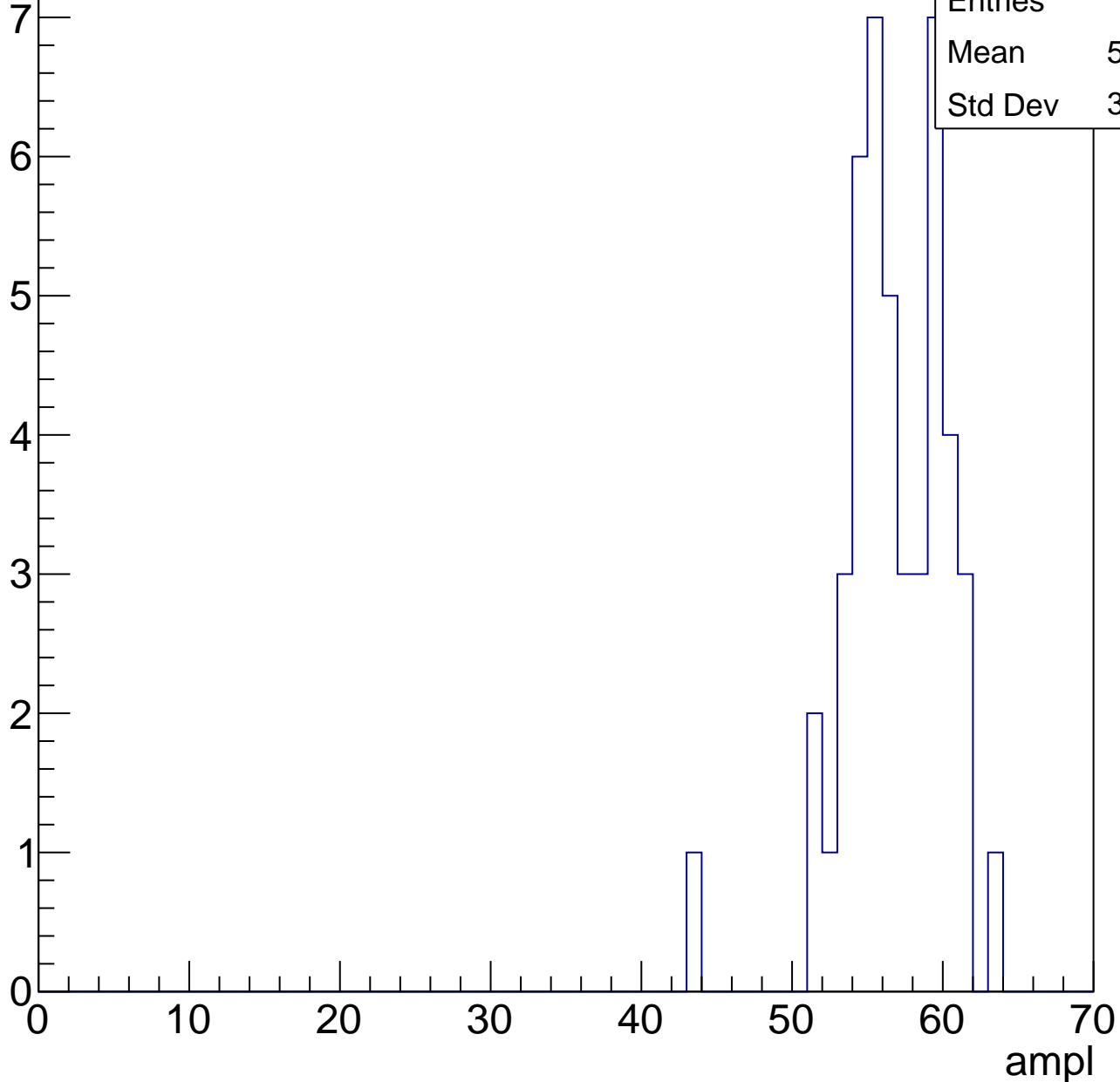
| | |
|---------|-------|
| Entries | 80 |
| Mean | 50.77 |
| Std Dev | 3.592 |



B0L001S, U7-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 46 |
| Mean | 56.28 |
| Std Dev | 3.475 |

B0L001S, U7-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.26 |
| Std Dev | 9.339 |

Entry

10

8

6

4

2

0

0

10

20

30

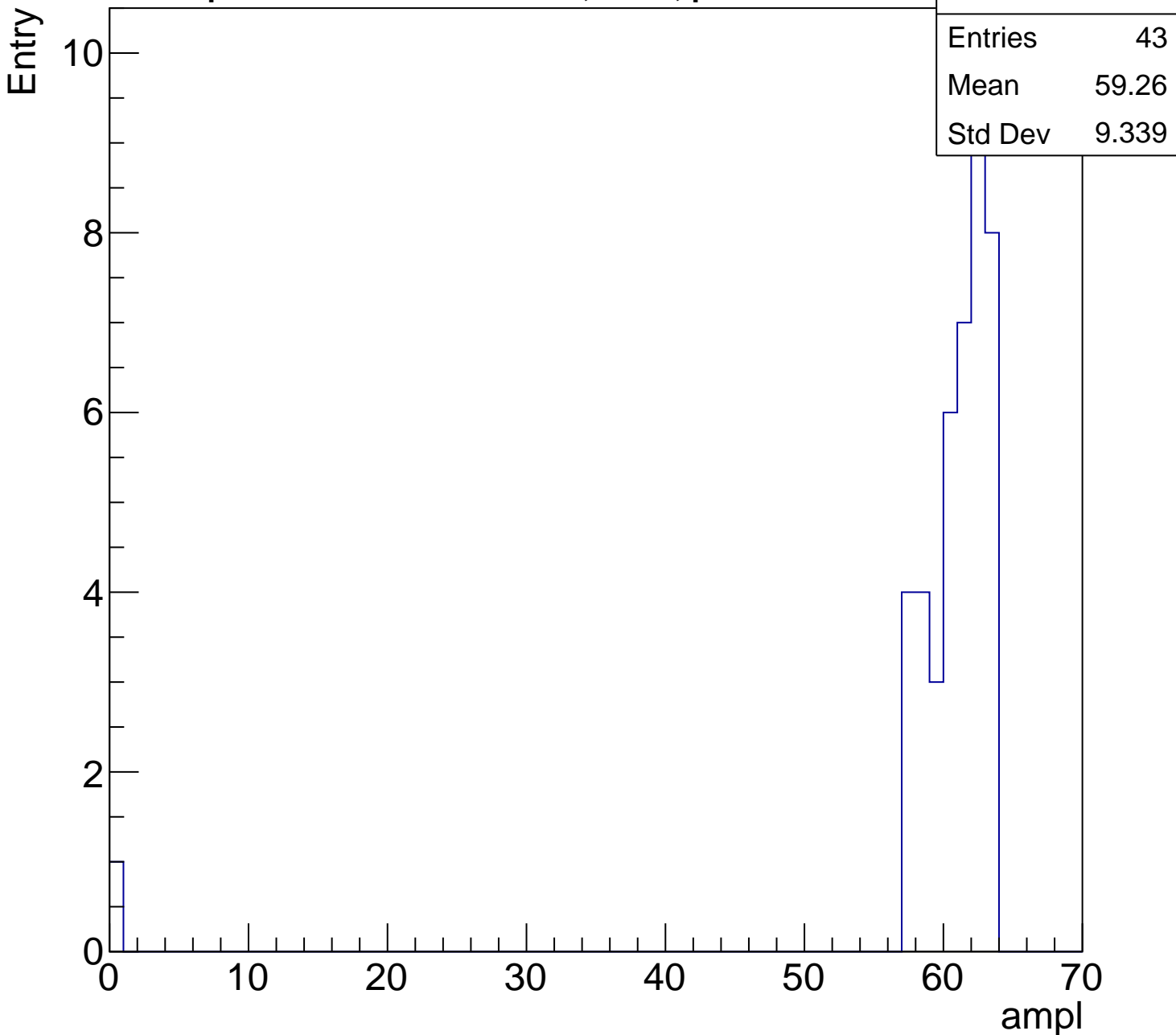
40

50

60

ampl

70



B0L001S, U7-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch108, adc0

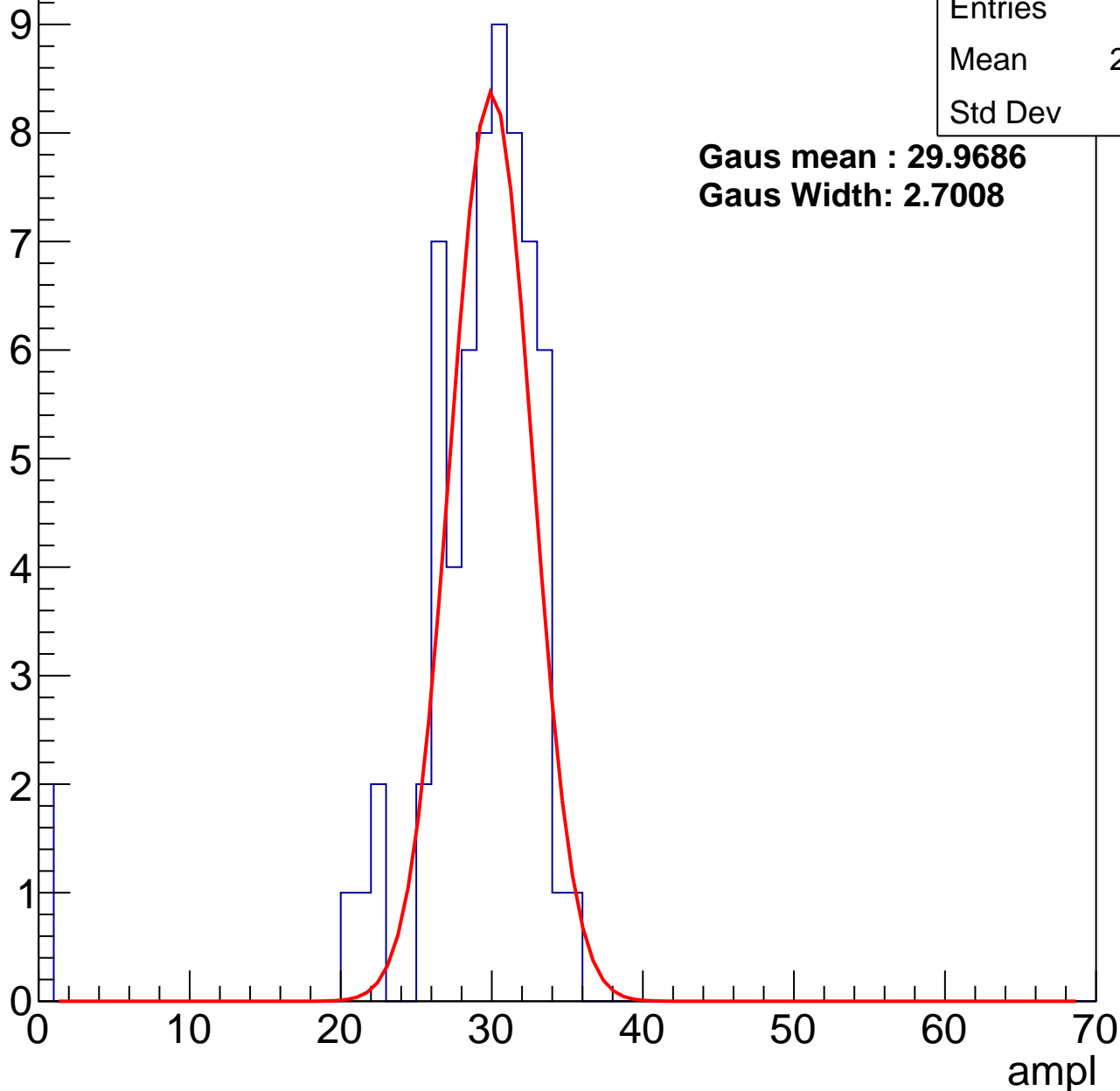
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 28.22 |
| Std Dev | 5.9 |

Gaus mean : 29.9686

Gaus Width: 2.7008

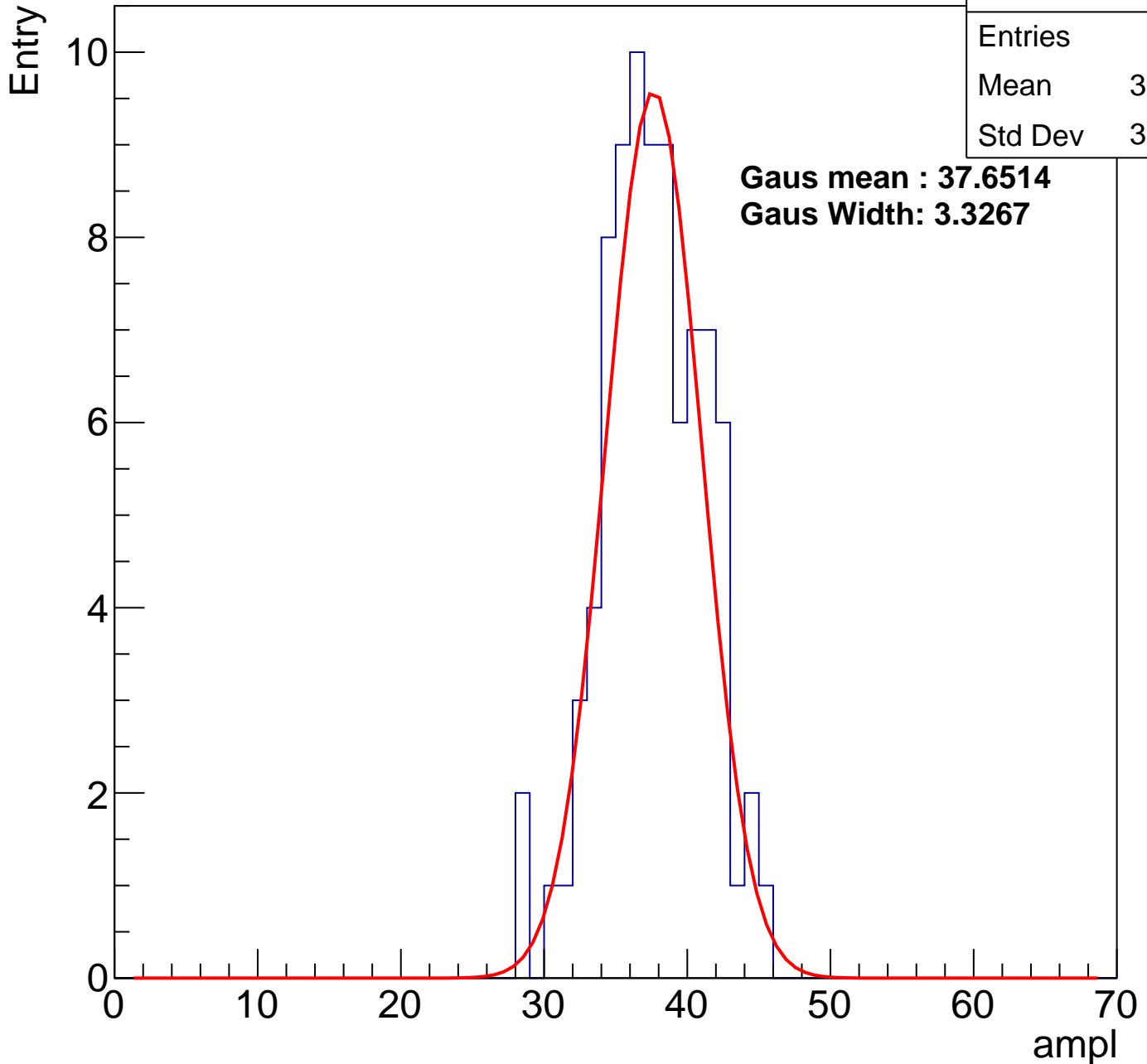


B0L001S, U7-ch108, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 86 |
| Mean | 37.16 |
| Std Dev | 3.507 |

Gaus mean : 37.6514
Gaus Width: 3.3267



B0L001S, U7-ch108, adc2

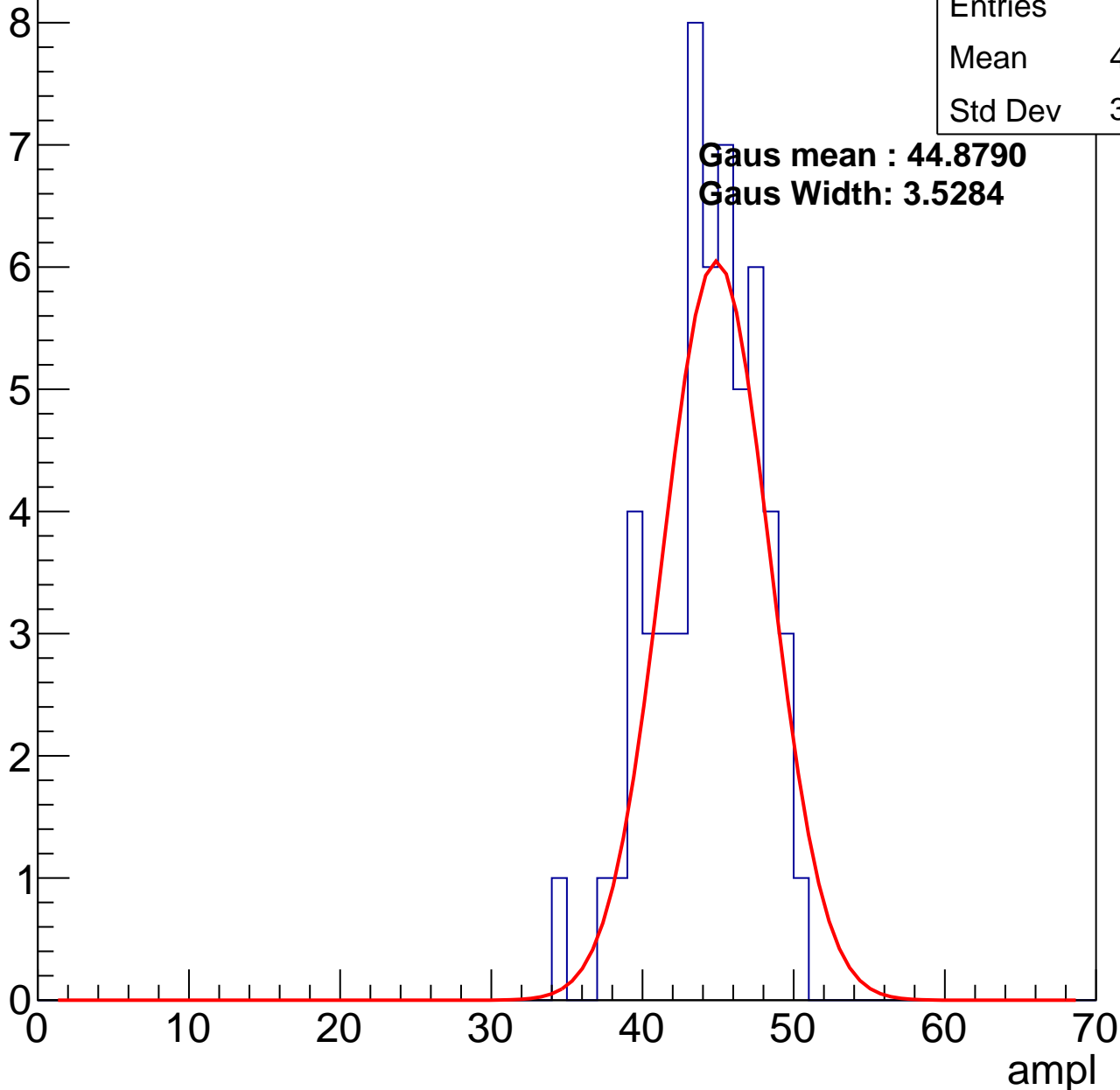
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43.89 |
| Std Dev | 3.368 |

Gaus mean : 44.8790

Gaus Width: 3.5284

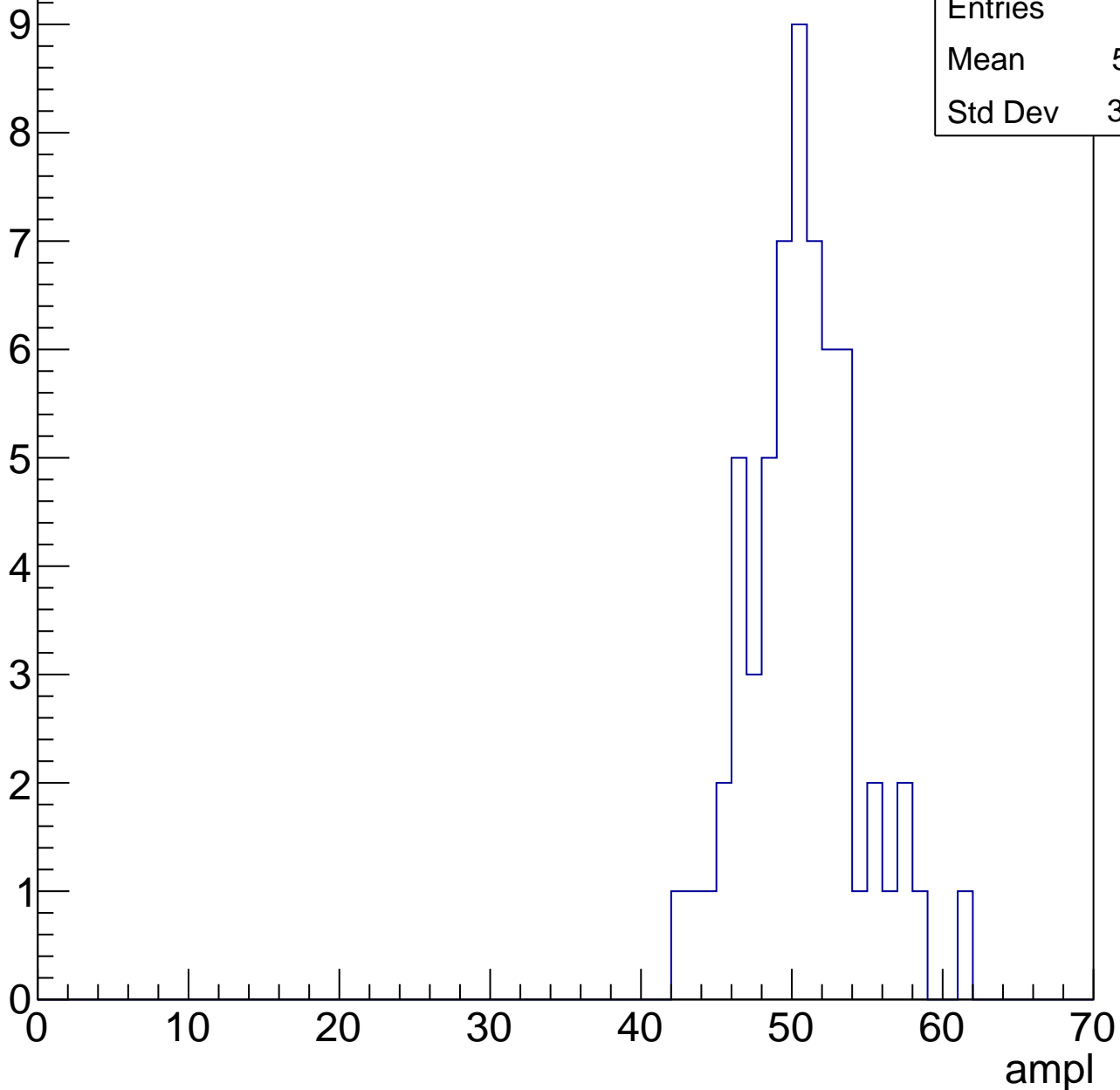


B0L001S, U7-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 50.21 |
| Std Dev | 3.622 |

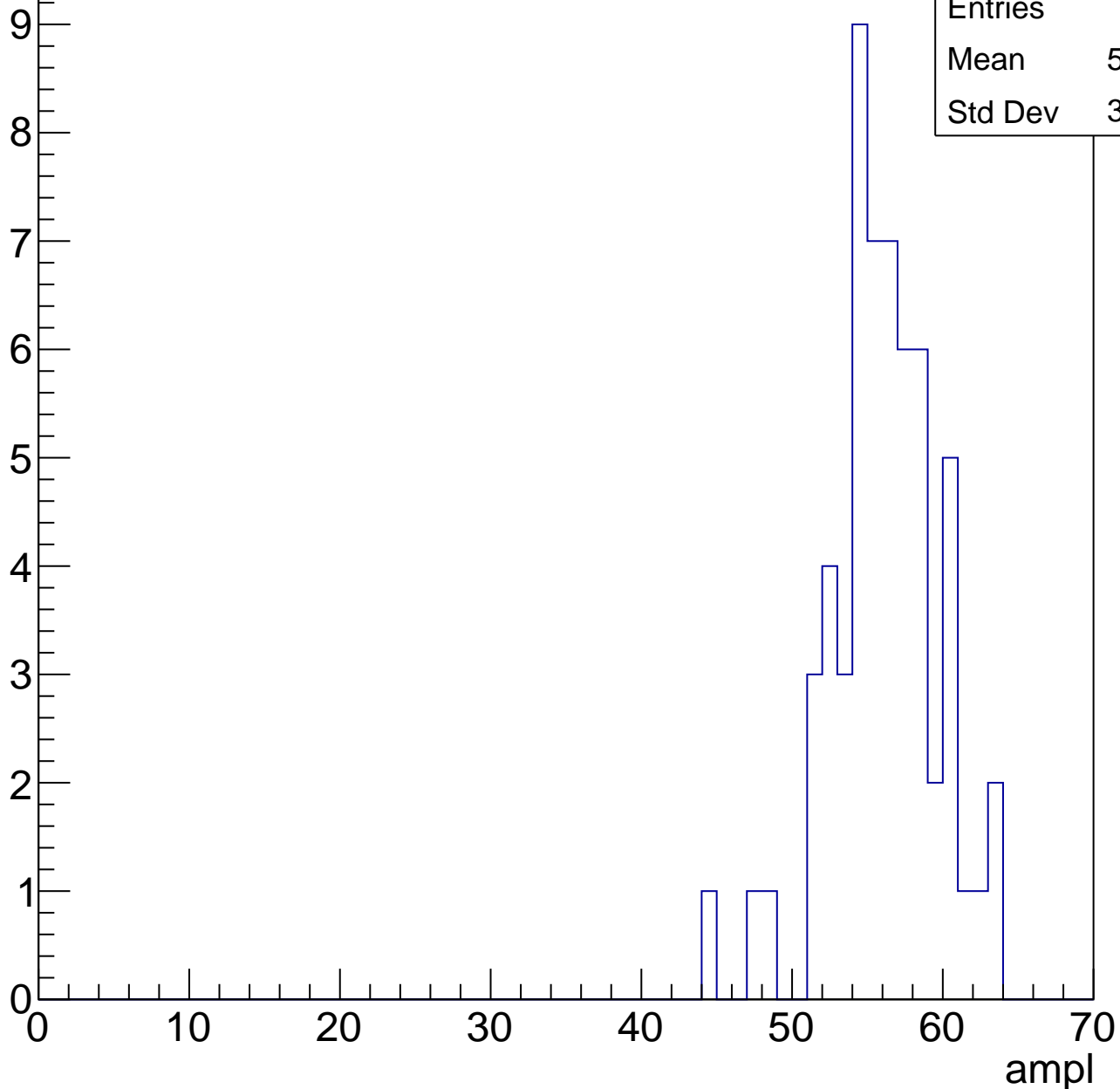


B0L001S, U7-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

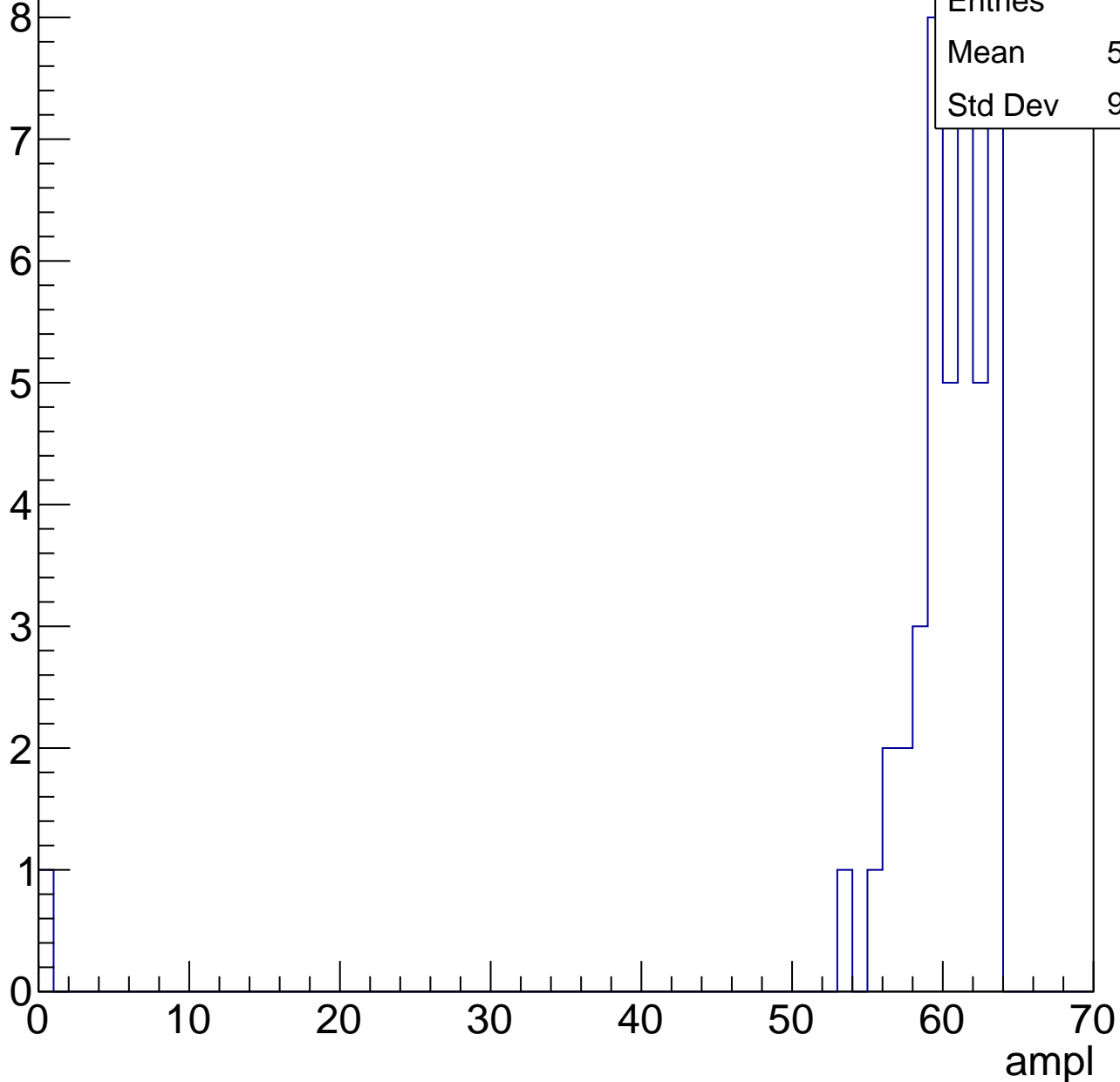
| | |
|---------|-------|
| Entries | 59 |
| Mean | 55.58 |
| Std Dev | 3.623 |



B0L001S, U7-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

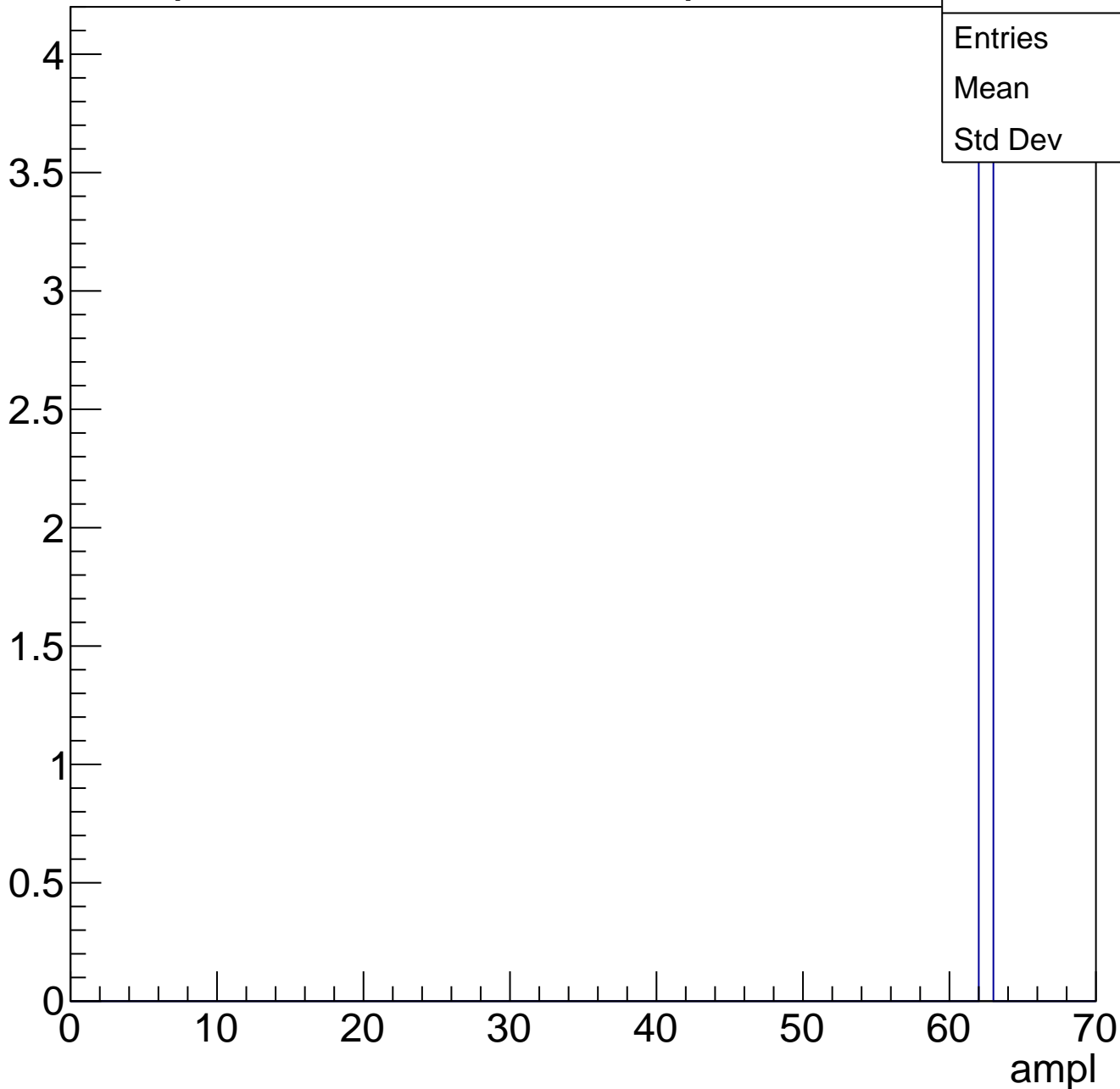
Entry



B0L001S, U7-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch109, adc0

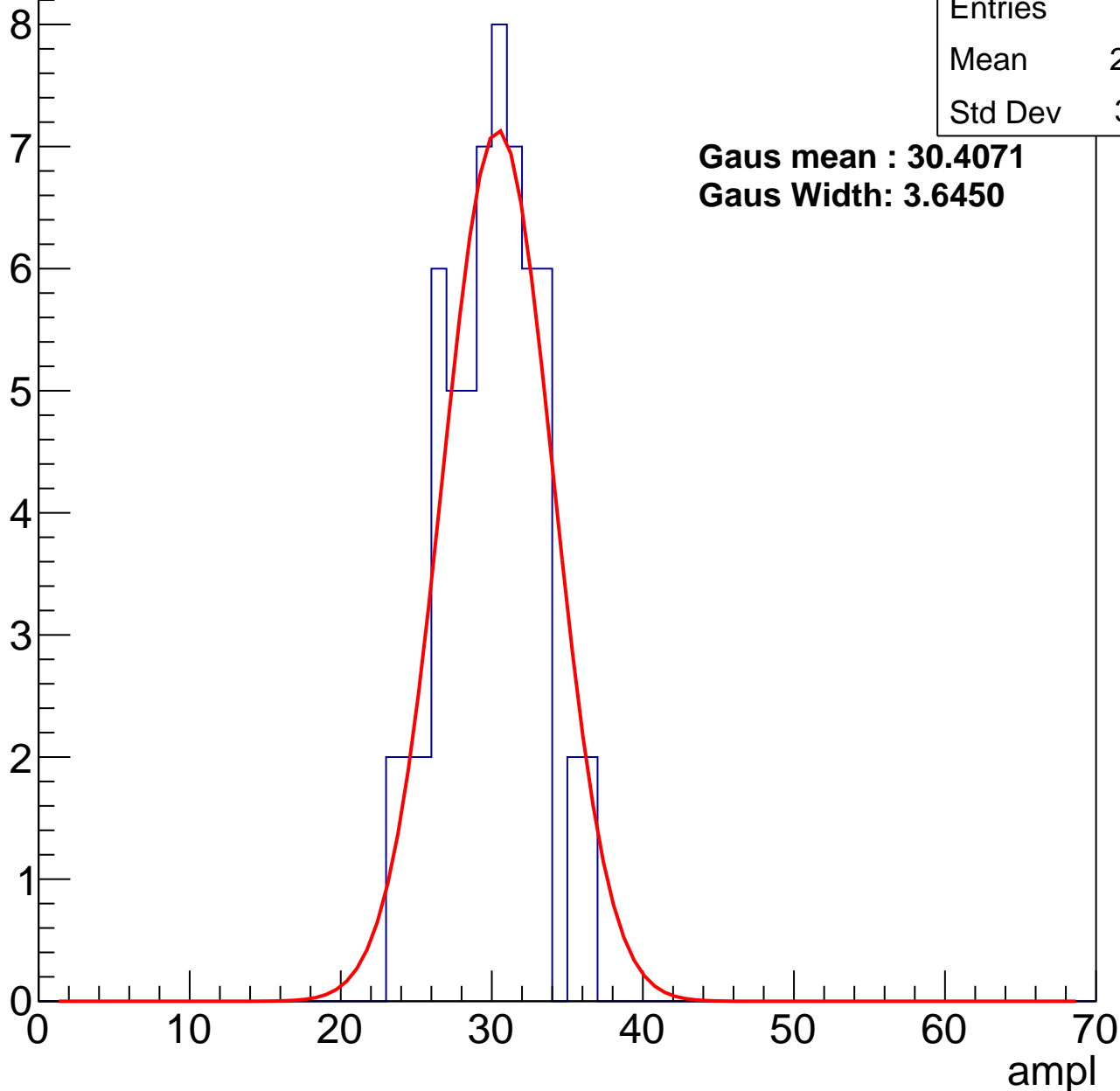
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 29.45 |
| Std Dev | 3.101 |

Gaus mean : 30.4071

Gaus Width: 3.6450



B0L001S, U7-ch109, adc1

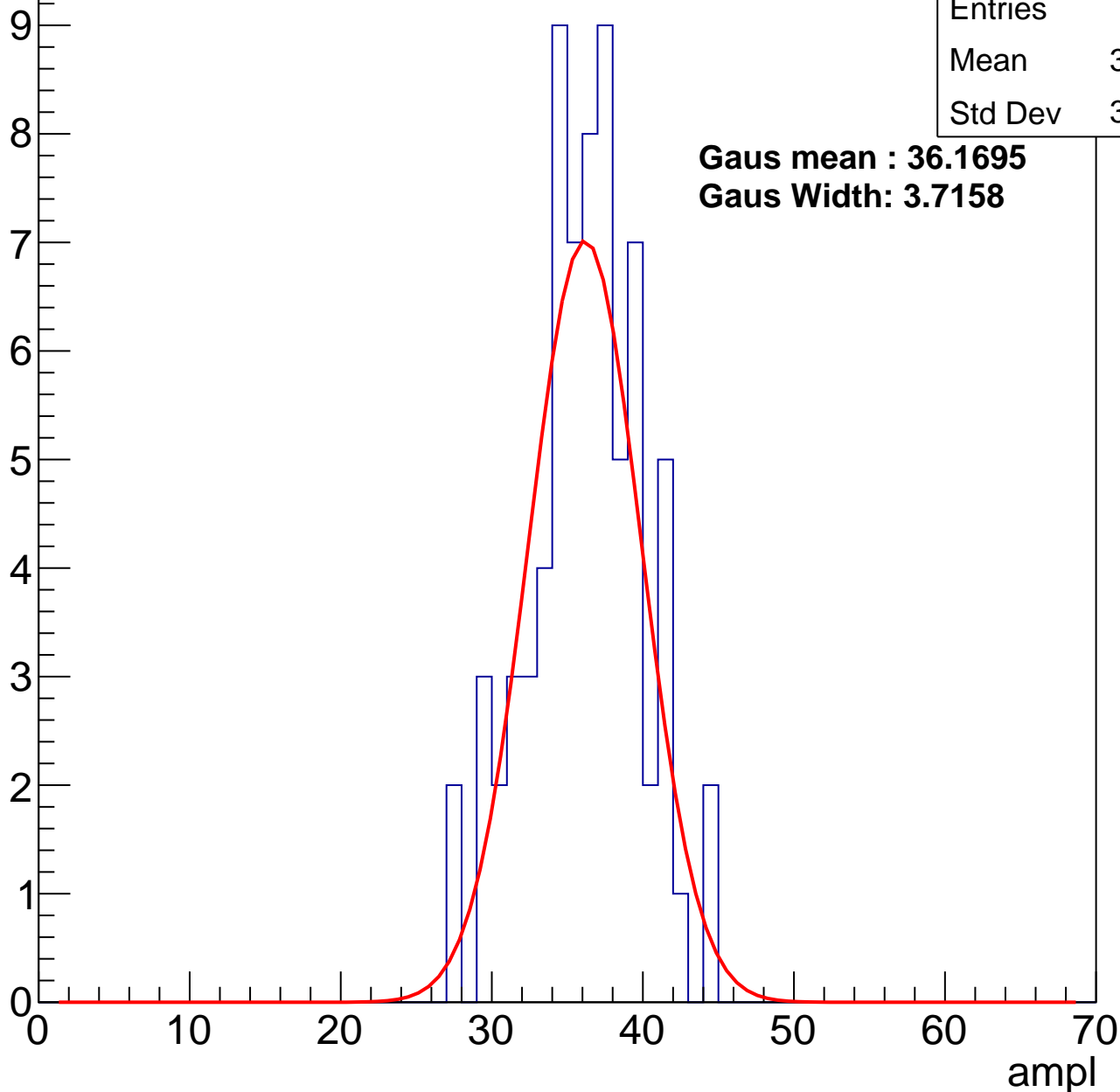
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.72 |
| Std Dev | 3.709 |

Gaus mean : 36.1695

Gaus Width: 3.7158



B0L001S, U7-ch109, adc2

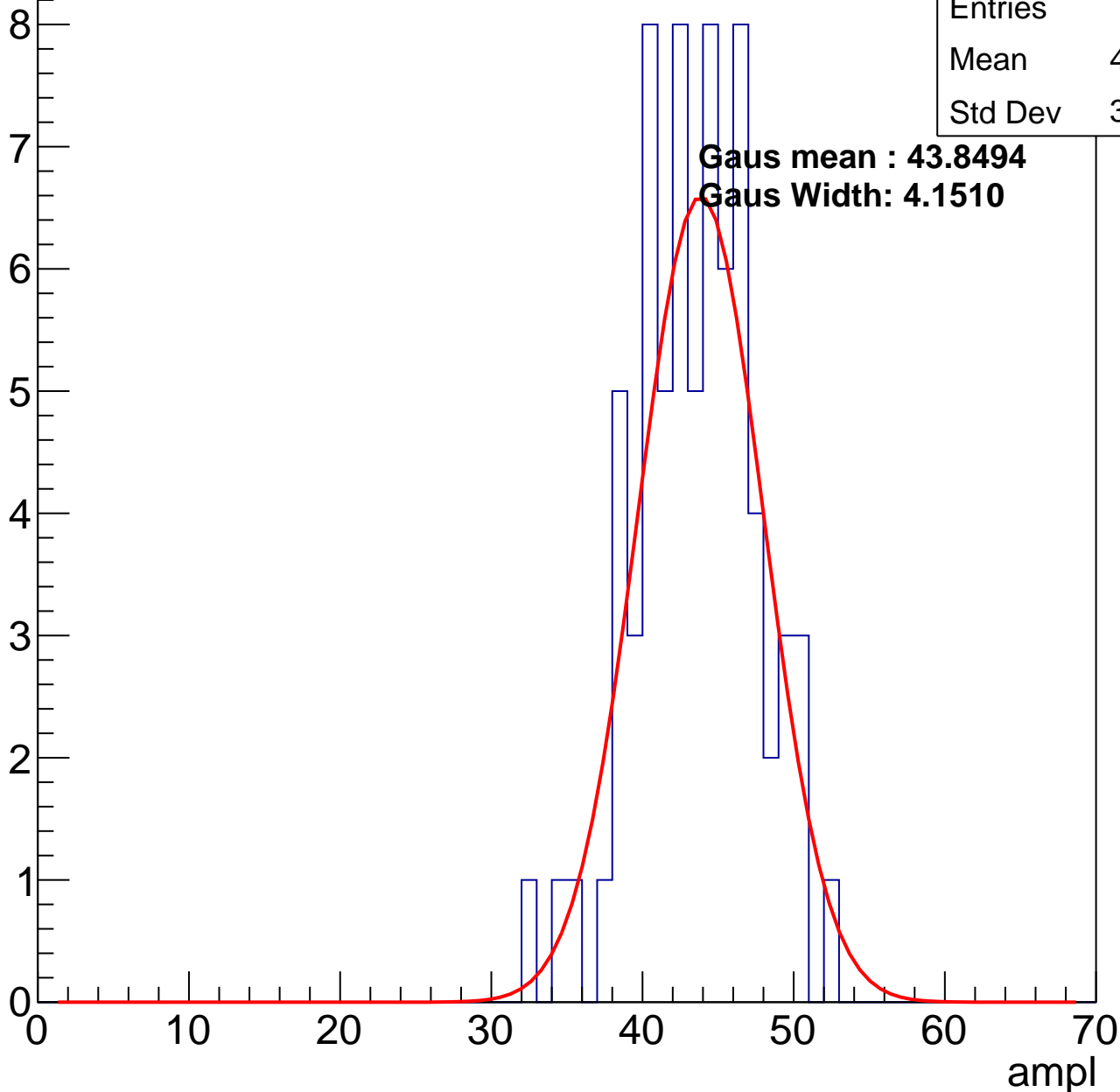
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 43.07 |
| Std Dev | 3.932 |

Gaus mean : 43.8494

Gaus Width: 4.1510

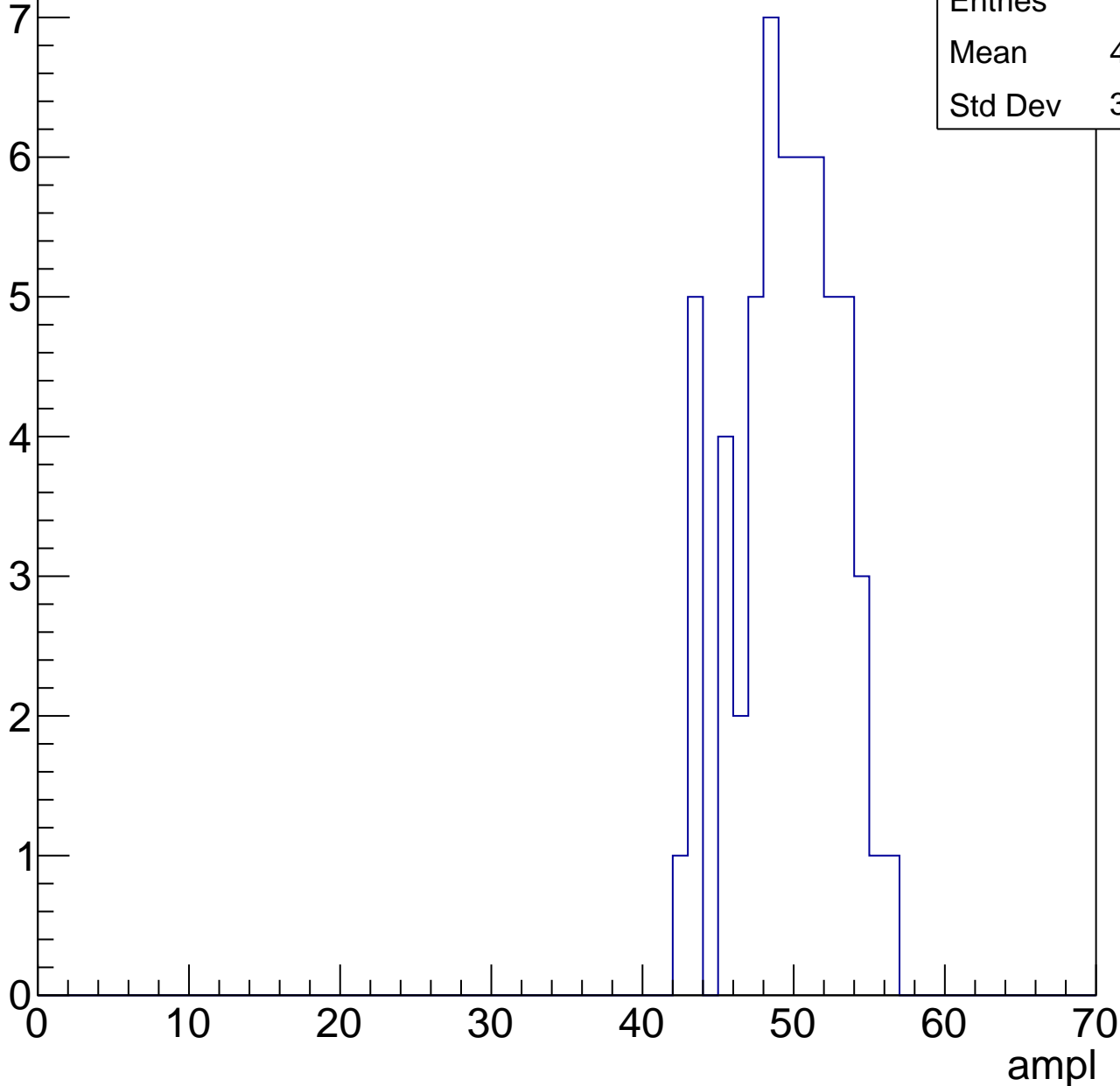


B0L001S, U7-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

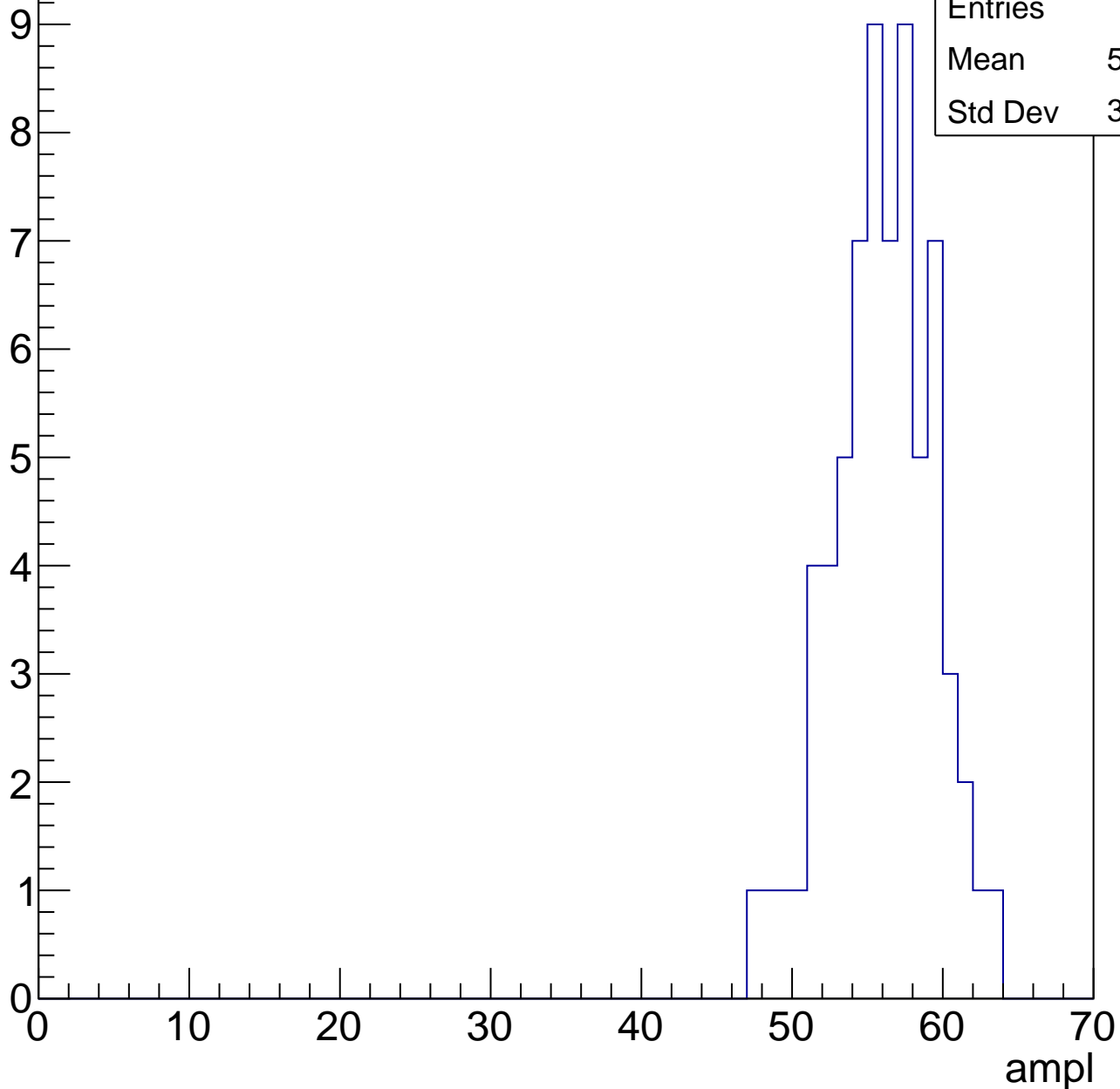
| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.09 |
| Std Dev | 3.384 |



B0L001S, U7-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

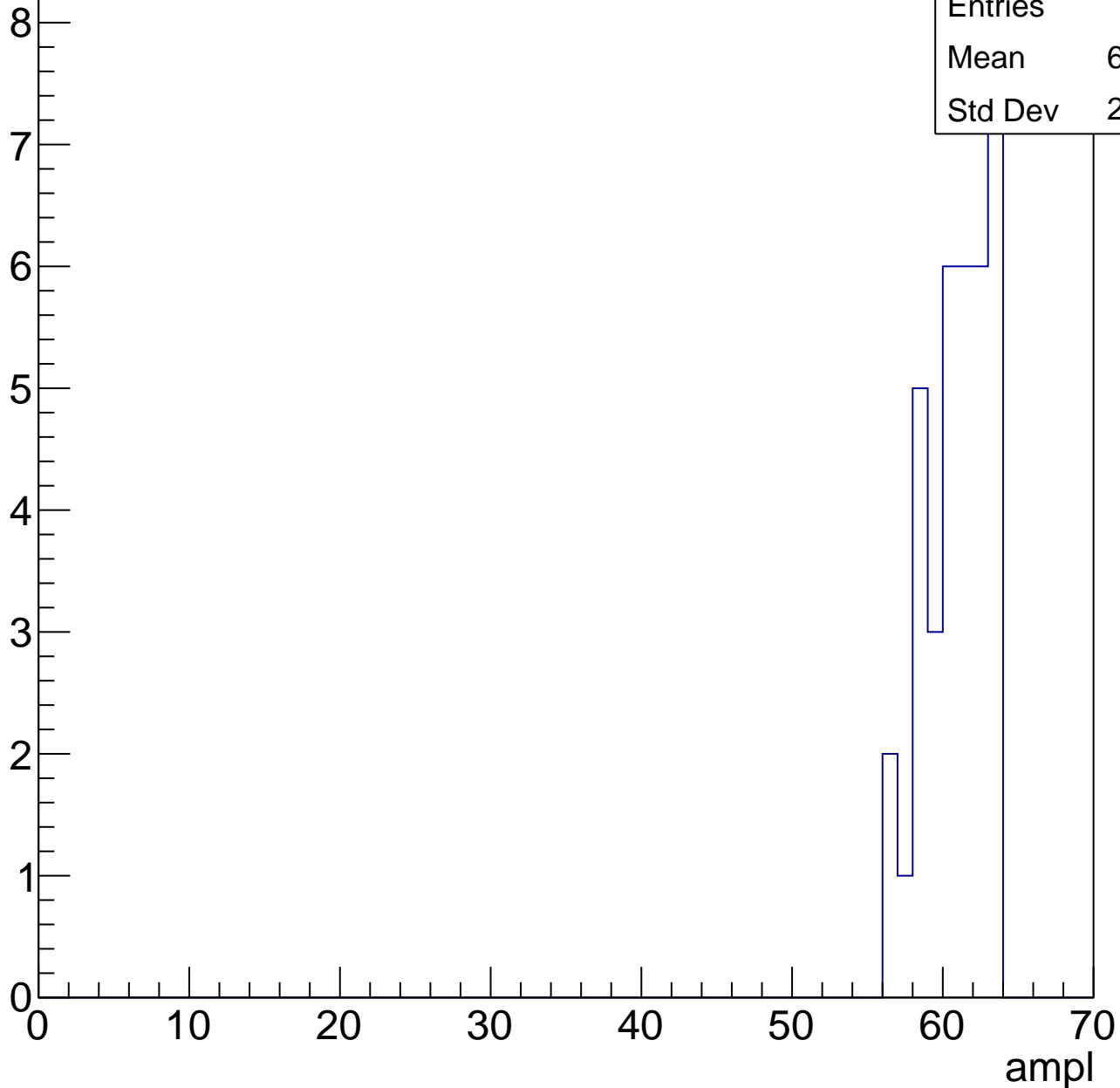


B0L001S, U7-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

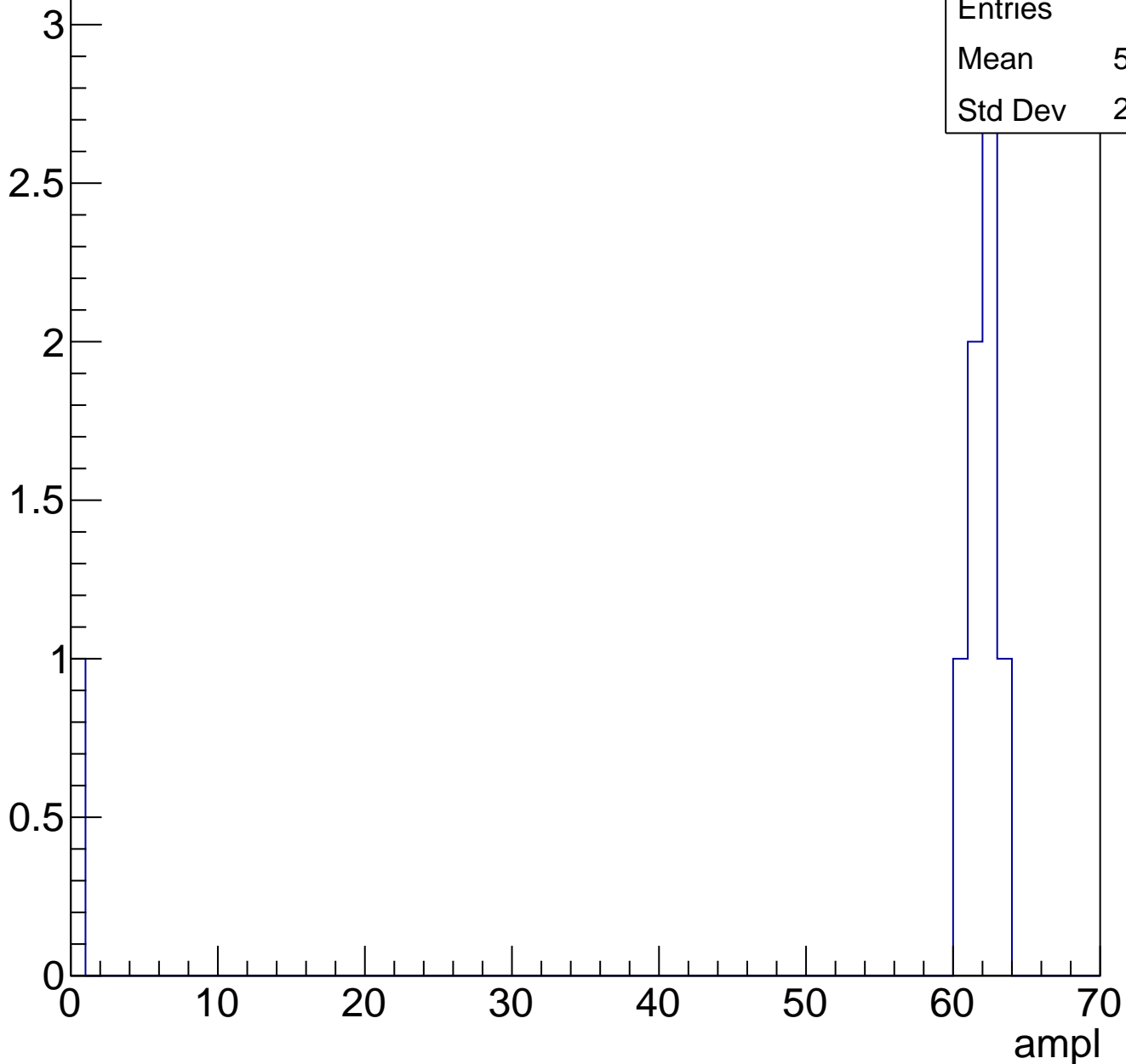
| | |
|---------|-------|
| Entries | 37 |
| Mean | 60.49 |
| Std Dev | 2.062 |



B0L001S, U7-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch110, adc0

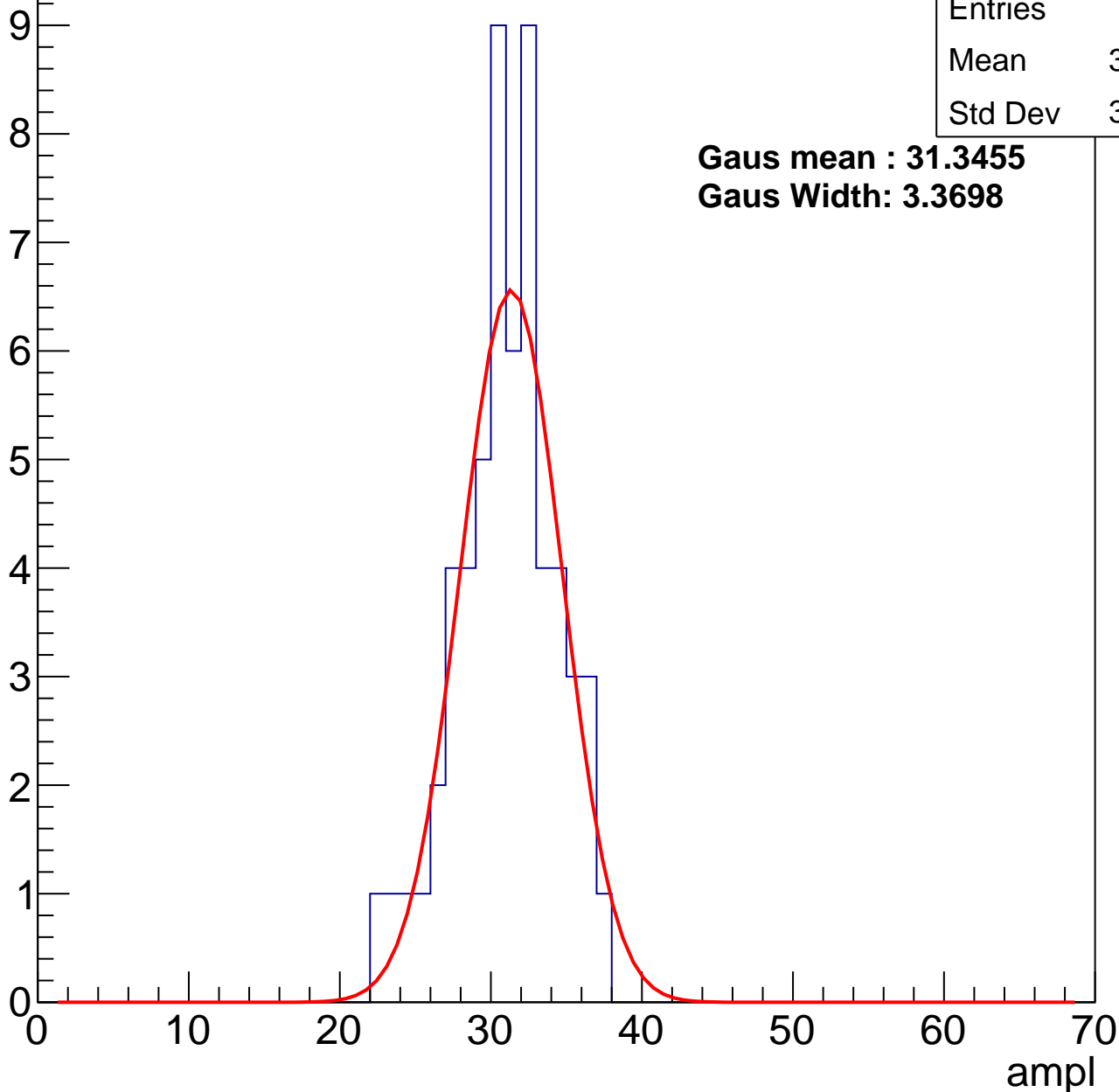
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 30.57 |
| Std Dev | 3.265 |

Gaus mean : 31.3455

Gaus Width: 3.3698



B0L001S, U7-ch110, adc1

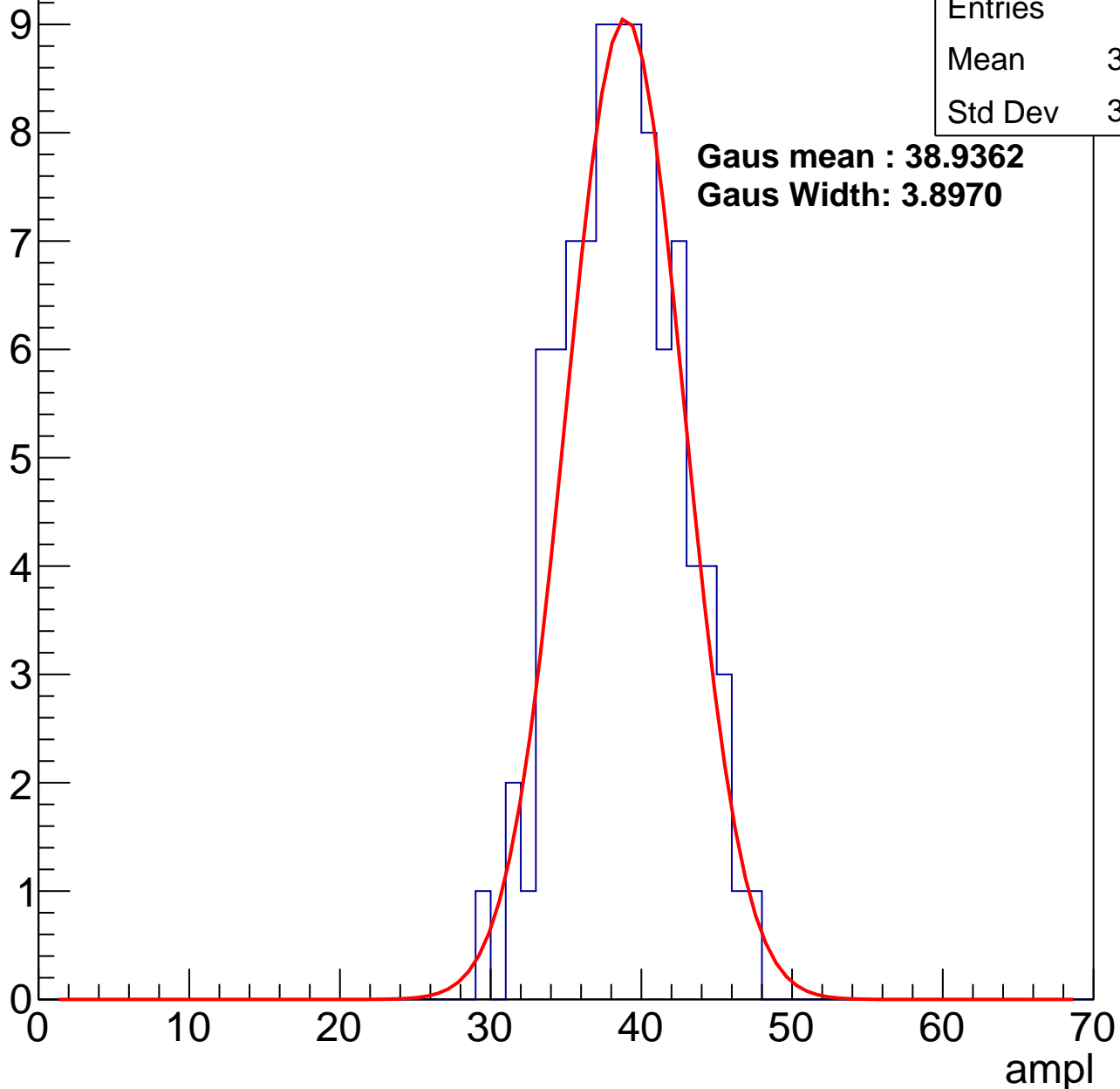
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 91 |
| Mean | 38.29 |
| Std Dev | 3.775 |

Gaus mean : 38.9362

Gaus Width: 3.8970



B0L001S, U7-ch110, adc2

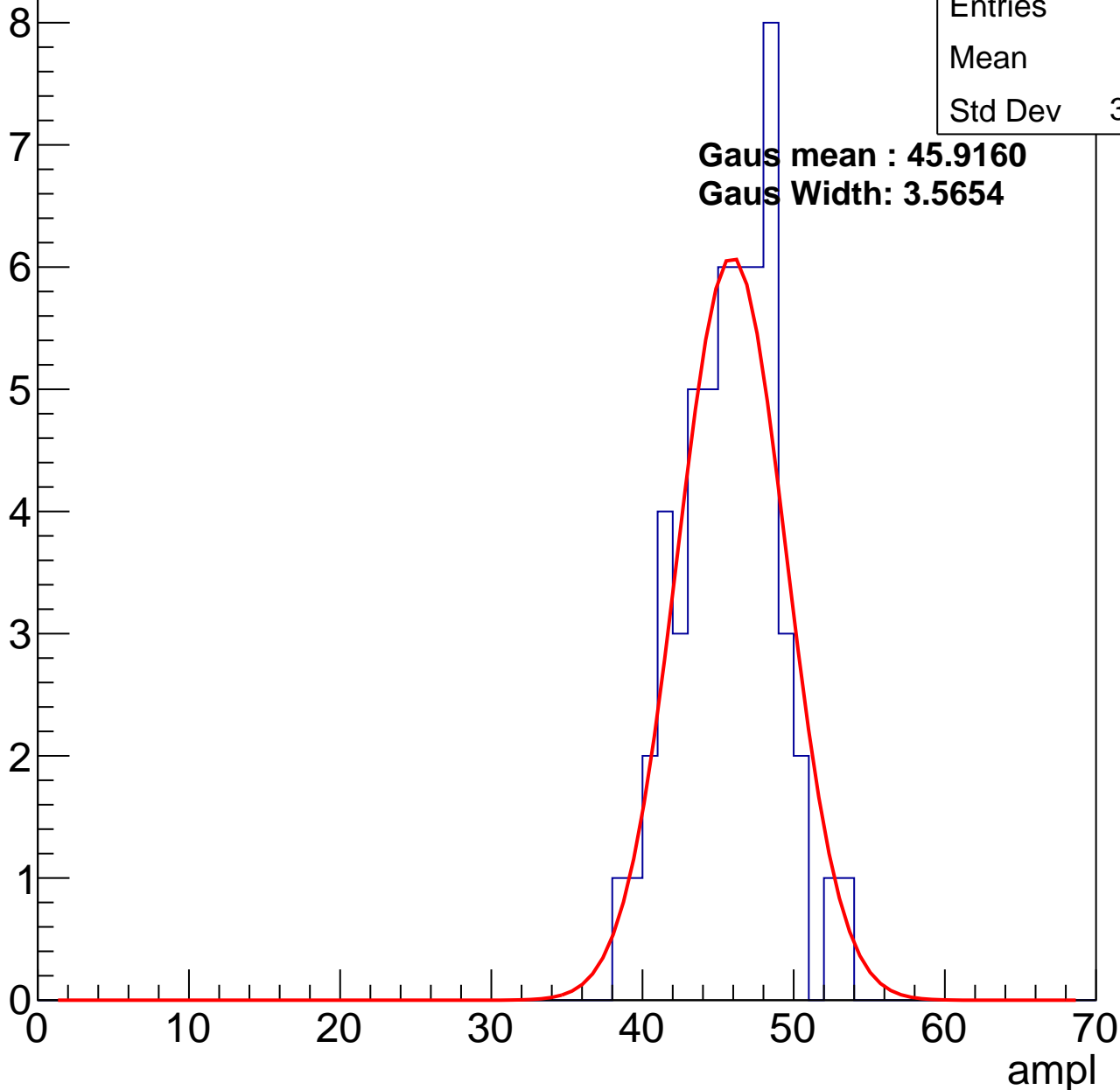
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 45.3 |
| Std Dev | 3.218 |

Gaus mean : 45.9160

Gaus Width: 3.5654



B0L001S, U7-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 71 |
| Mean | 51.86 |
| Std Dev | 3.791 |

Entry

10

8

6

4

2

0

0

10

20

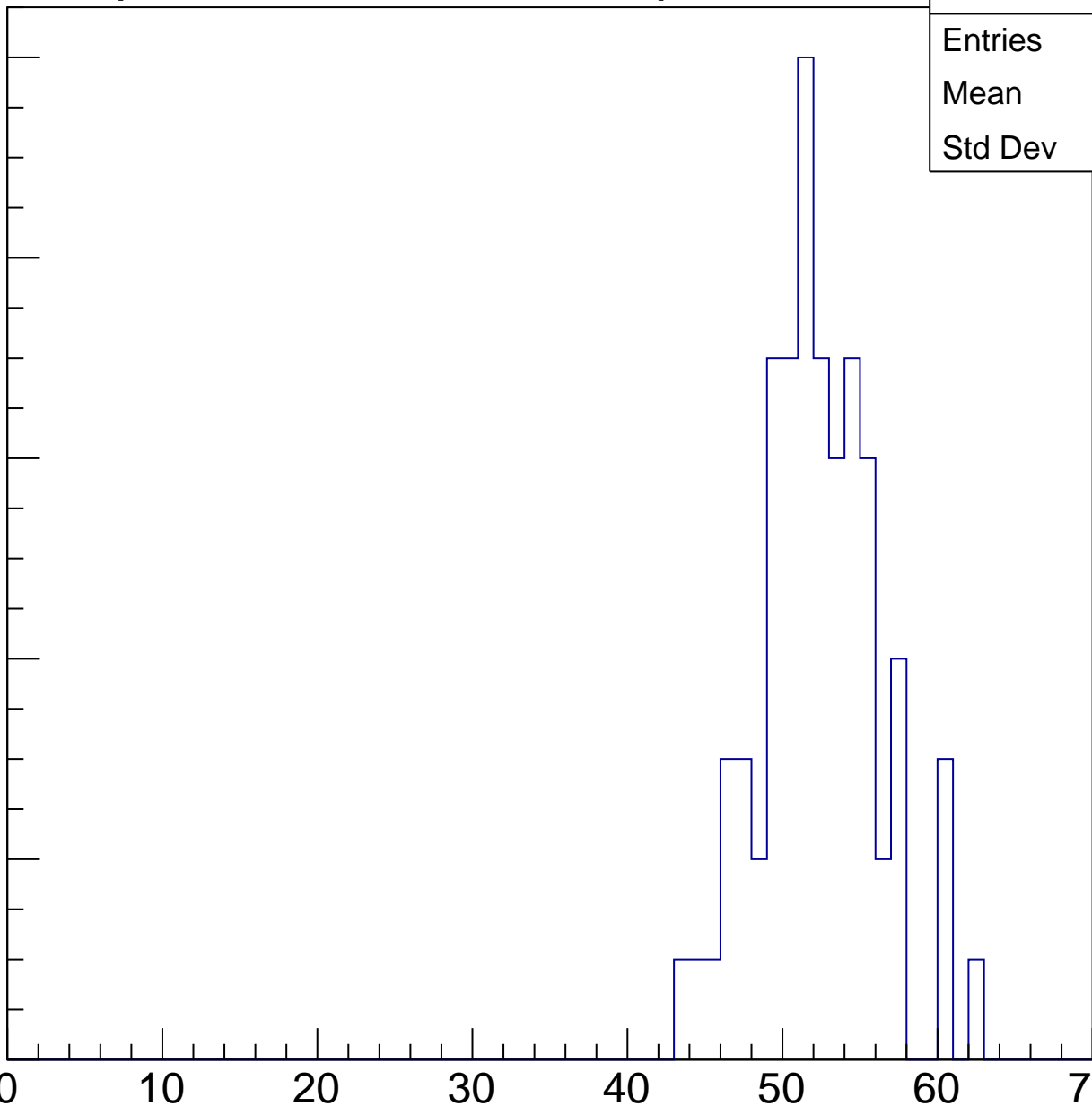
30

40

50

60

ampl

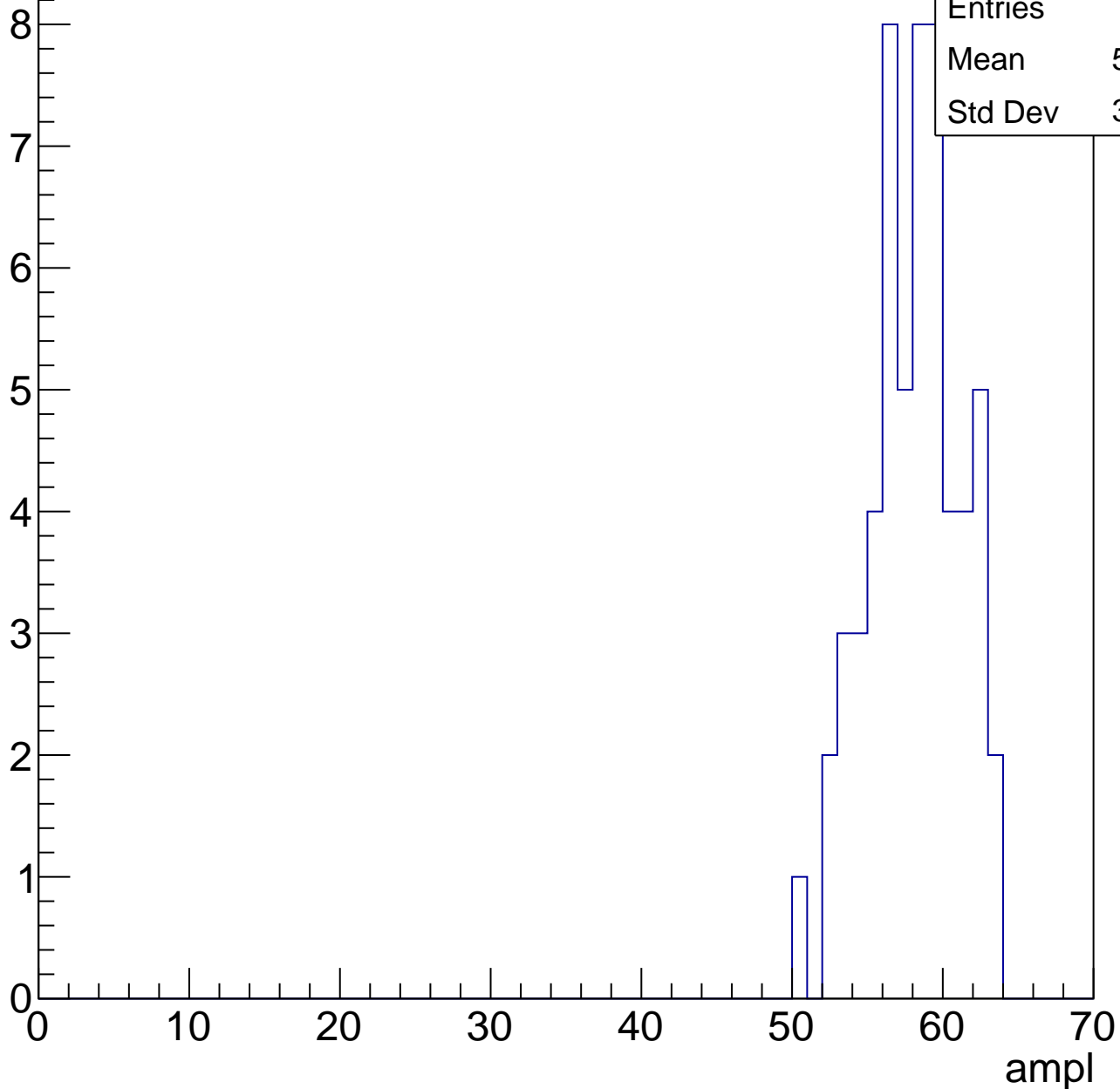


B0L001S, U7-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.61 |
| Std Dev | 3.001 |

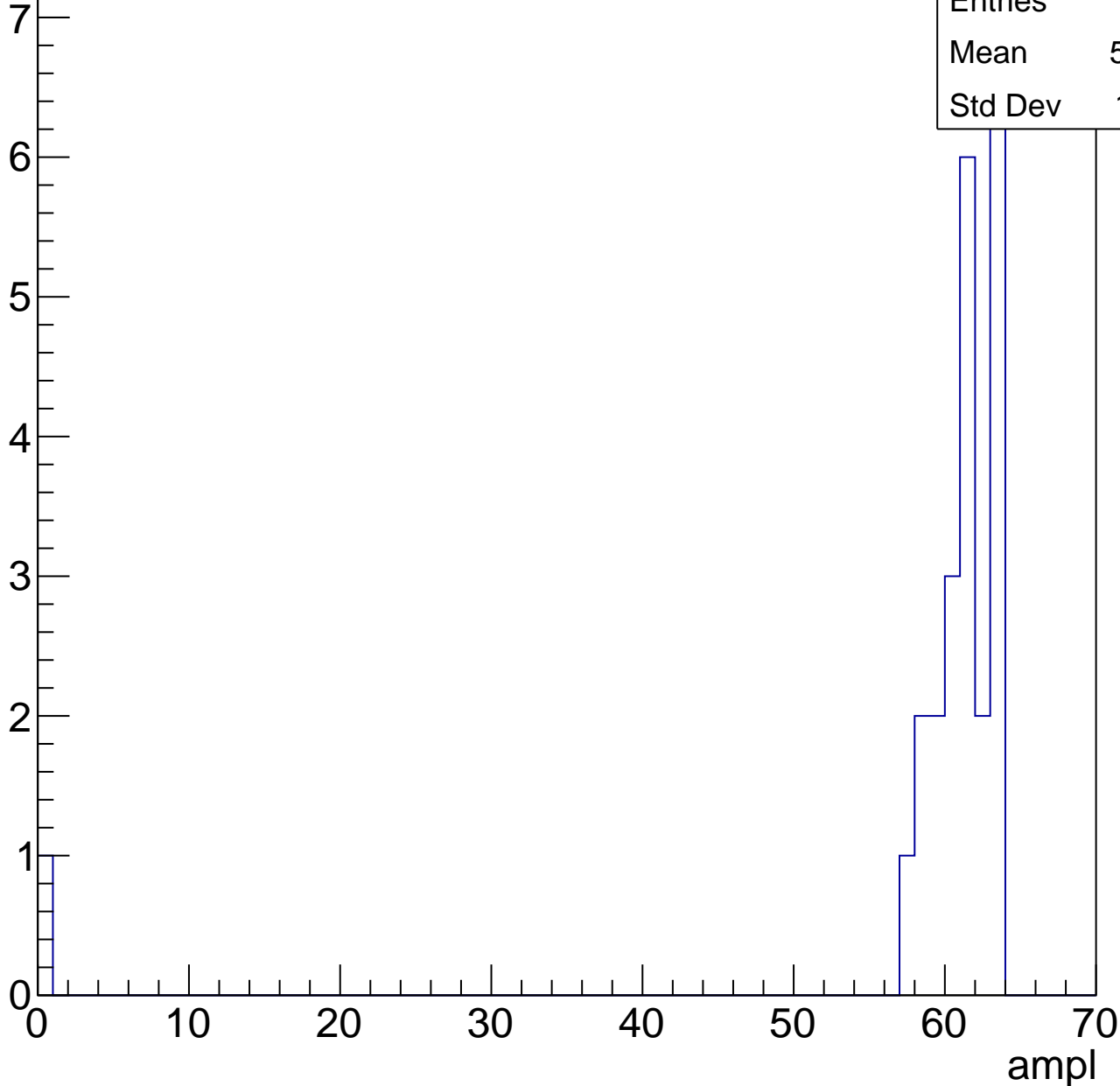


B0L001S, U7-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 58.42 |
| Std Dev | 12.31 |



B0L001S, U7-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 62.33 |
| Std Dev | 0.4714 |

ampl



B0L001S, U7-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch111, adc0

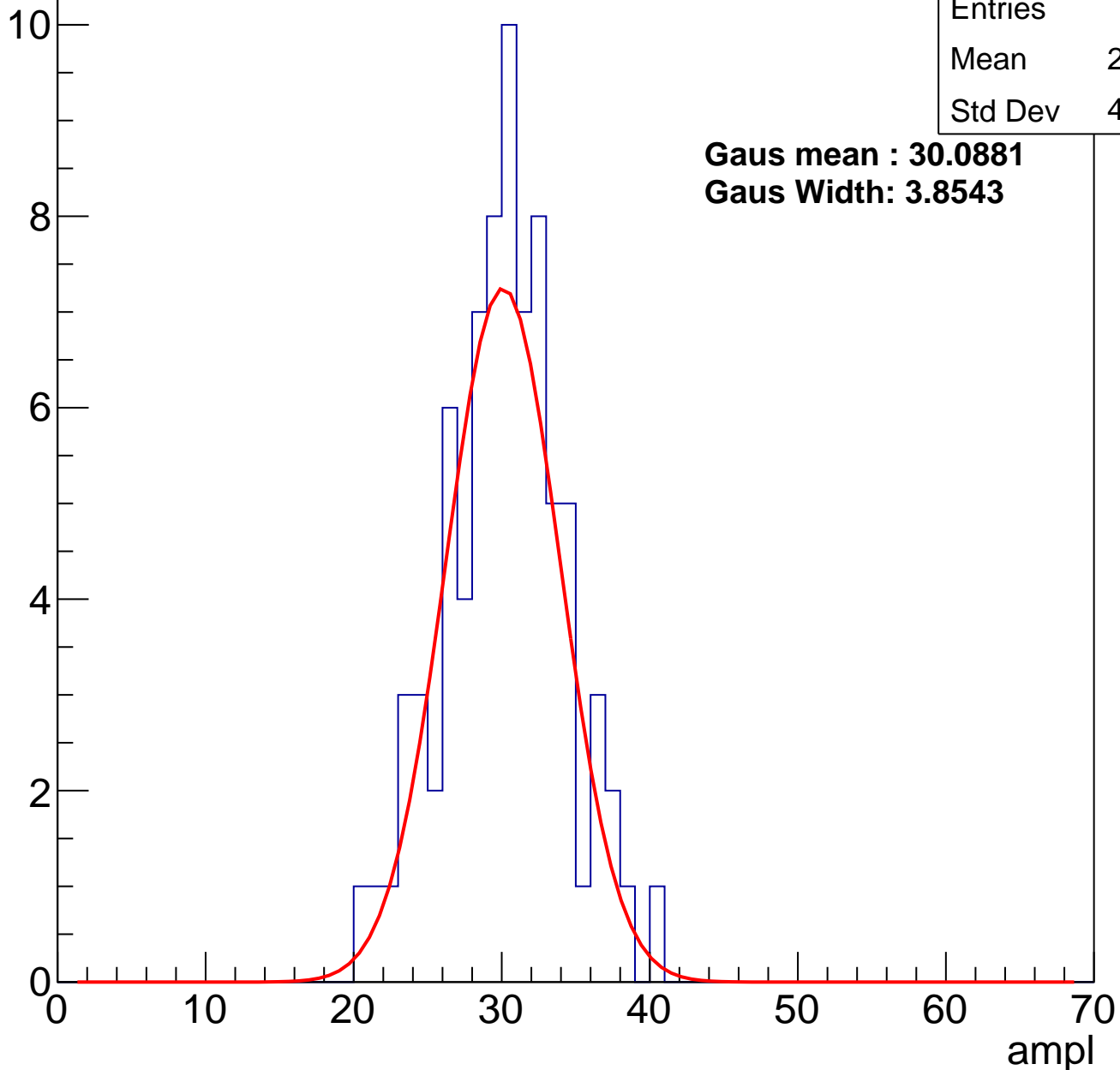
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 79 |
| Mean | 29.73 |
| Std Dev | 4.012 |

Gaus mean : 30.0881

Gaus Width: 3.8543

Entry



B0L001S, U7-ch111, adc1

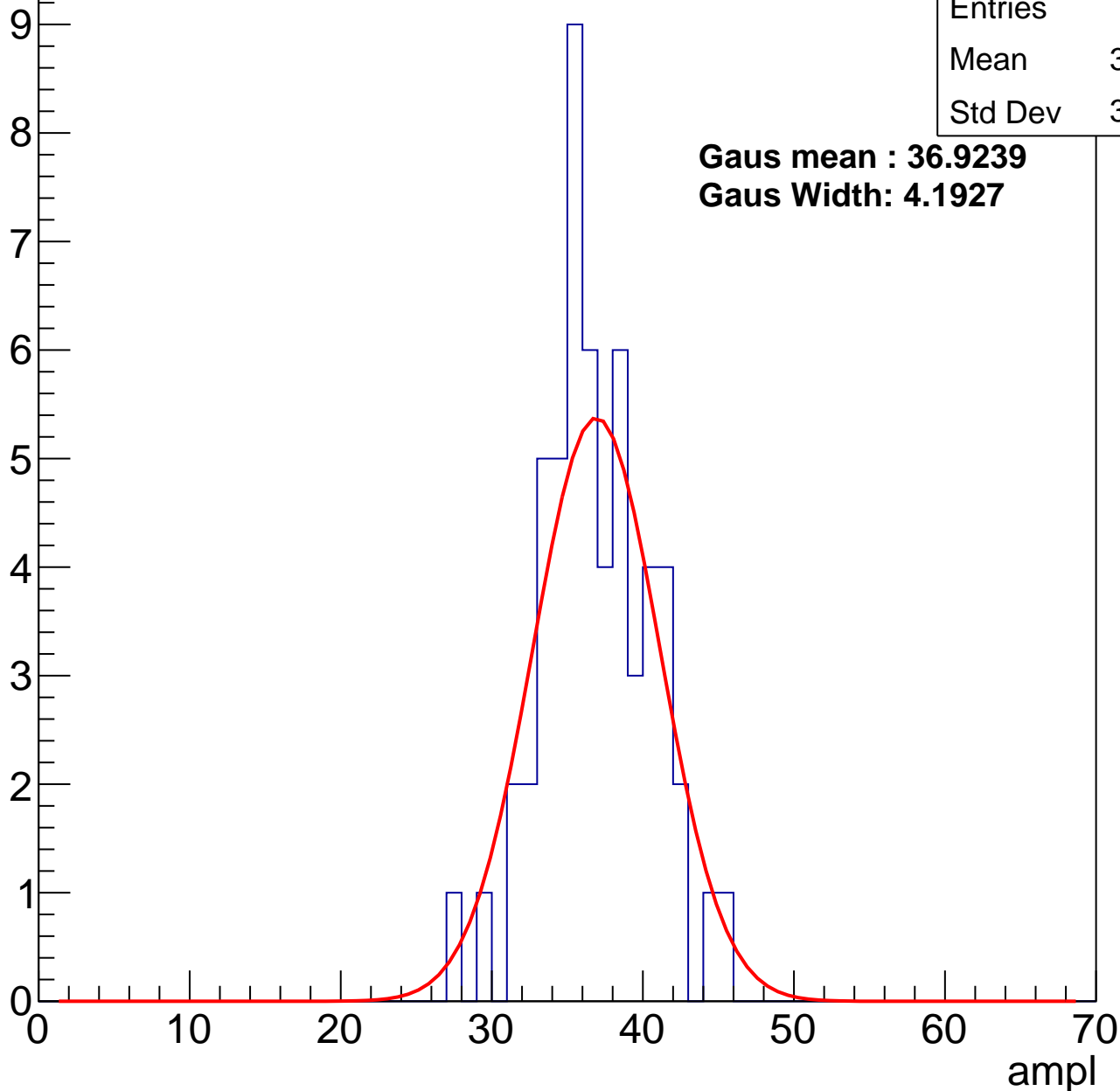
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 36.39 |
| Std Dev | 3.569 |

Gaus mean : 36.9239

Gaus Width: 4.1927



B0L001S, U7-ch111, adc2

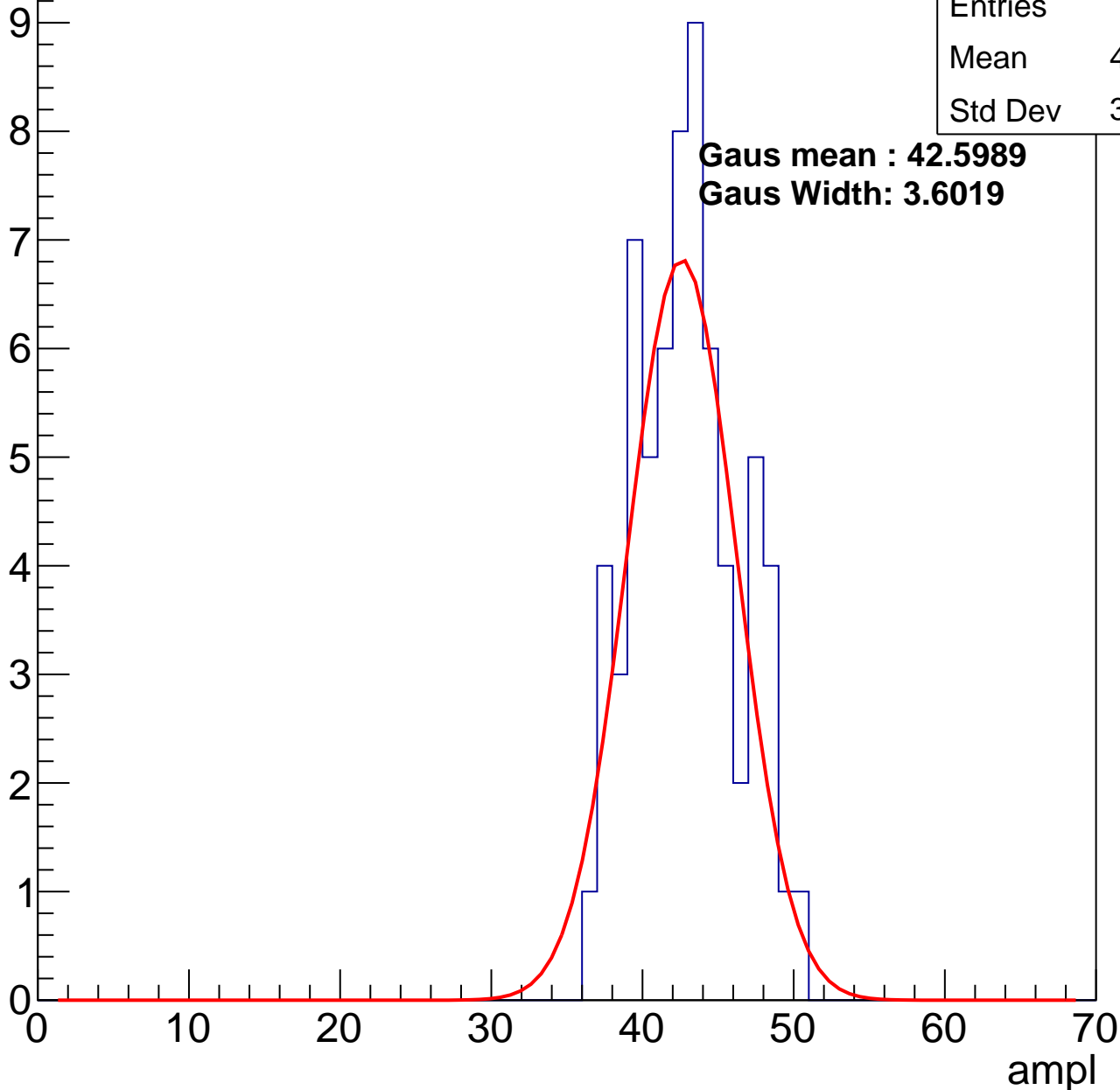
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 42.45 |
| Std Dev | 3.354 |

Gaus mean : 42.5989

Gaus Width: 3.6019

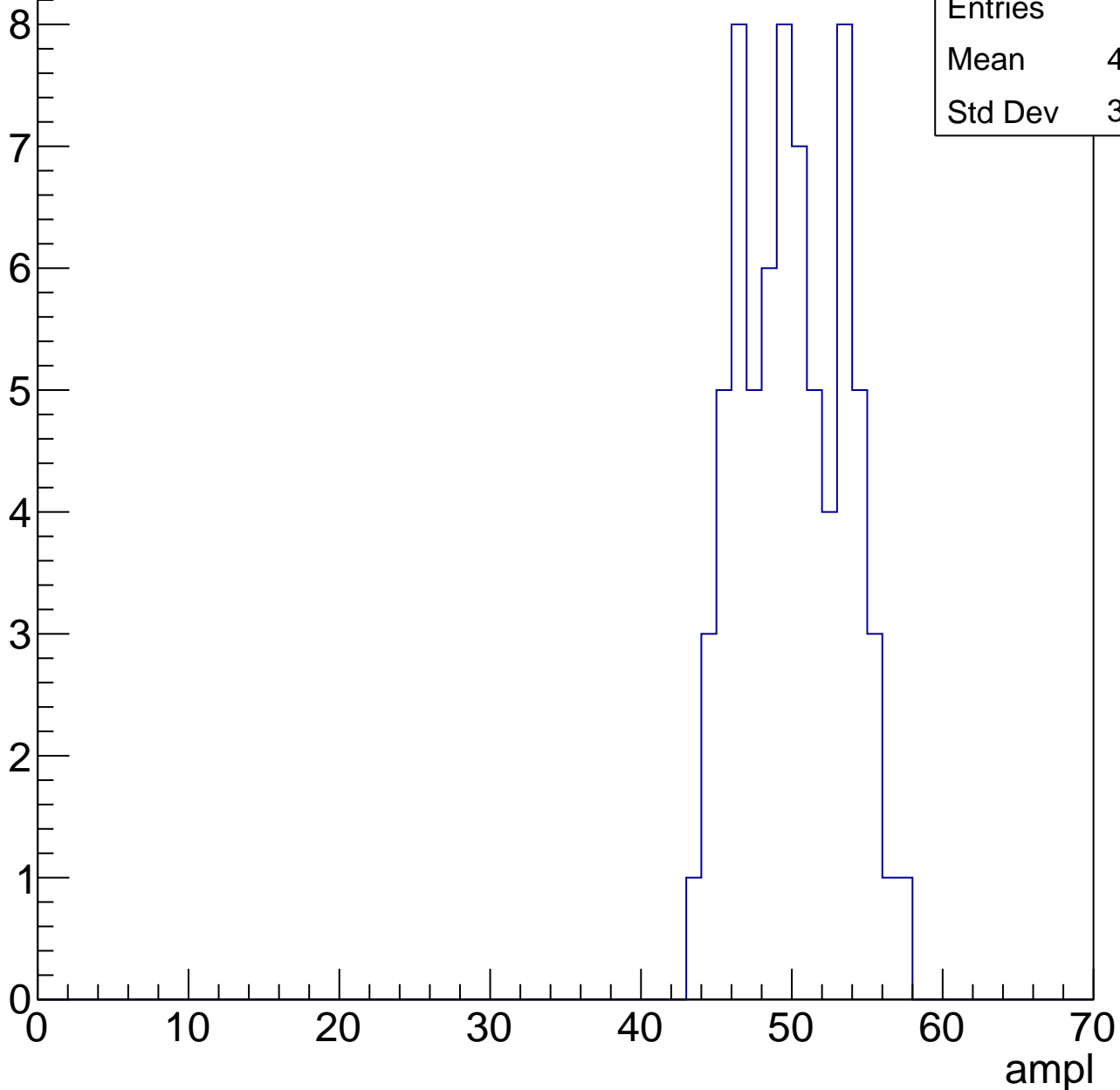


B0L001S, U7-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.54 |
| Std Dev | 3.392 |

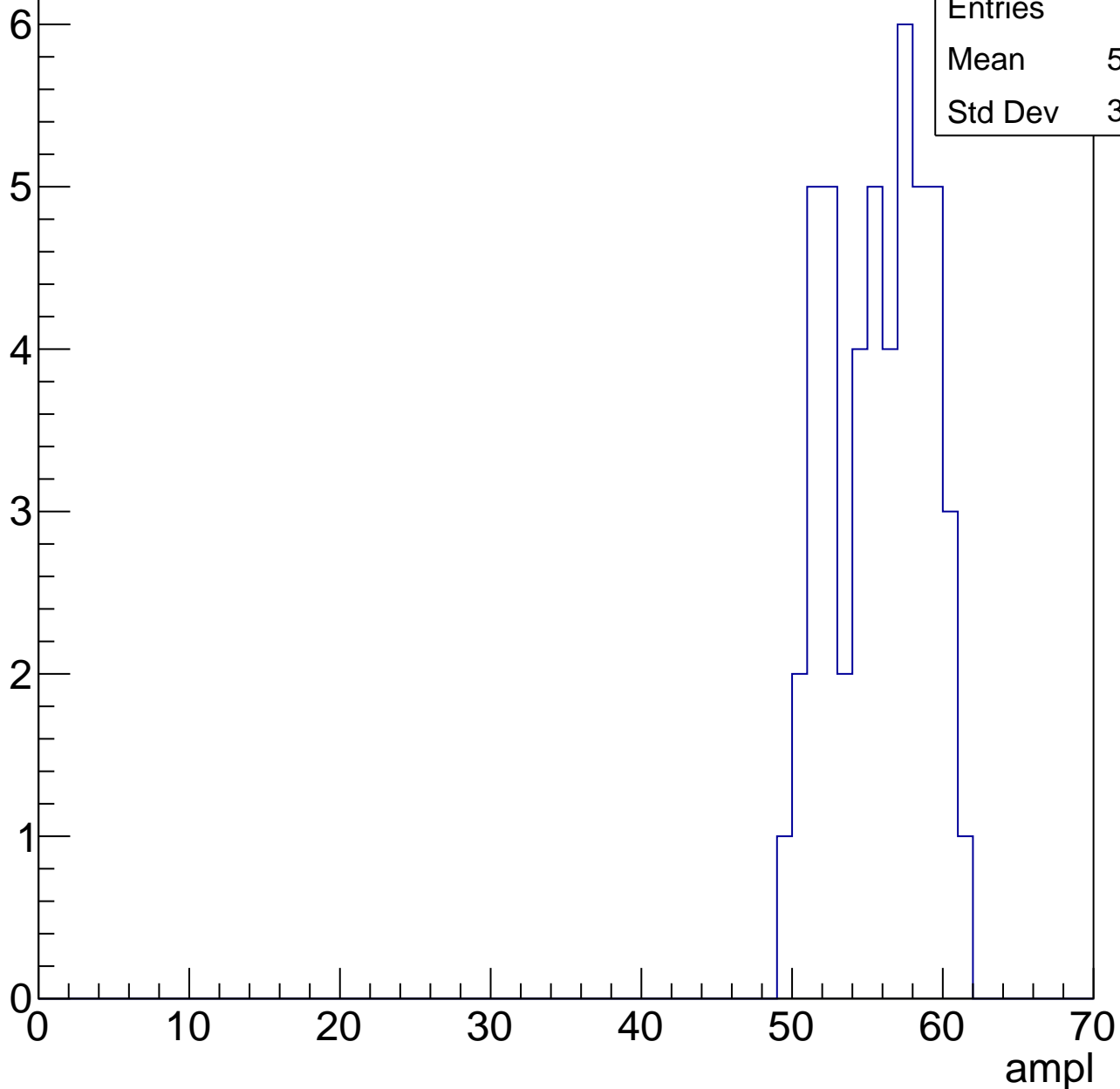


B0L001S, U7-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 55.27 |
| Std Dev | 3.174 |

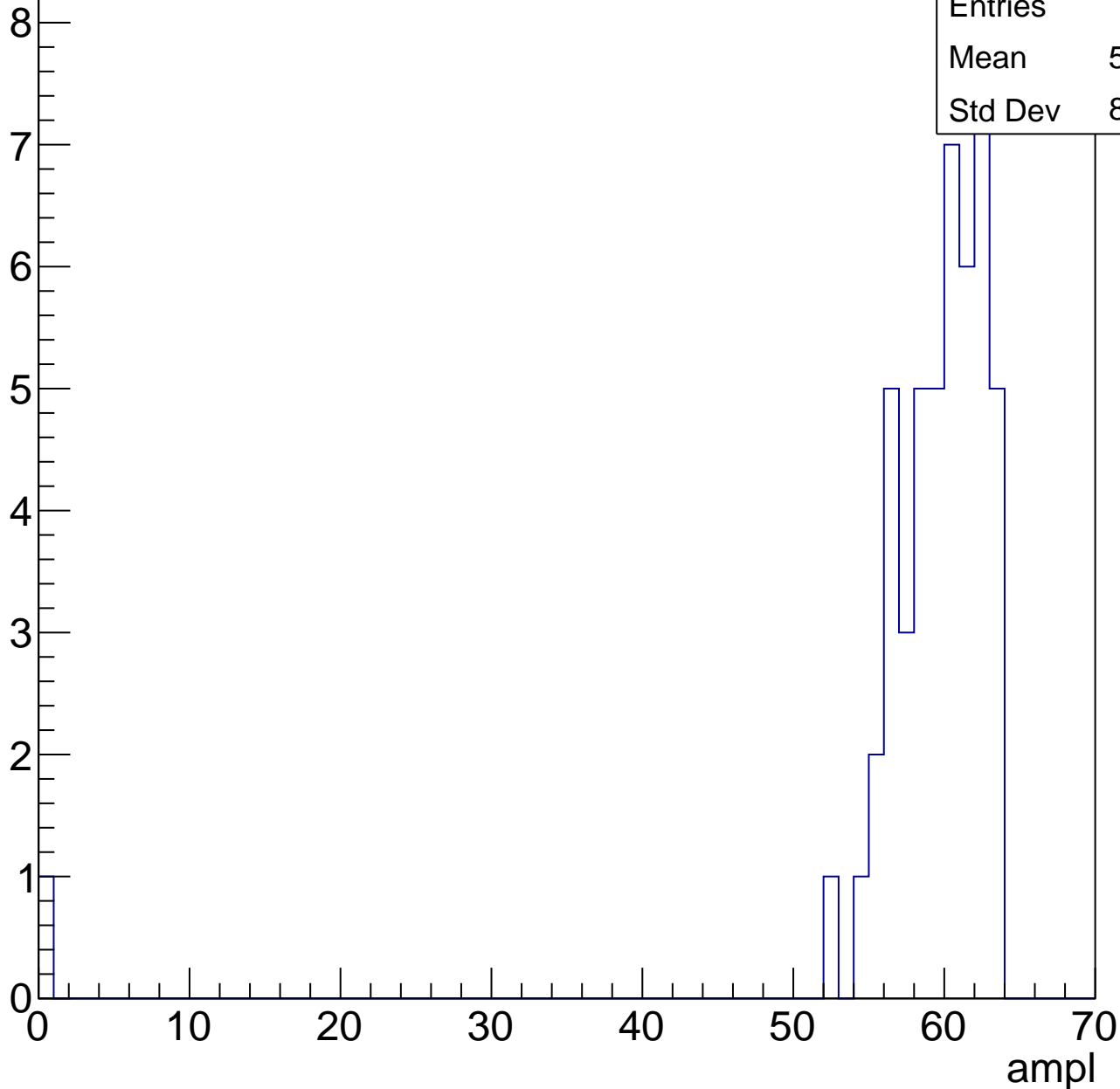


B0L001S, U7-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.14 |
| Std Dev | 8.802 |

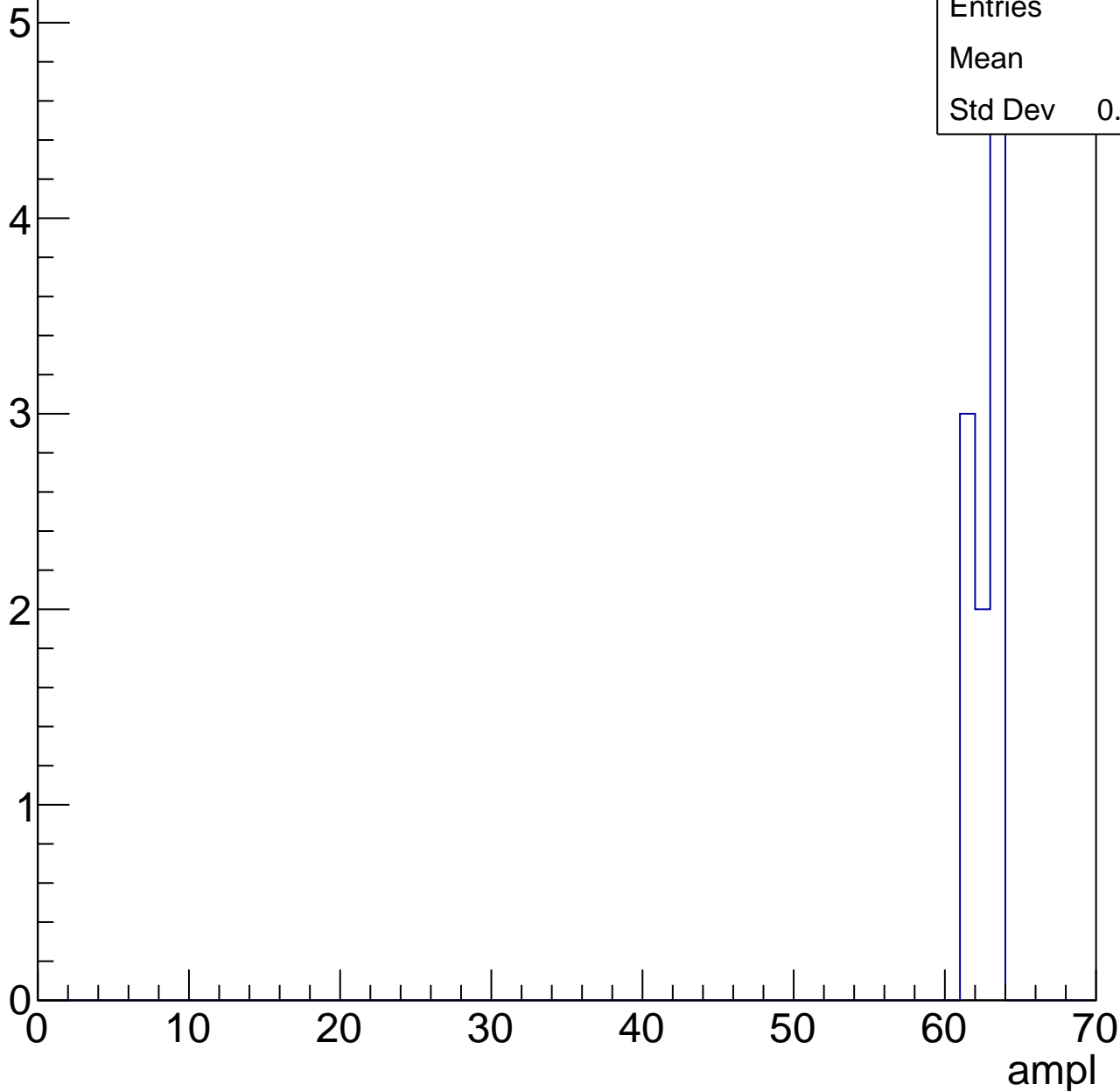


B0L001S, U7-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|--------|
| Entries | 10 |
| Mean | 62.2 |
| Std Dev | 0.8718 |



B0L001S, U7-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch112, adc0

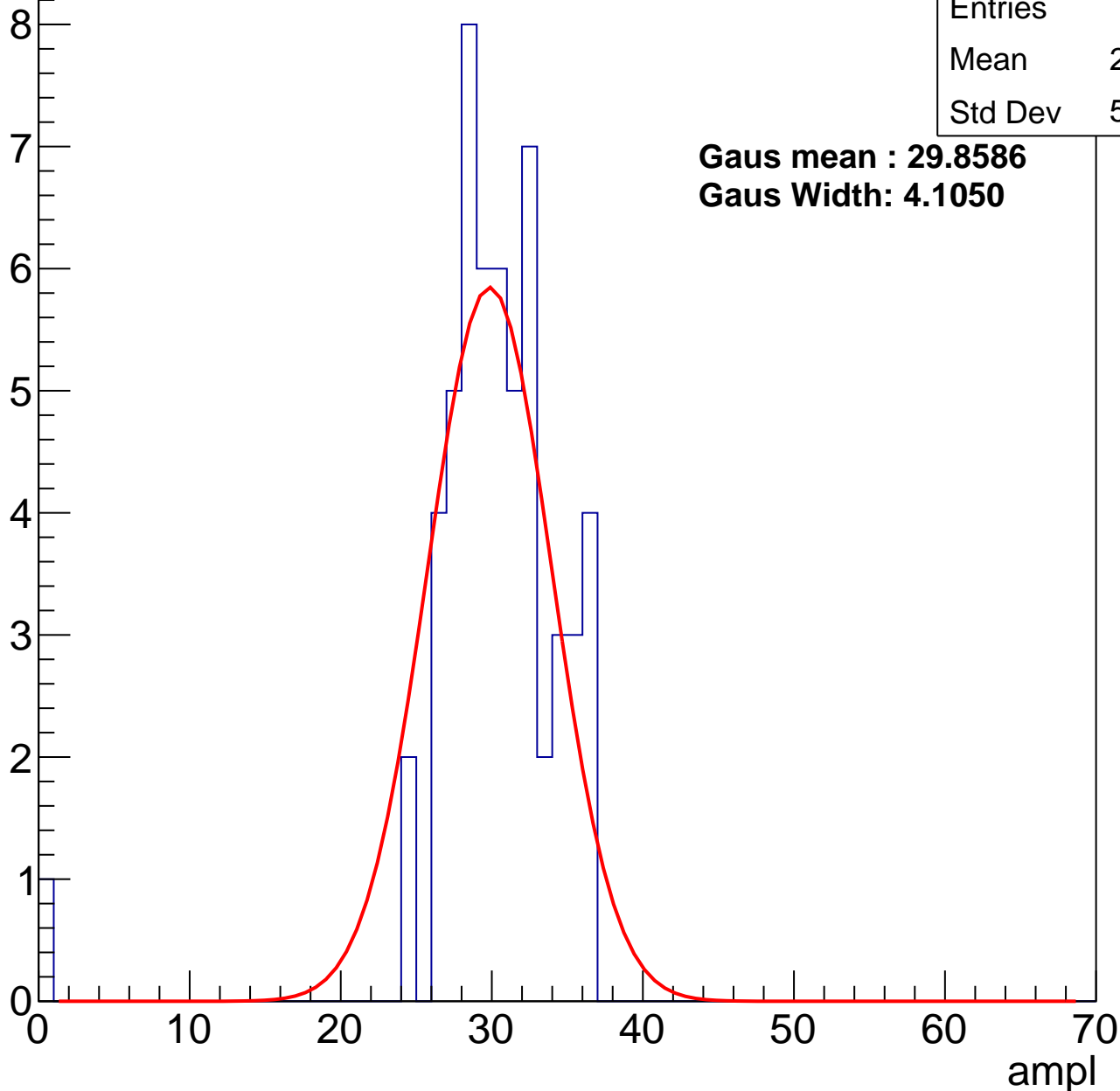
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 29.66 |
| Std Dev | 5.054 |

Gaus mean : 29.8586

Gaus Width: 4.1050



B0L001S, U7-ch112, adc1

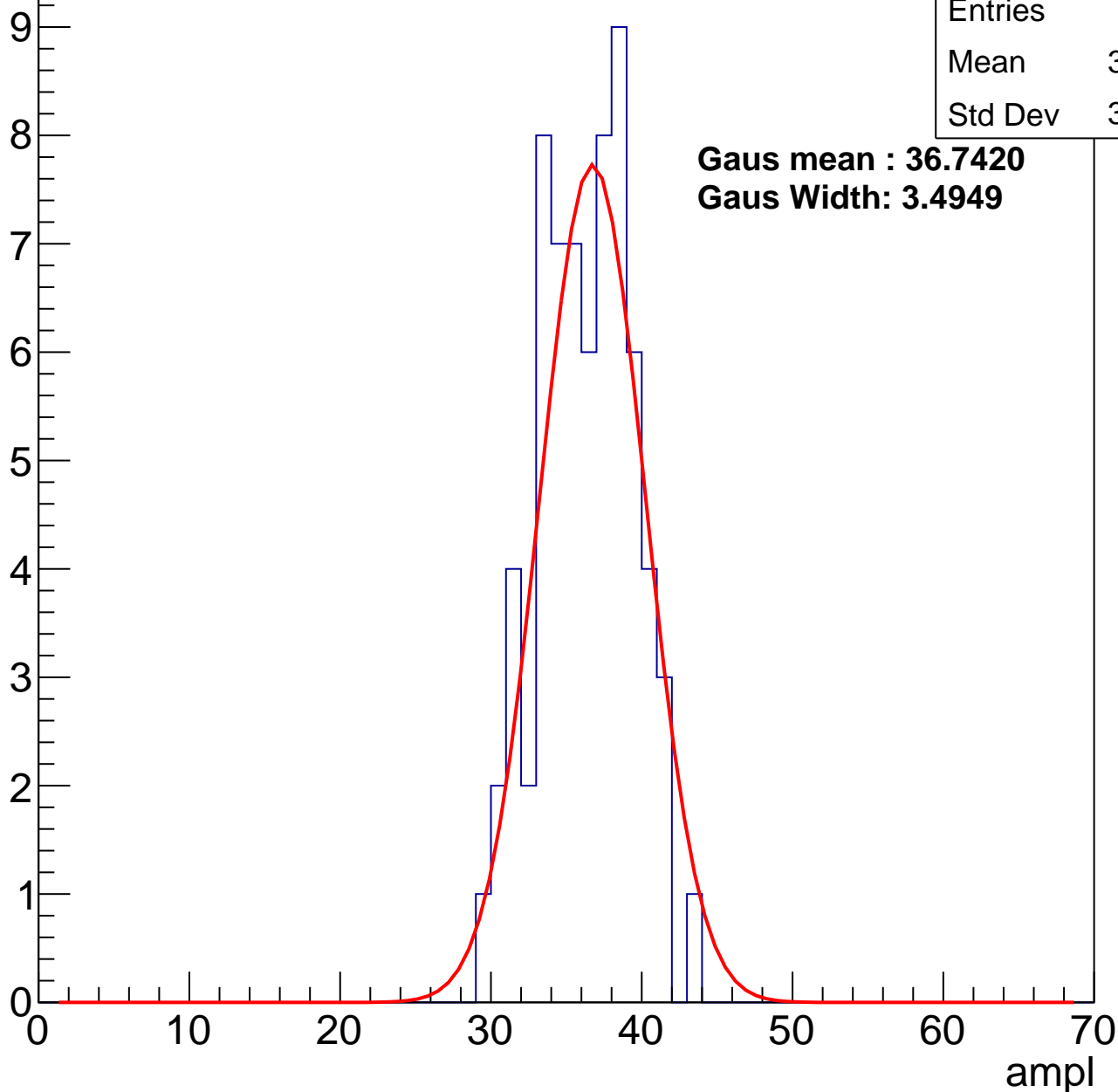
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 35.85 |
| Std Dev | 3.079 |

Gaus mean : 36.7420

Gaus Width: 3.4949



B0L001S, U7-ch112, adc2

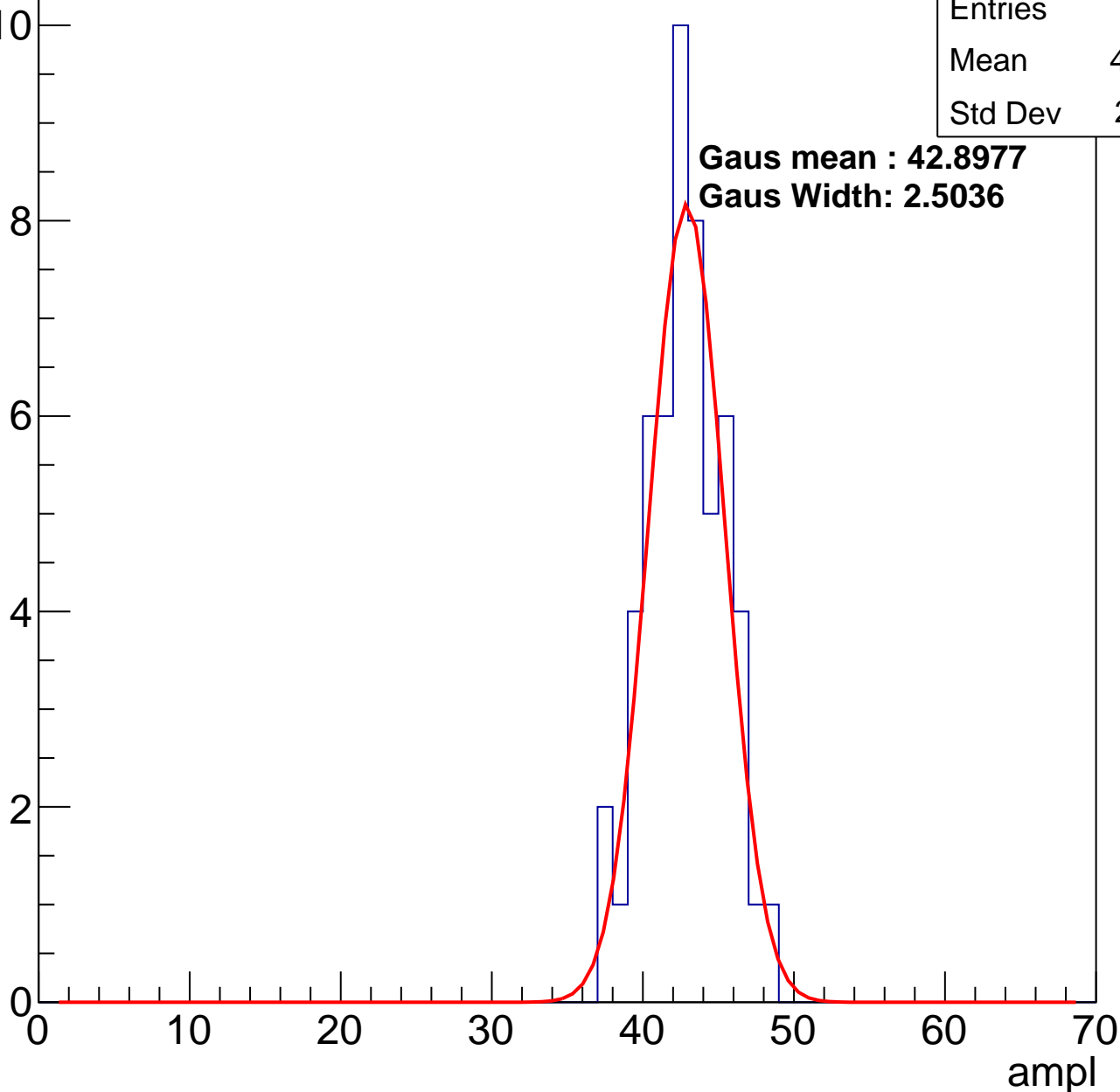
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 42.35 |
| Std Dev | 2.481 |

Gaus mean : 42.8977

Gaus Width: 2.5036

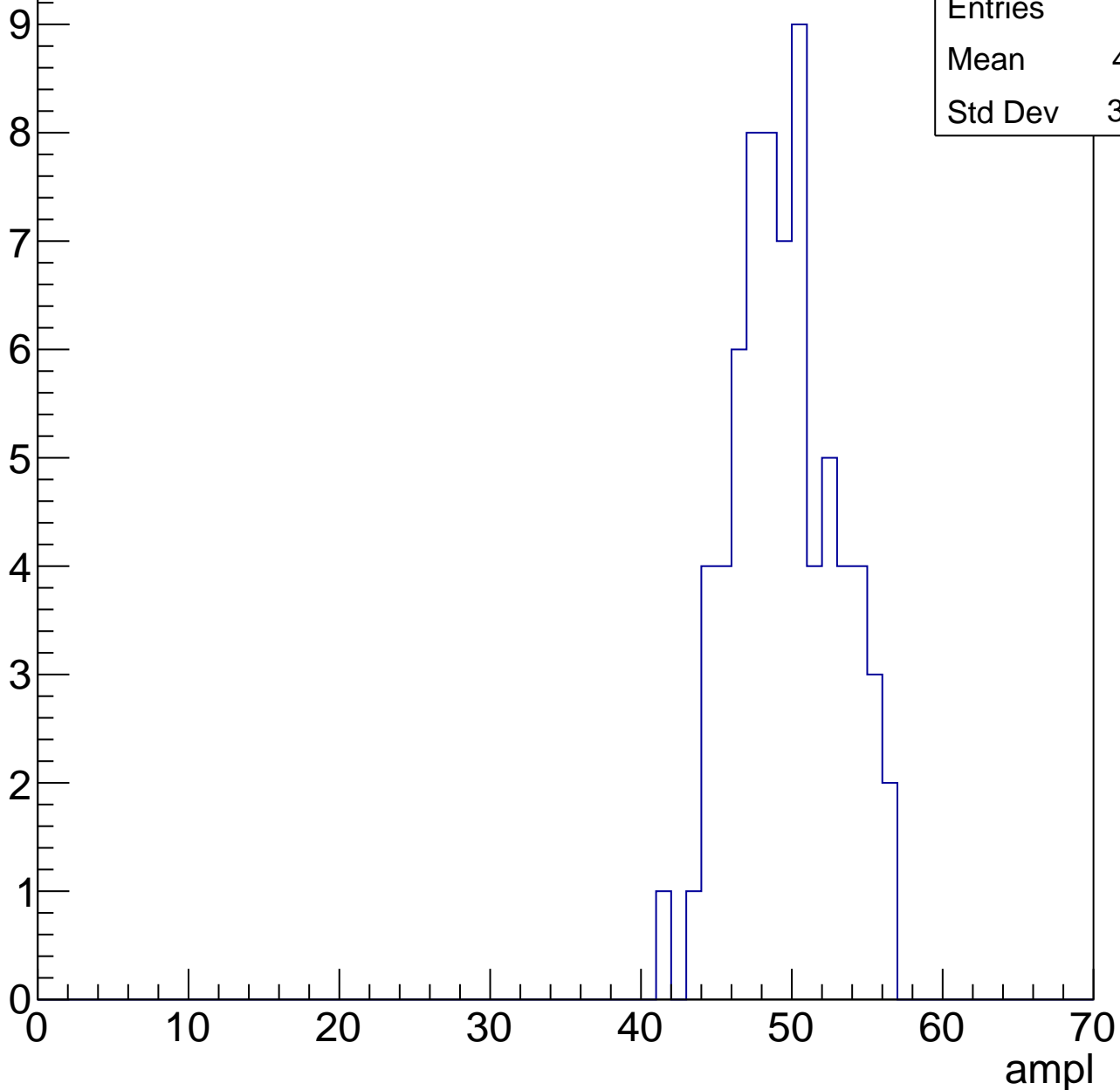


B0L001S, U7-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.11 |
| Std Dev | 3.374 |

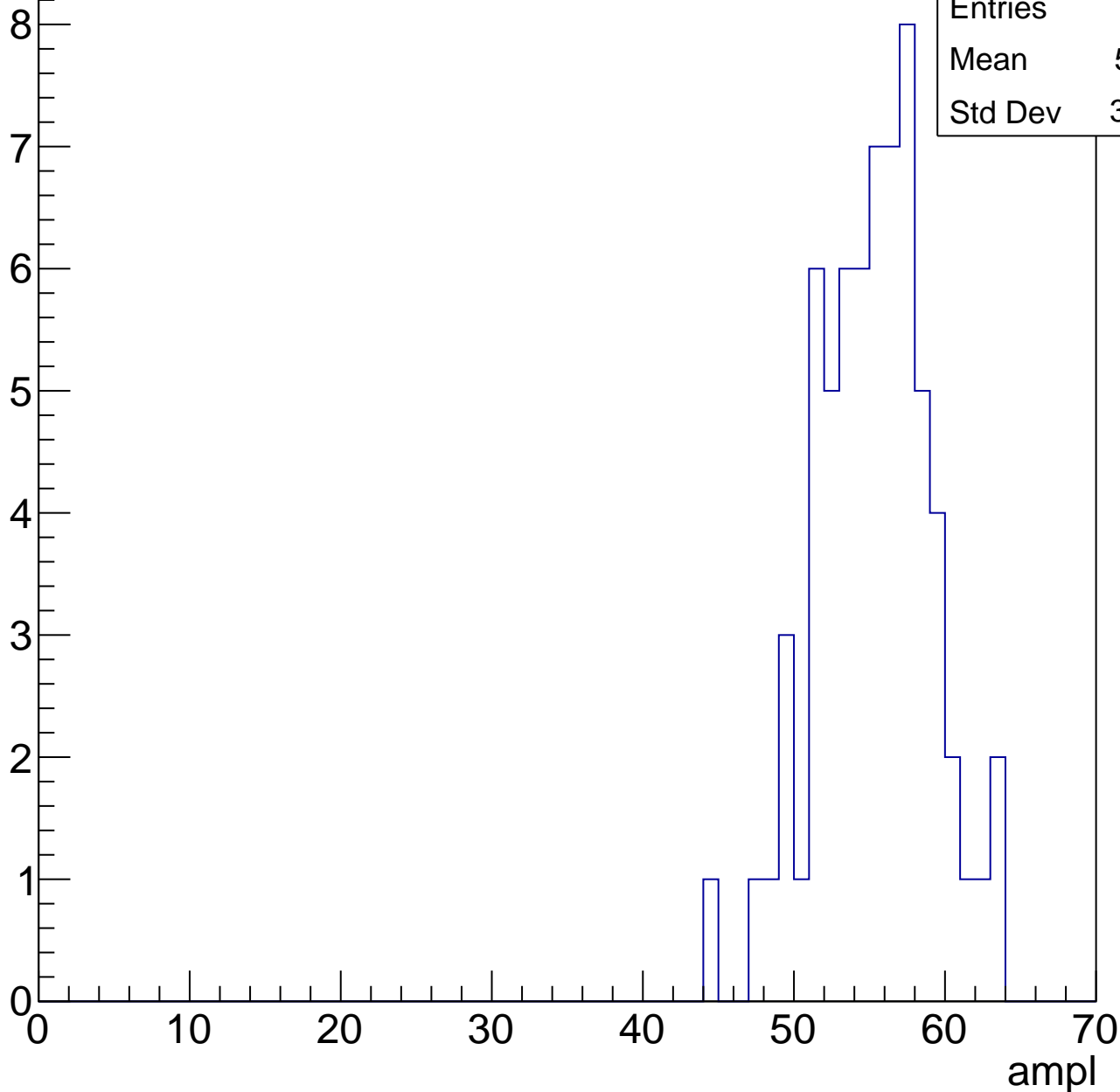


B0L001S, U7-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 54.81 |
| Std Dev | 3.759 |

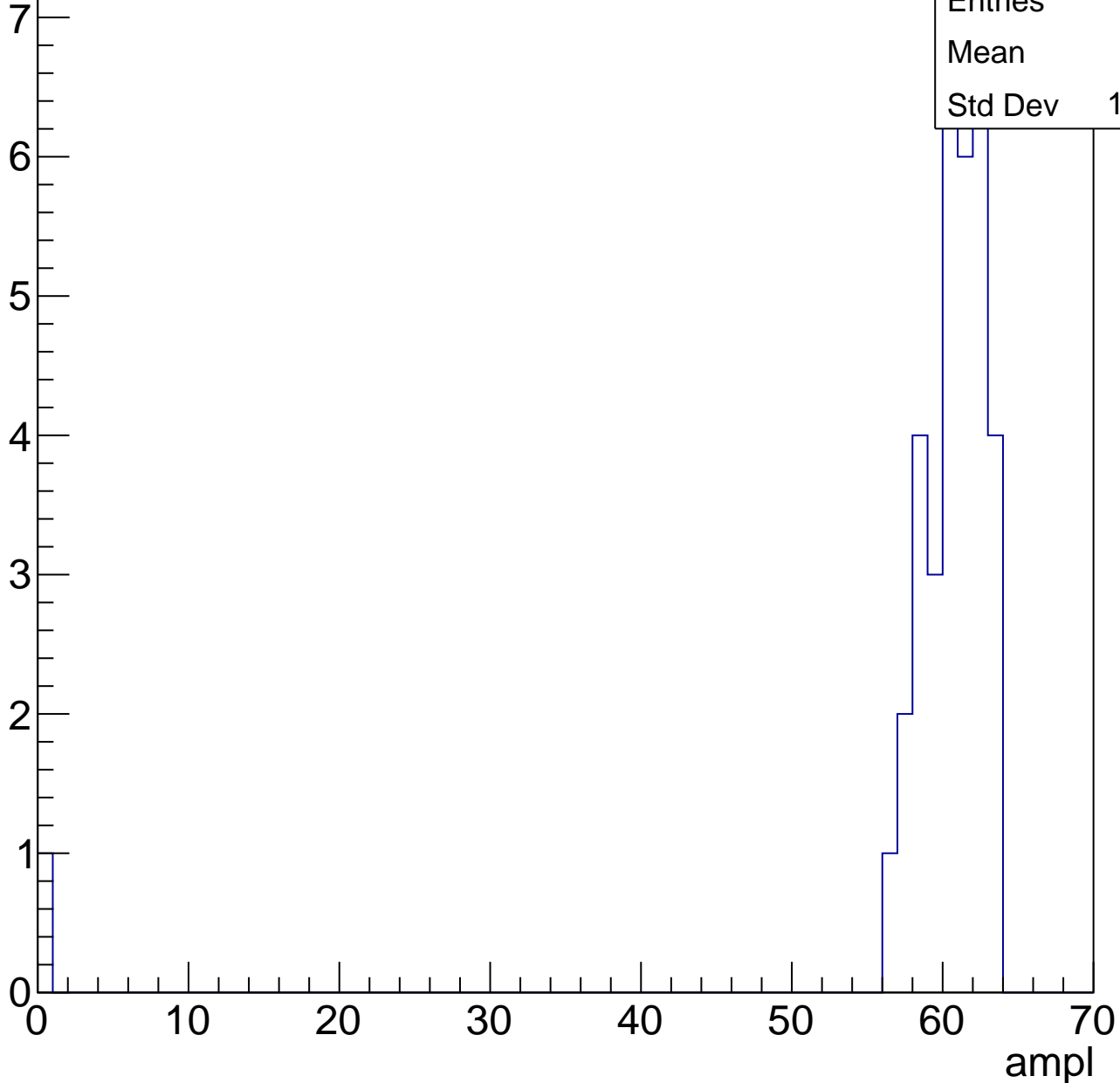


B0L001S, U7-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

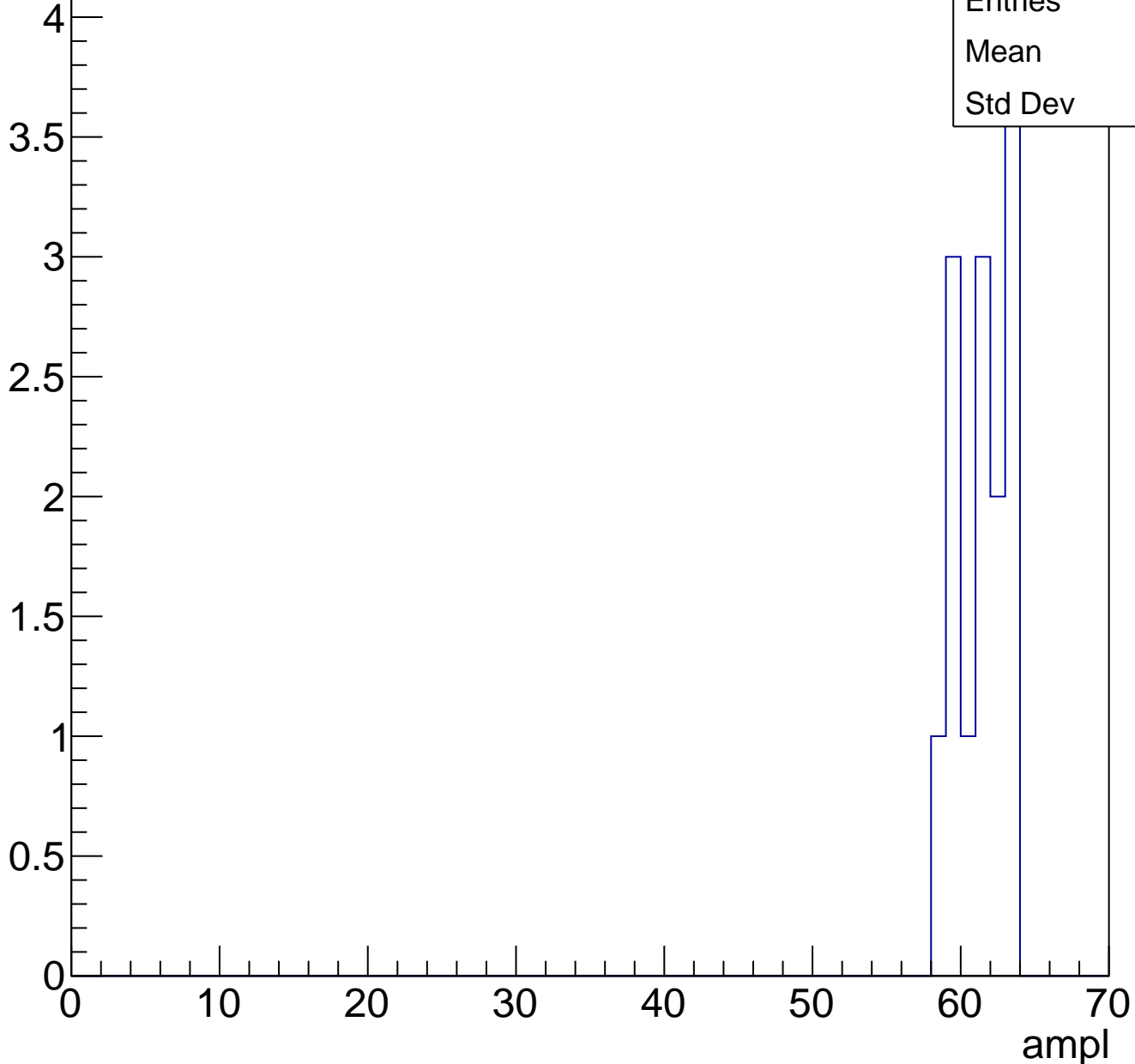
| | |
|---------|-------|
| Entries | 35 |
| Mean | 58.6 |
| Std Dev | 10.22 |



B0L001S, U7-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|------|
| Entries | 14 |
| Mean | 61 |
| Std Dev | 1.69 |

B0L001S, U7-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch113, adc0

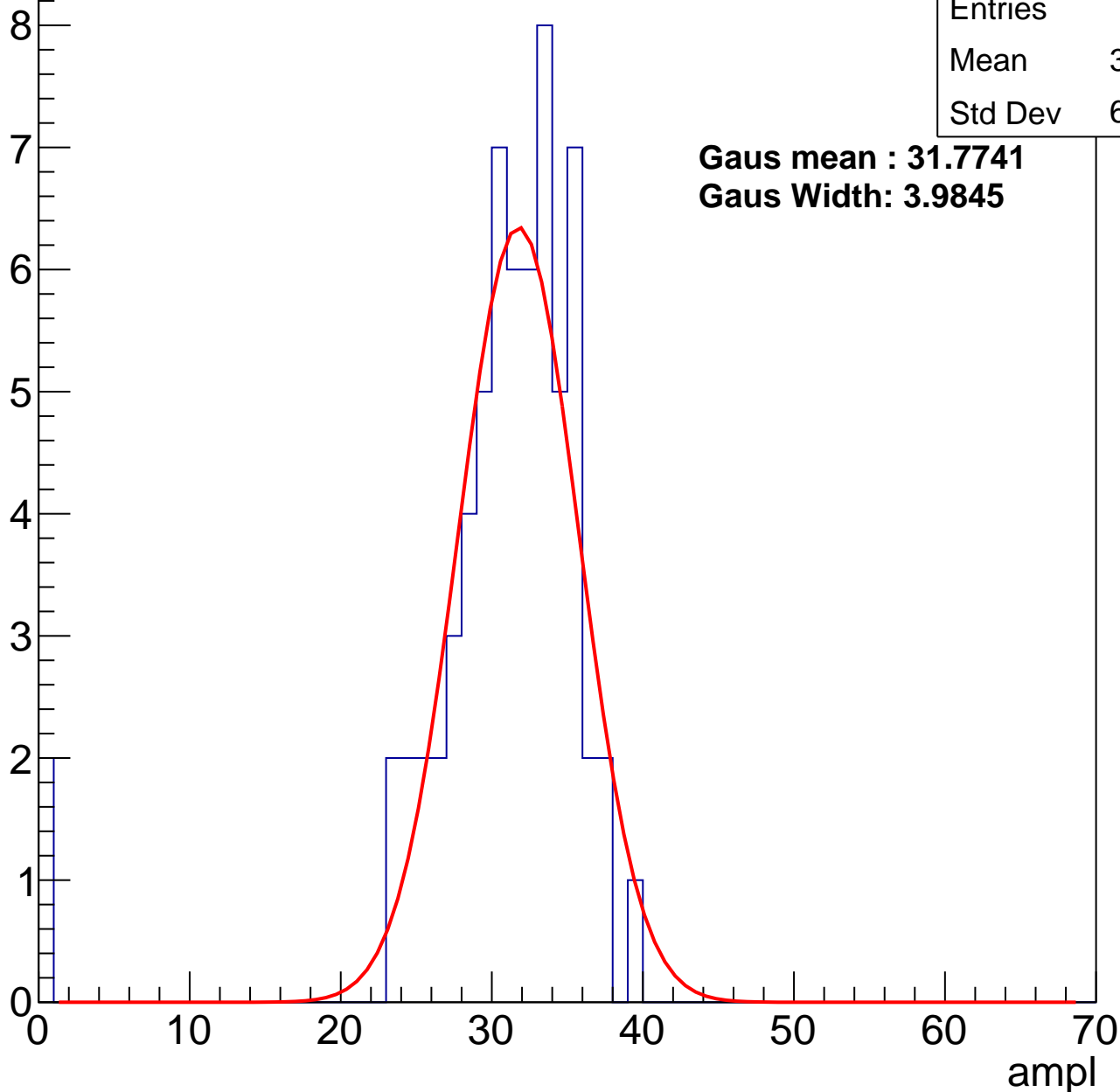
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 30.09 |
| Std Dev | 6.405 |

Gaus mean : 31.7741

Gaus Width: 3.9845

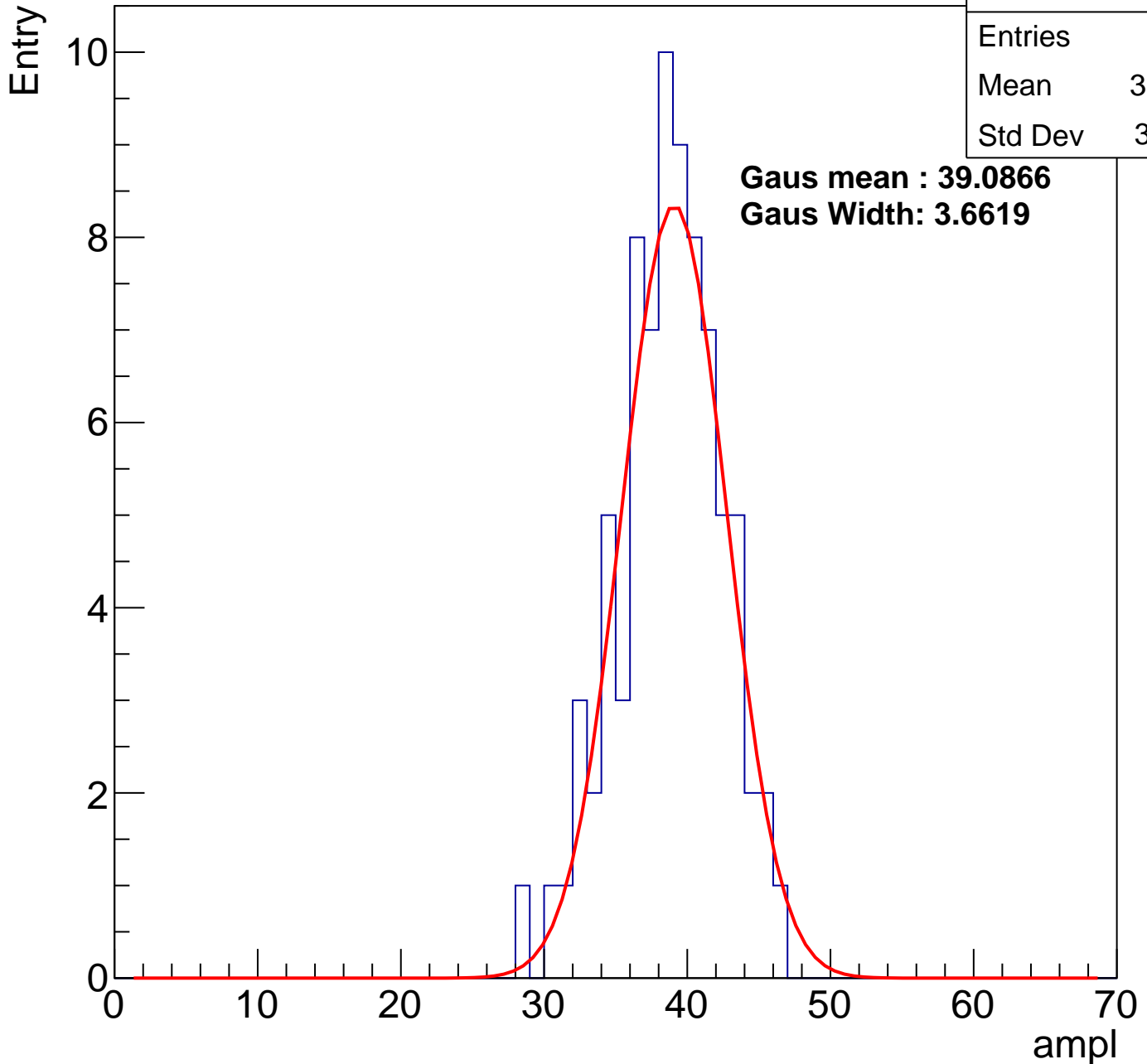


B0L001S, U7-ch113, adc1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 80 |
| Mean | 38.25 |
| Std Dev | 3.631 |

Gaus mean : 39.0866
Gaus Width: 3.6619



B0L001S, U7-ch113, adc2

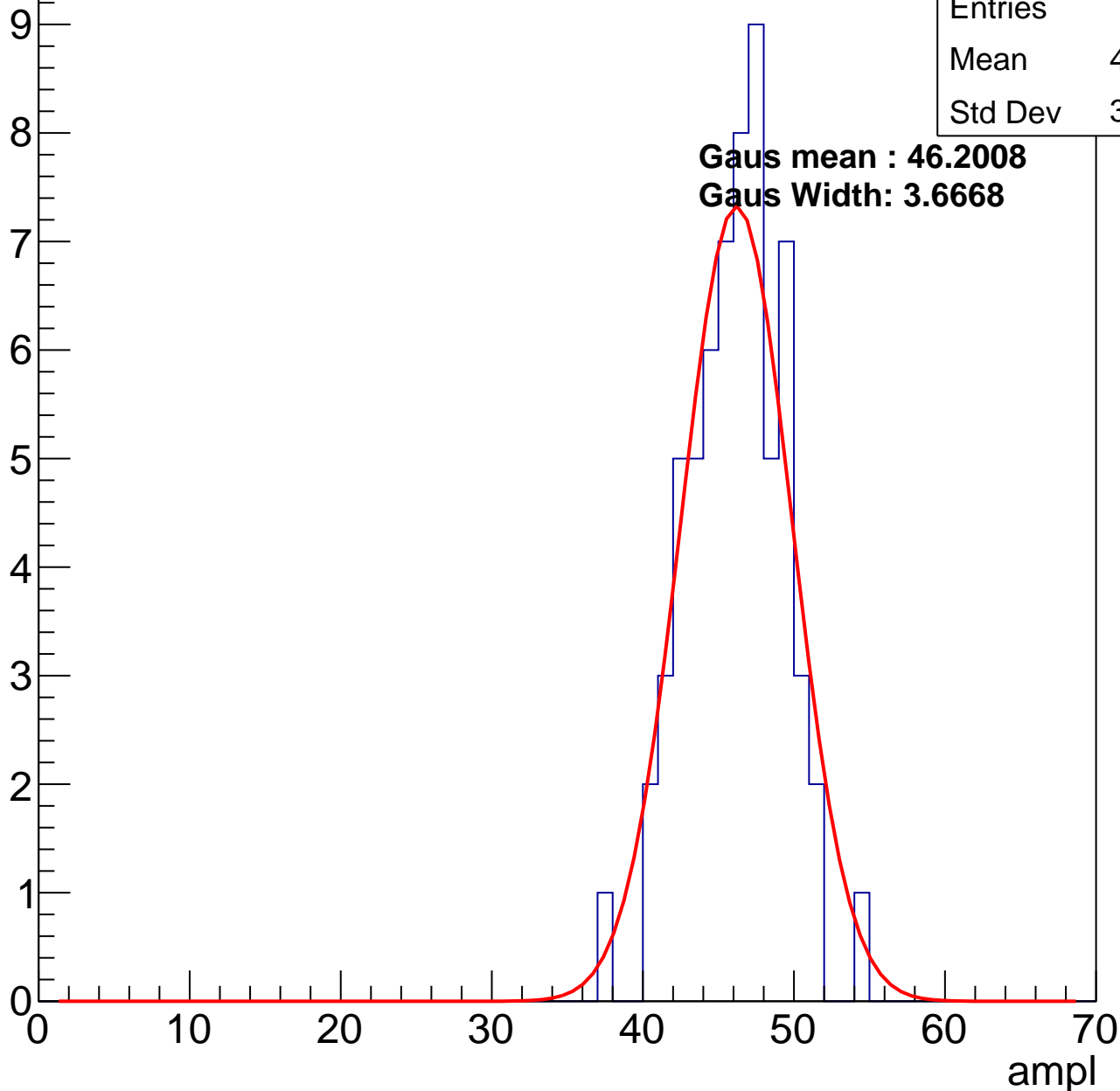
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.69 |
| Std Dev | 3.142 |

Gaus mean : 46.2008

Gaus Width: 3.6668

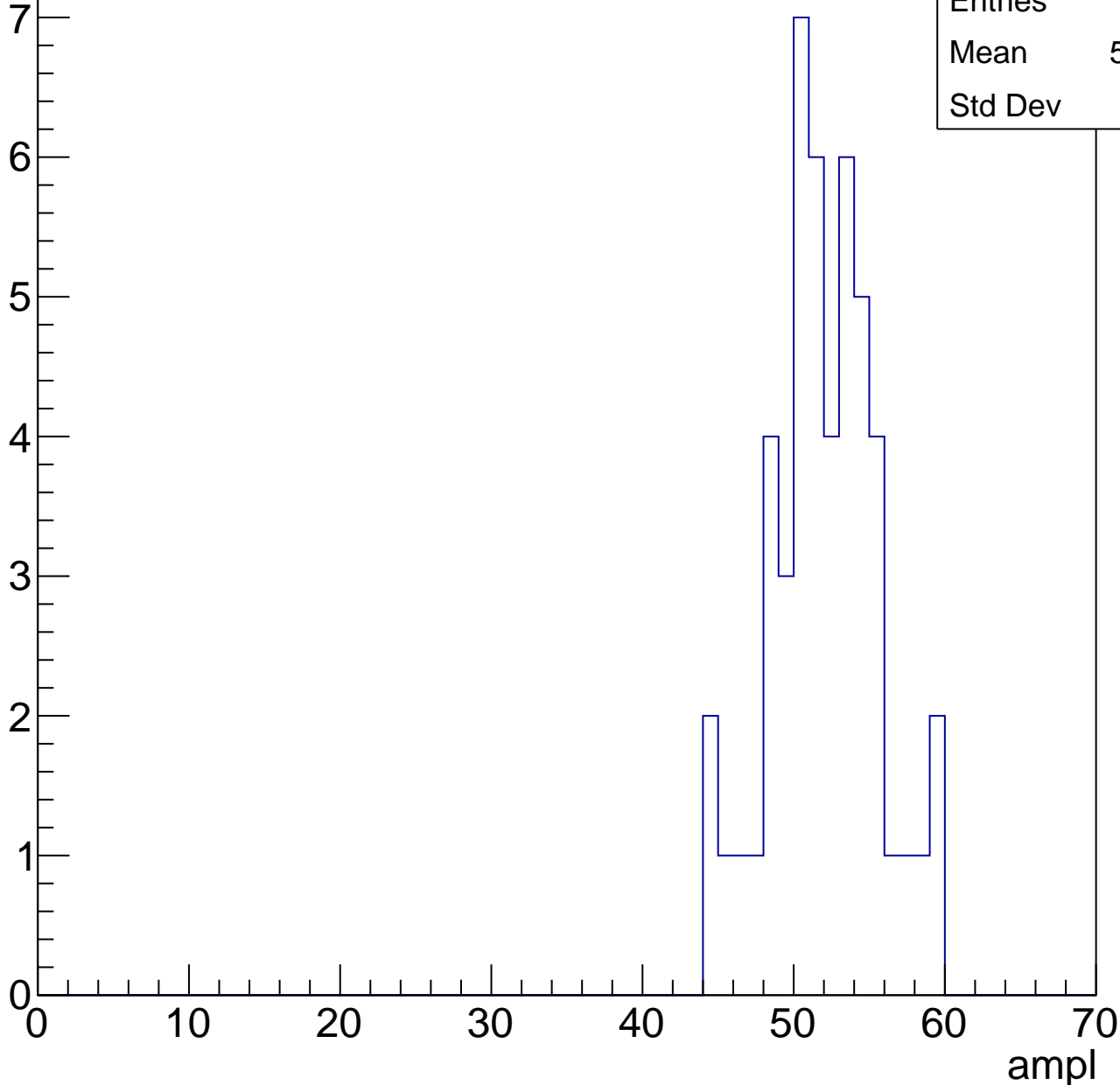


B0L001S, U7-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 51.55 |
| Std Dev | 3.47 |

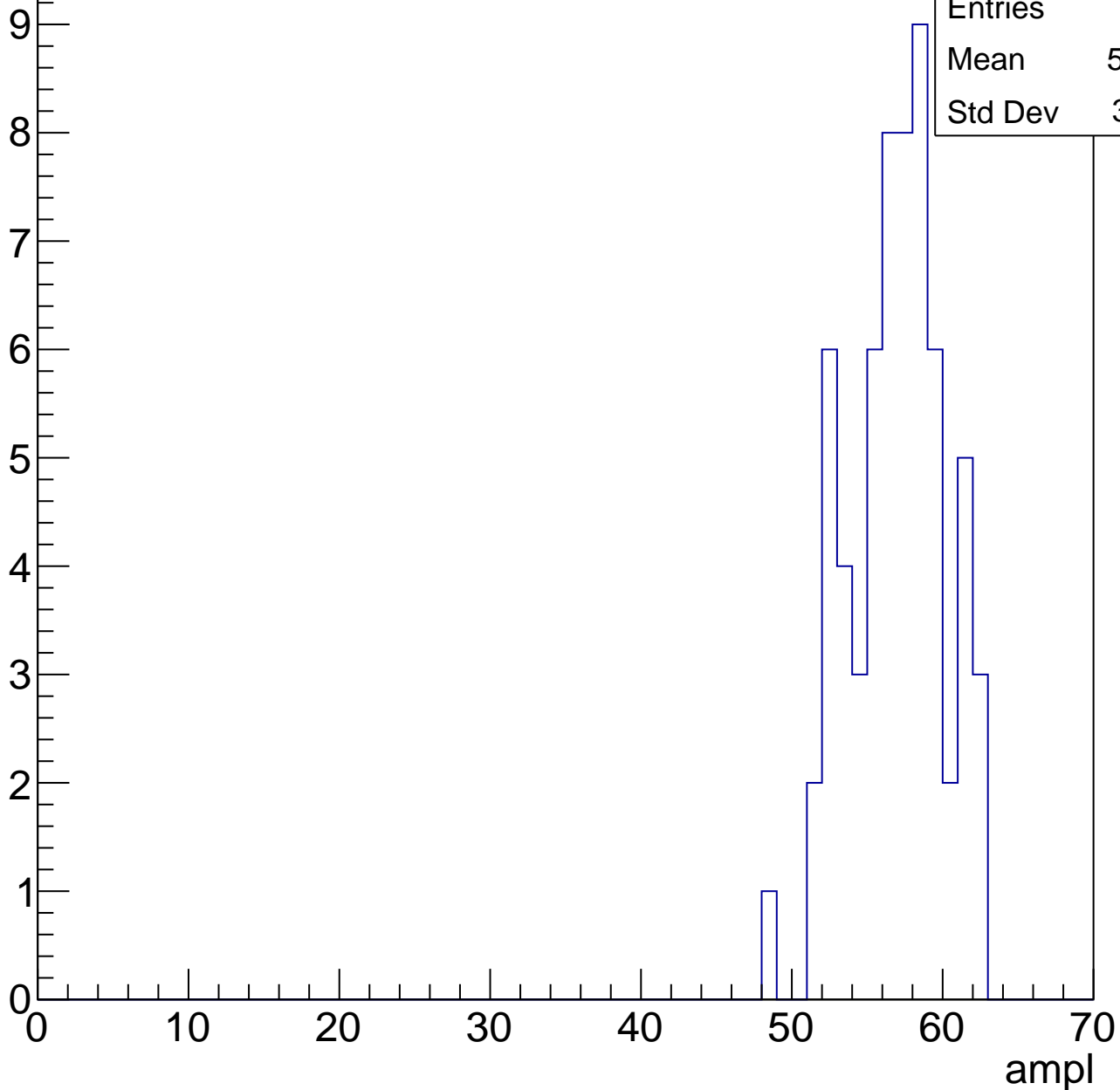


B0L001S, U7-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 56.46 |
| Std Dev | 3.121 |

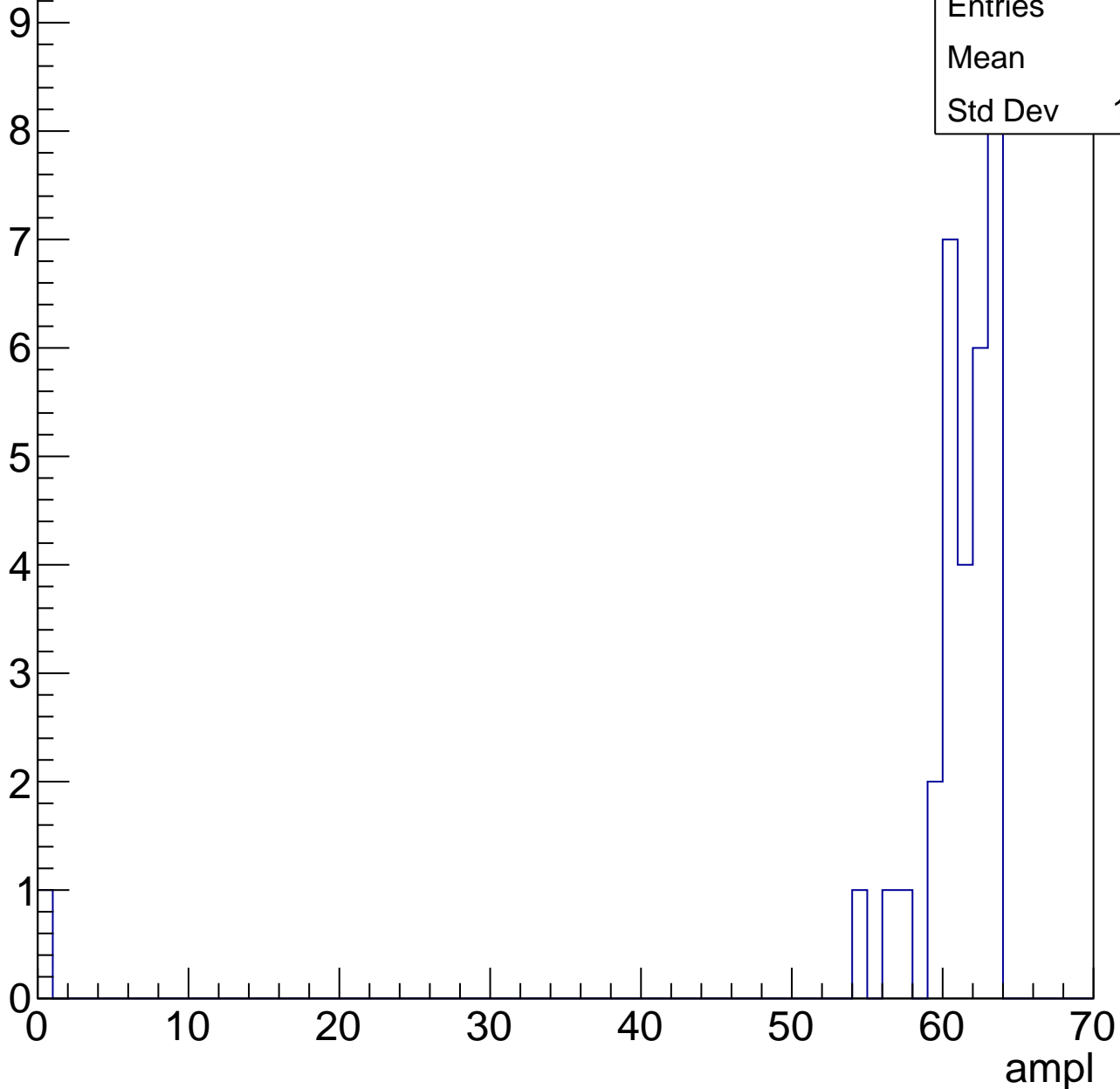


B0L001S, U7-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 59 |
| Std Dev | 10.81 |



B0L001S, U7-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

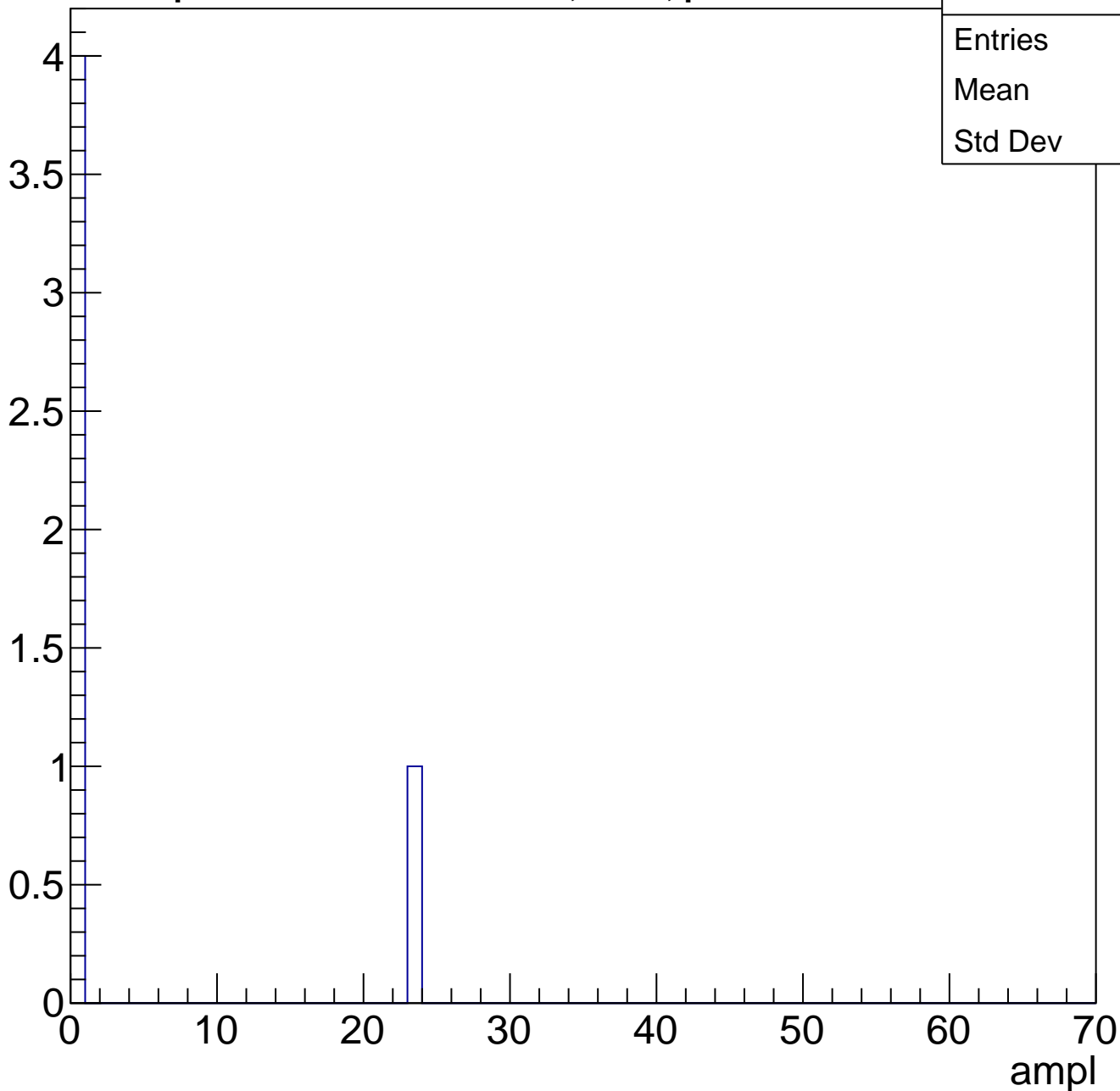
Entry



B0L001S, U7-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch114, adc0

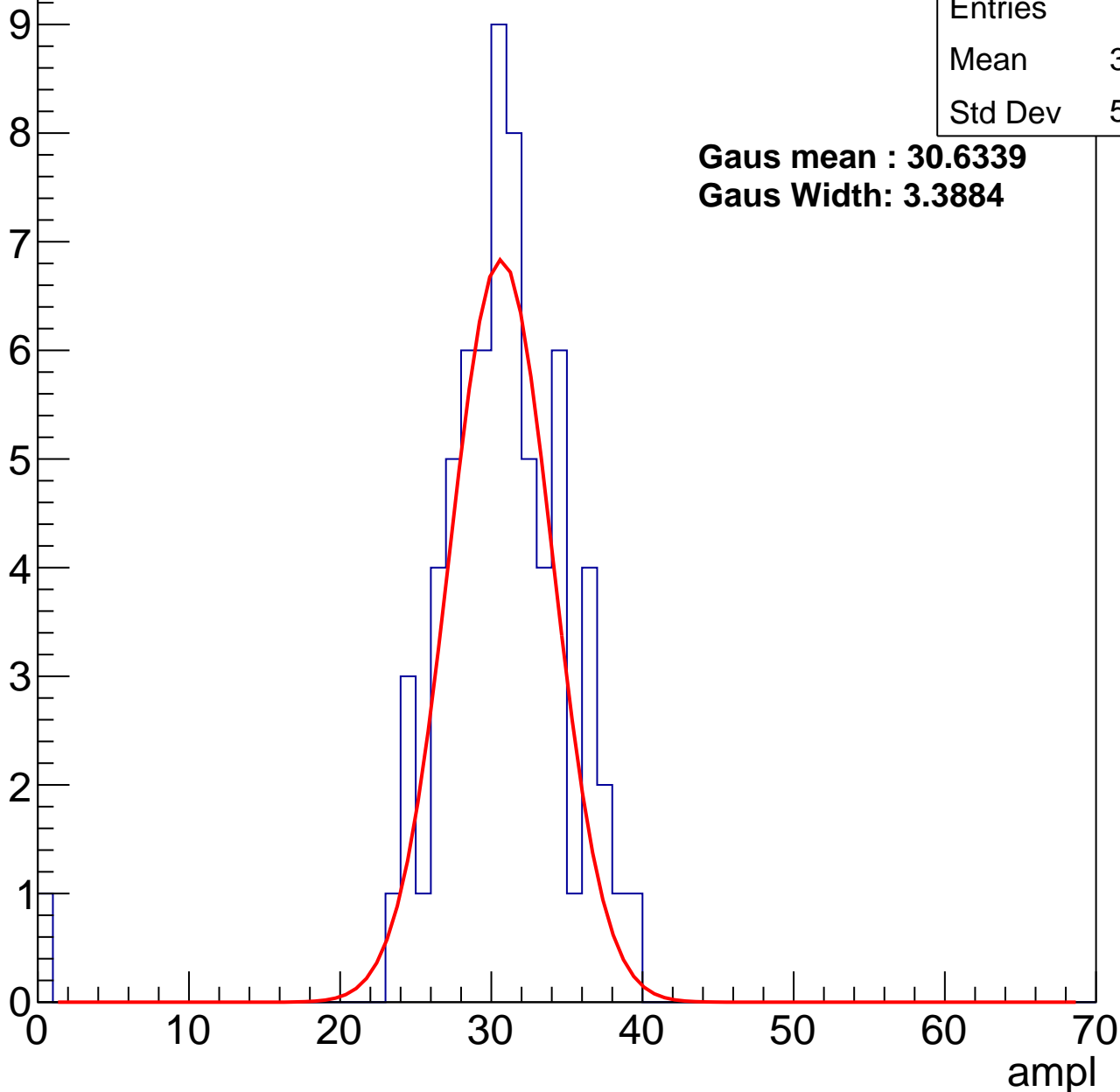
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 30.07 |
| Std Dev | 5.134 |

Gaus mean : 30.6339

Gaus Width: 3.3884



B0L001S, U7-ch114, adc1

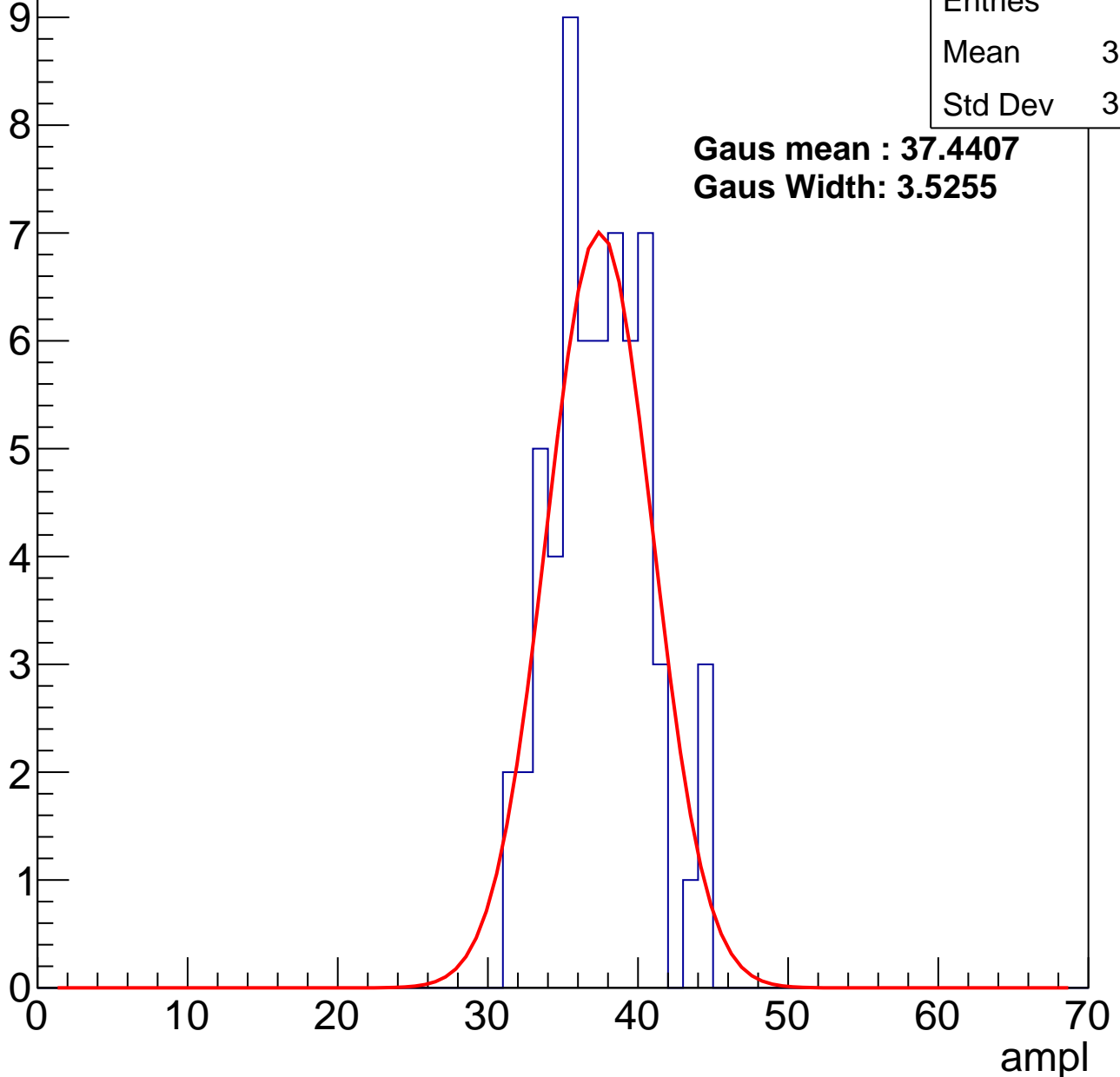
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 37.02 |
| Std Dev | 3.149 |

Gaus mean : 37.4407

Gaus Width: 3.5255



B0L001S, U7-ch114, adc2

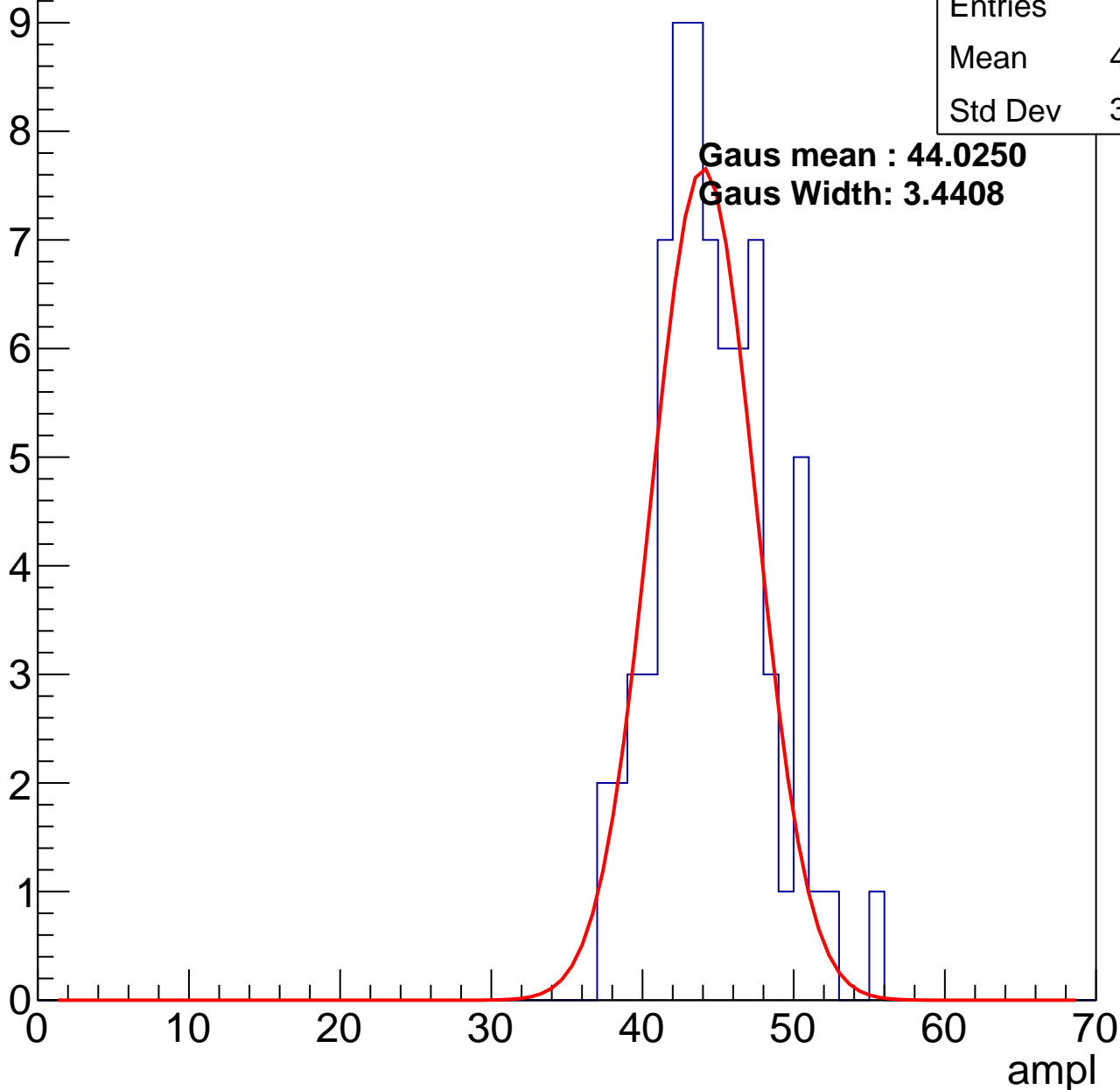
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 44.15 |
| Std Dev | 3.663 |

Gaus mean : 44.0250

Gaus Width: 3.4408

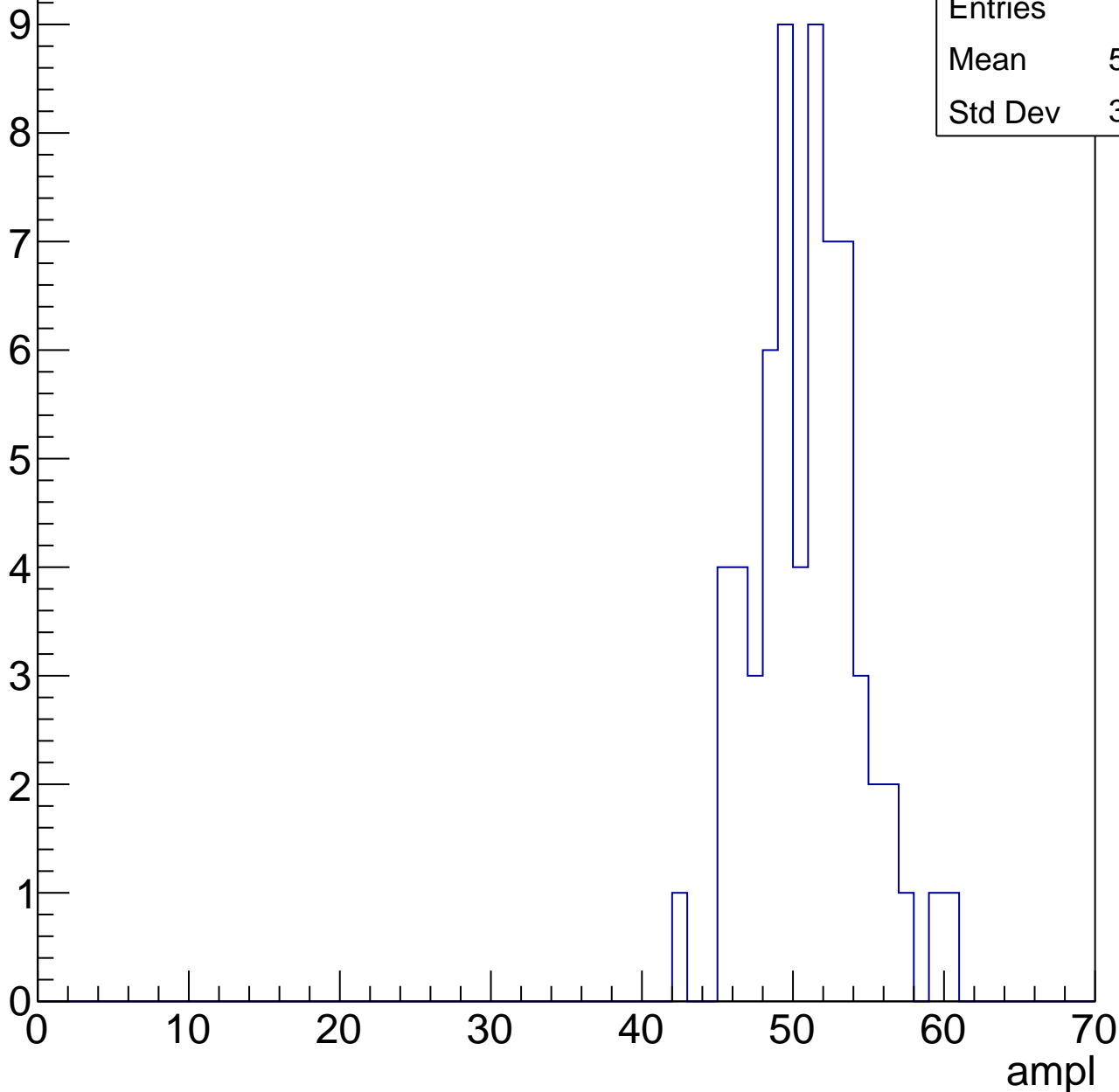


B0L001S, U7-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 50.47 |
| Std Dev | 3.477 |

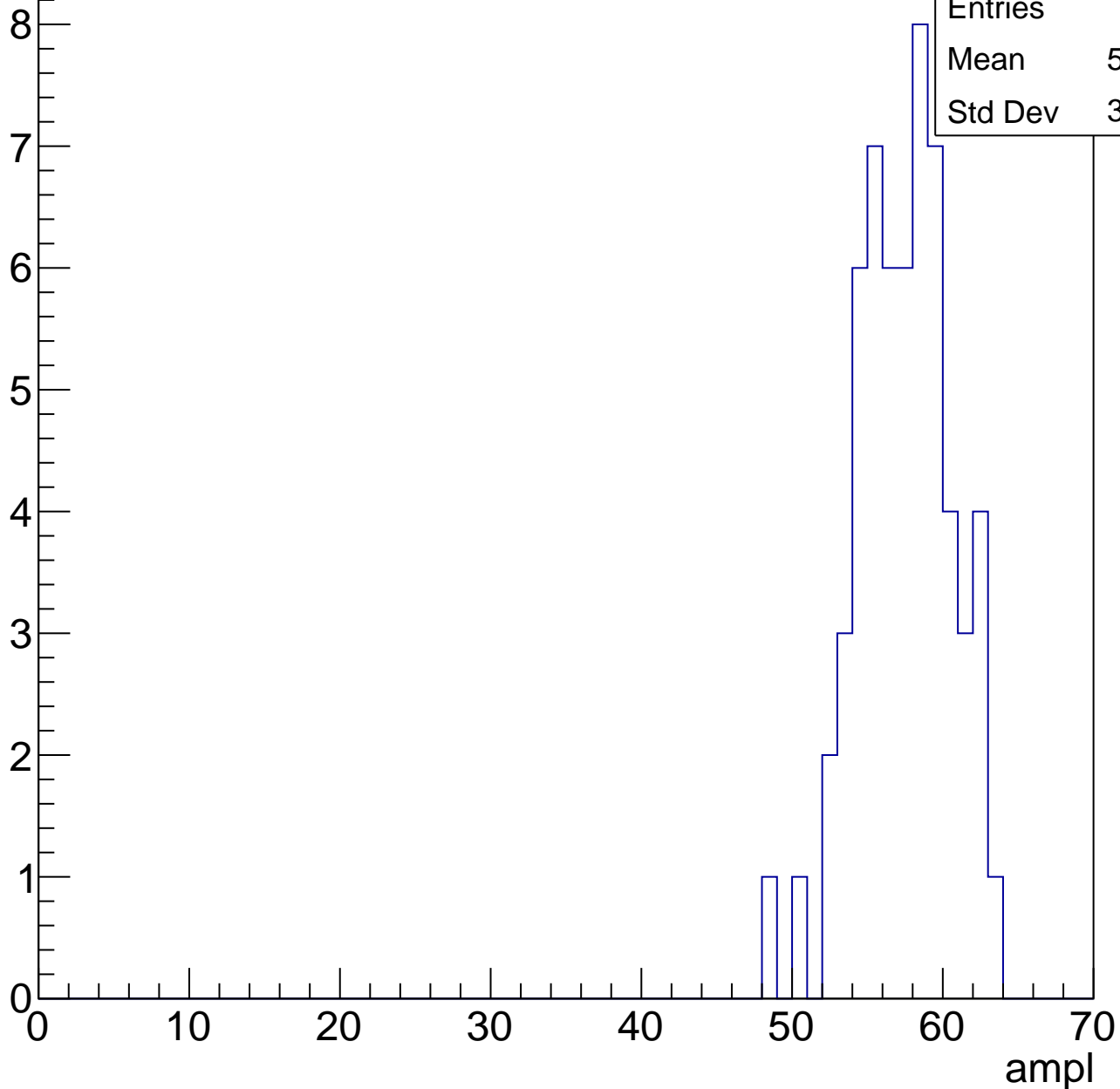


B0L001S, U7-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 56.93 |
| Std Dev | 3.113 |

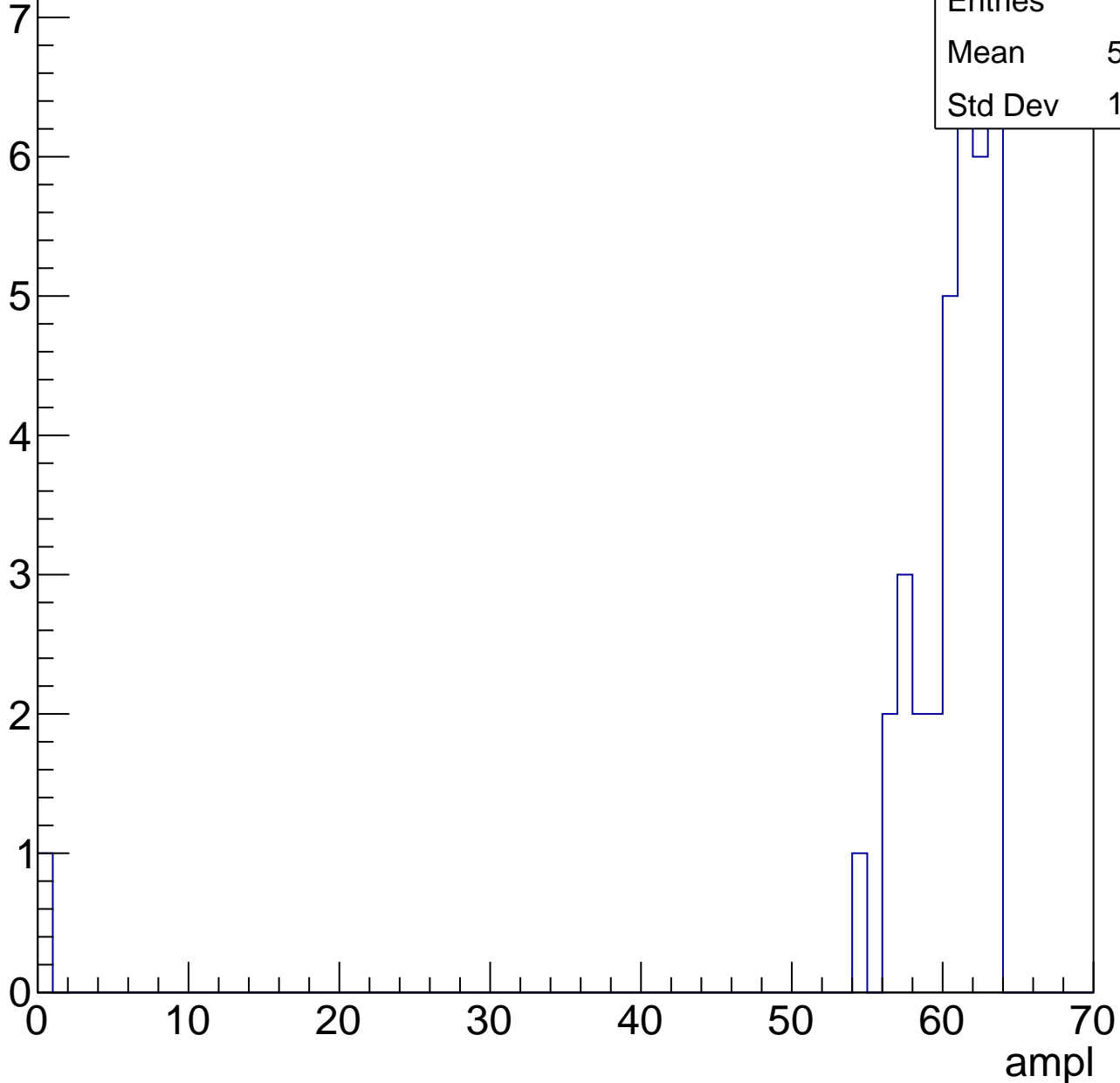


B0L001S, U7-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 36 |
| Mean | 58.64 |
| Std Dev | 10.18 |



B0L001S, U7-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch115, adc0

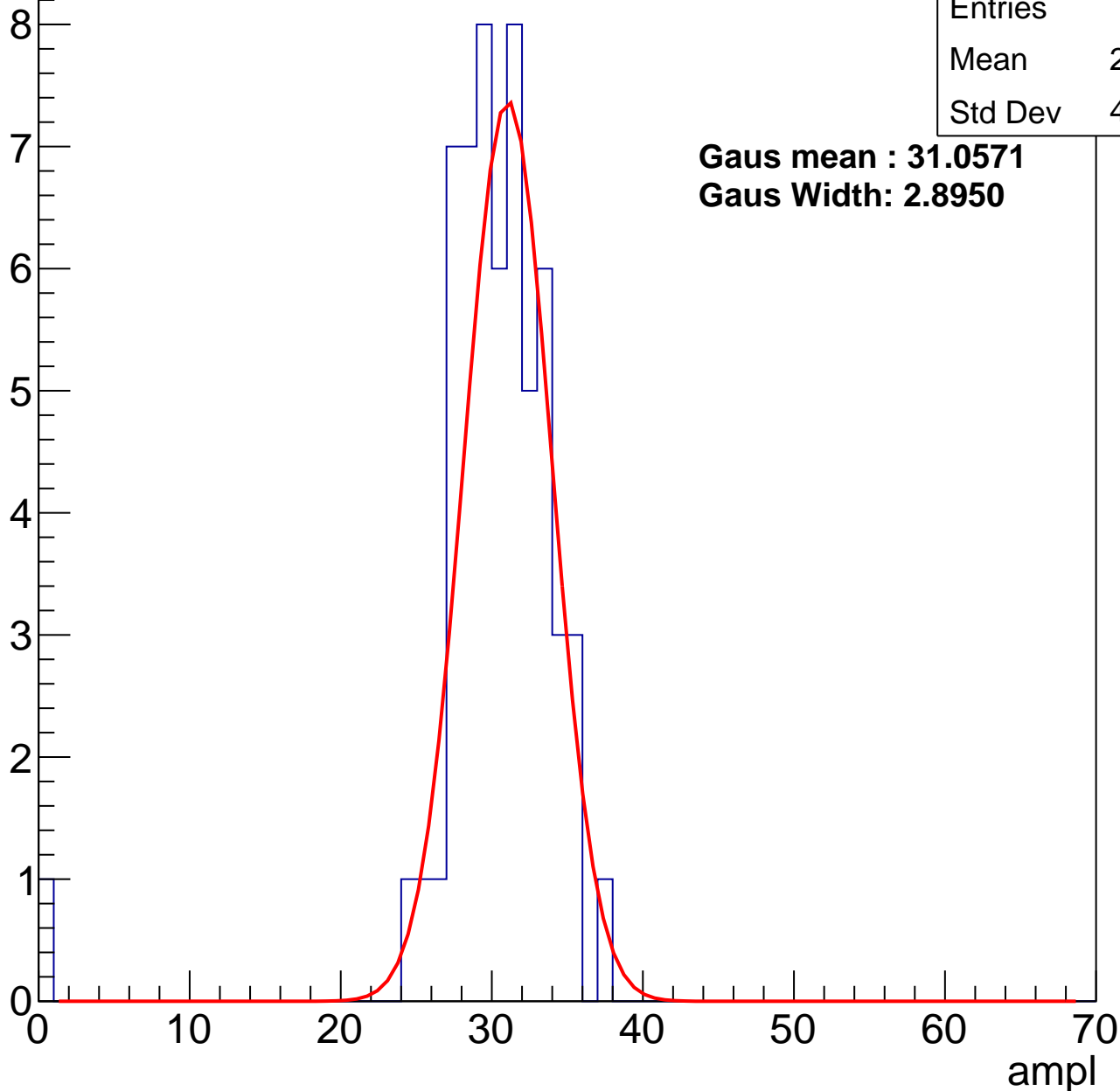
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 29.69 |
| Std Dev | 4.779 |

Gaus mean : 31.0571

Gaus Width: 2.8950



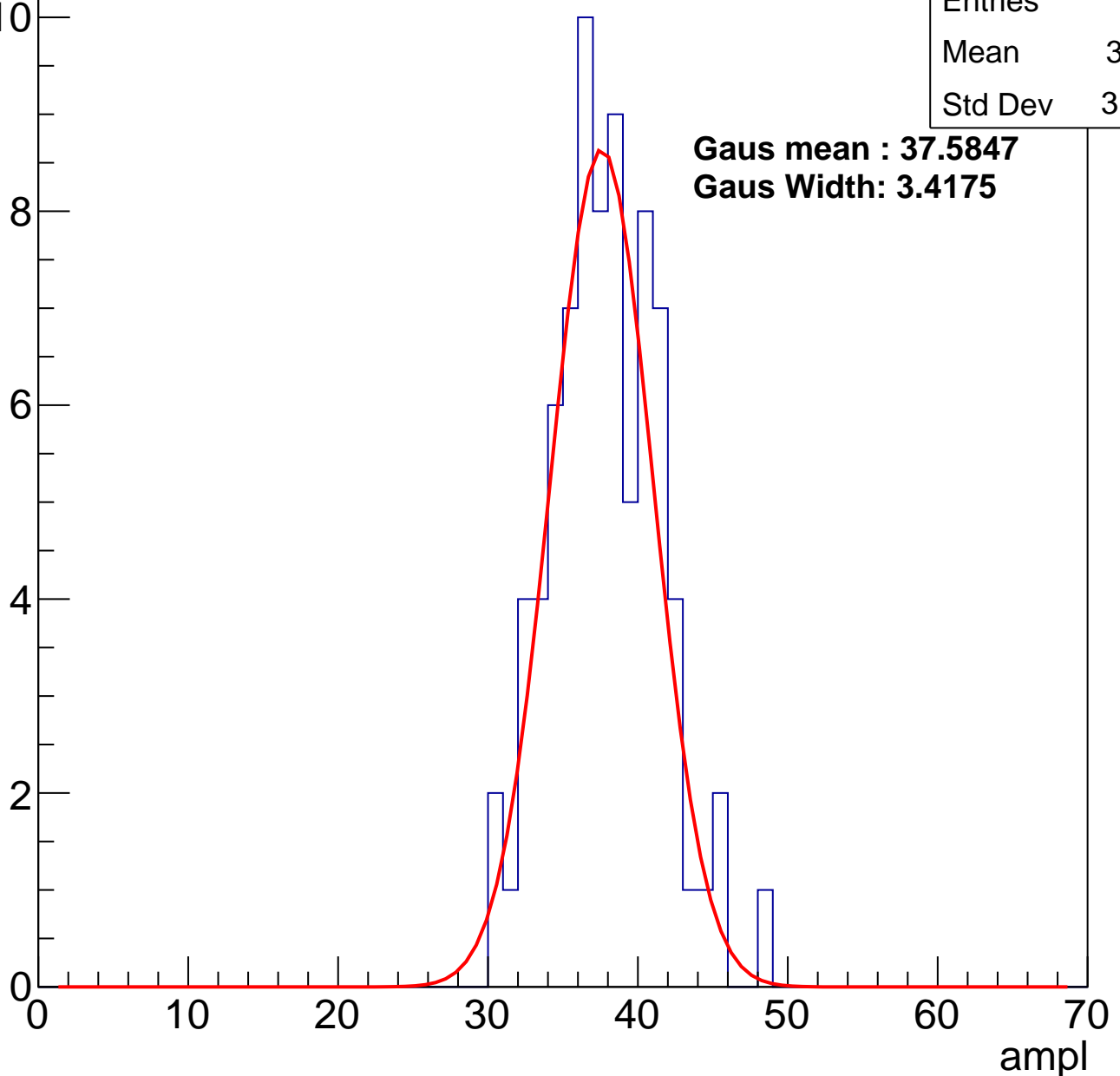
B0L001S, U7-ch115, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 37.41 |
| Std Dev | 3.577 |

Gaus mean : 37.5847
Gaus Width: 3.4175



B0L001S, U7-ch115, adc2

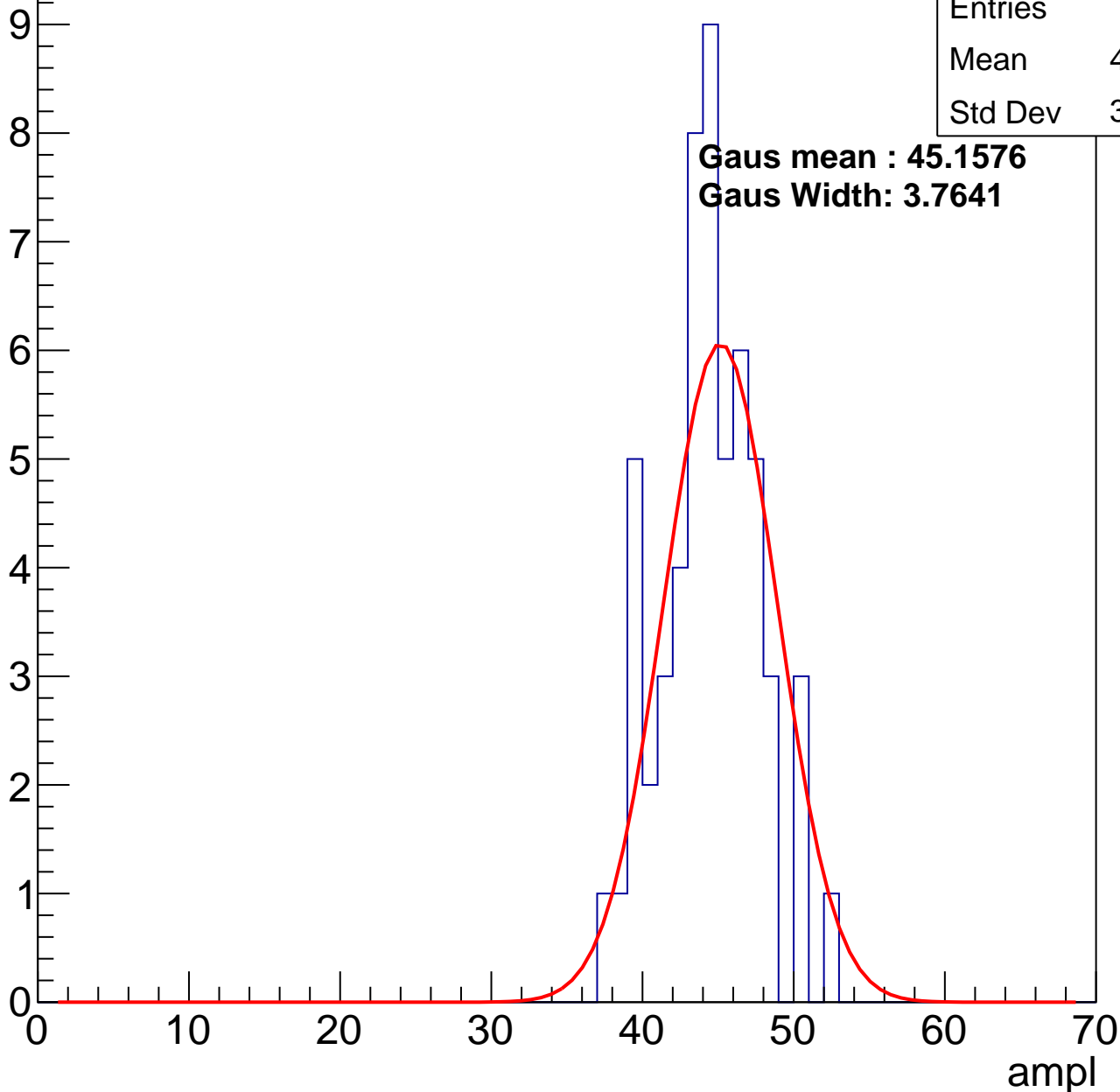
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 43.98 |
| Std Dev | 3.238 |

Gaus mean : 45.1576

Gaus Width: 3.7641

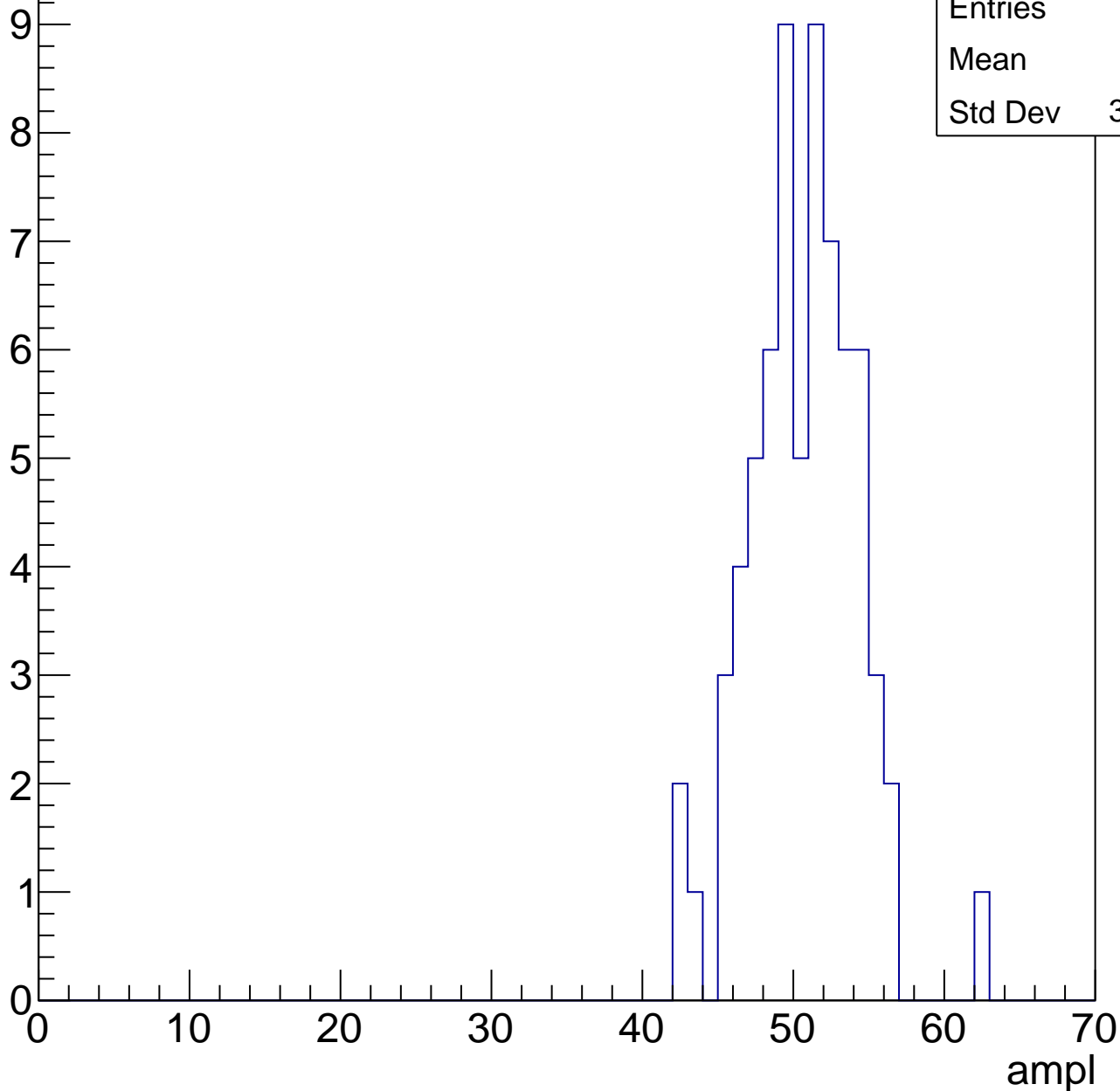


B0L001S, U7-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 50.2 |
| Std Dev | 3.549 |

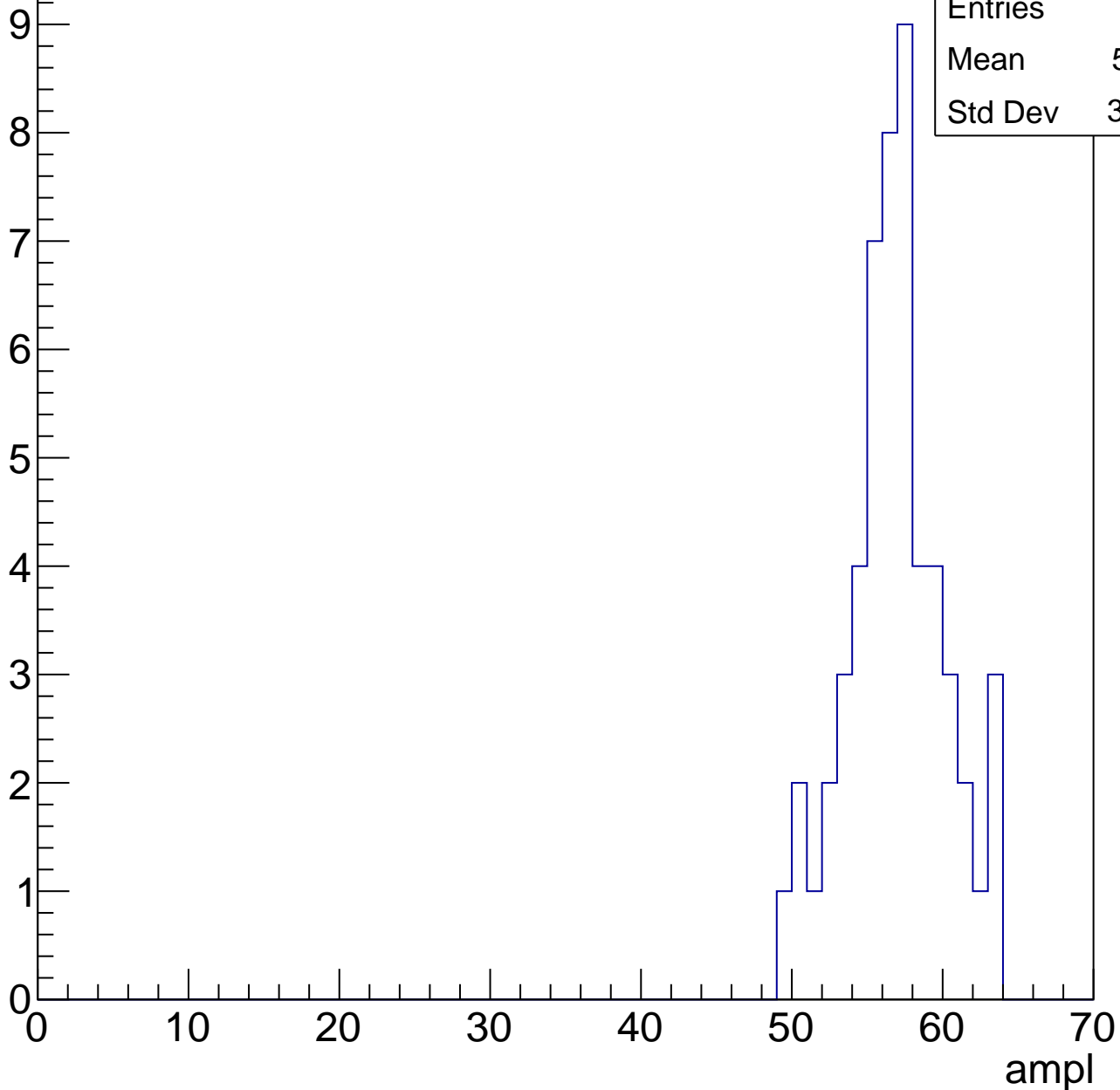


B0L001S, U7-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

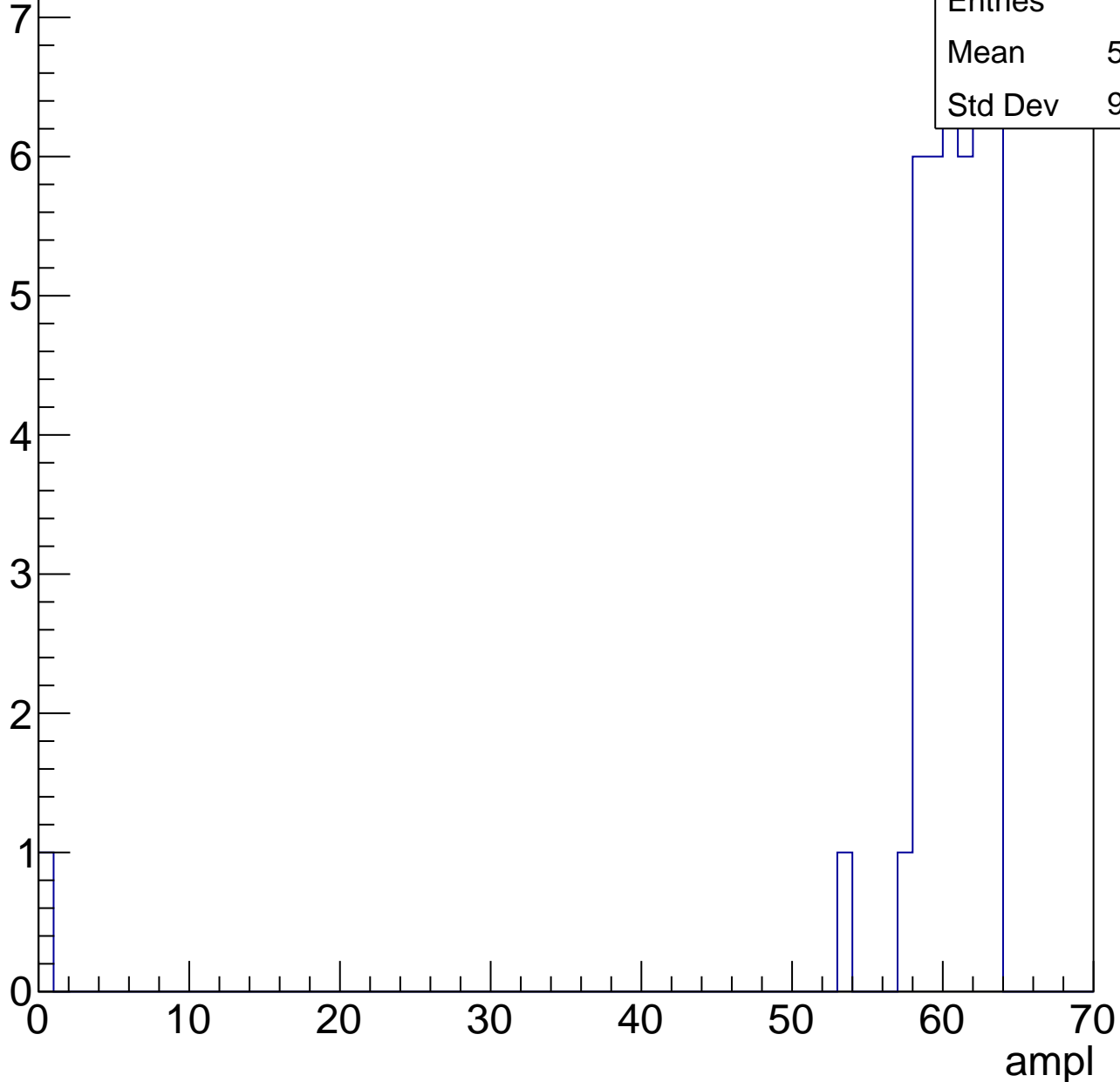
| | |
|---------|-------|
| Entries | 54 |
| Mean | 56.41 |
| Std Dev | 3.223 |



B0L001S, U7-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch115, adc6

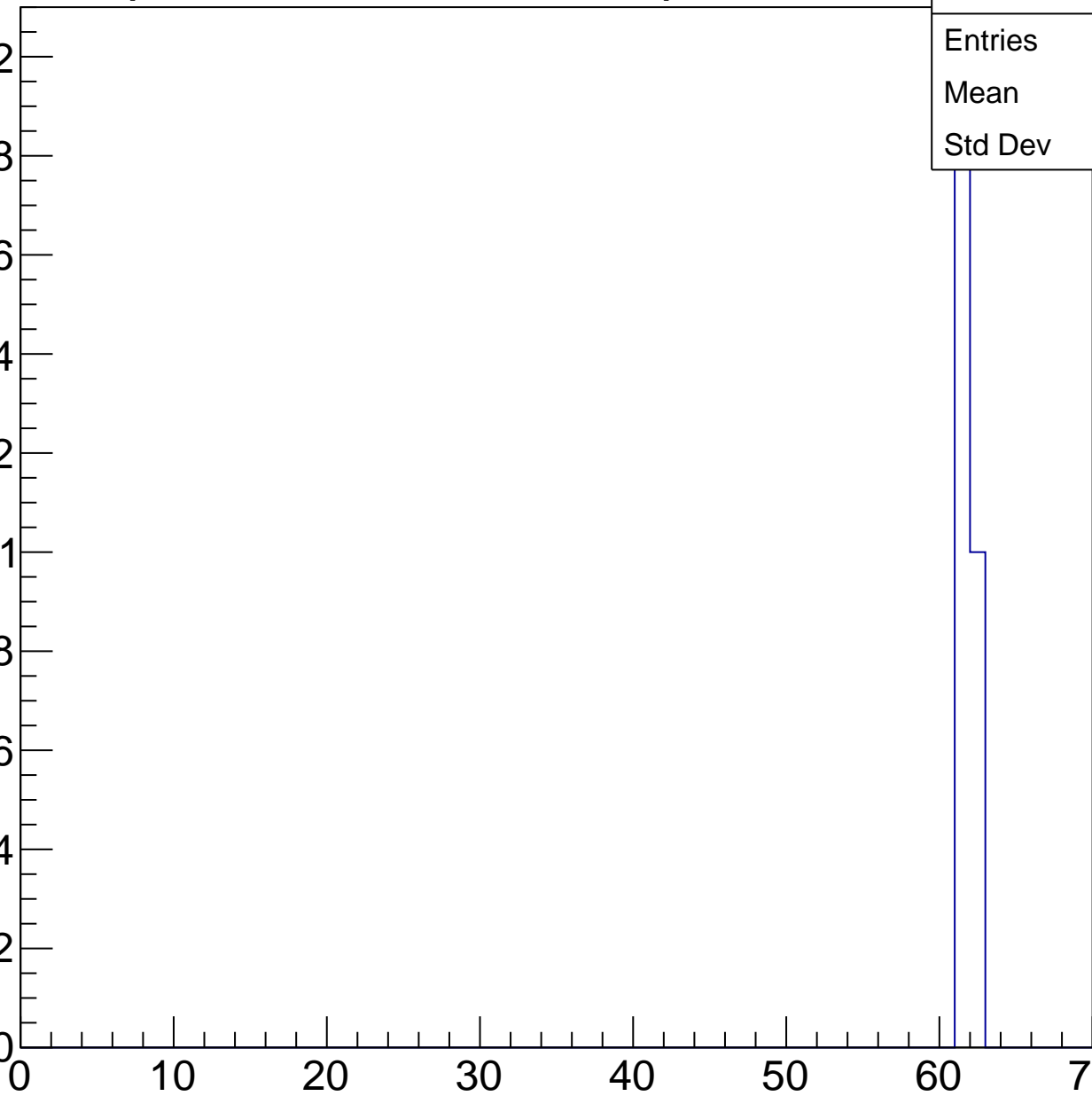
calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 3 |
| Mean | 61.33 |
| Std Dev | 0.4714 |

ampl



B0L001S, U7-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch116, adc0

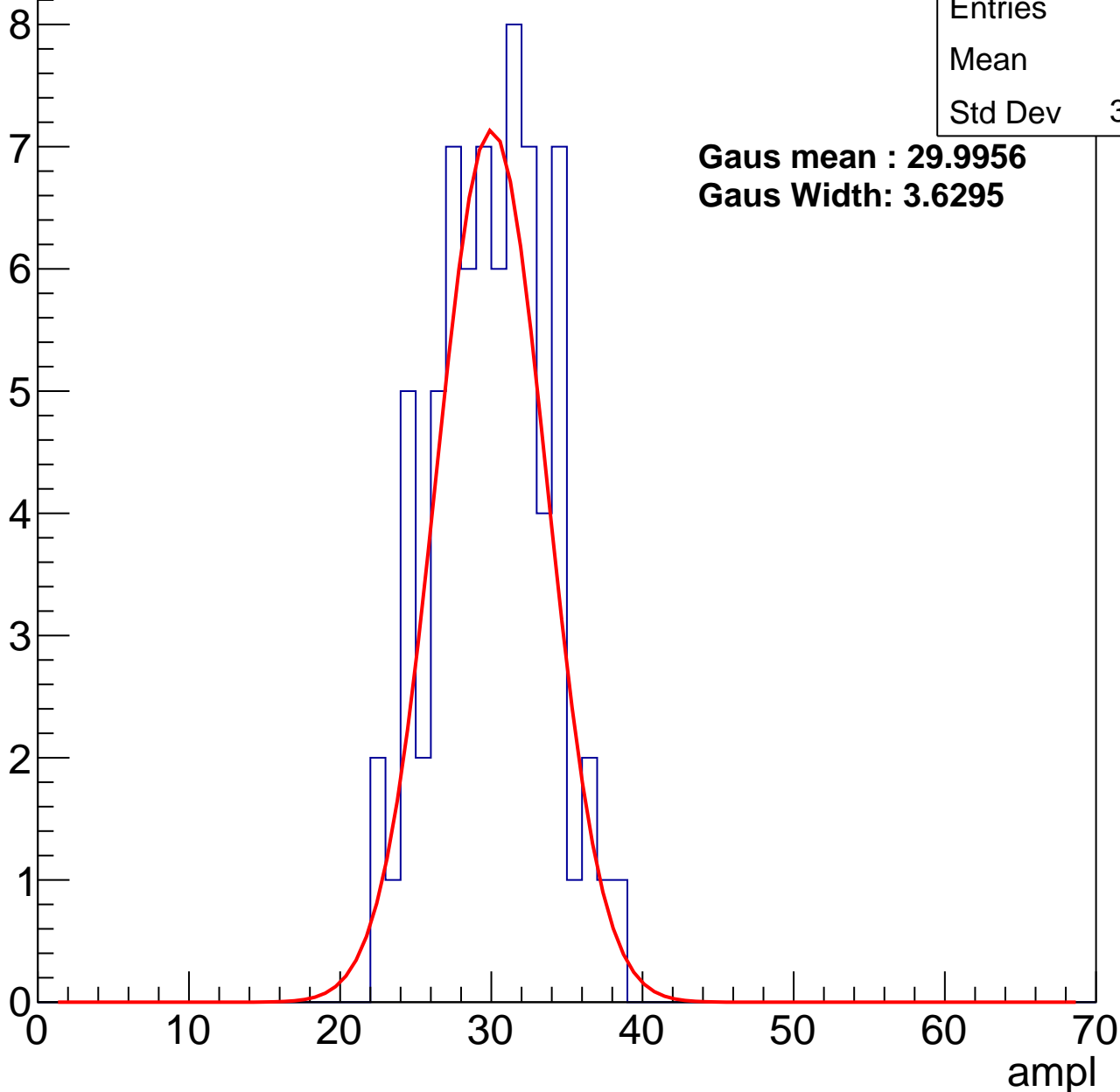
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 29.6 |
| Std Dev | 3.658 |

Gaus mean : 29.9956

Gaus Width: 3.6295



B0L001S, U7-ch116, adc1

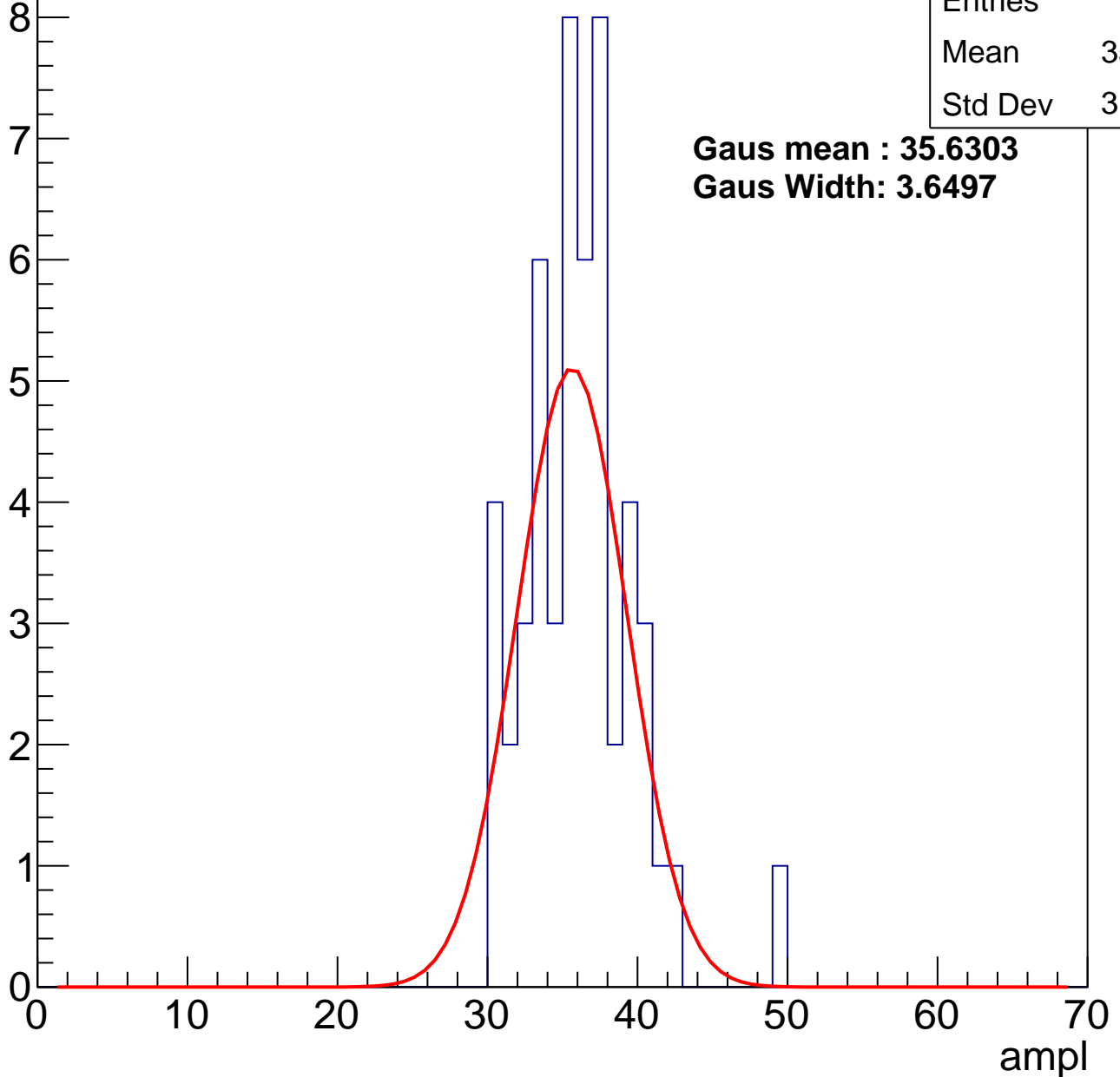
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 35.65 |
| Std Dev | 3.508 |

Gaus mean : 35.6303

Gaus Width: 3.6497



B0L001S, U7-ch116, adc2

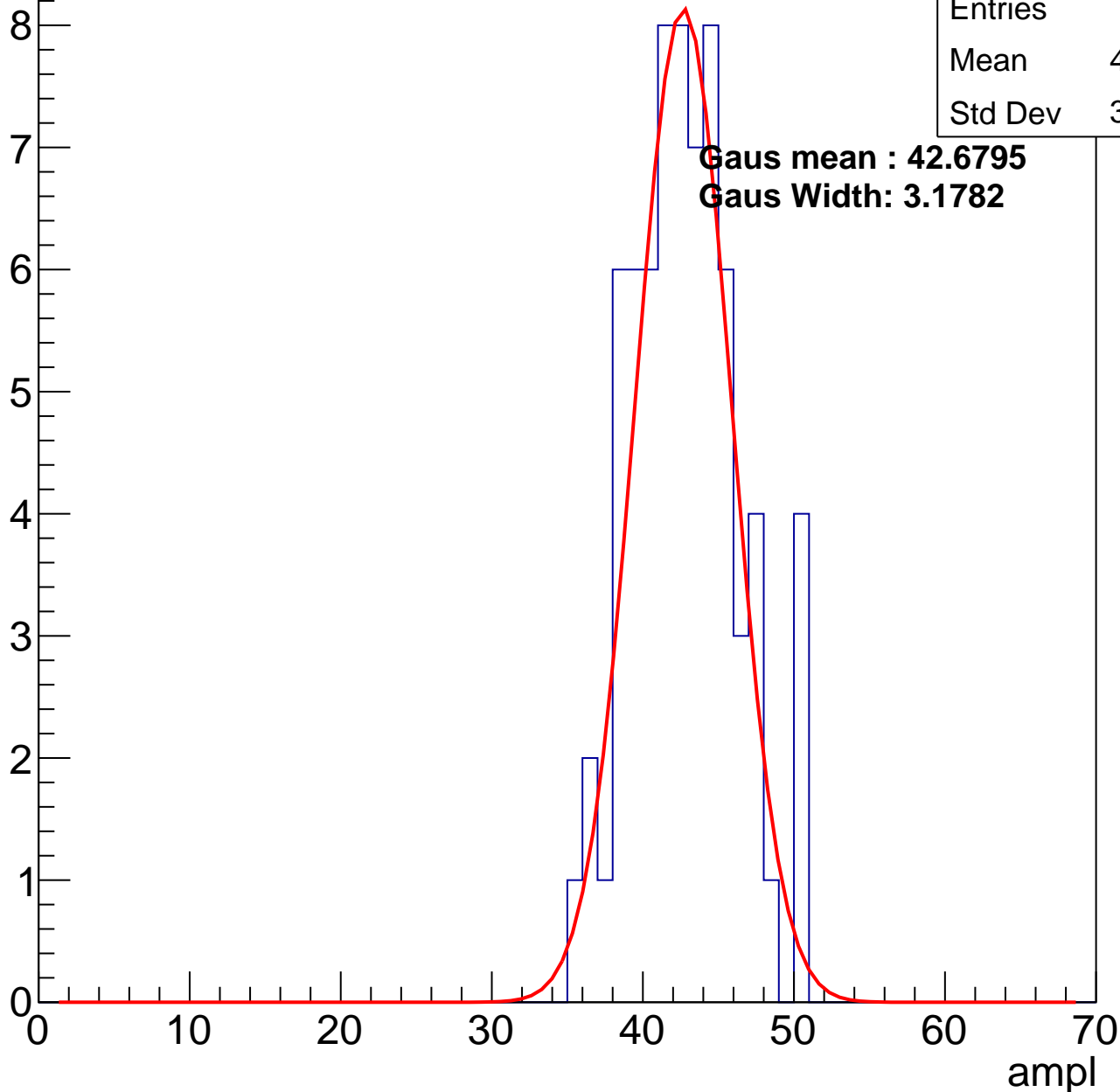
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.35 |
| Std Dev | 3.465 |

Gaus mean : 42.6795

Gaus Width: 3.1782

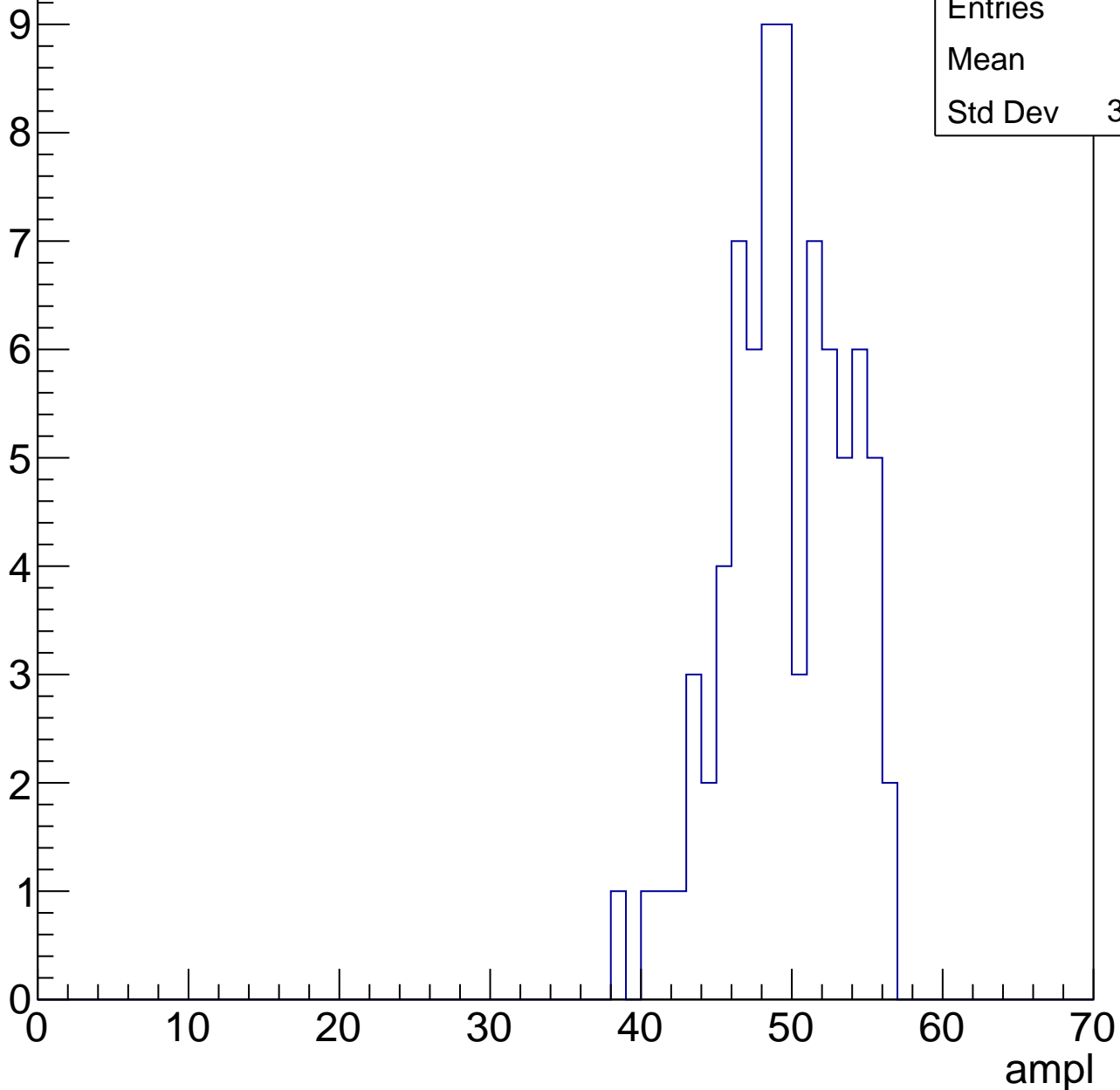


B0L001S, U7-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 49.1 |
| Std Dev | 3.963 |

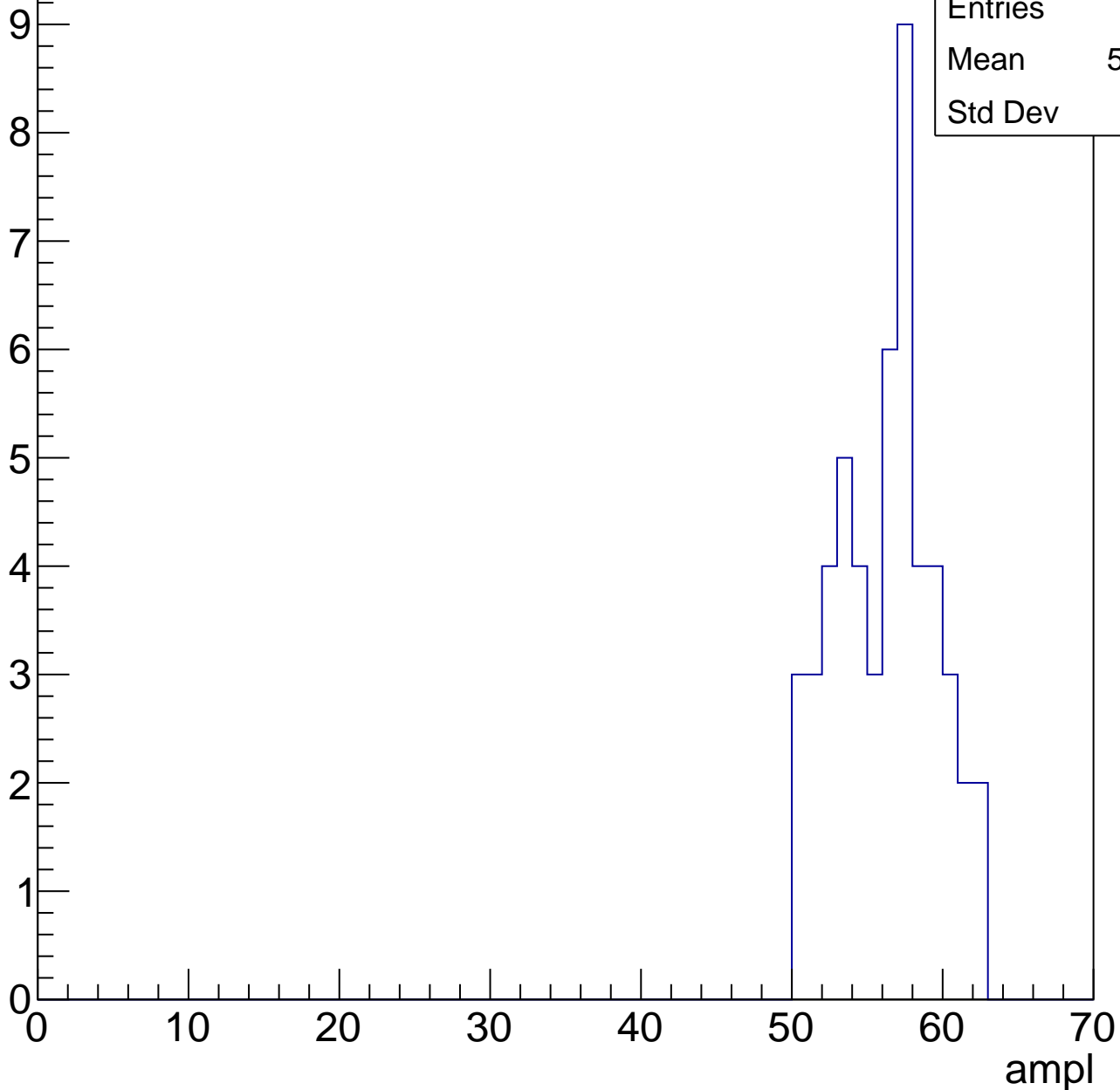


B0L001S, U7-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 55.77 |
| Std Dev | 3.22 |

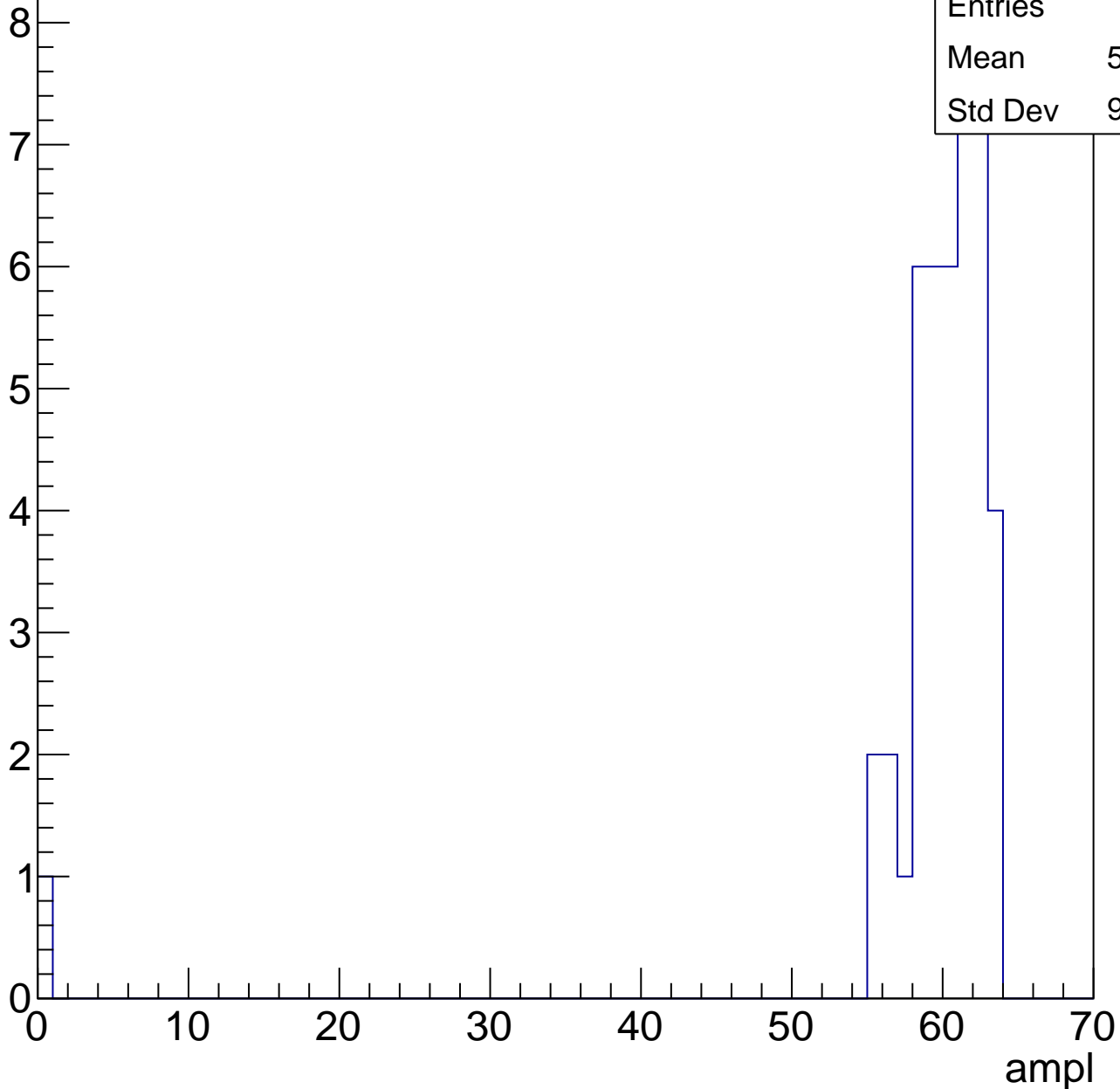


B0L001S, U7-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.57 |
| Std Dev | 9.179 |

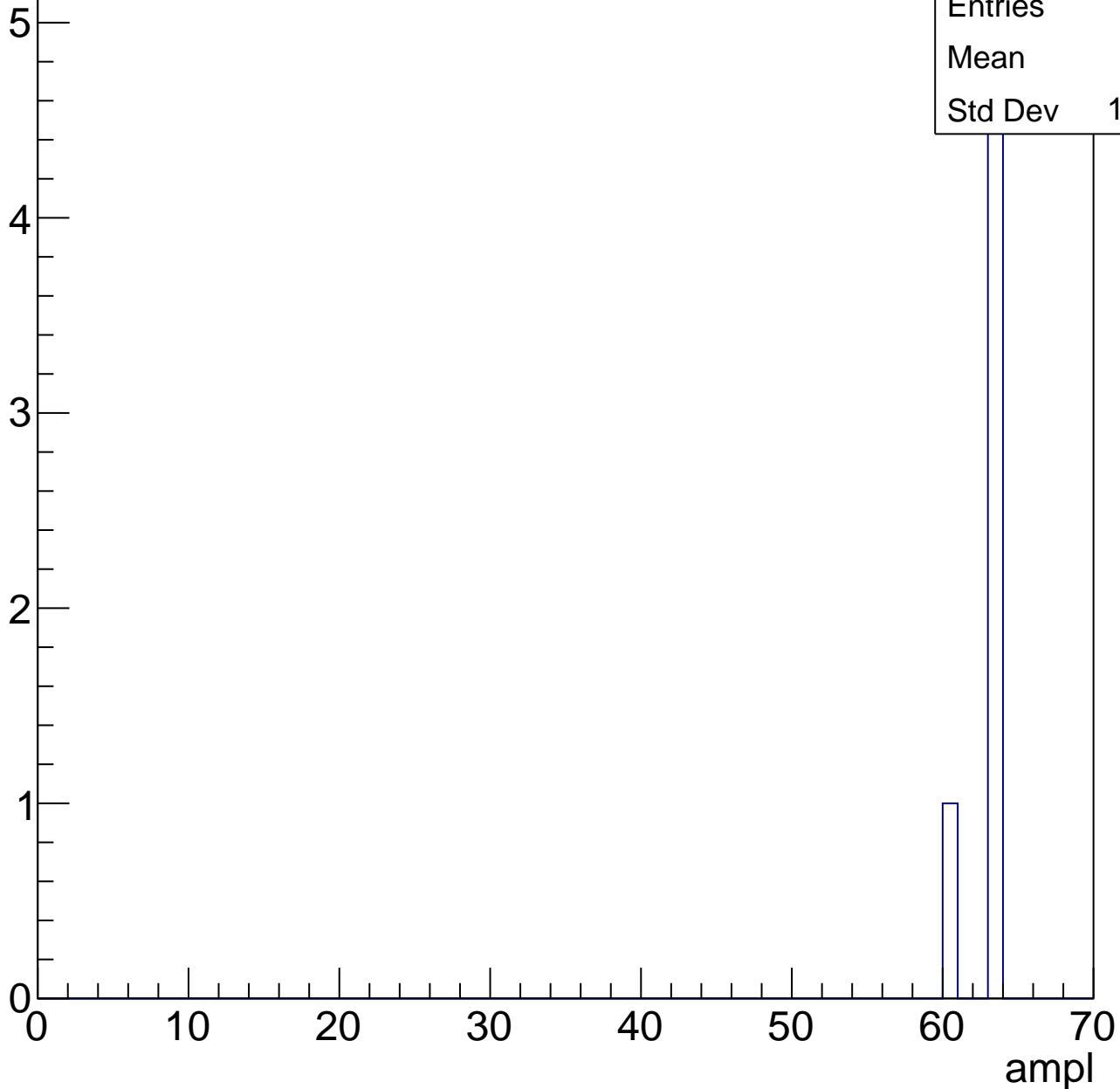


B0L001S, U7-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 6 |
| Mean | 62.5 |
| Std Dev | 1.118 |



B0L001S, U7-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch117, adc0

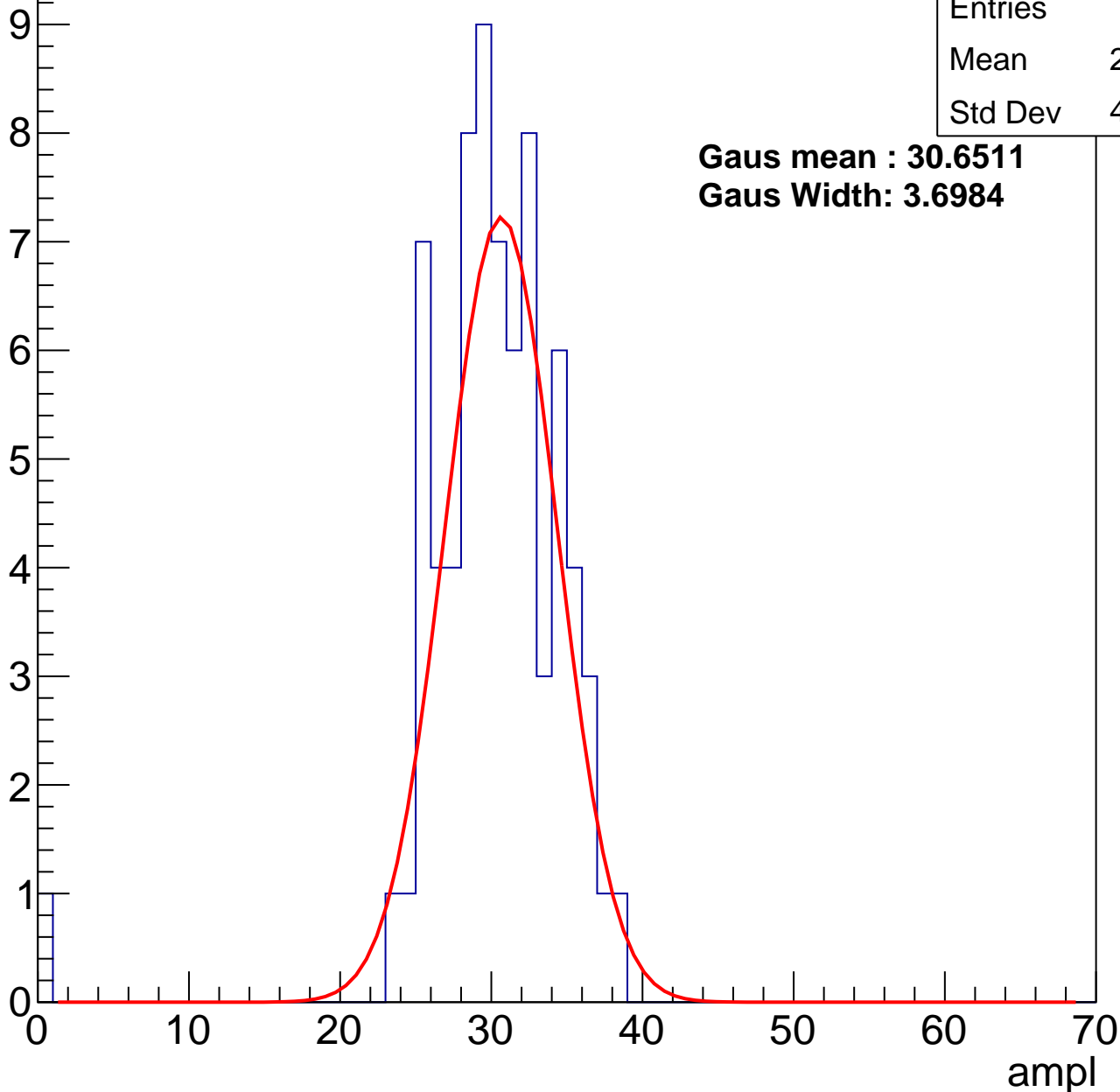
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.69 |
| Std Dev | 4.899 |

Gaus mean : 30.6511

Gaus Width: 3.6984



B0L001S, U7-ch117, adc1

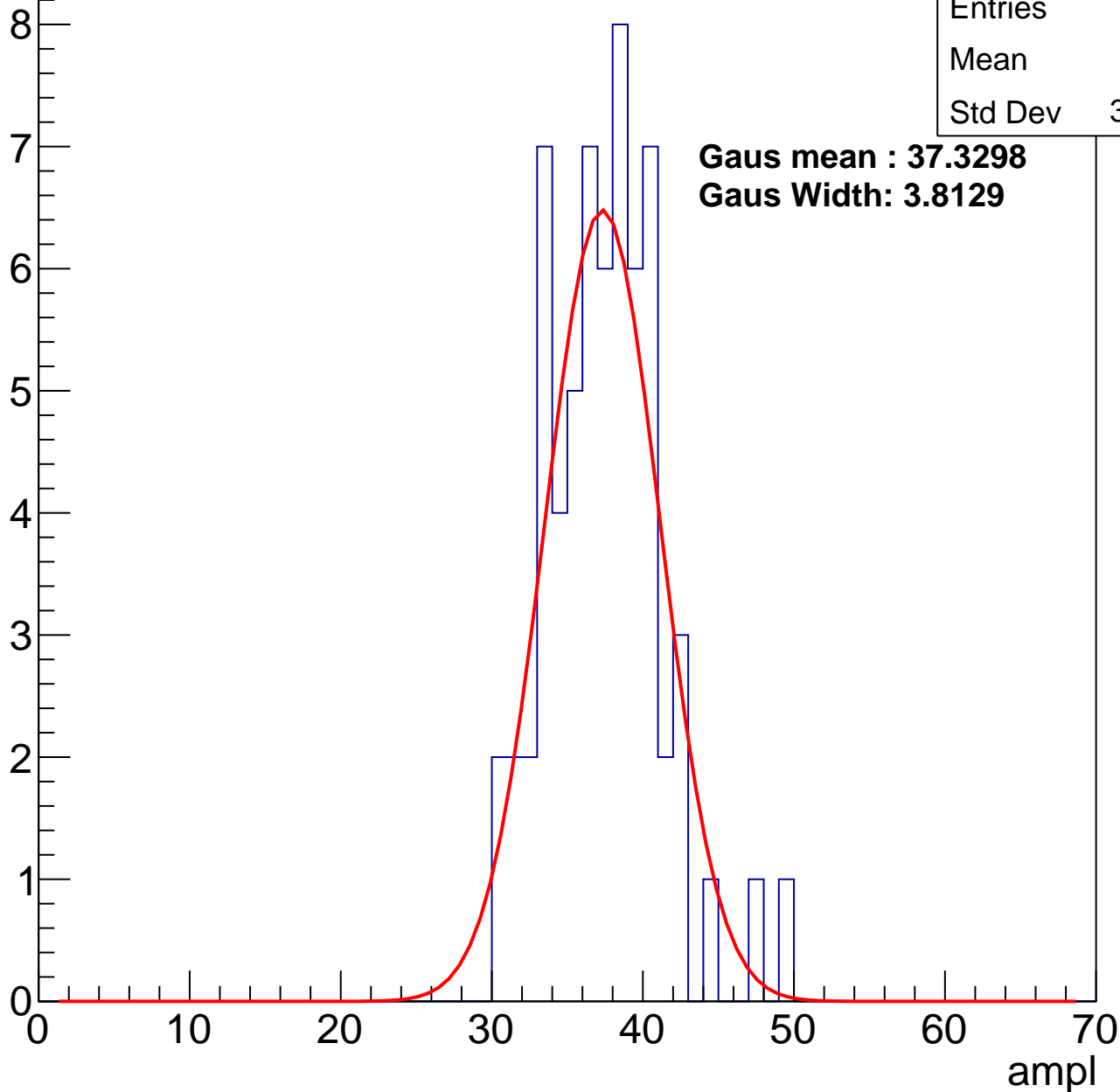
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 37 |
| Std Dev | 3.729 |

Gaus mean : 37.3298

Gaus Width: 3.8129



B0L001S, U7-ch117, adc2

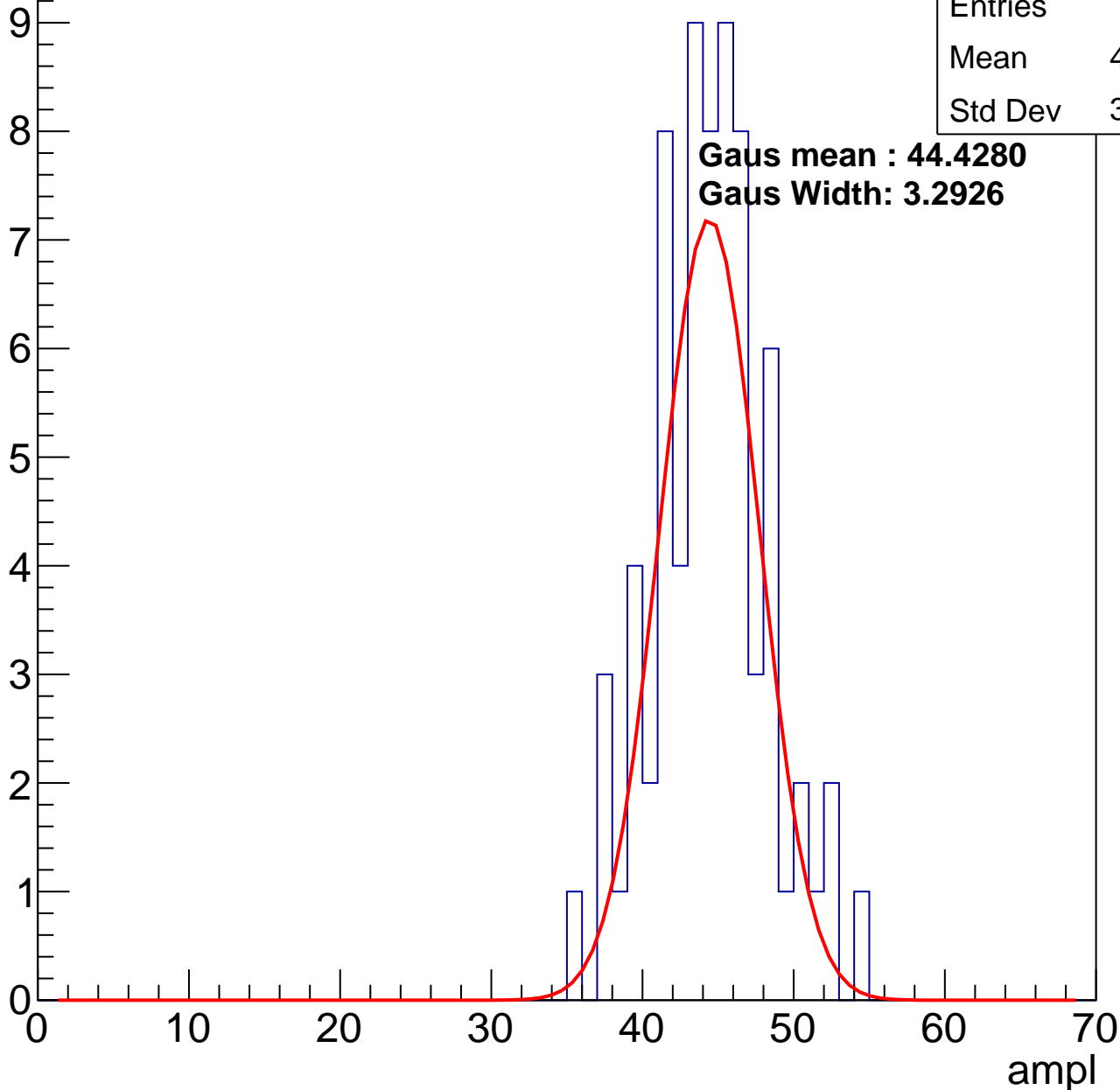
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 44.04 |
| Std Dev | 3.758 |

Gaus mean : 44.4280

Gaus Width: 3.2926

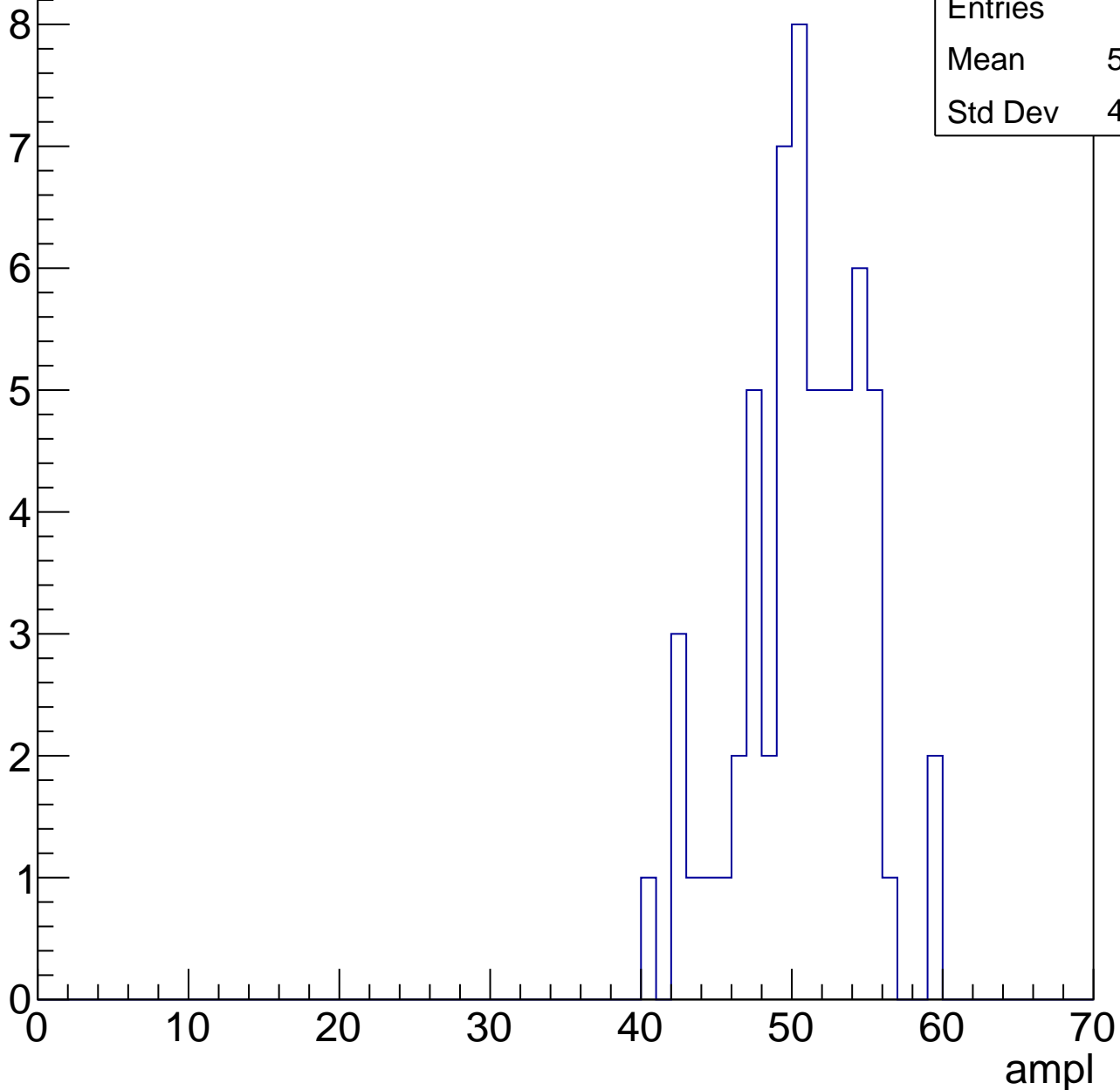


B0L001S, U7-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 50.28 |
| Std Dev | 4.038 |

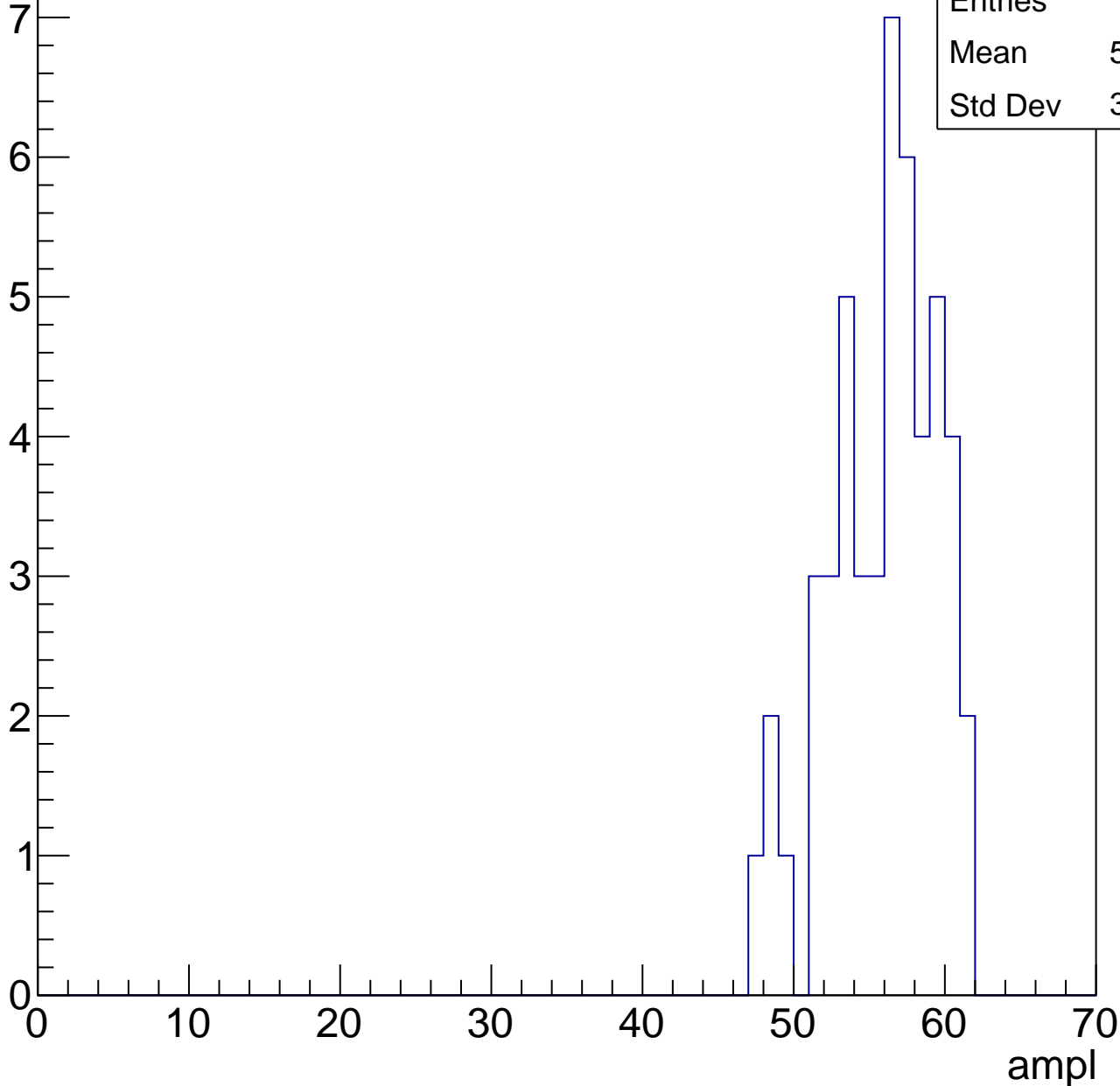


B0L001S, U7-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 55.43 |
| Std Dev | 3.517 |

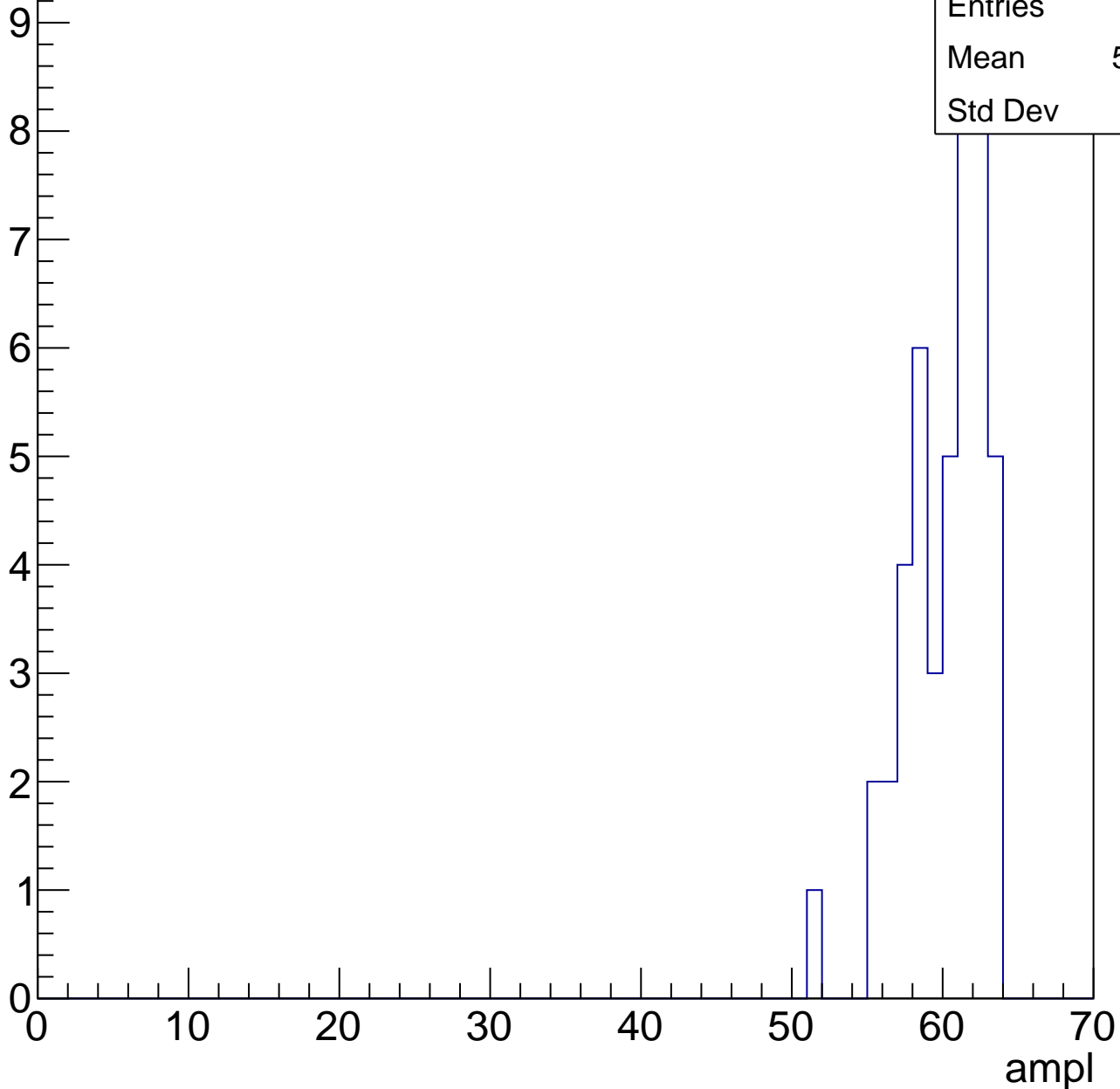


B0L001S, U7-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.71 |
| Std Dev | 2.63 |

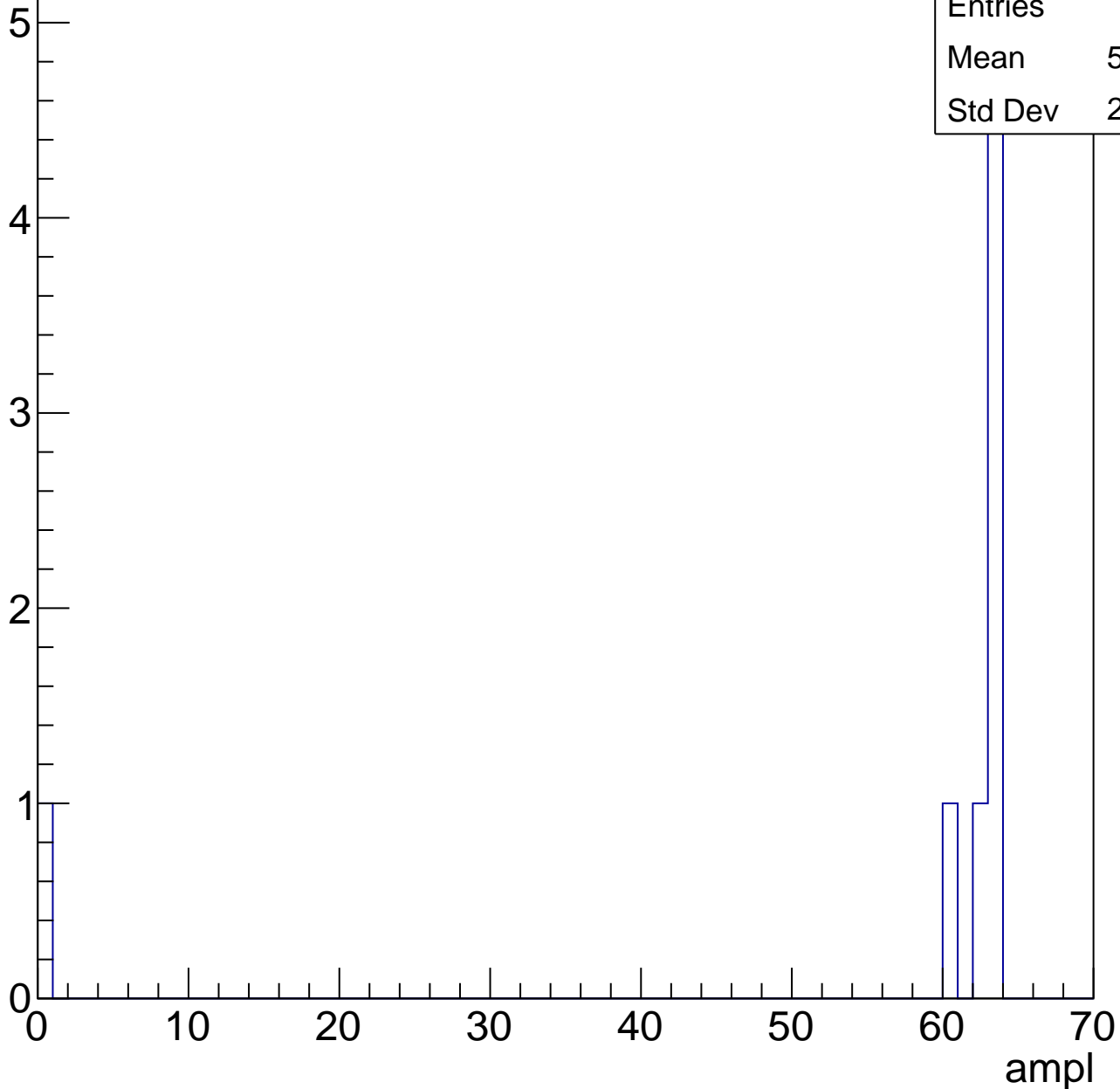


B0L001S, U7-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 54.62 |
| Std Dev | 20.67 |



B0L001S, U7-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch118, adc0

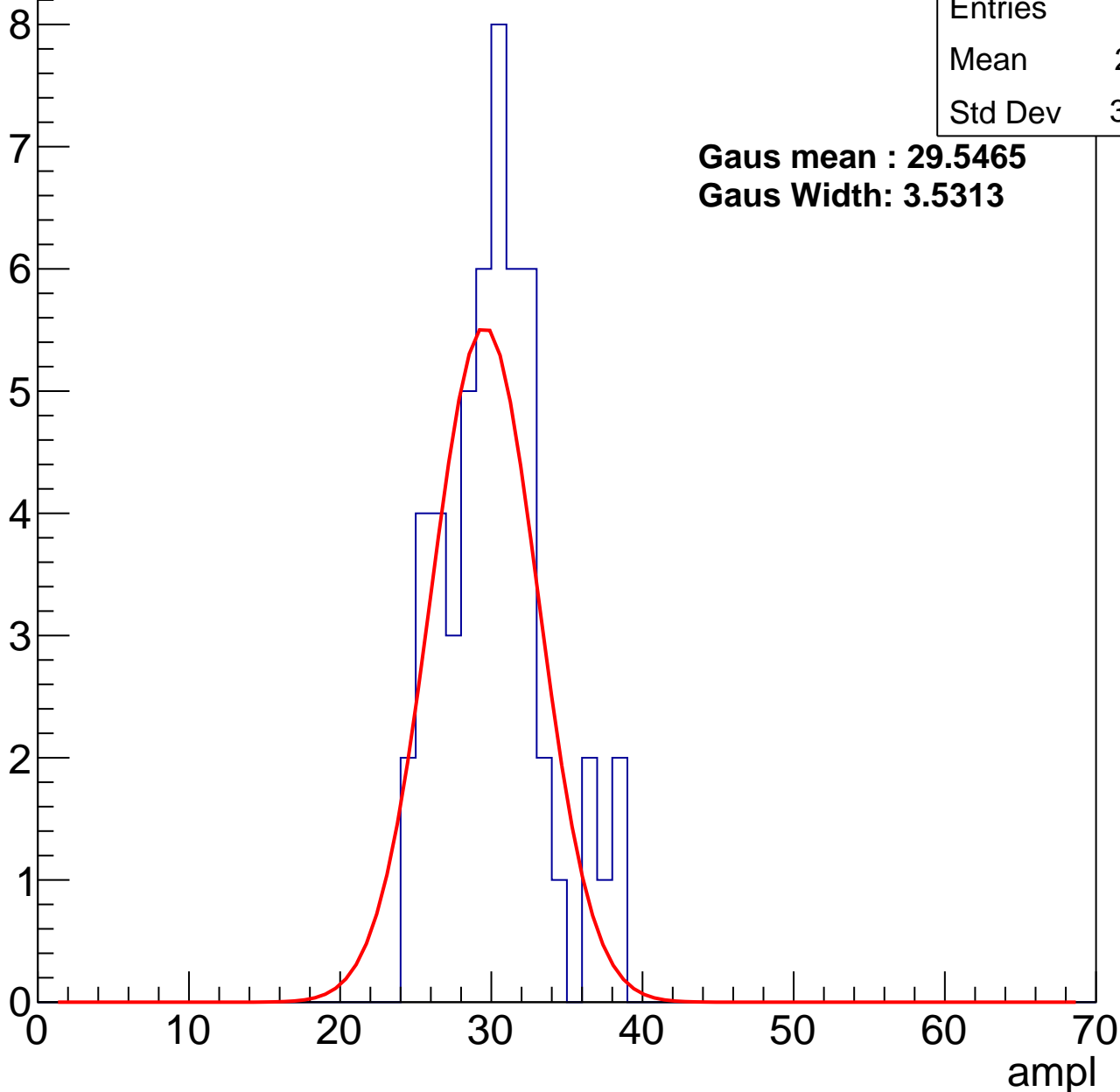
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 29.81 |
| Std Dev | 3.397 |

Gaus mean : 29.5465

Gaus Width: 3.5313



B0L001S, U7-ch118, adc1

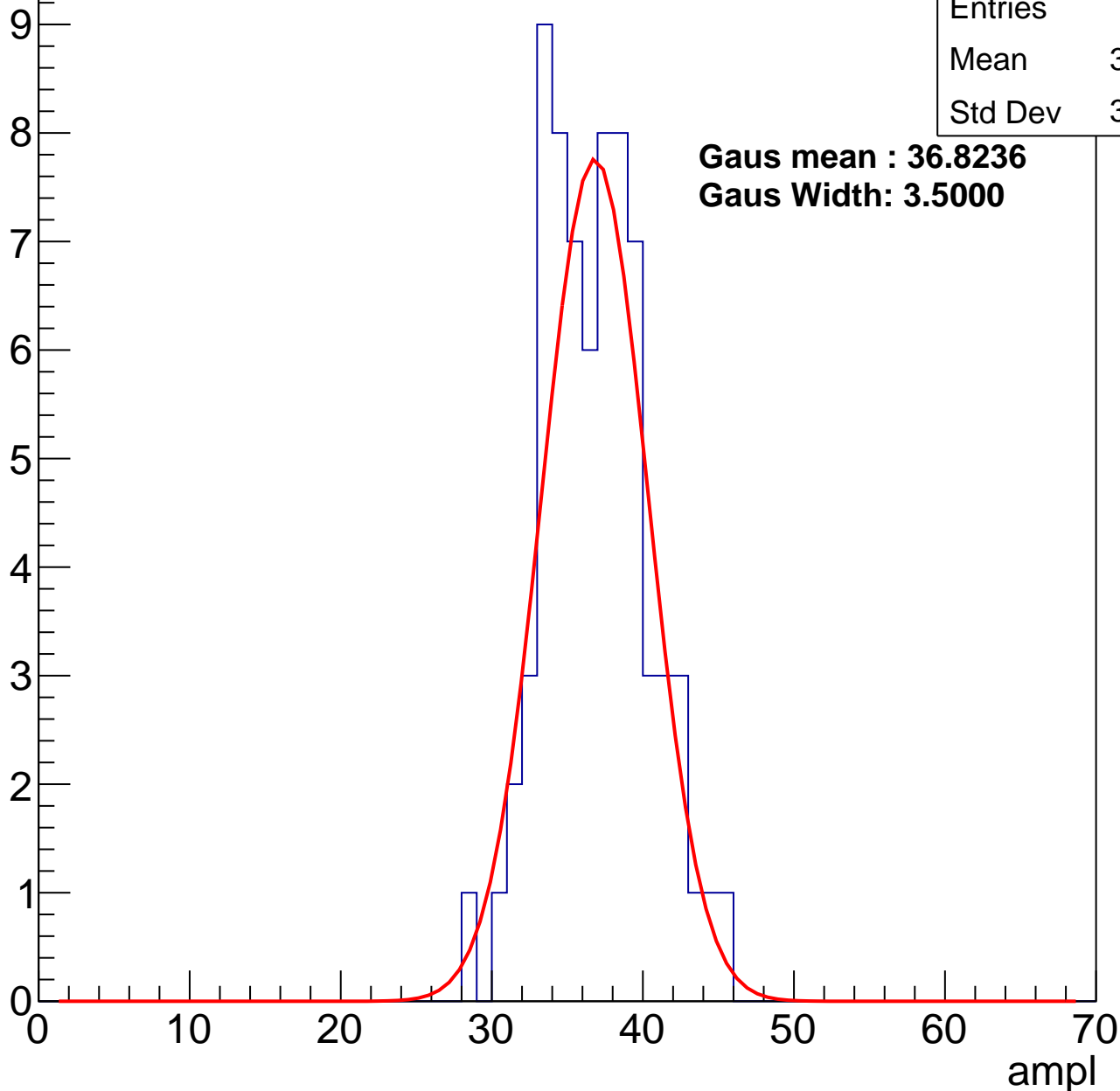
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 36.39 |
| Std Dev | 3.406 |

Gaus mean : 36.8236

Gaus Width: 3.5000



B0L001S, U7-ch118, adc2

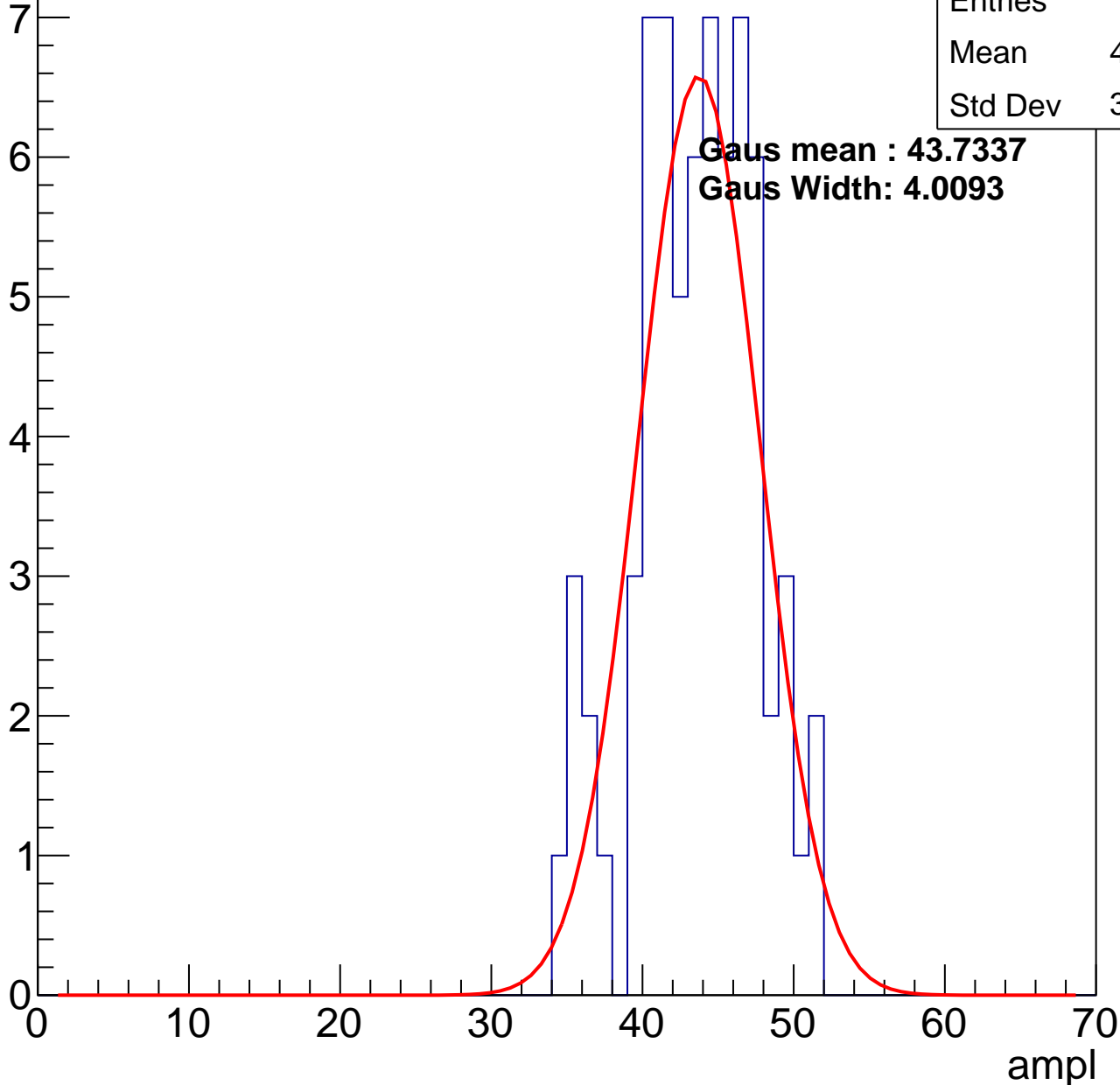
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.14 |
| Std Dev | 3.946 |

Gaus mean : 43.7337

Gaus Width: 4.0093

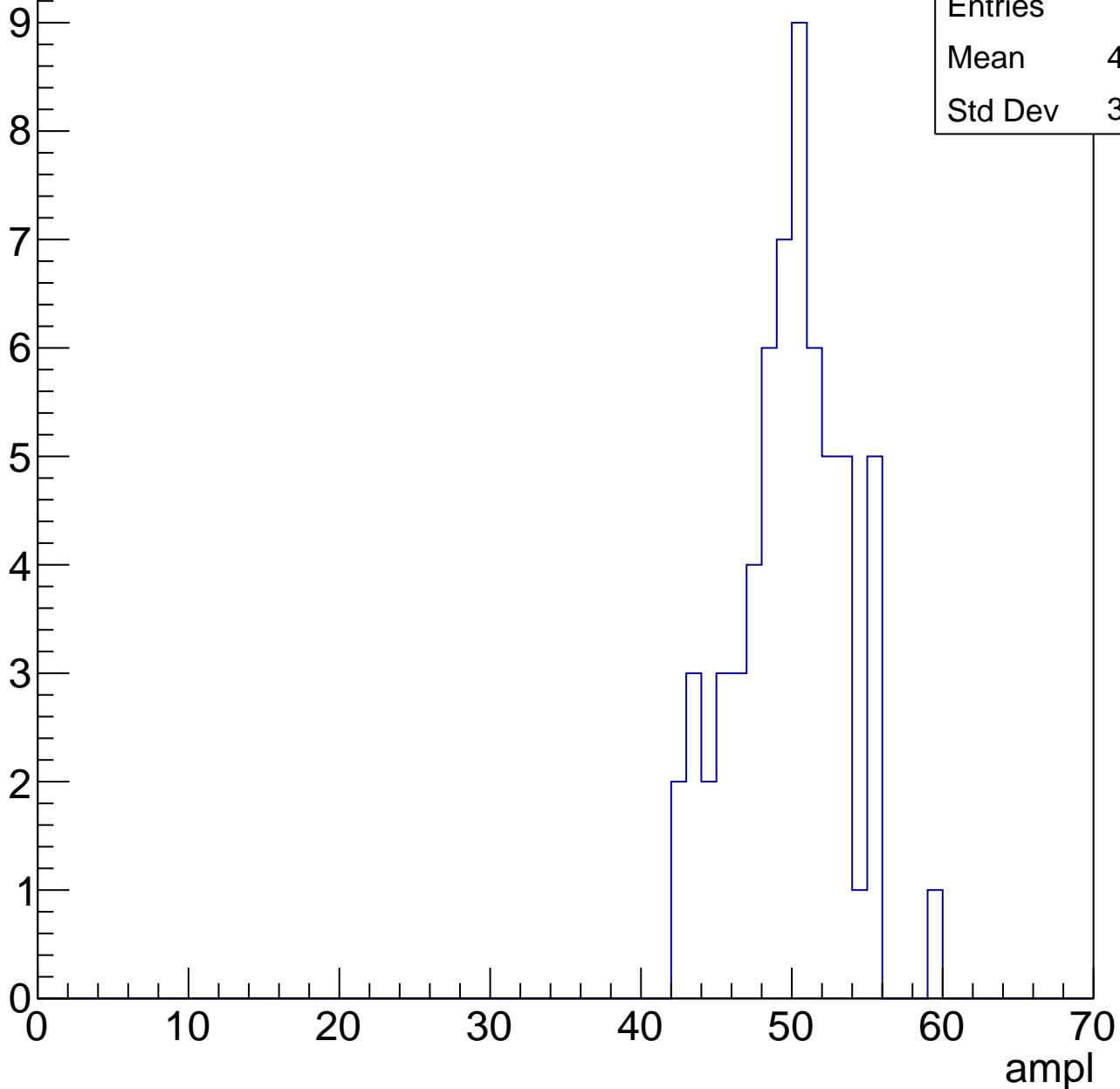


B0L001S, U7-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.39 |
| Std Dev | 3.607 |

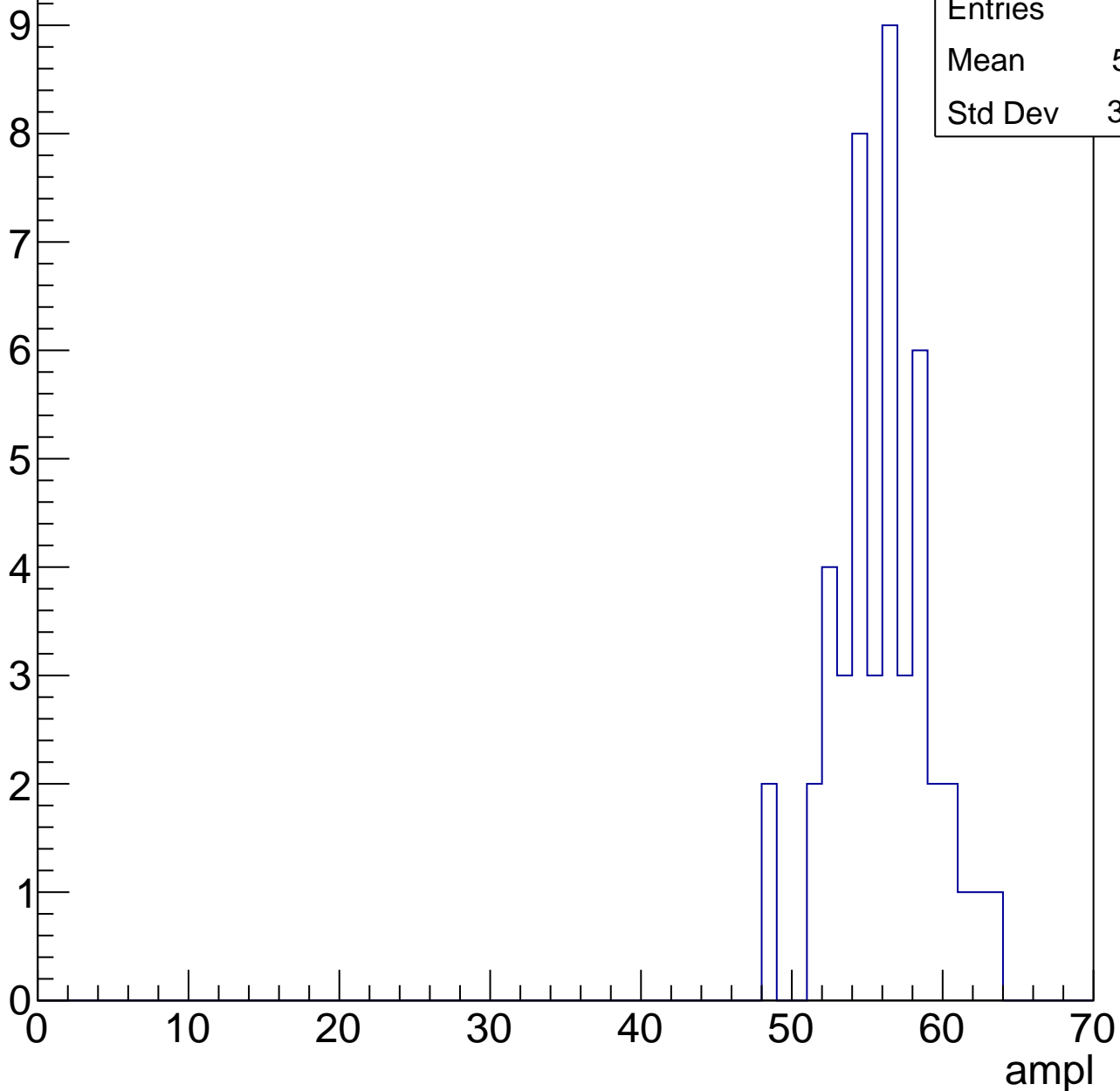


B0L001S, U7-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

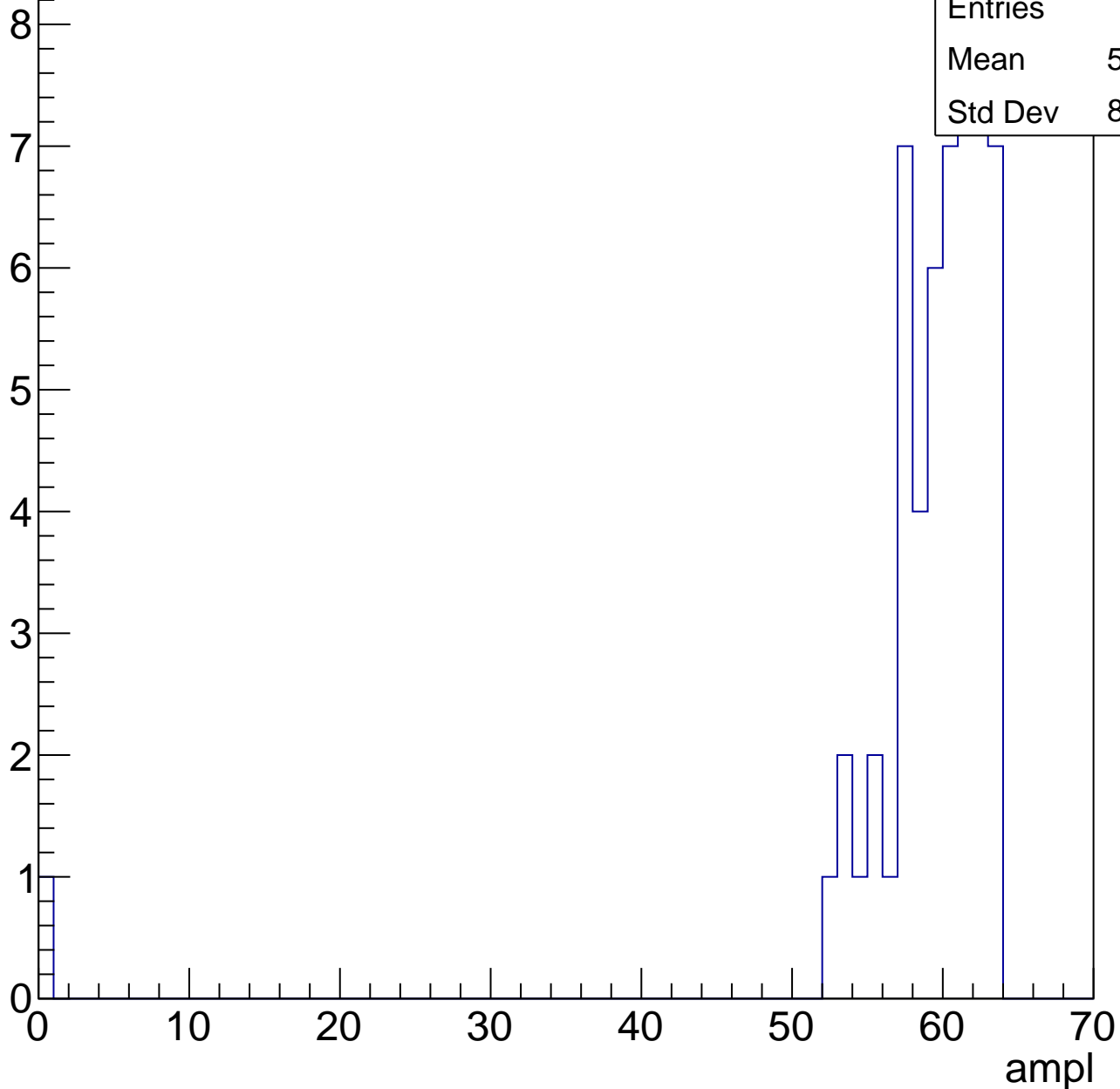
| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.51 |
| Std Dev | 3.195 |



B0L001S, U7-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

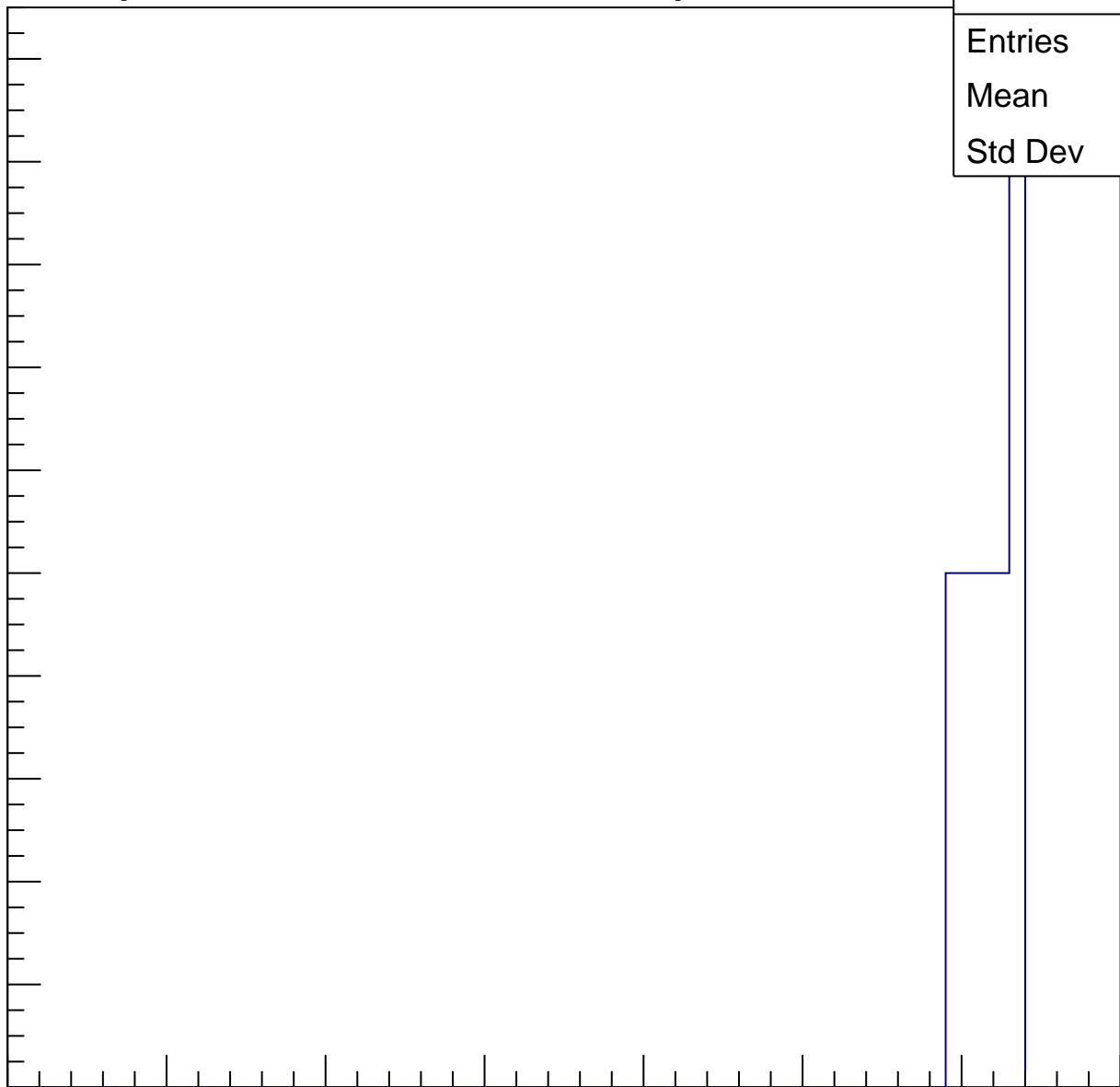
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 61.33 |
| Std Dev | 1.491 |

0 10 20 30 40 50 60 70

ampl



B0L001S, U7-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch119, adc0

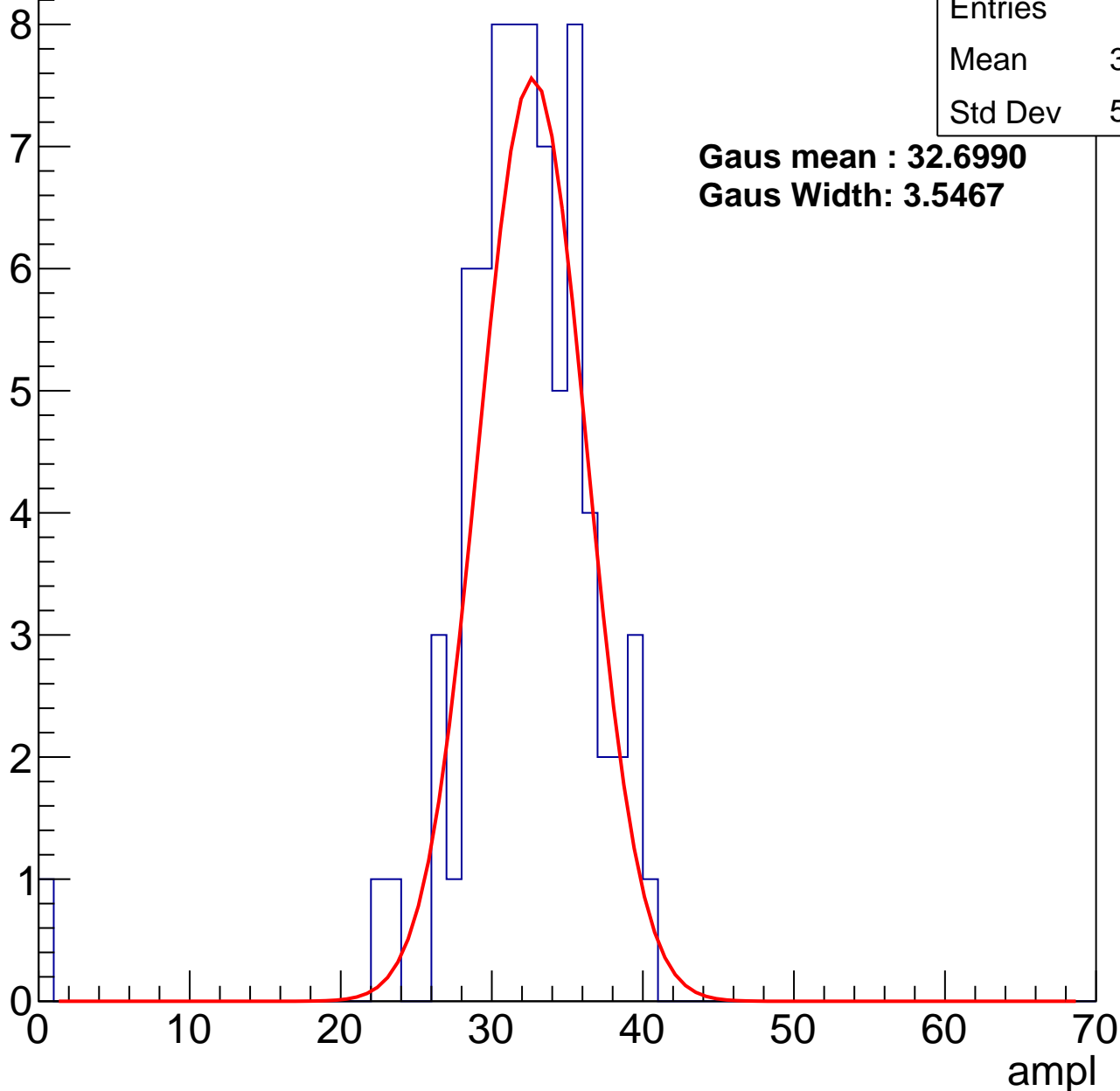
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 31.57 |
| Std Dev | 5.177 |

Gaus mean : 32.6990

Gaus Width: 3.5467



B0L001S, U7-ch119, adc1

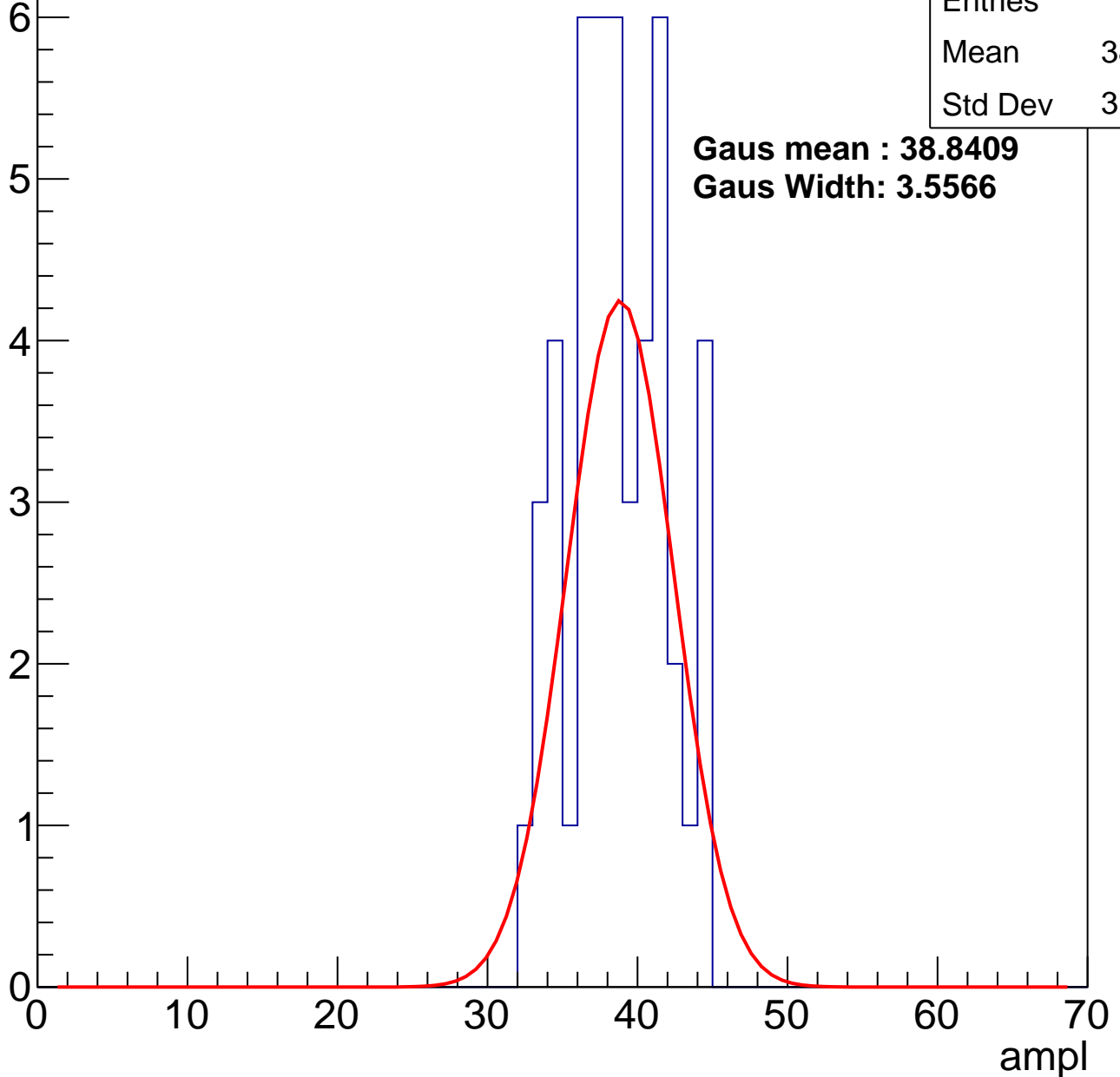
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 38.17 |
| Std Dev | 3.218 |

Gaus mean : 38.8409

Gaus Width: 3.5566



B0L001S, U7-ch119, adc2

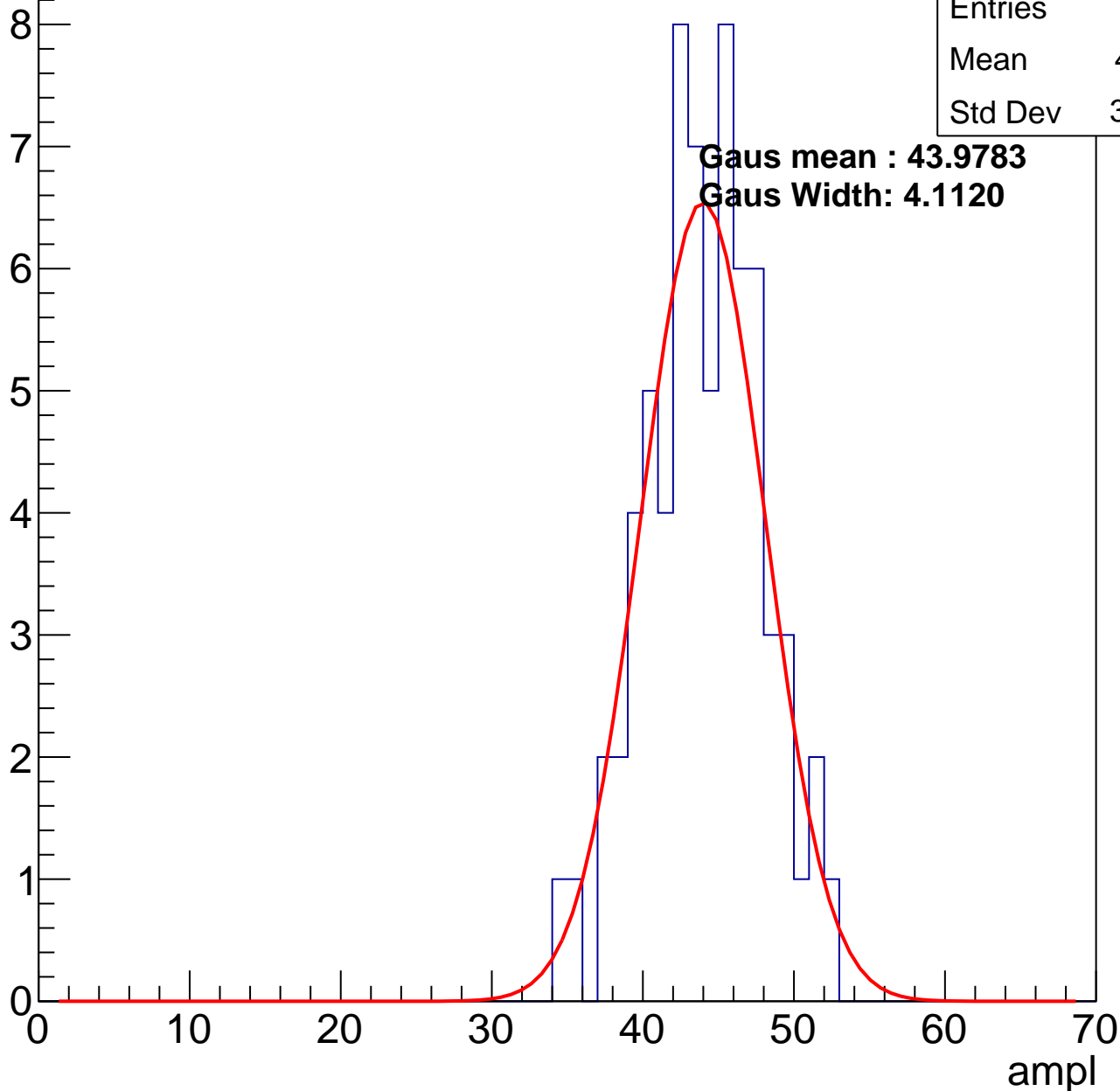
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 43.61 |
| Std Dev | 3.812 |

Gaus mean : 43.9783

Gaus Width: 4.1120

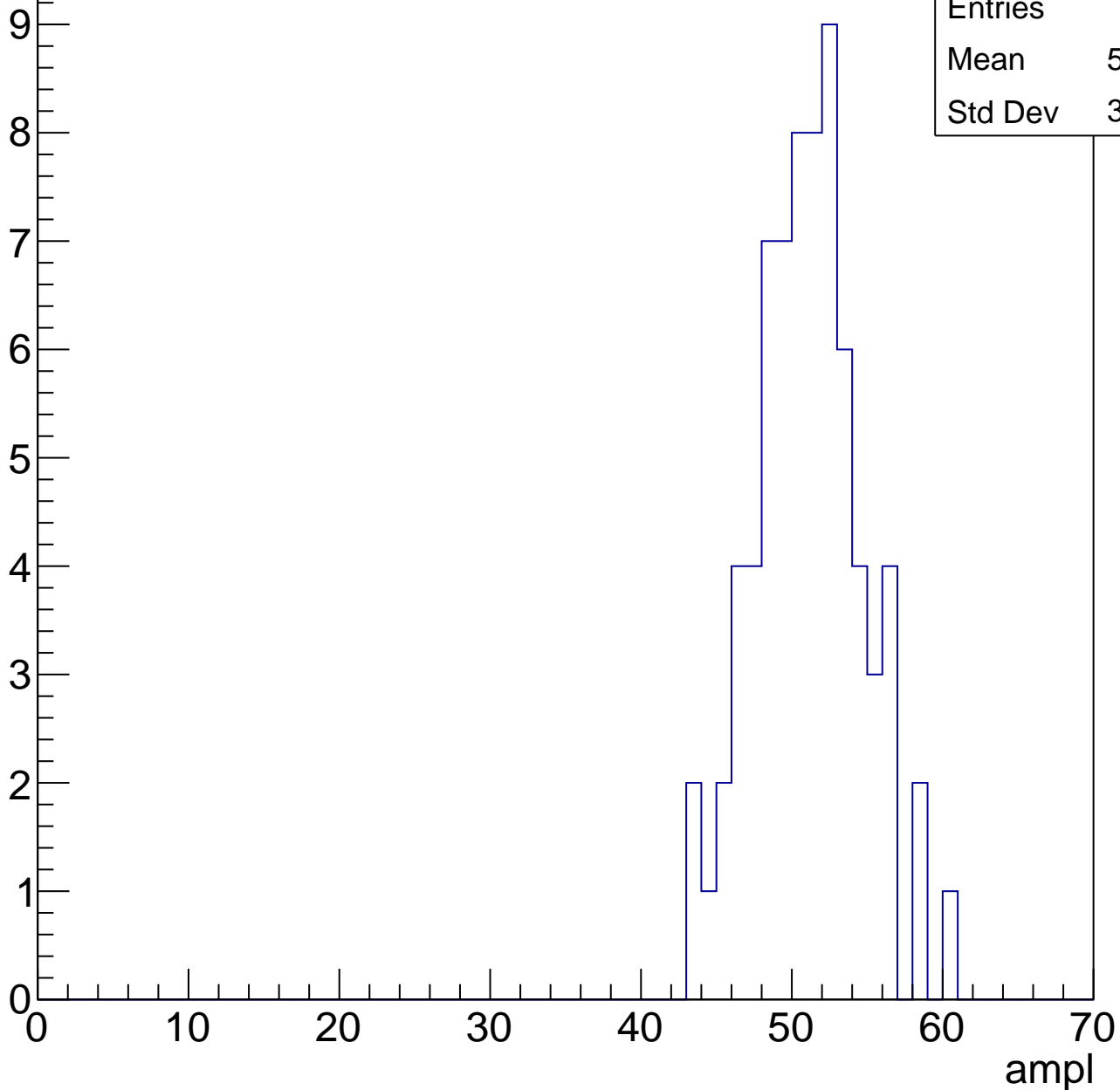


B0L001S, U7-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 50.64 |
| Std Dev | 3.533 |

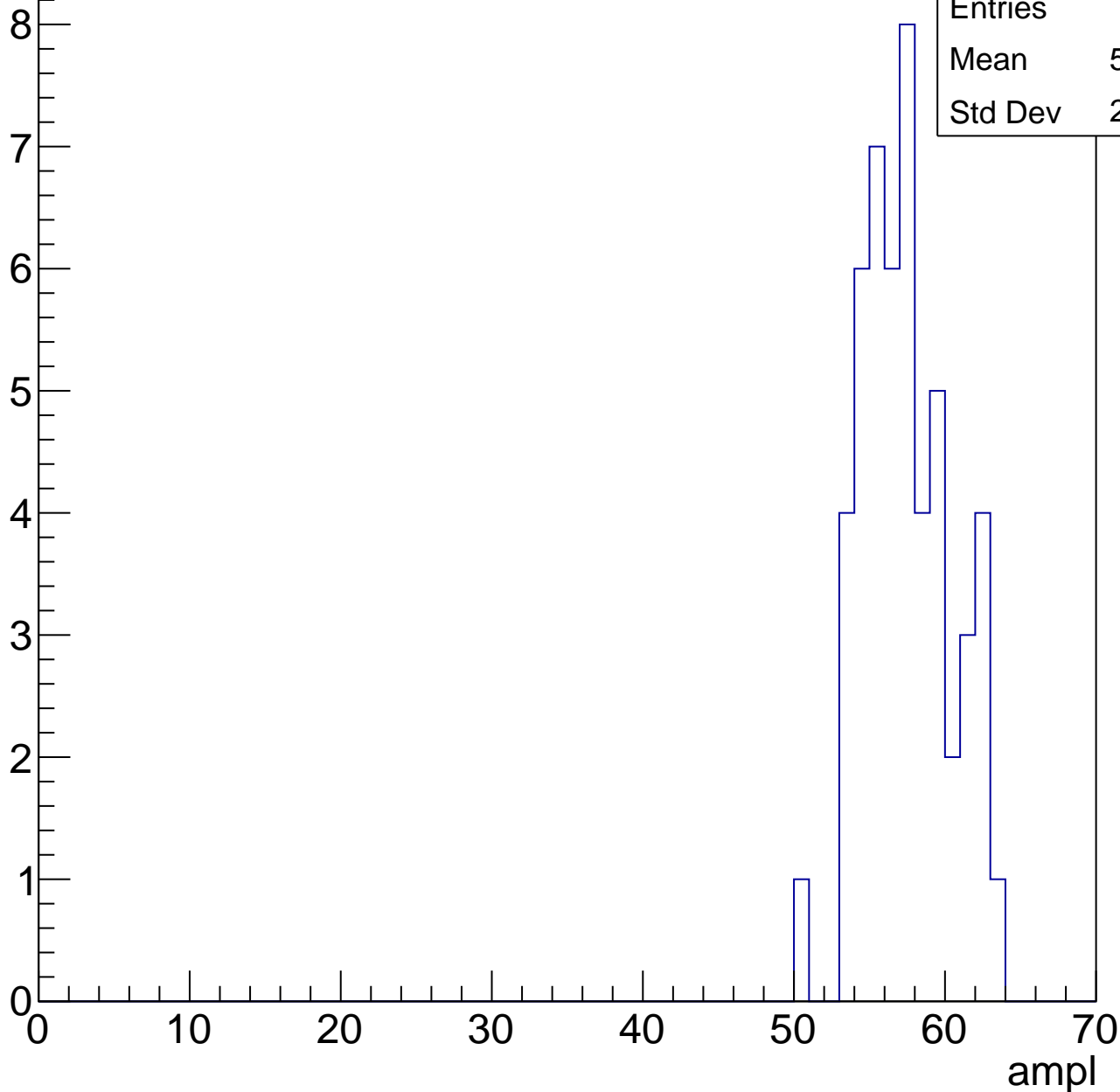


B0L001S, U7-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 56.94 |
| Std Dev | 2.893 |

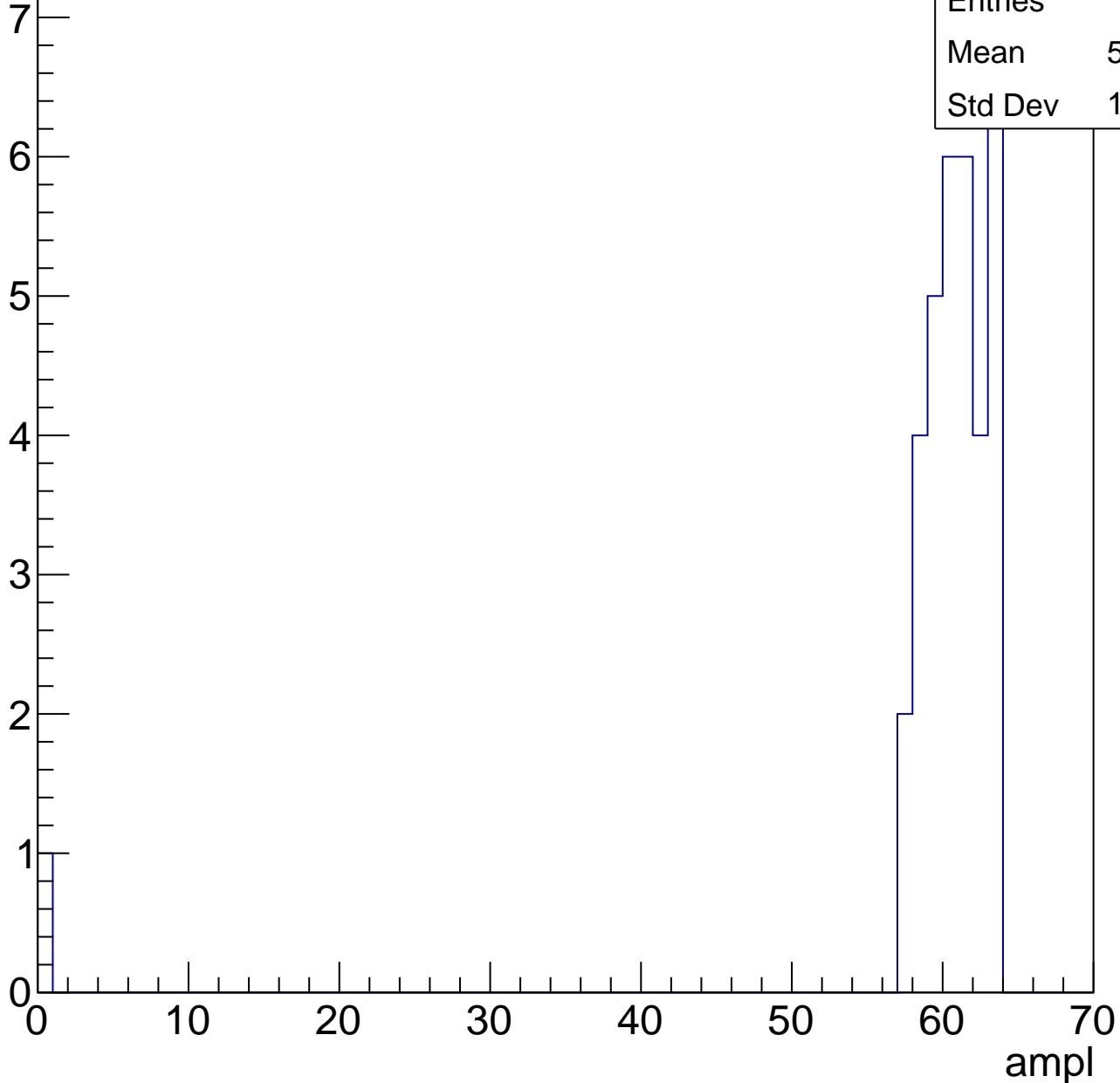


B0L001S, U7-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

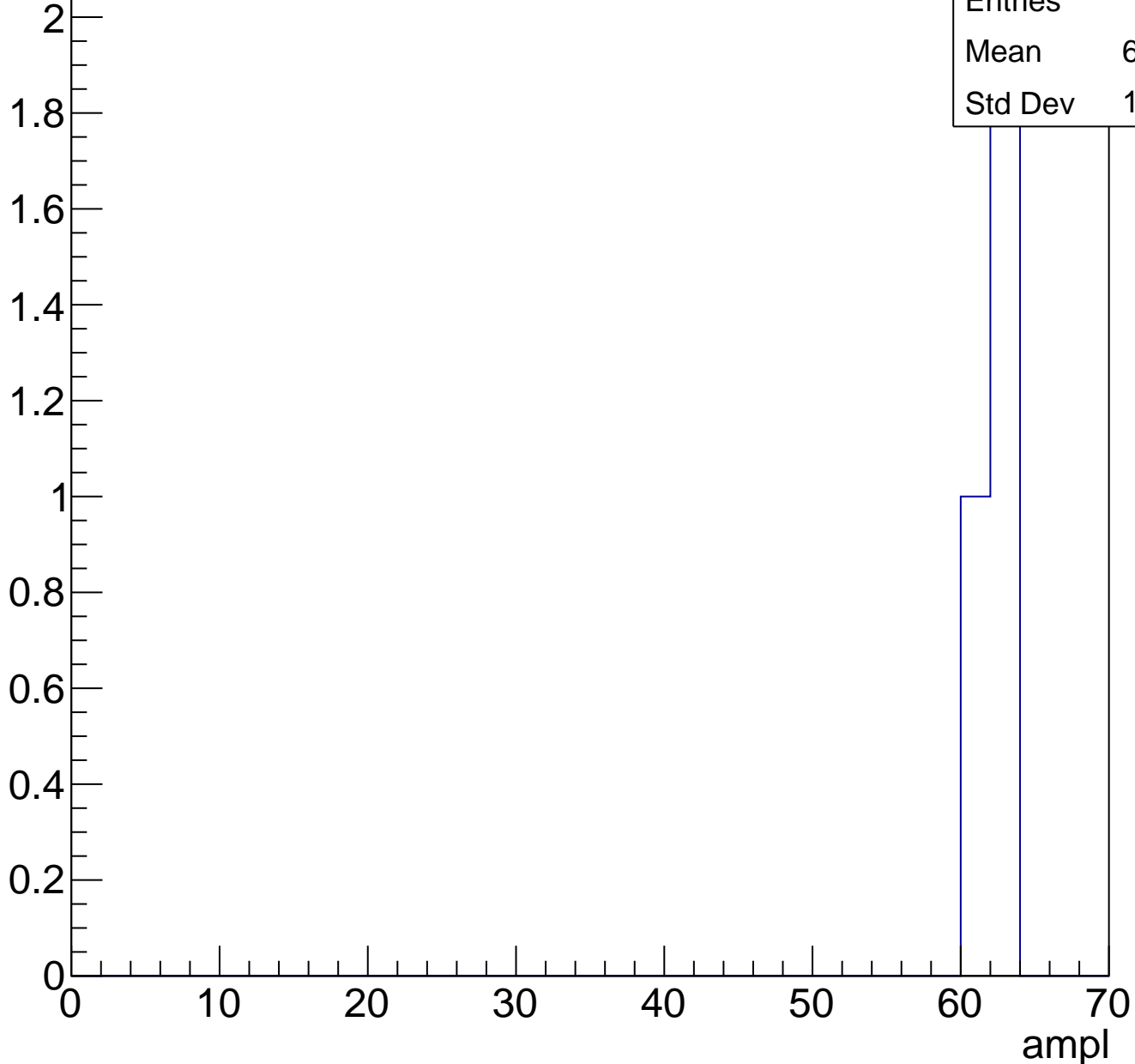
| | |
|---------|-------|
| Entries | 35 |
| Mean | 58.74 |
| Std Dev | 10.24 |



B0L001S, U7-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch120, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 69 |
| Mean | 29.55 |
| Std Dev | 5.975 |

Gaus mean : 31.0241

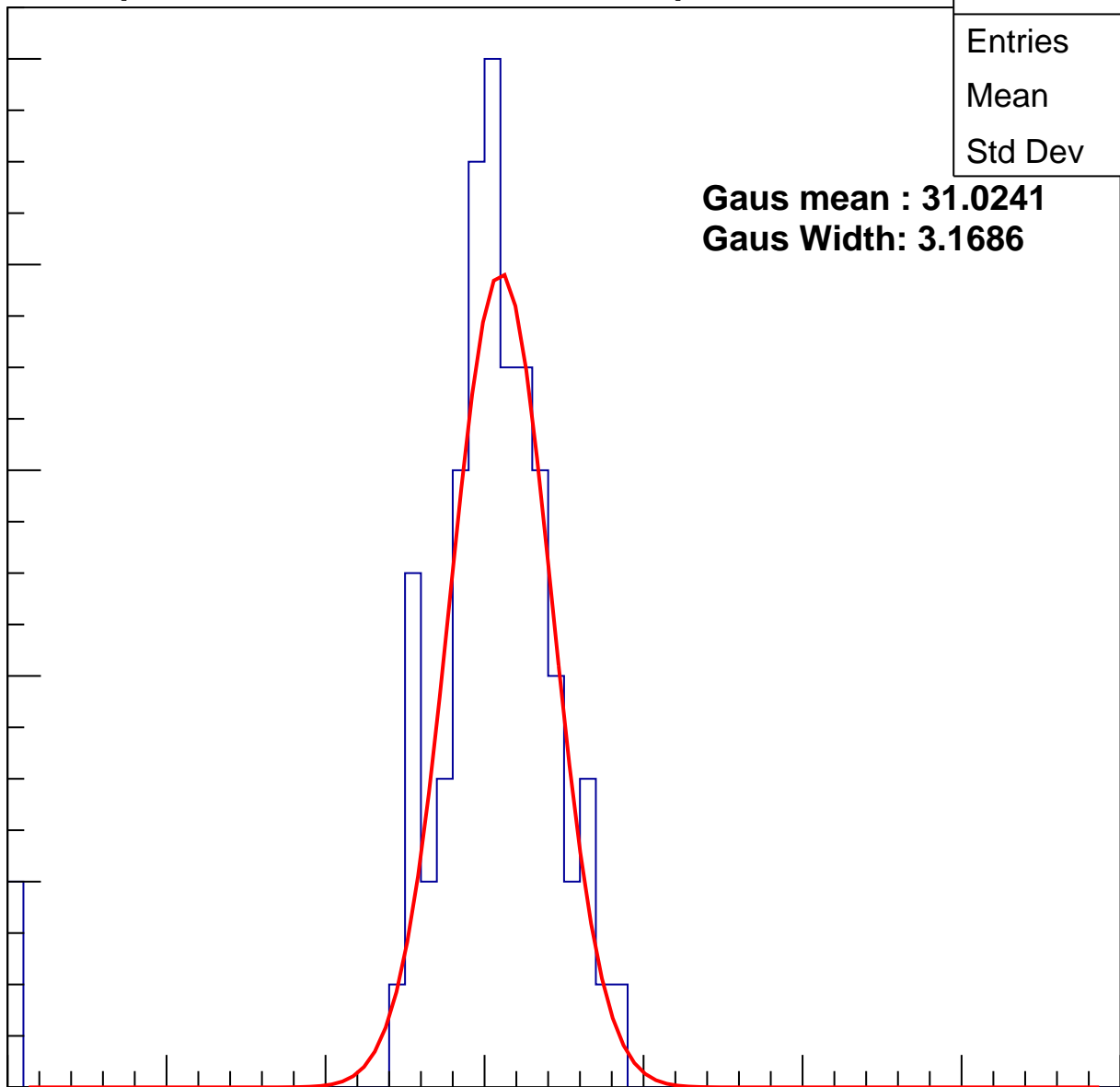
Gaus Width: 3.1686

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch120, adc1

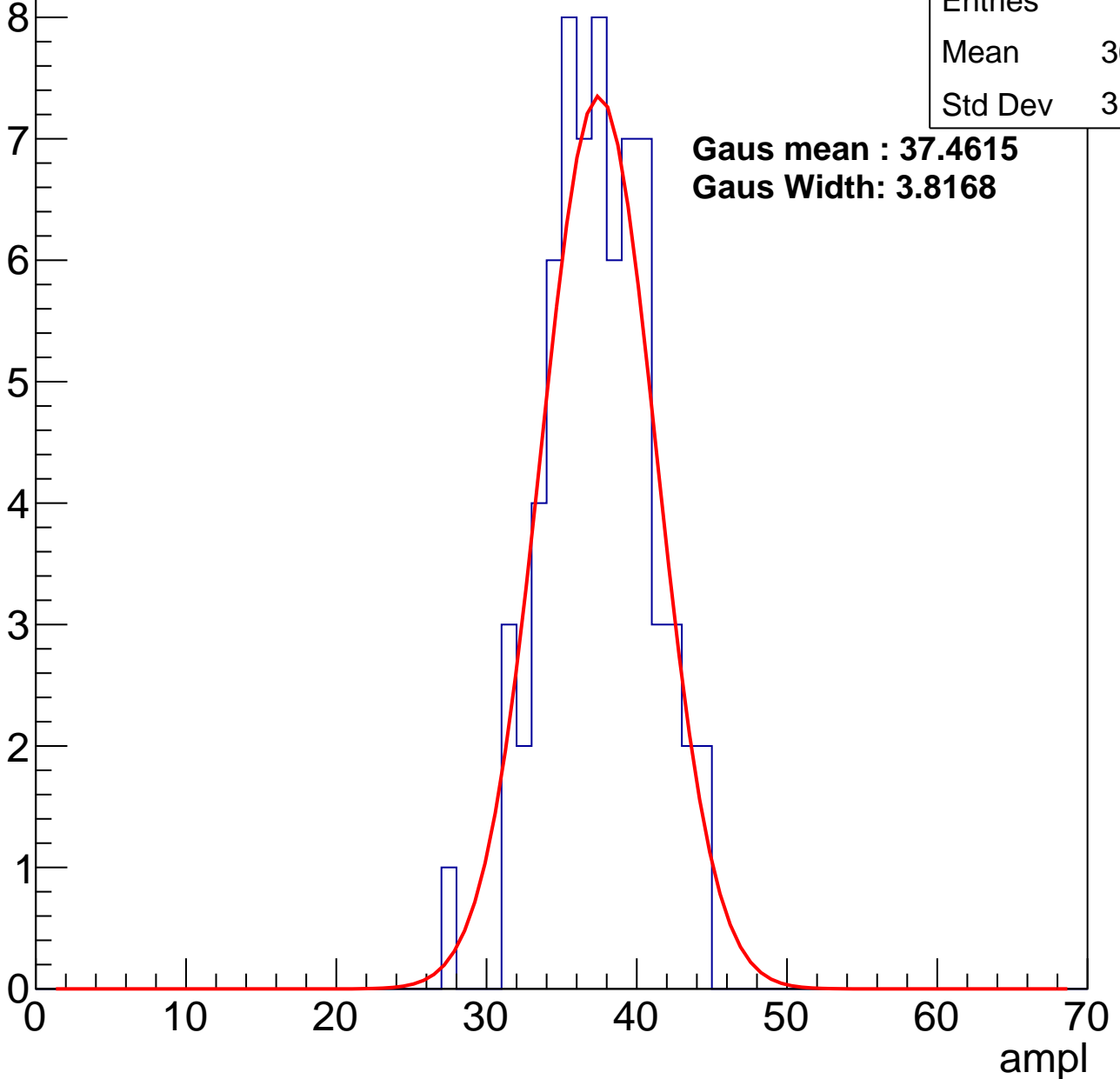
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.99 |
| Std Dev | 3.416 |

Gaus mean : 37.4615

Gaus Width: 3.8168



B0L001S, U7-ch120, adc2

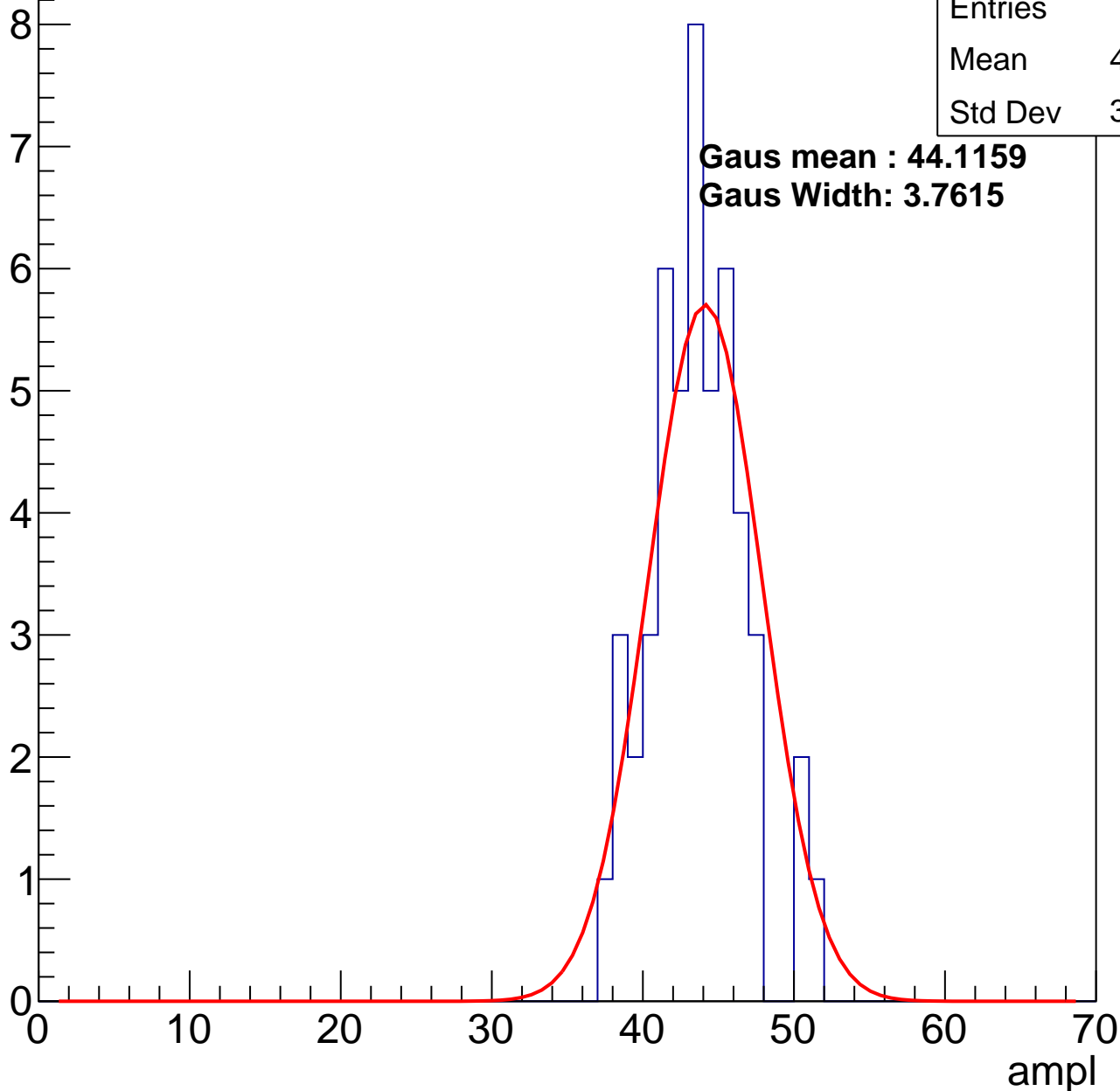
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 43.16 |
| Std Dev | 3.106 |

Gaus mean : 44.1159

Gaus Width: 3.7615

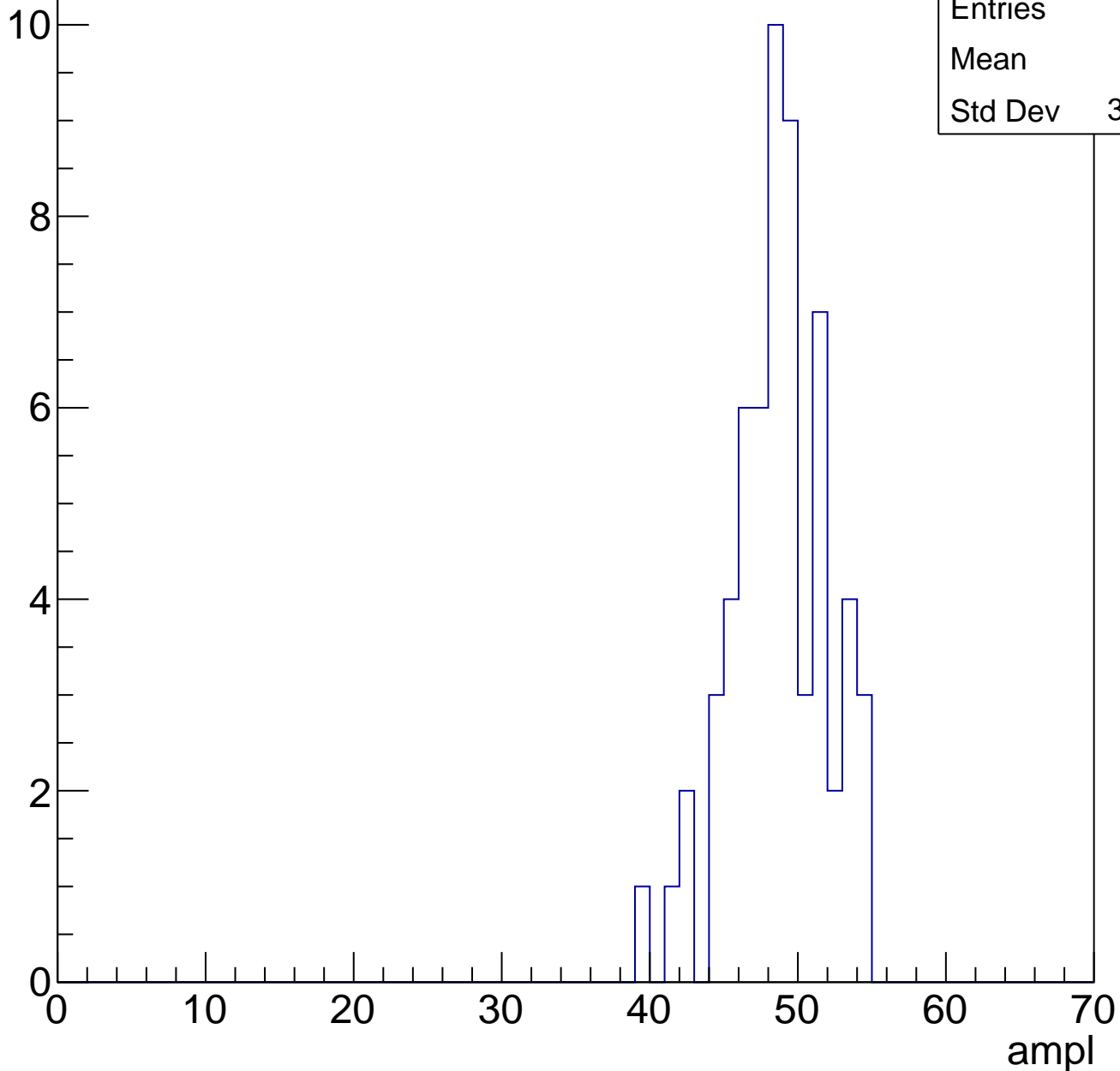


B0L001S, U7-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 61 |
| Mean | 48.2 |
| Std Dev | 3.233 |

Entry

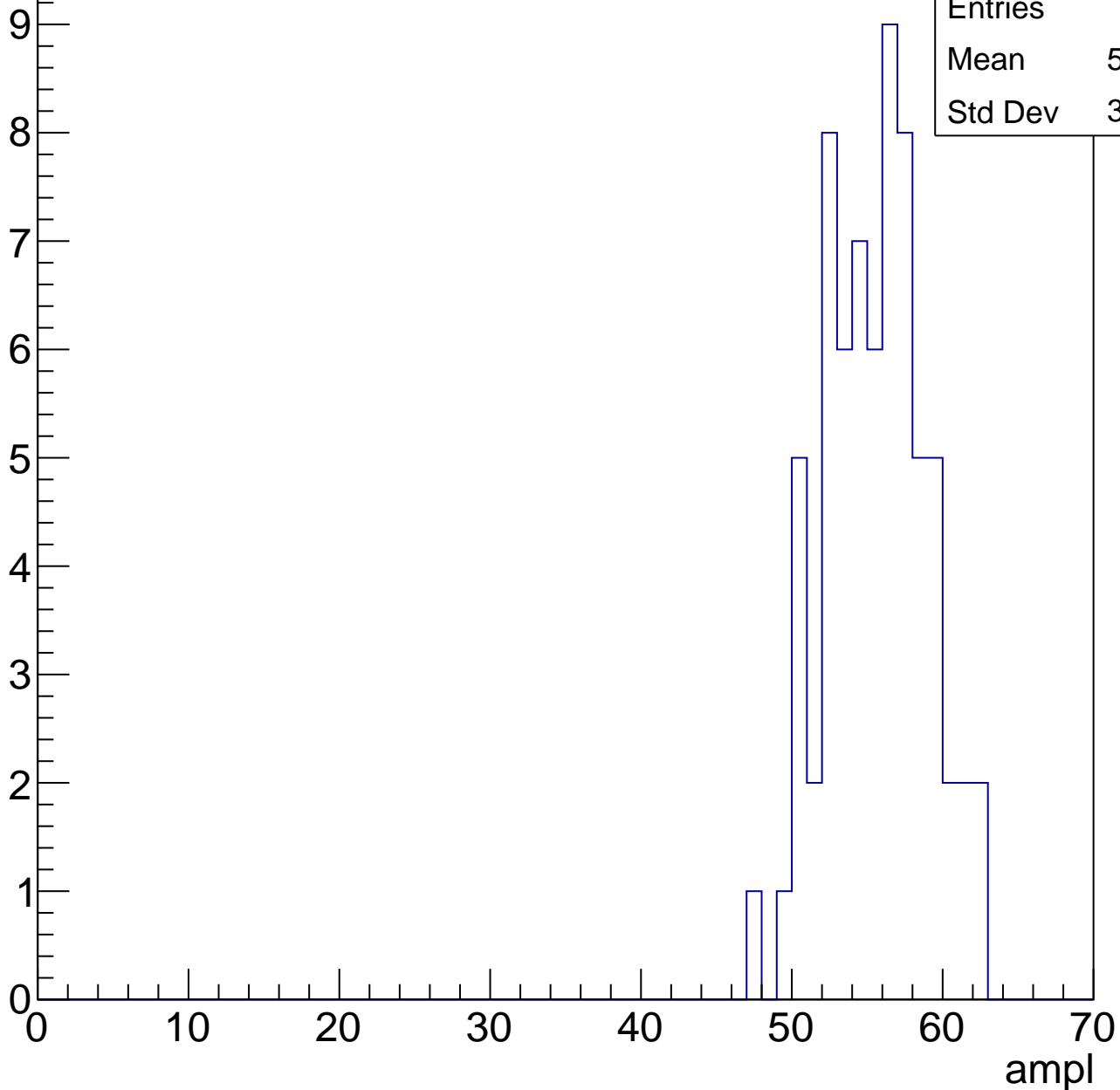


B0L001S, U7-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 55.09 |
| Std Dev | 3.287 |

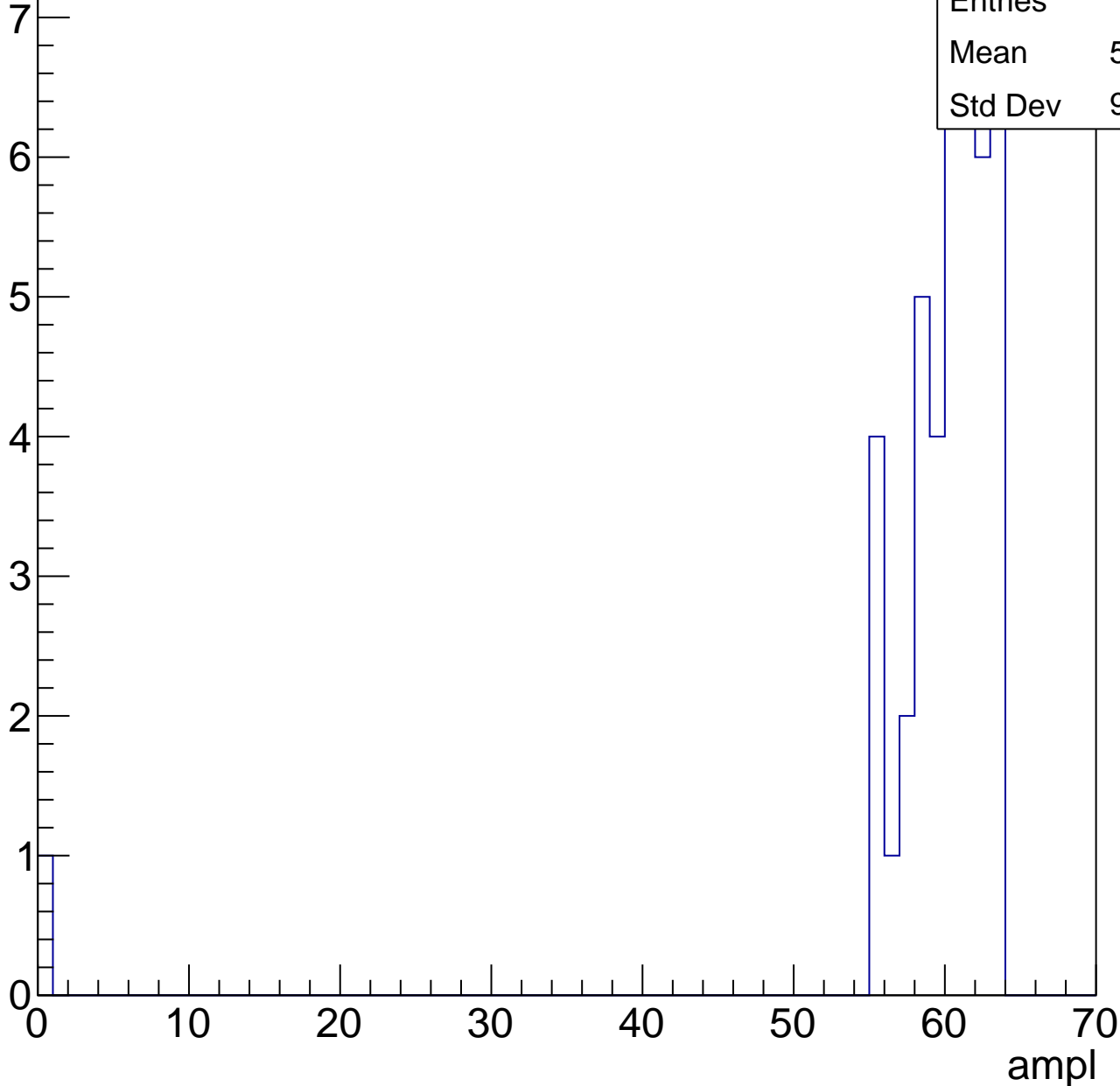


B0L001S, U7-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

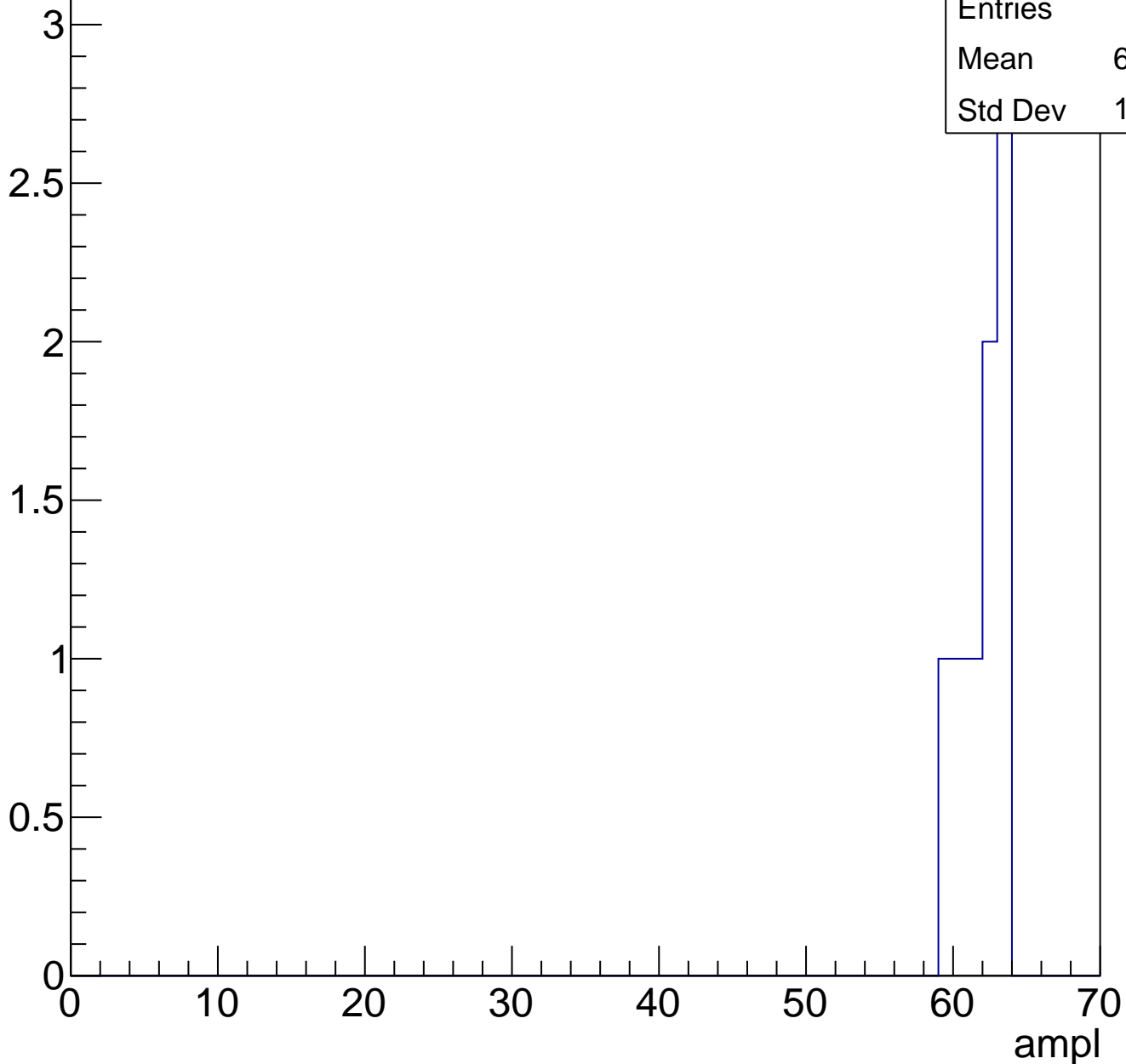
| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.55 |
| Std Dev | 9.243 |



B0L001S, U7-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch121, adc0

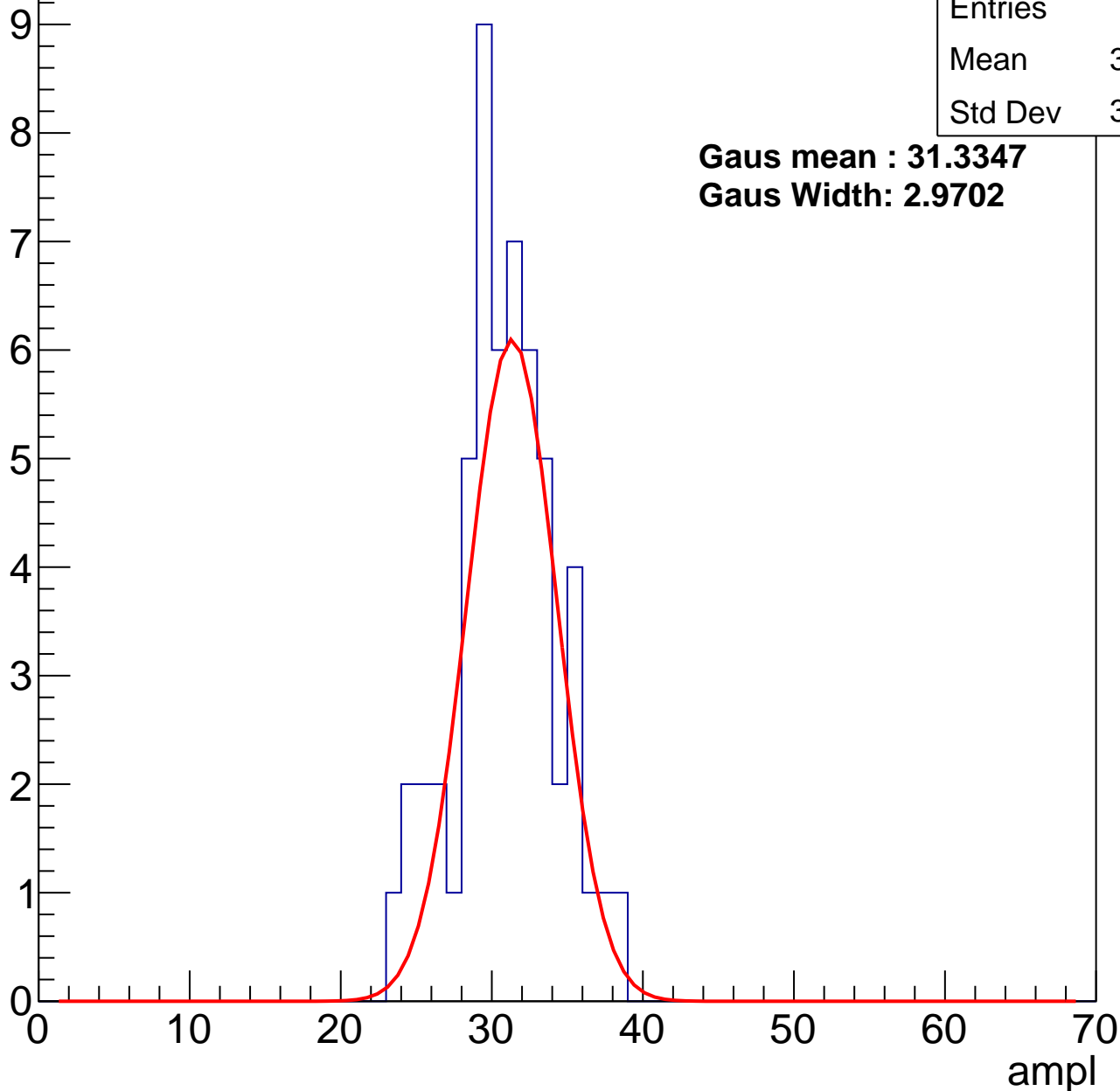
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 30.44 |
| Std Dev | 3.268 |

Gaus mean : 31.3347

Gaus Width: 2.9702



B0L001S, U7-ch121, adc1

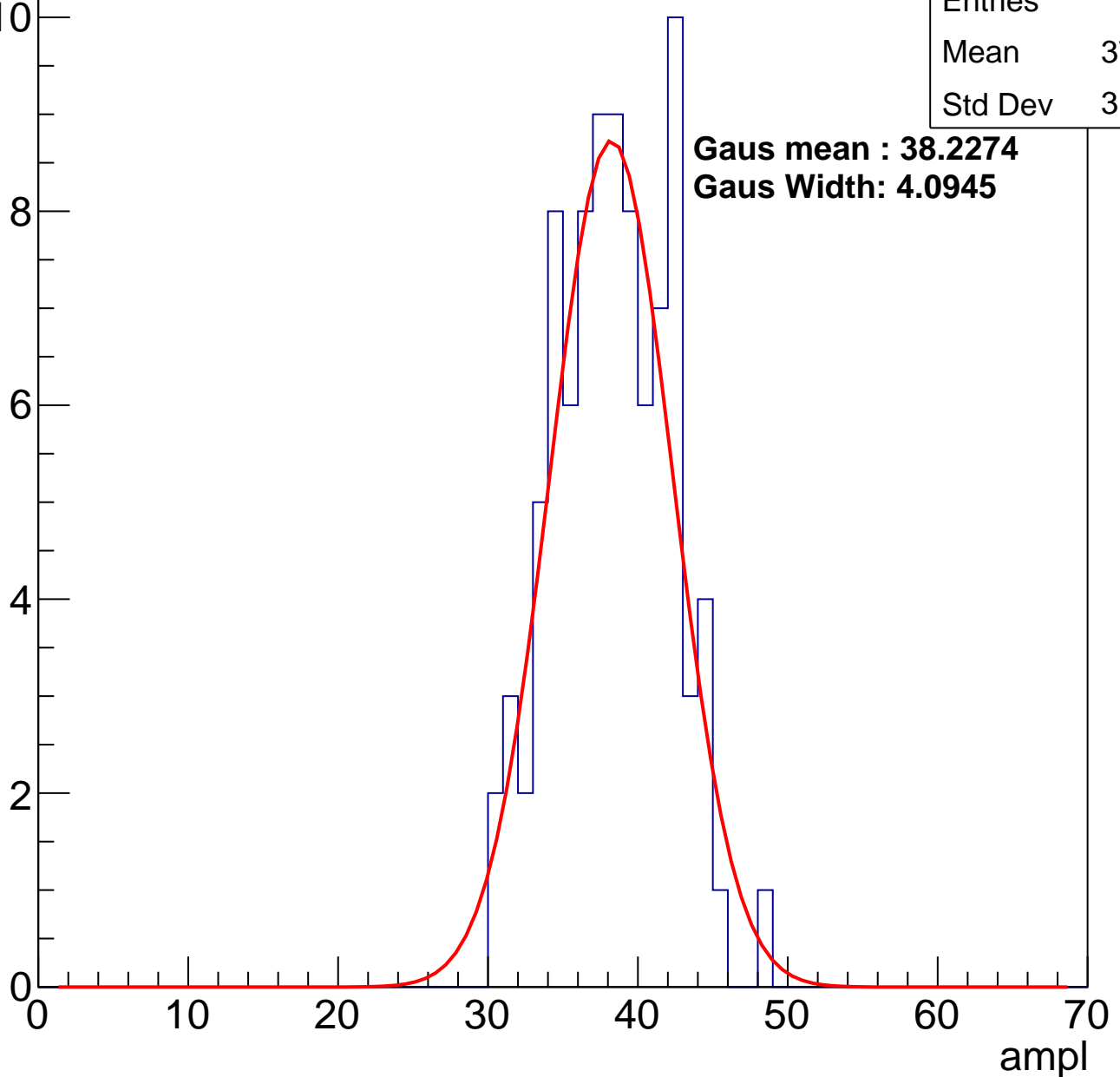
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 92 |
| Mean | 37.87 |
| Std Dev | 3.783 |

Gaus mean : 38.2274

Gaus Width: 4.0945



B0L001S, U7-ch121, adc2

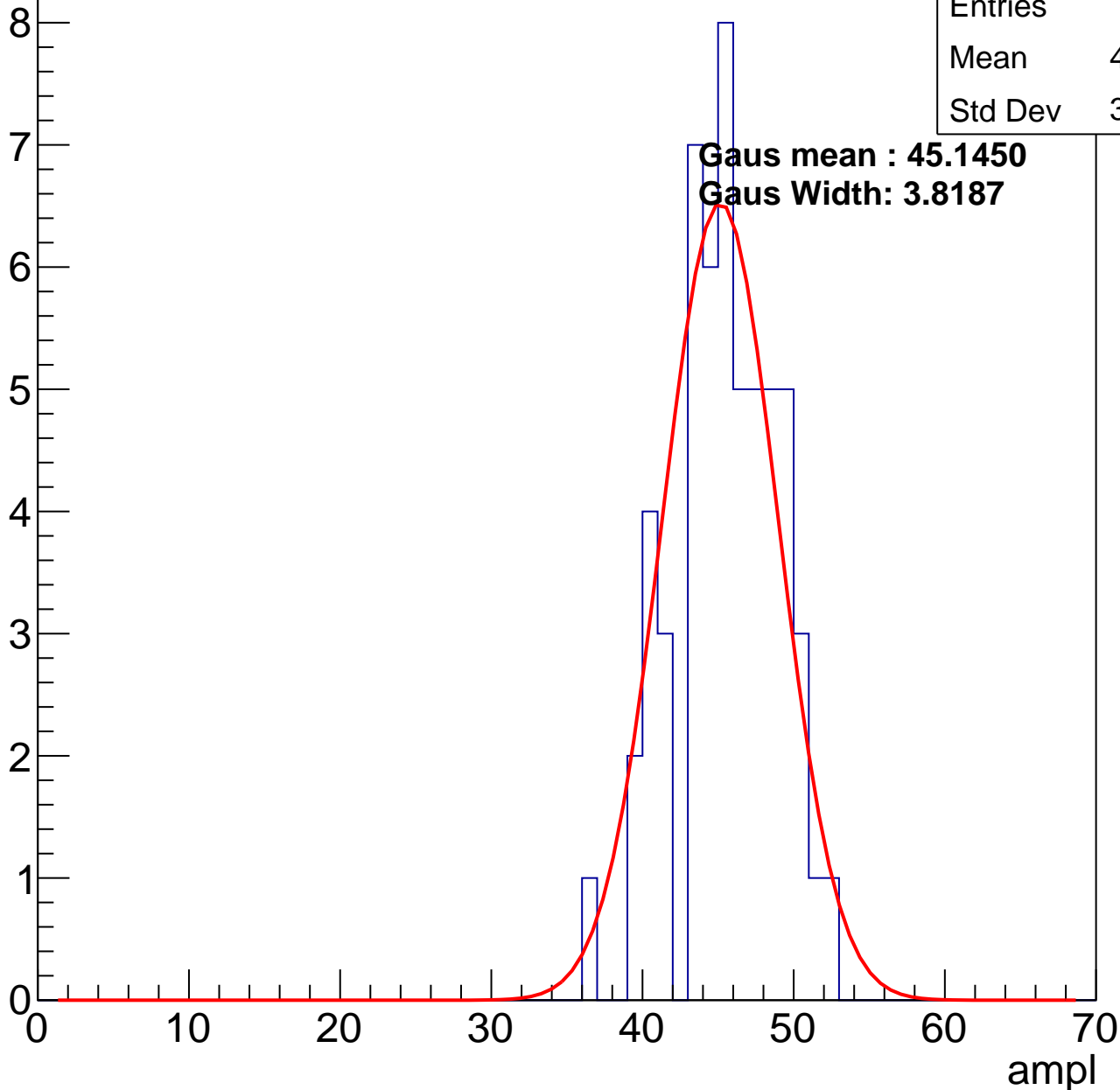
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 45.09 |
| Std Dev | 3.393 |

Gaus mean : 45.1450

Gaus Width: 3.8187

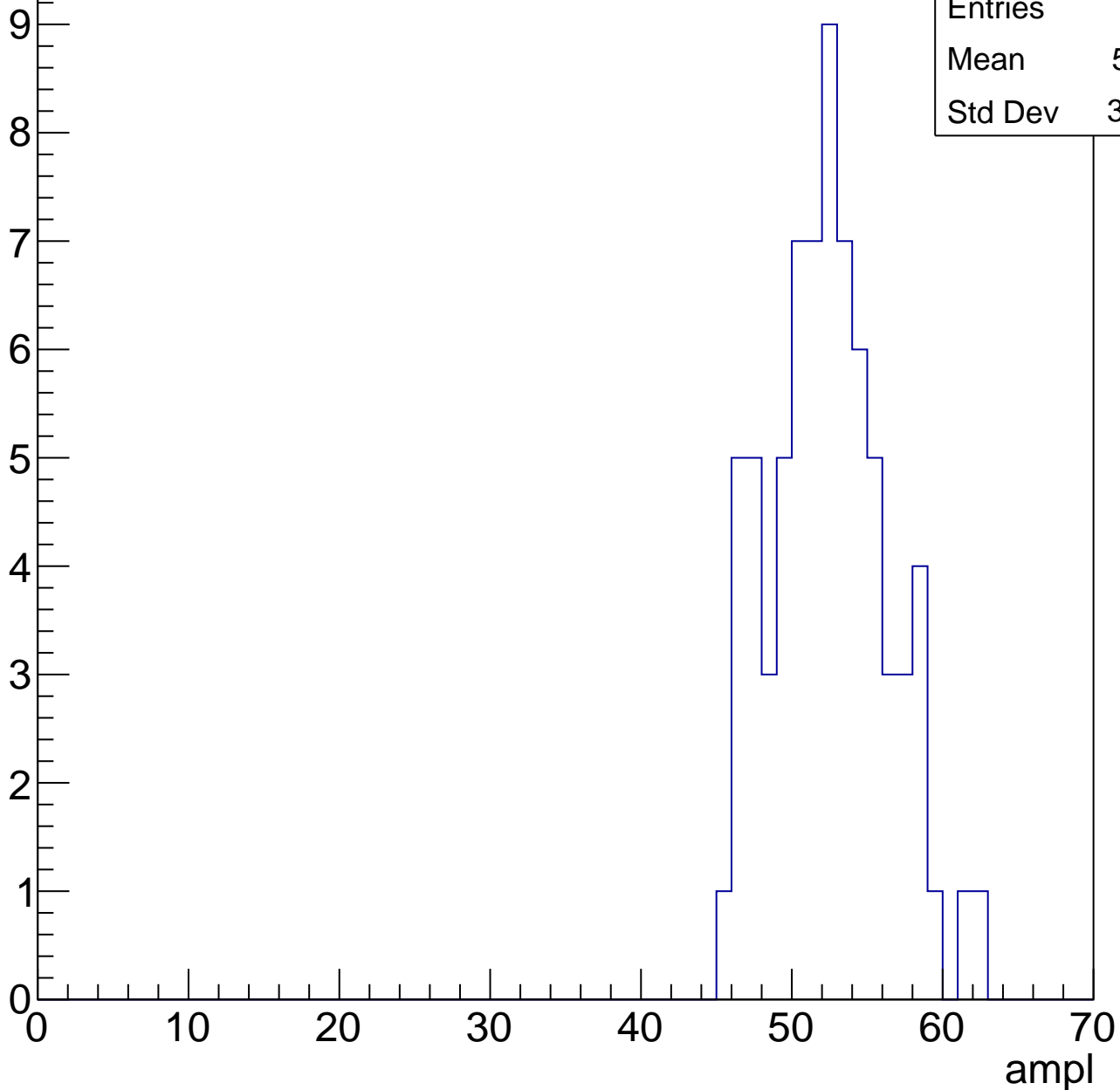


B0L001S, U7-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

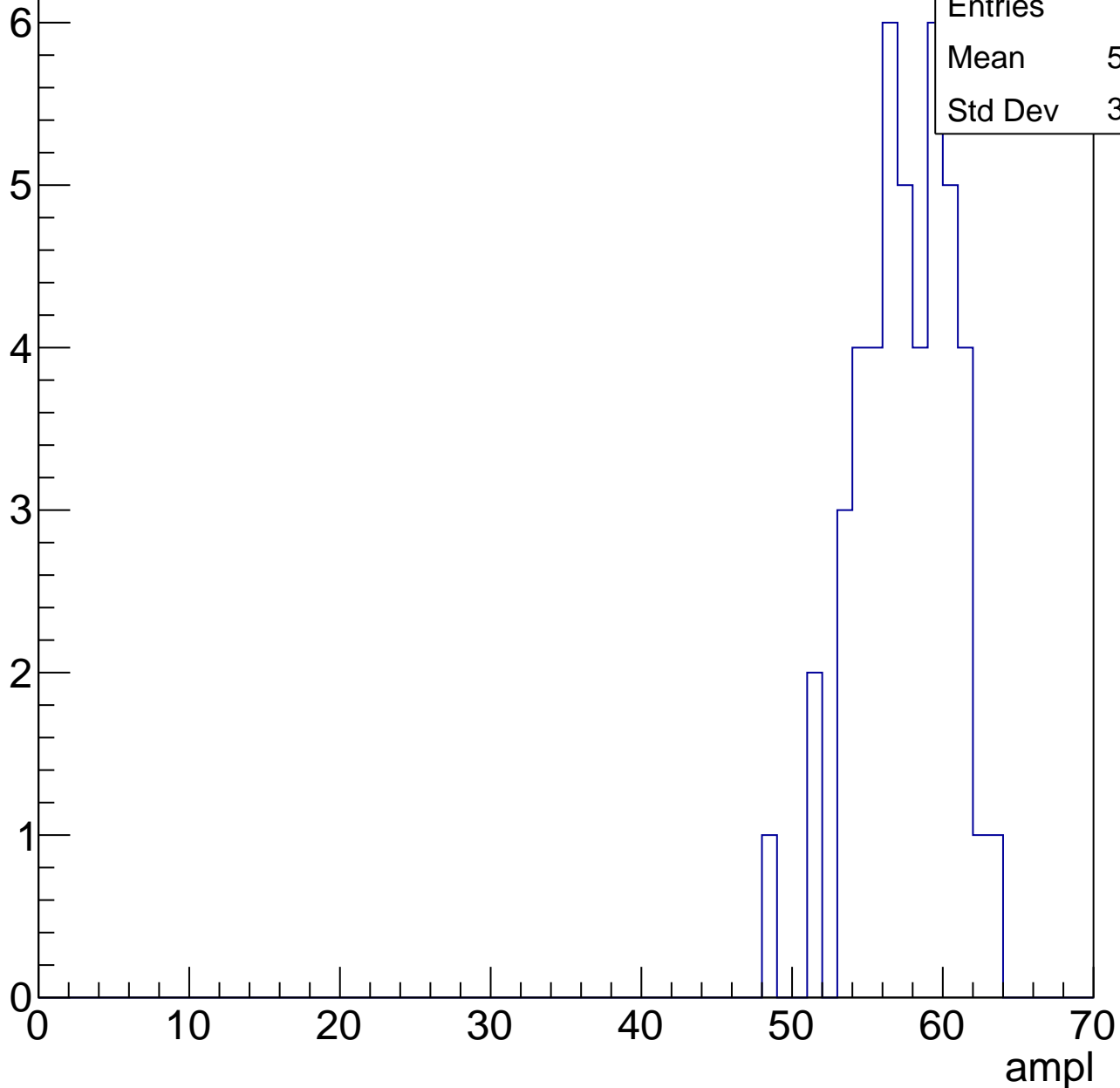
| | |
|---------|-------|
| Entries | 73 |
| Mean | 52.01 |
| Std Dev | 3.802 |



B0L001S, U7-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

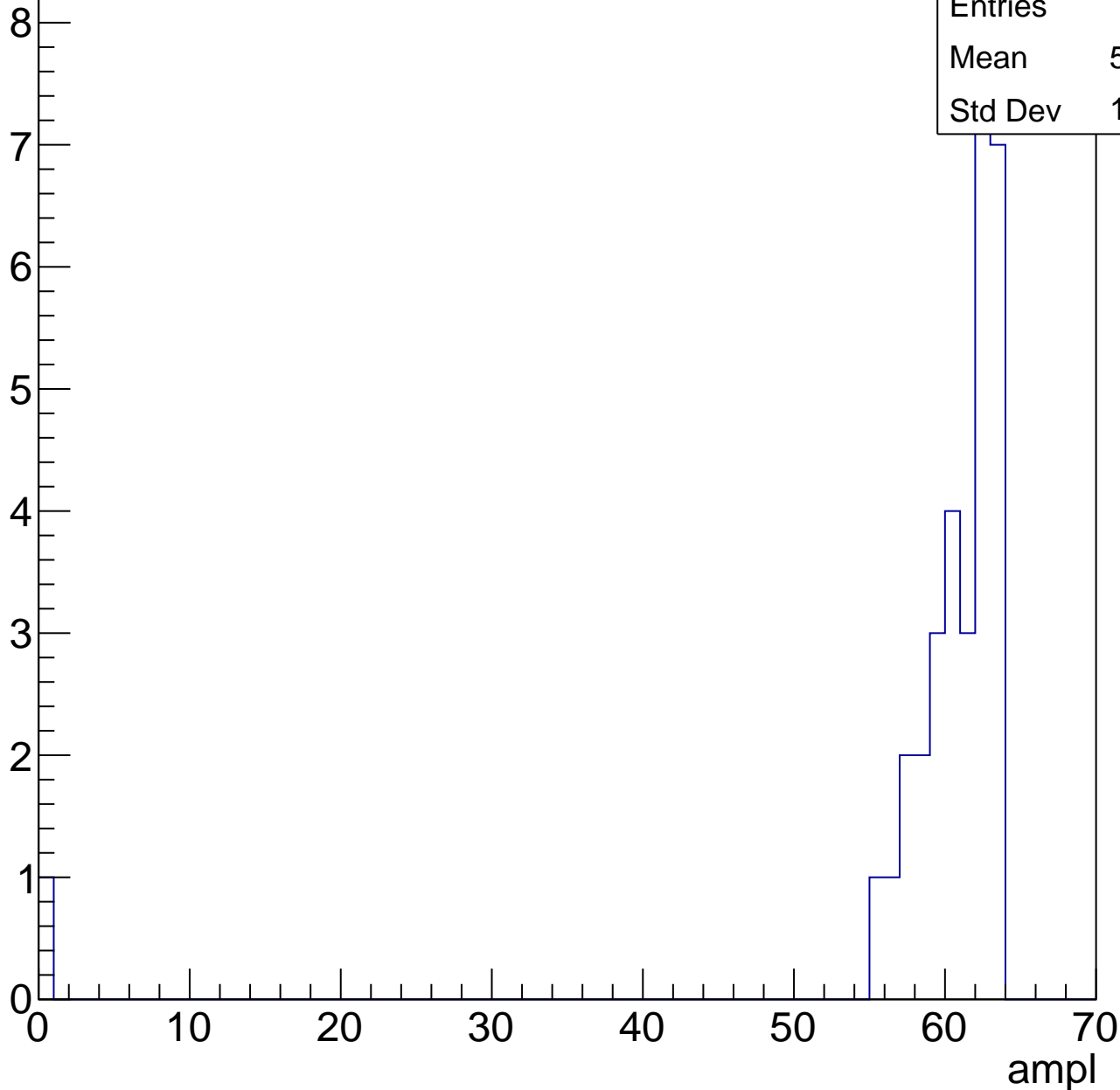


B0L001S, U7-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 58.69 |
| Std Dev | 10.77 |



B0L001S, U7-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|------|
| Entries | 5 |
| Mean | 61.6 |
| Std Dev | 1.2 |

ampl

0 10 20 30 40 50 60 70

B0L001S, U7-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch122, adc0

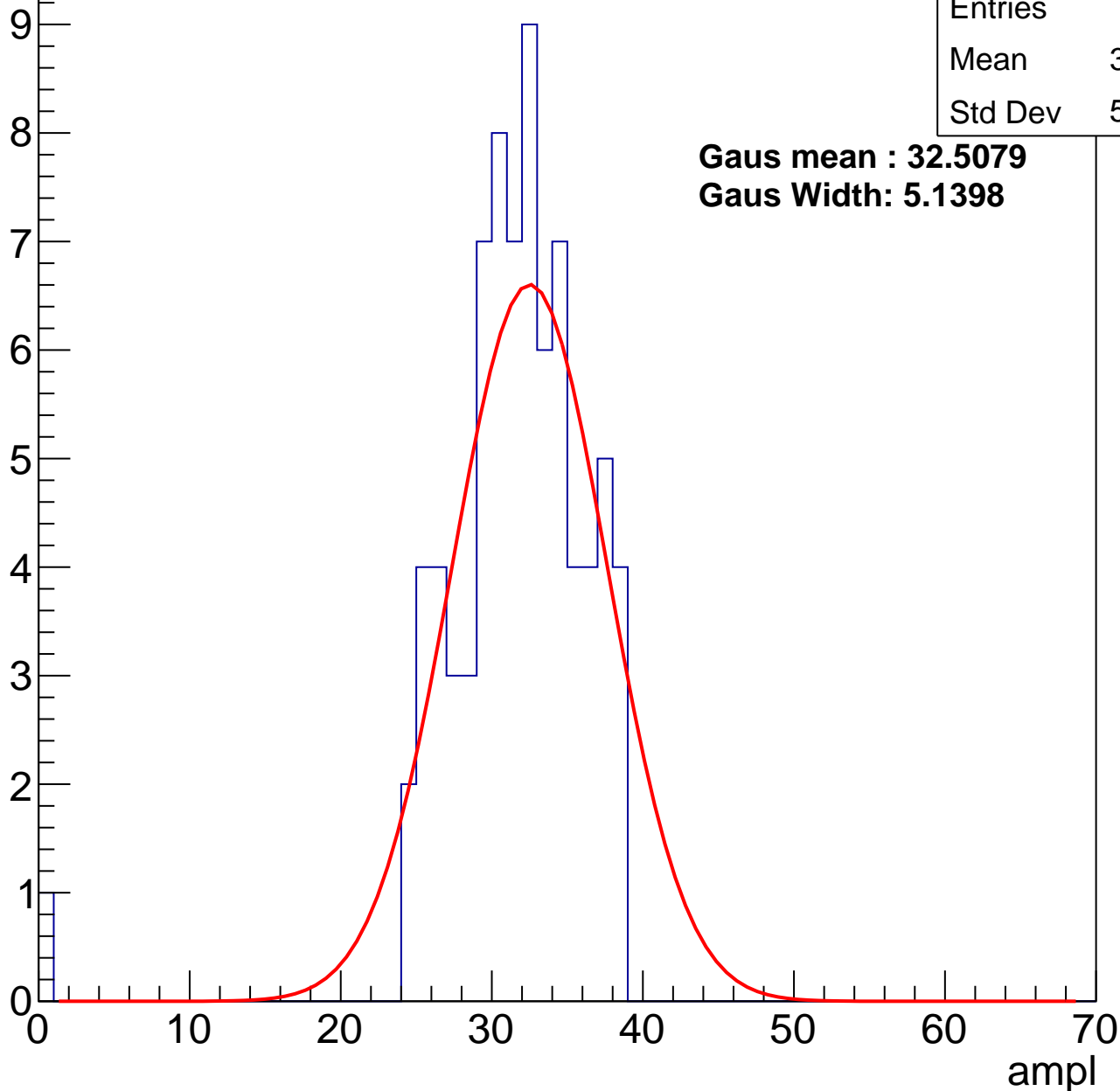
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 31.05 |
| Std Dev | 5.126 |

Gaus mean : 32.5079

Gaus Width: 5.1398



B0L001S, U7-ch122, adc1

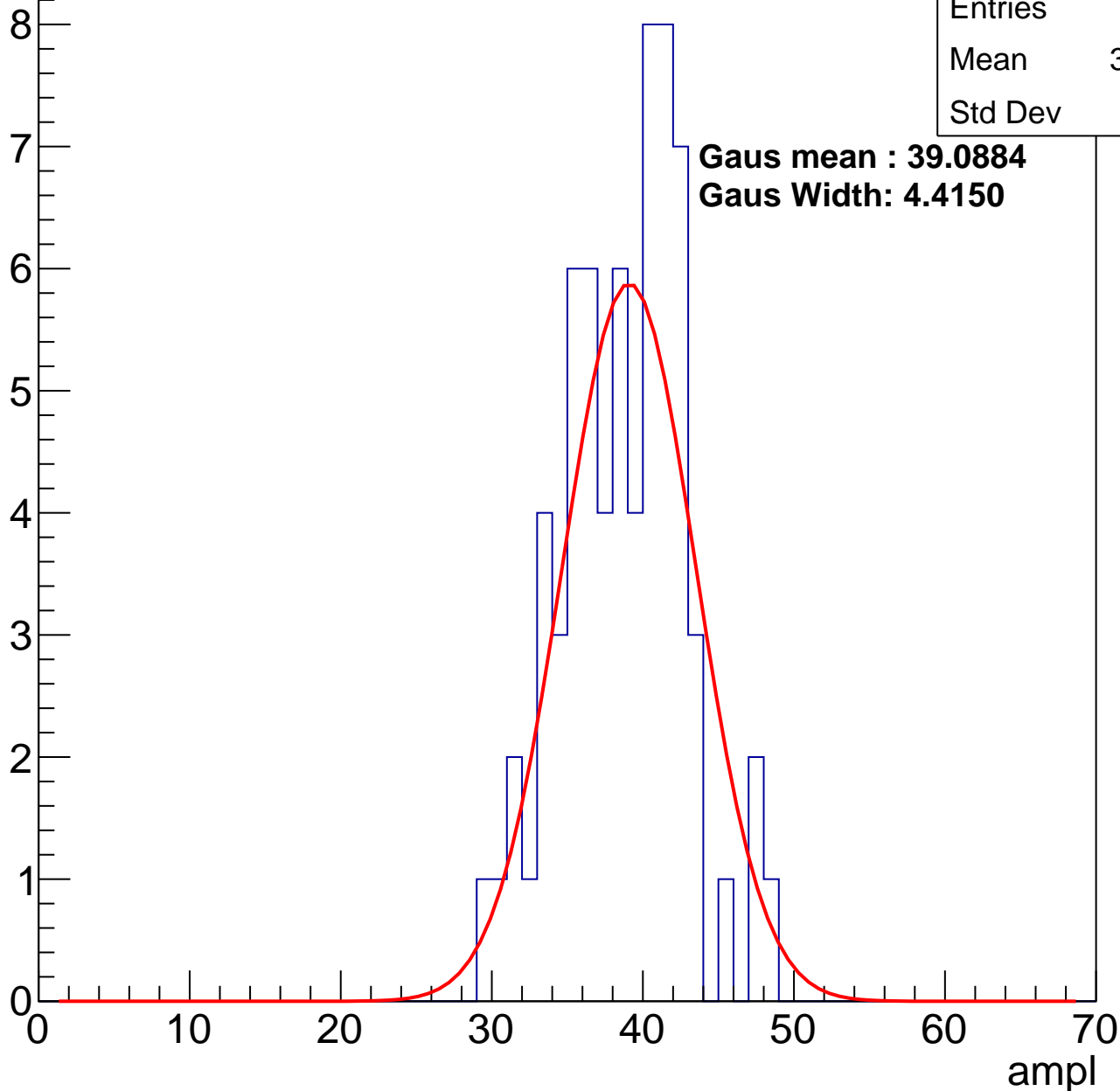
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 38.28 |
| Std Dev | 4.04 |

Gaus mean : 39.0884

Gaus Width: 4.4150



B0L001S, U7-ch122, adc2

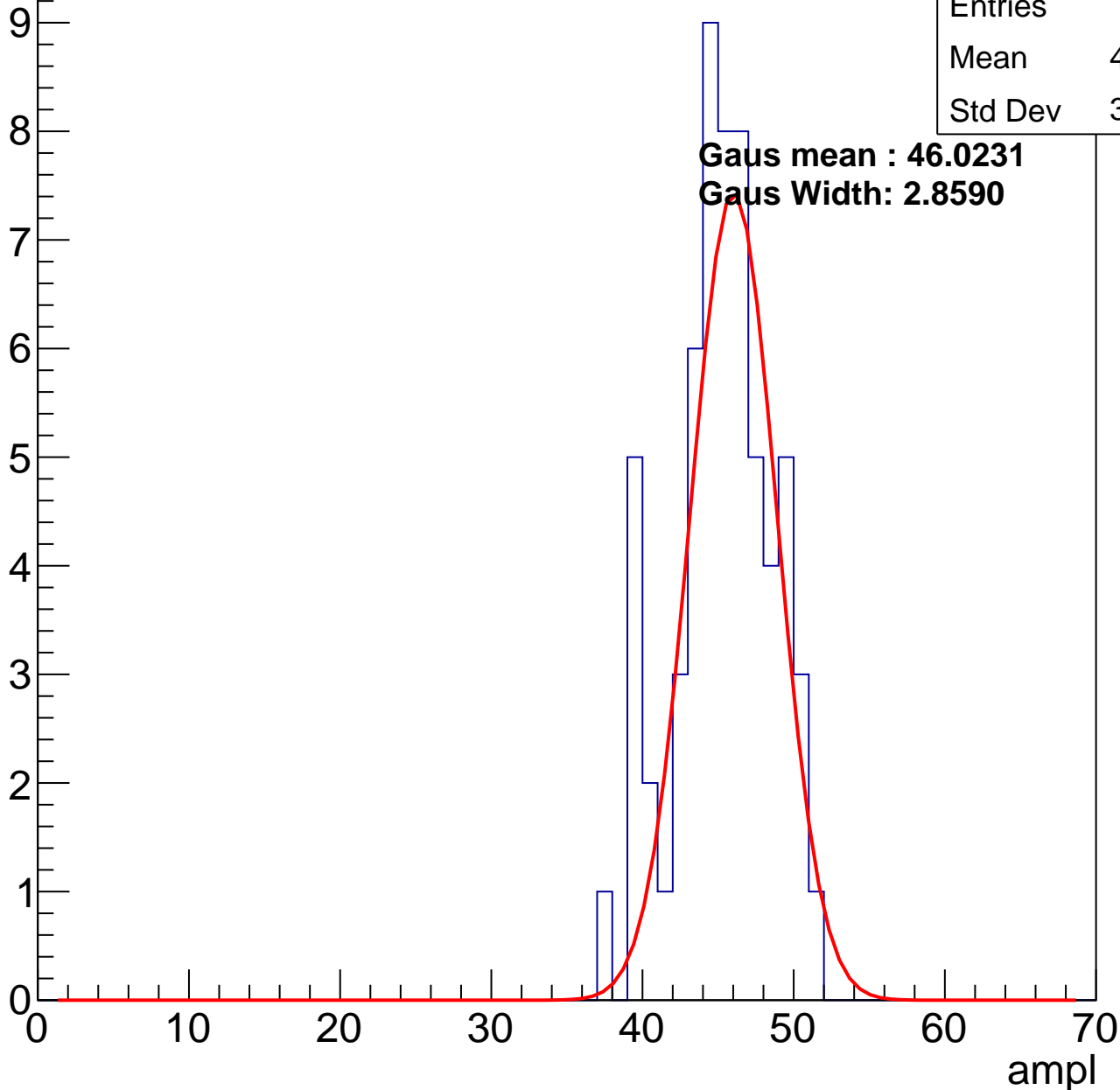
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 44.82 |
| Std Dev | 3.196 |

Gaus mean : 46.0231

Gaus Width: 2.8590

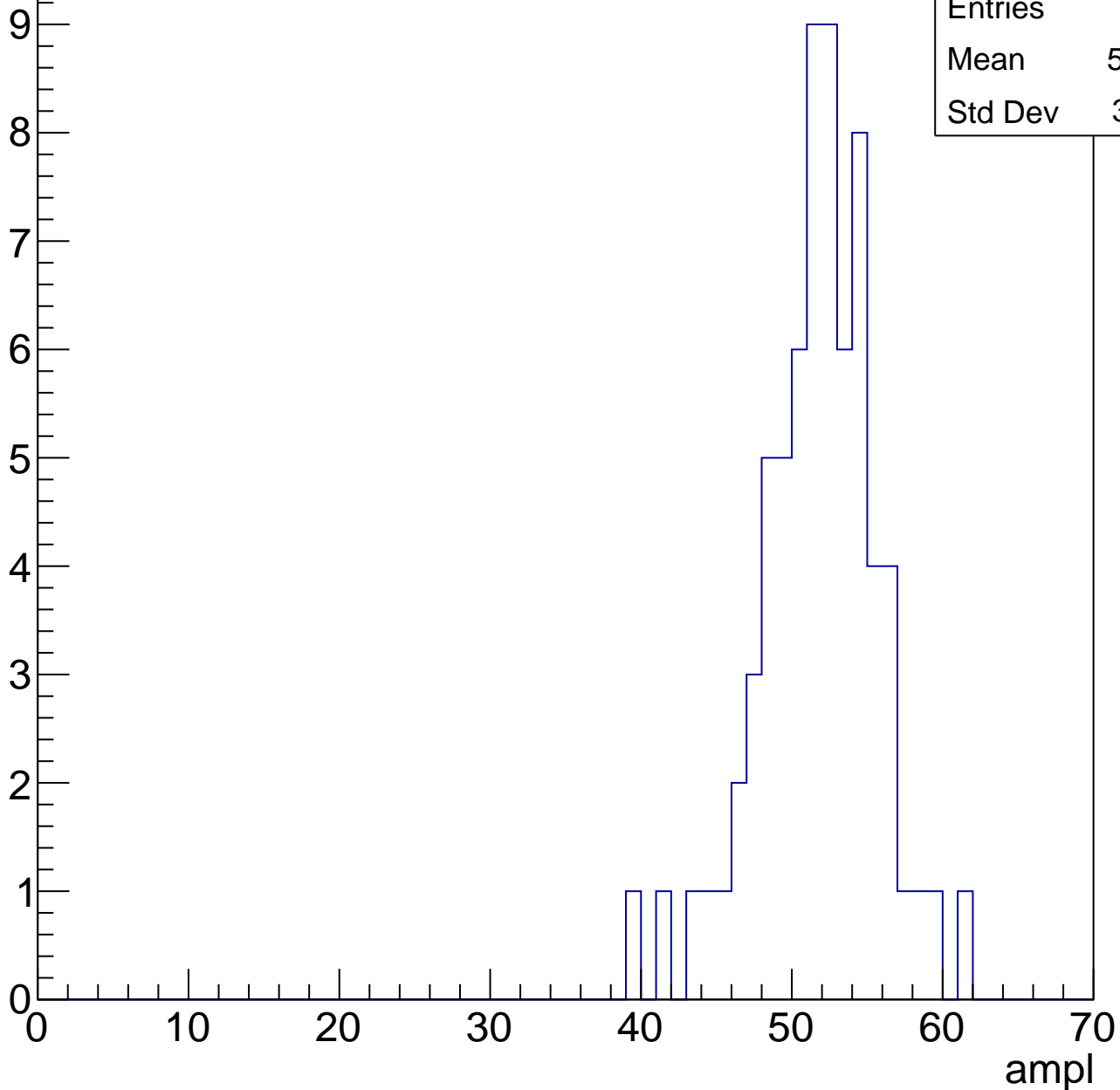


B0L001S, U7-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 51.23 |
| Std Dev | 3.921 |

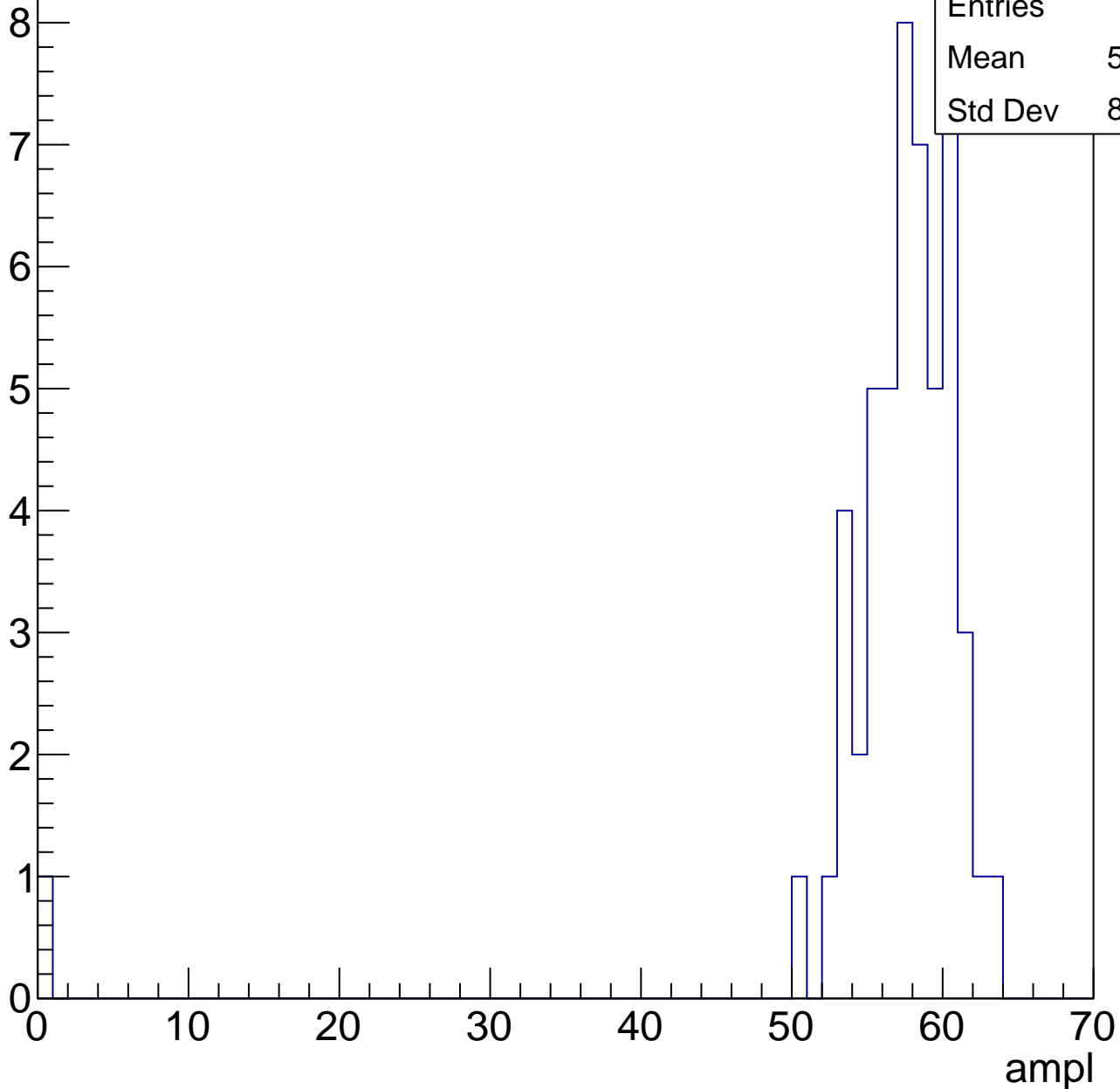


B0L001S, U7-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 56.19 |
| Std Dev | 8.325 |

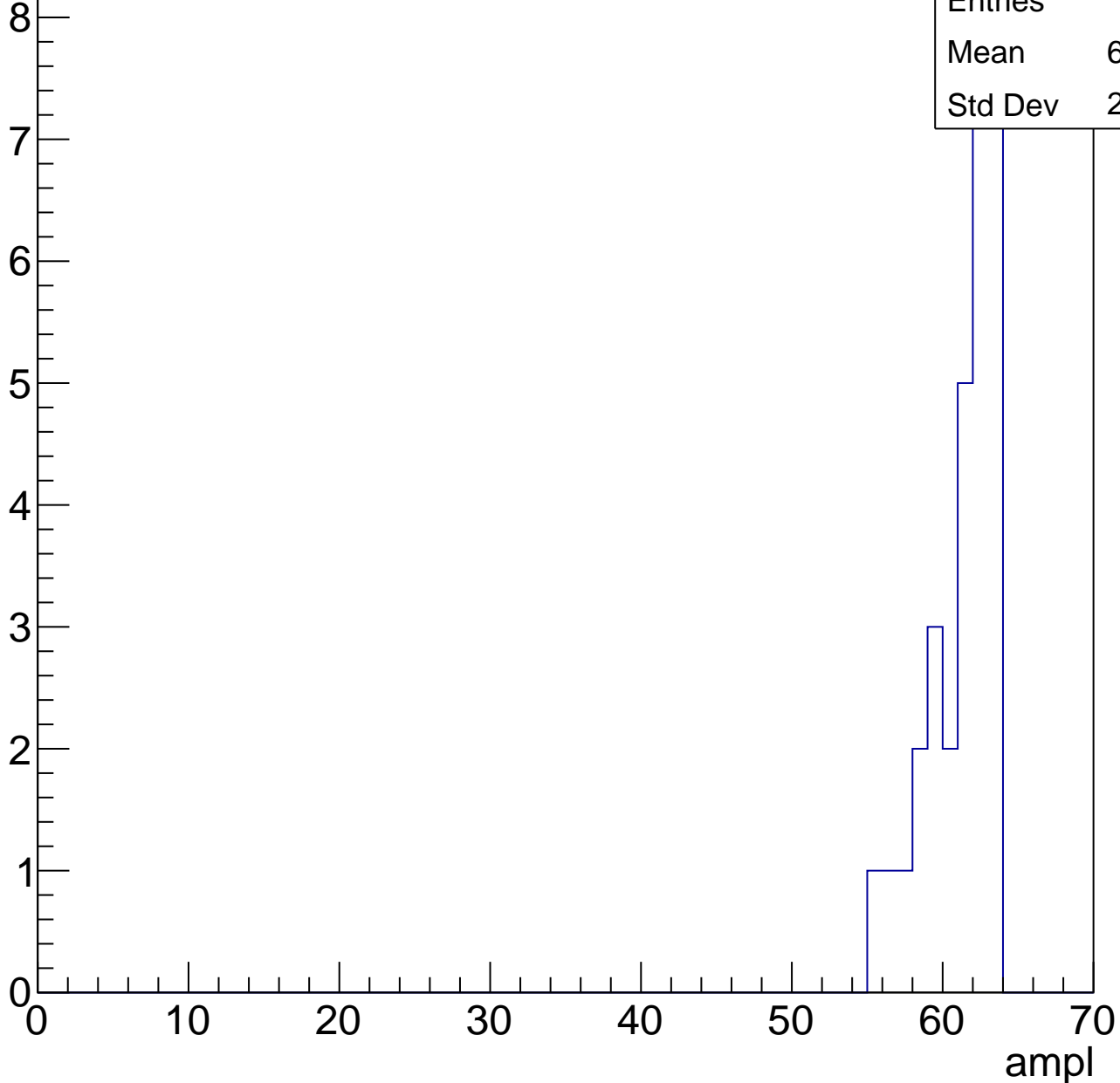


B0L001S, U7-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

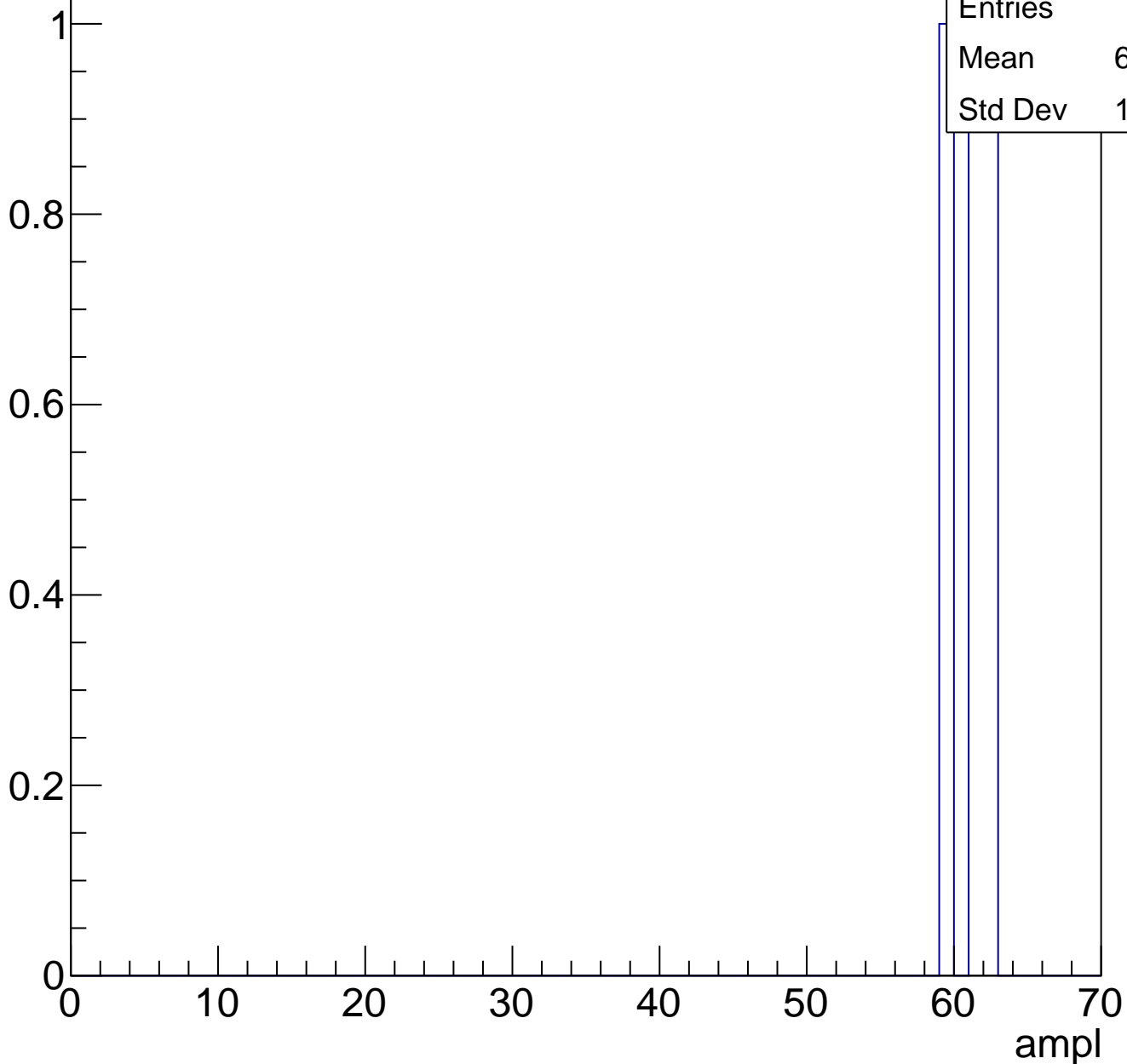
| | |
|---------|-------|
| Entries | 31 |
| Mean | 60.84 |
| Std Dev | 2.186 |



B0L001S, U7-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

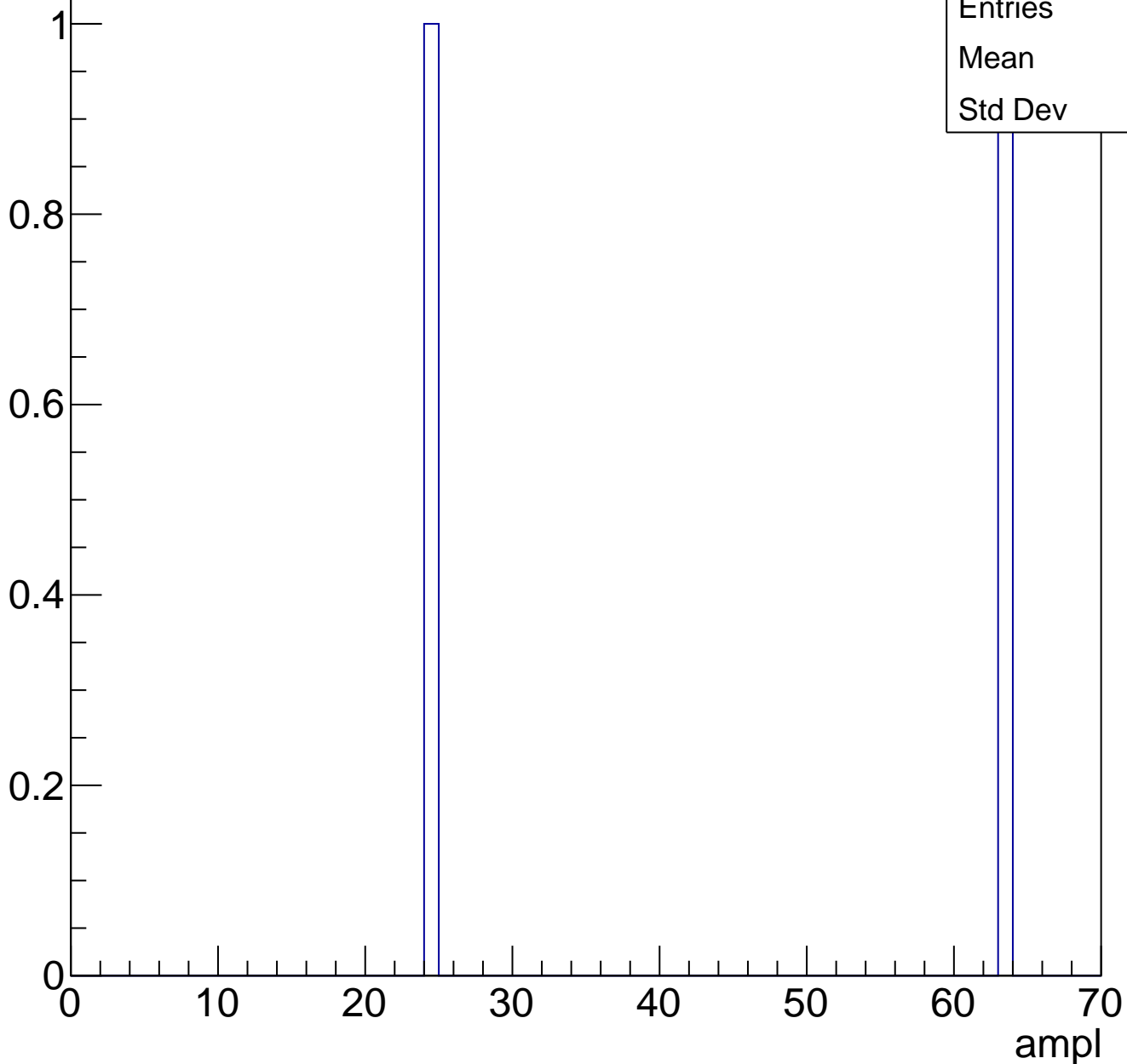
Entry



B0L001S, U7-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch123, adc0

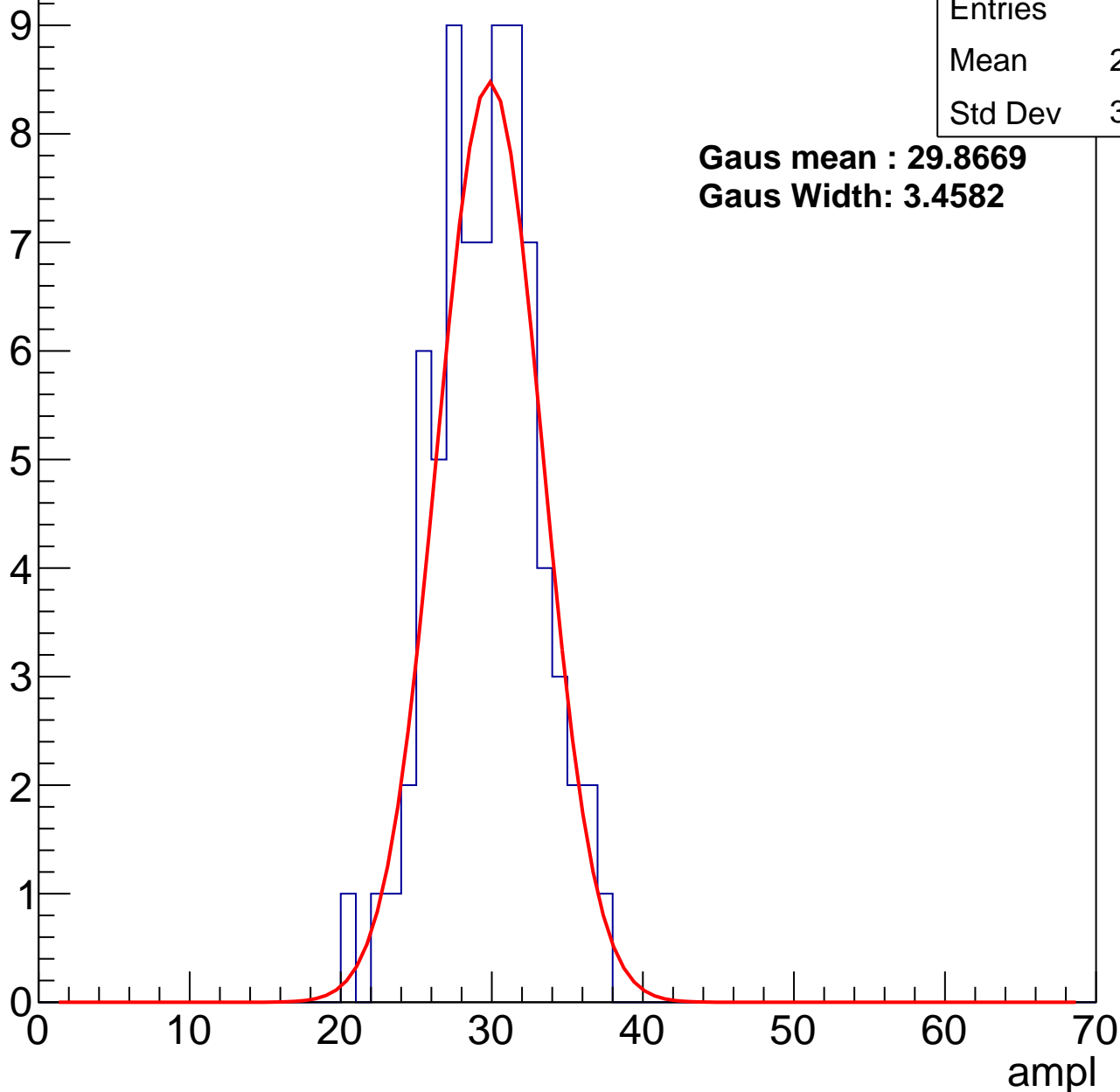
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.22 |
| Std Dev | 3.397 |

Gaus mean : 29.8669

Gaus Width: 3.4582



B0L001S, U7-ch123, adc1

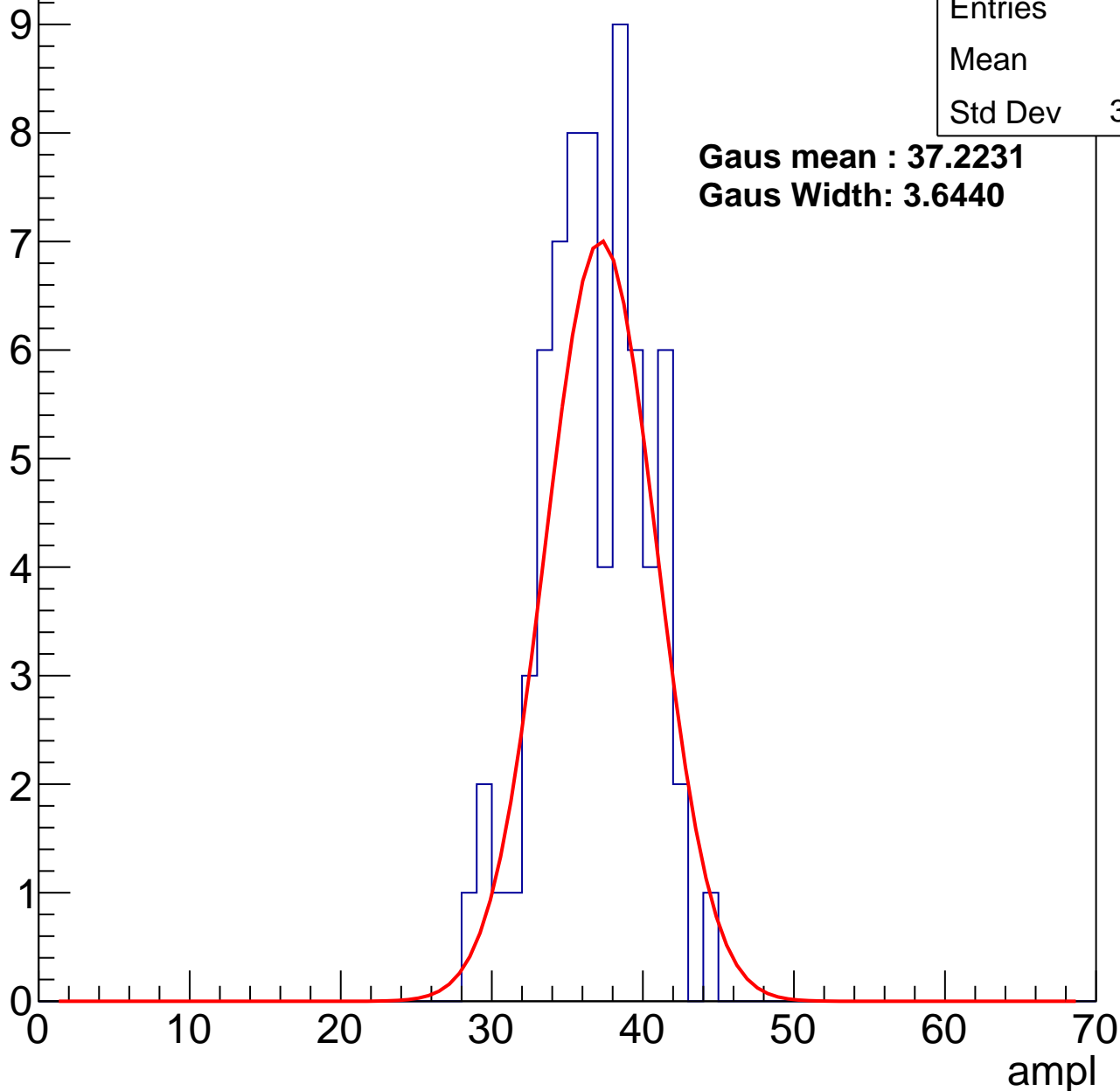
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.3 |
| Std Dev | 3.402 |

Gaus mean : 37.2231

Gaus Width: 3.6440



B0L001S, U7-ch123, adc2

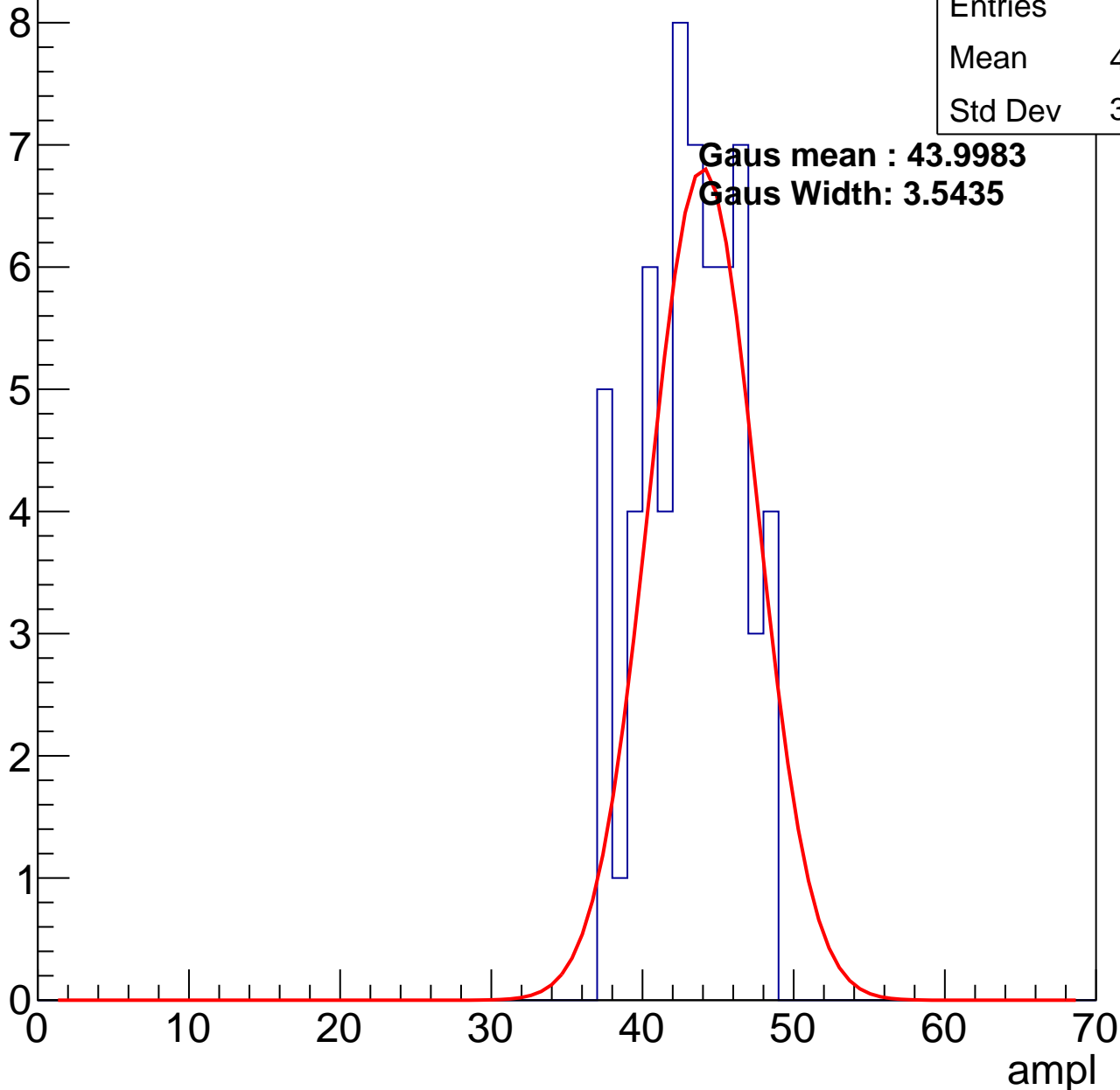
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.77 |
| Std Dev | 3.096 |

Gaus mean : 43.9983

Gaus Width: 3.5435



B0L001S, U7-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

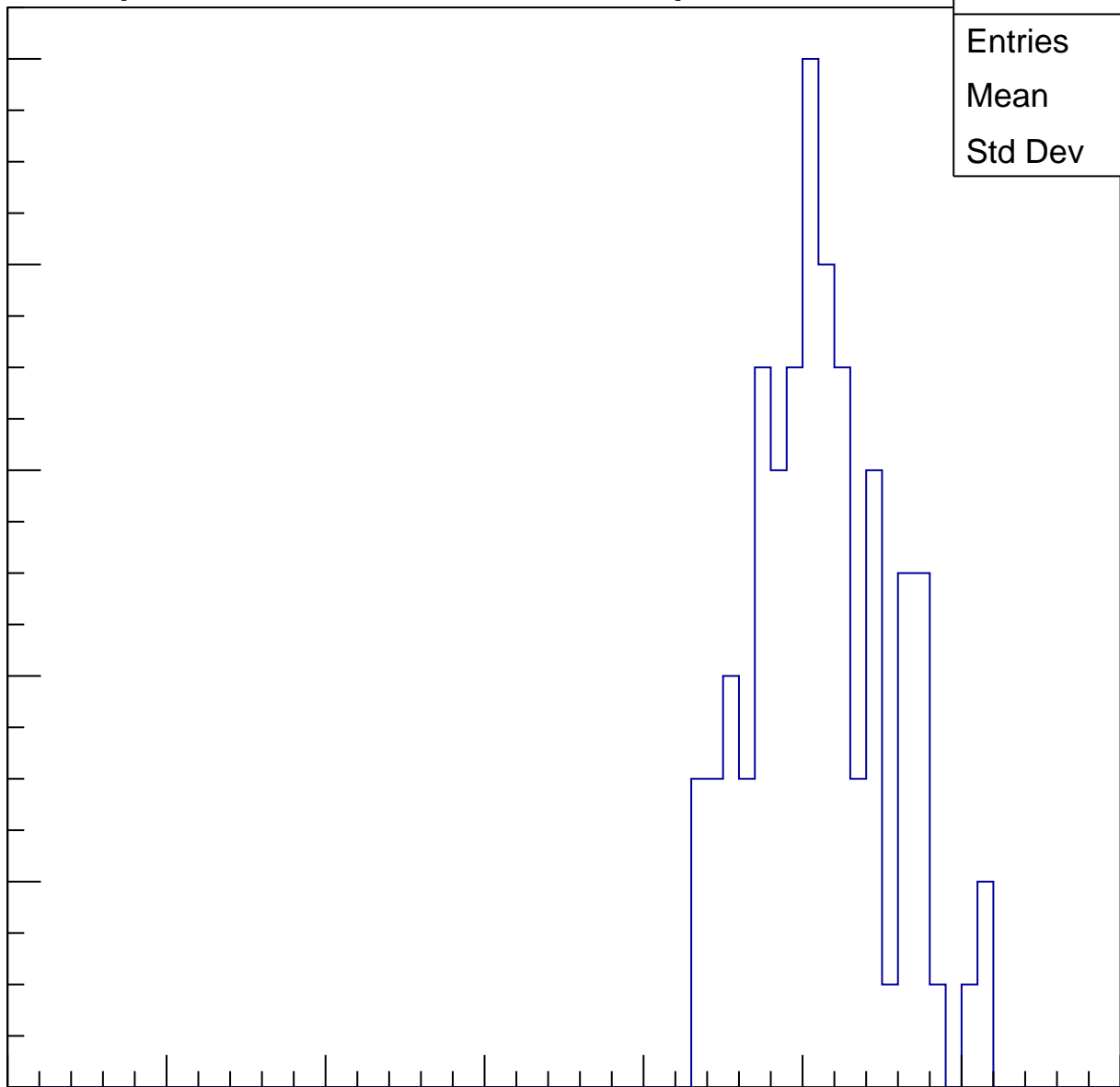
| | |
|---------|-------|
| Entries | 82 |
| Mean | 50.66 |
| Std Dev | 4.243 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

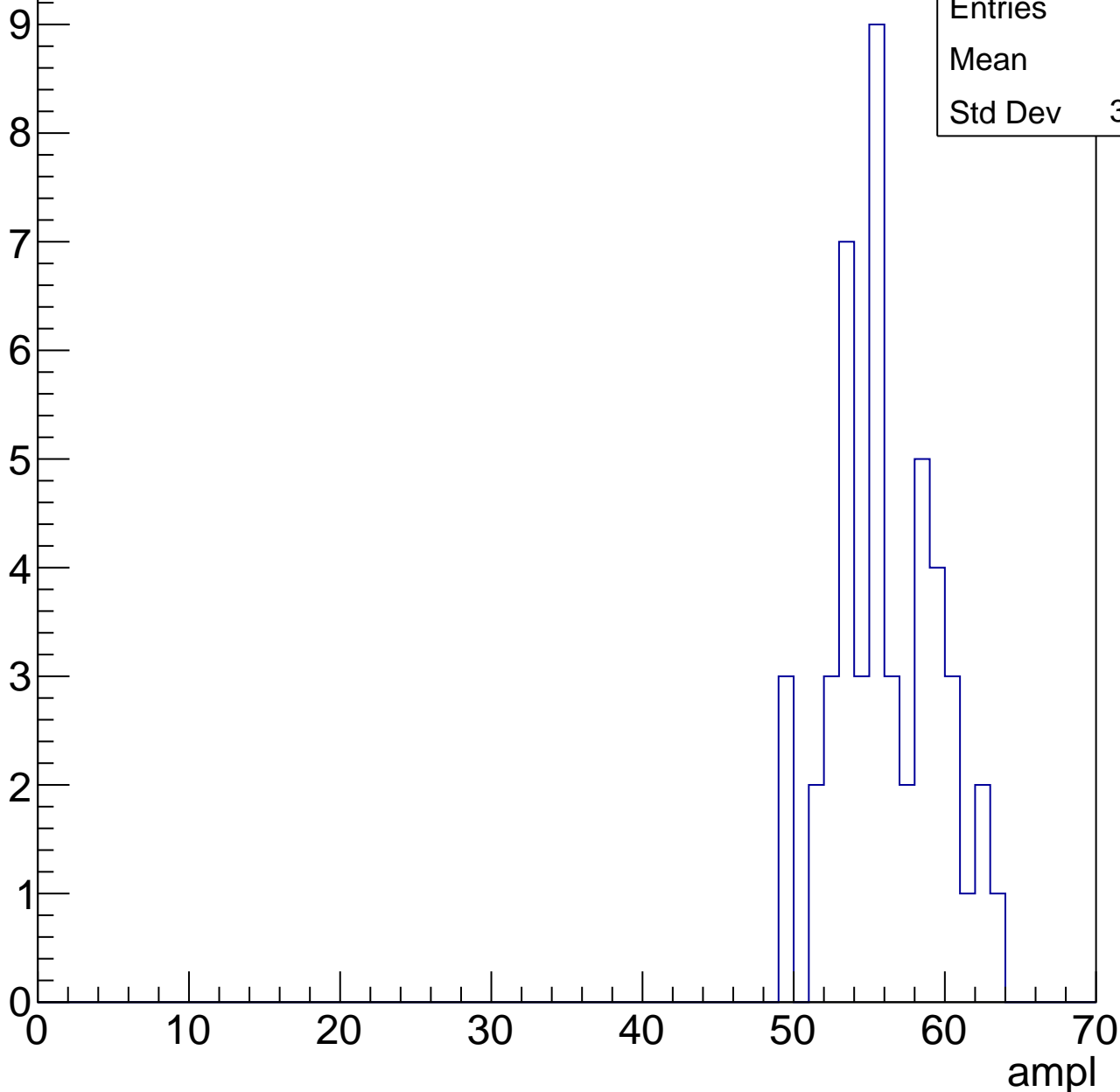


B0L001S, U7-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

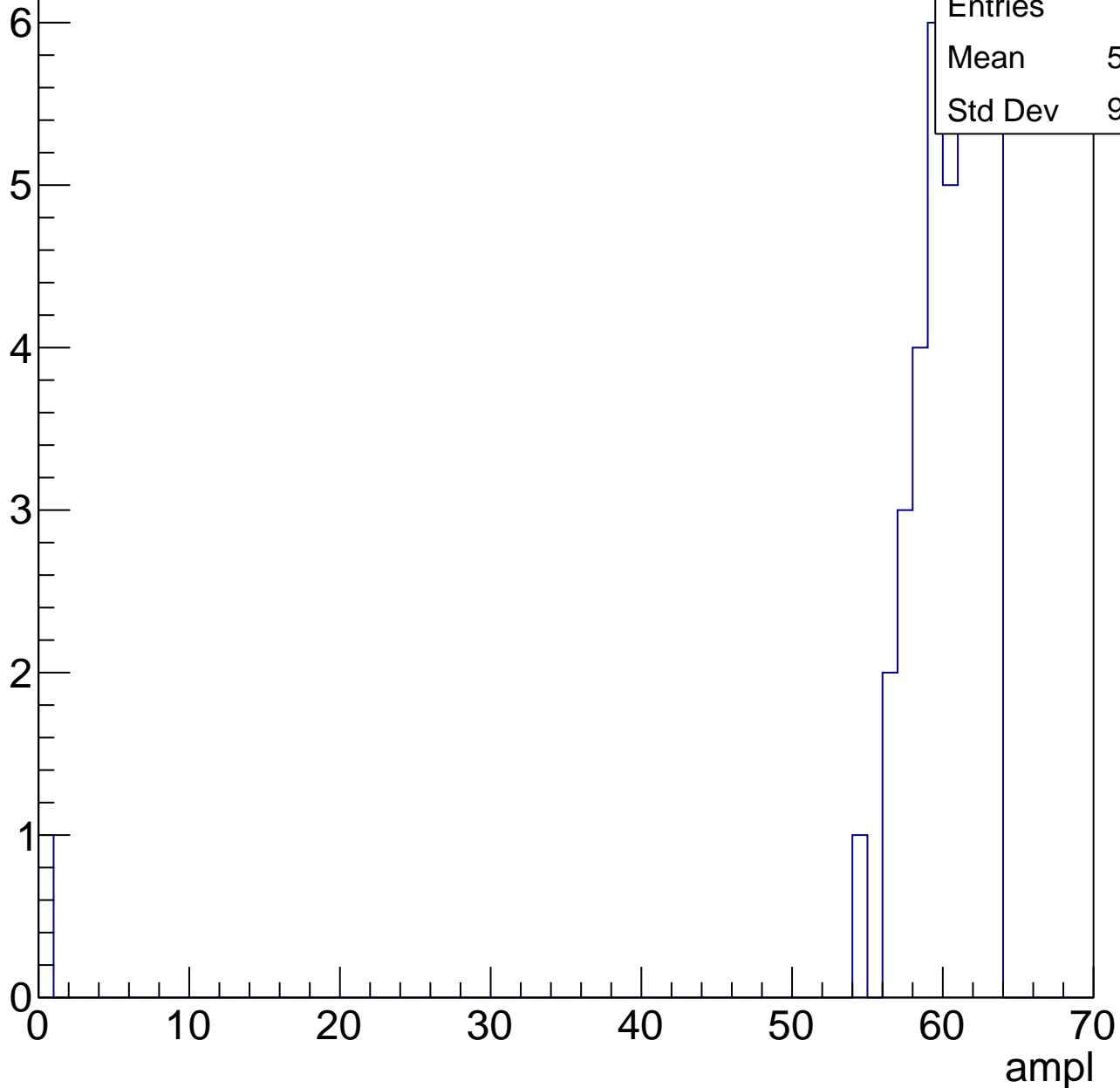
| | |
|---------|-------|
| Entries | 48 |
| Mean | 55.6 |
| Std Dev | 3.457 |



B0L001S, U7-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch124, adc0

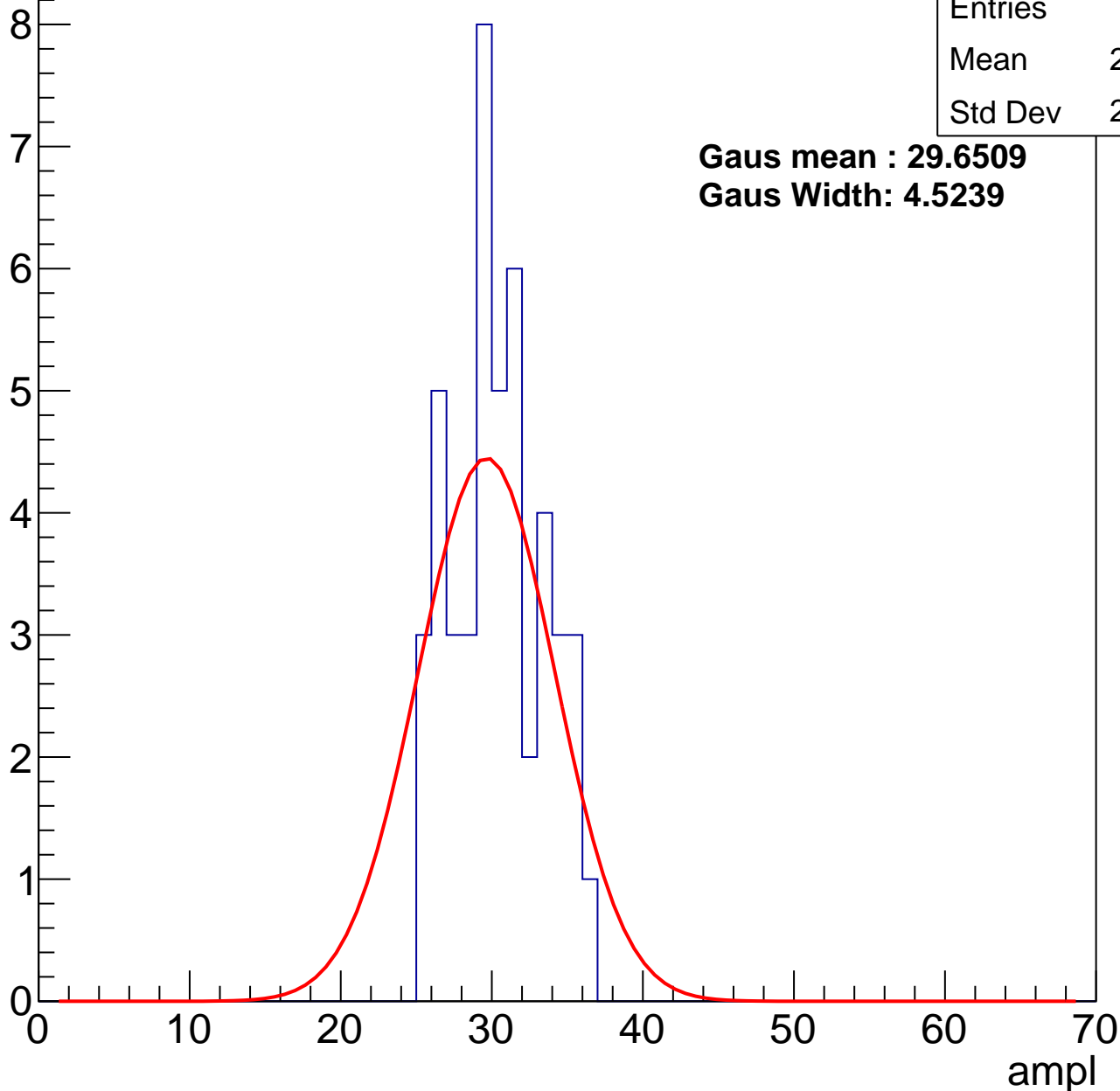
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 29.93 |
| Std Dev | 2.988 |

Gaus mean : 29.6509

Gaus Width: 4.5239



B0L001S, U7-ch124, adc1

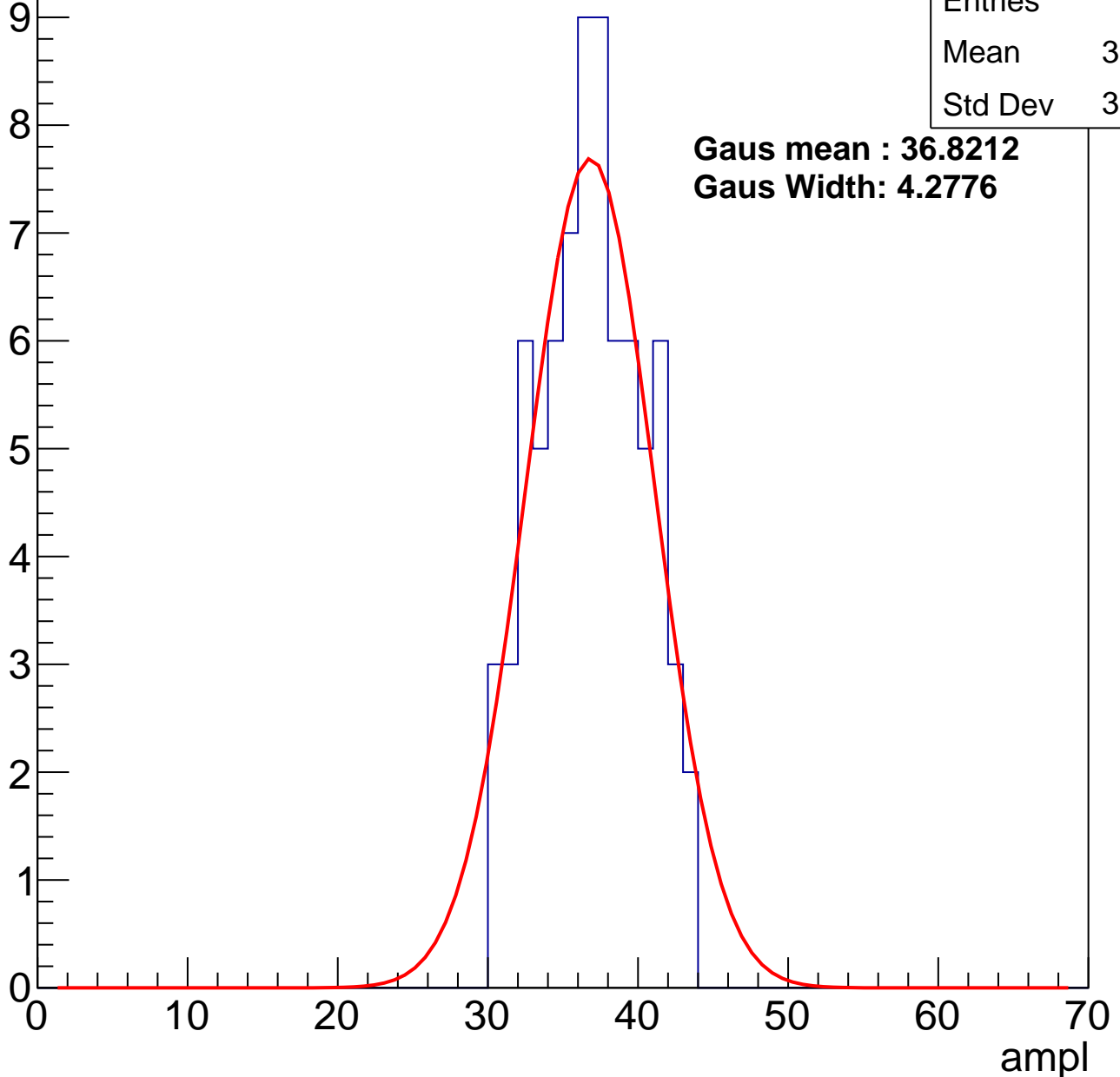
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 36.39 |
| Std Dev | 3.376 |

Gaus mean : 36.8212

Gaus Width: 4.2776



B0L001S, U7-ch124, adc2

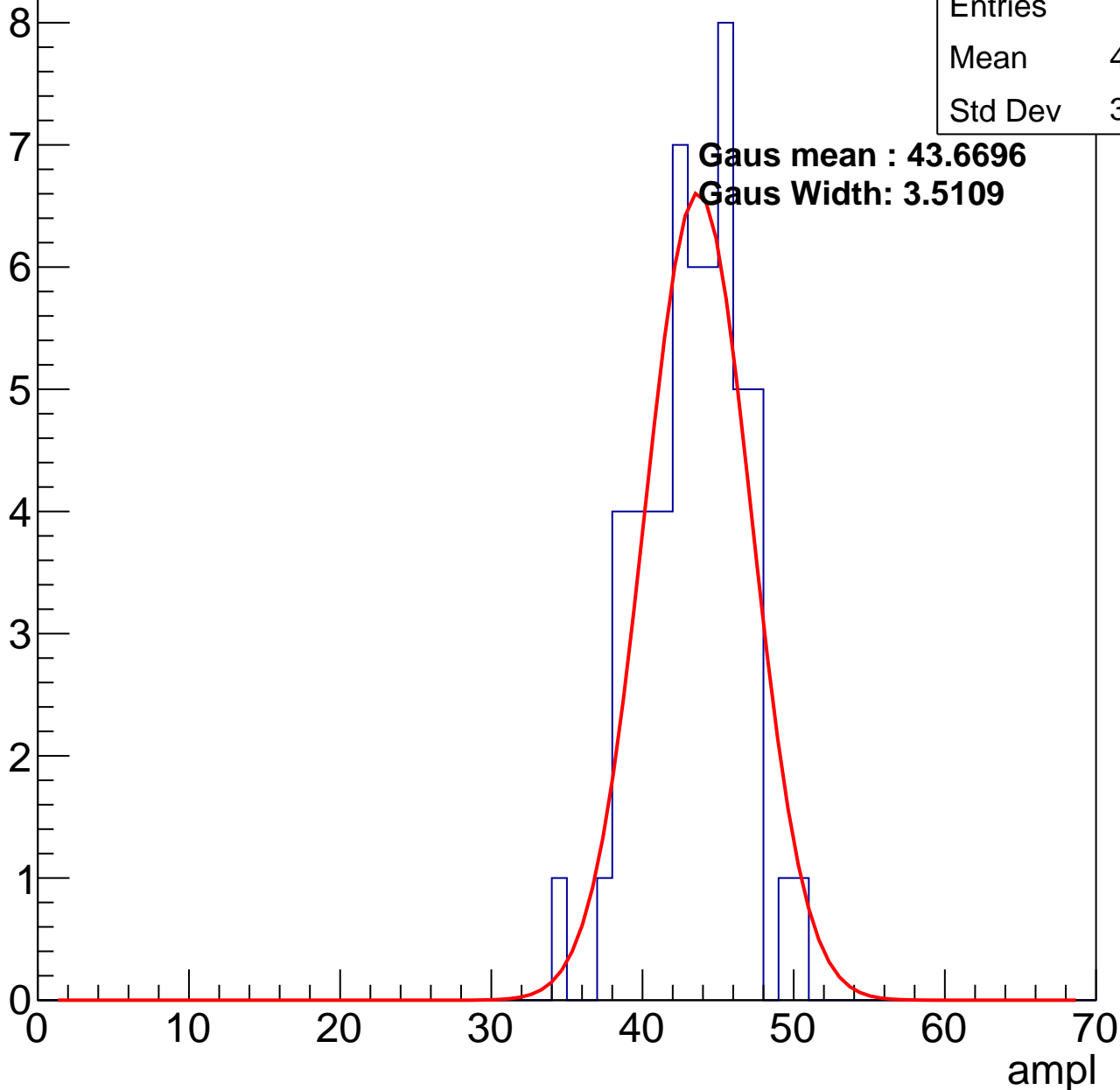
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 42.86 |
| Std Dev | 3.209 |

Gaus mean : 43.6696

Gaus Width: 3.5109

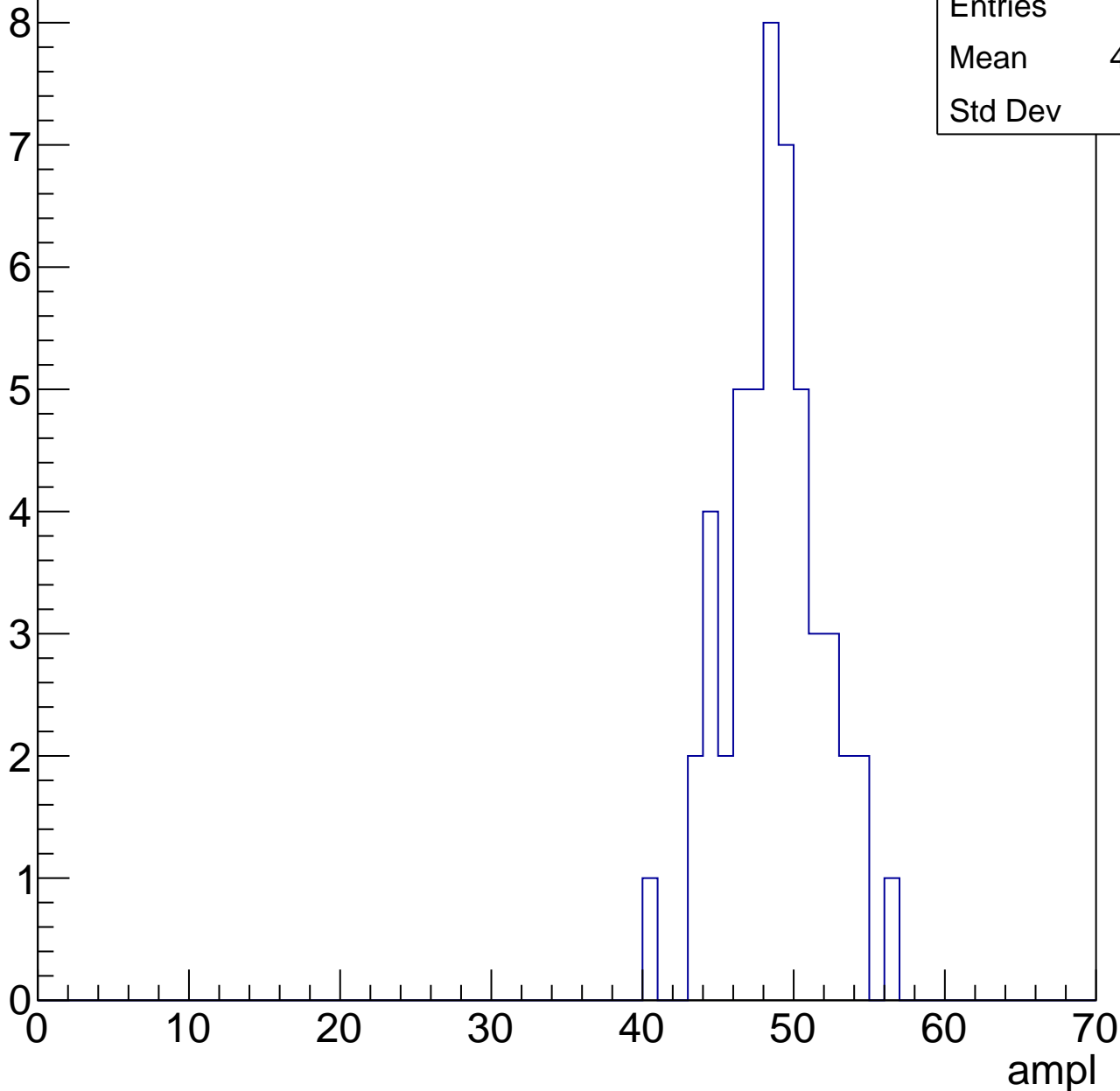


B0L001S, U7-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

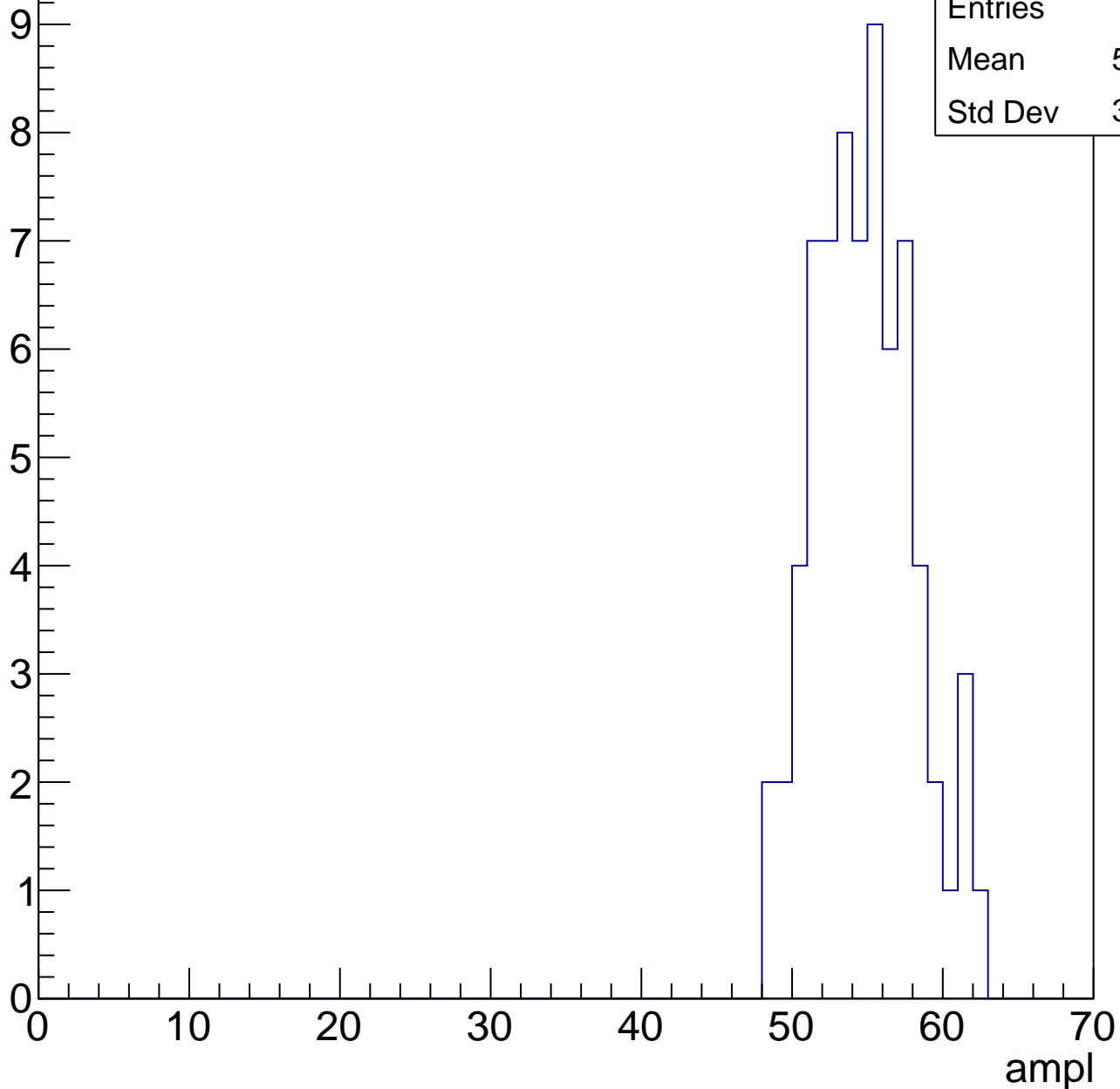
| | |
|---------|-------|
| Entries | 50 |
| Mean | 48.26 |
| Std Dev | 3.18 |



B0L001S, U7-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

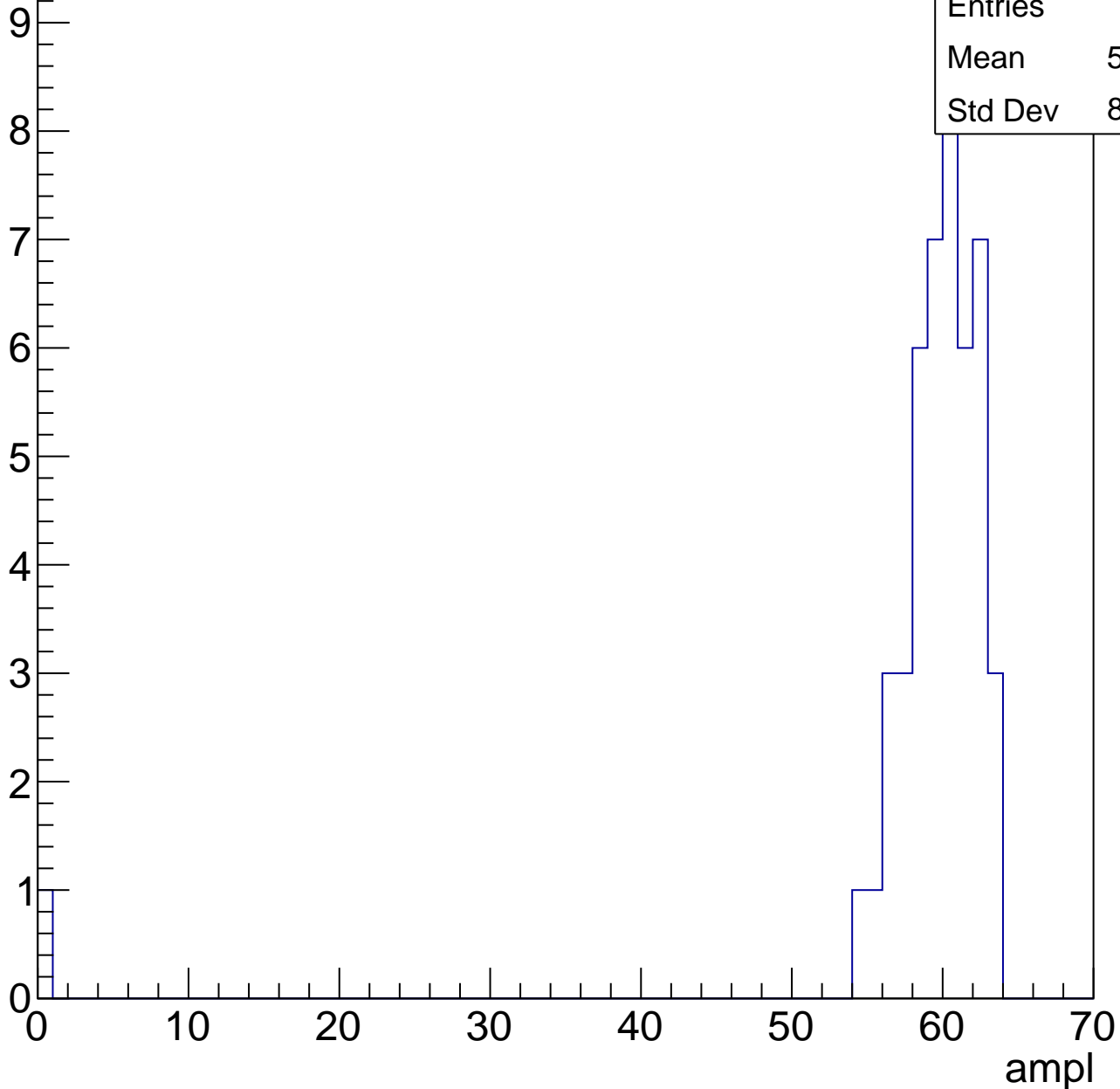


B0L001S, U7-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.26 |
| Std Dev | 8.855 |

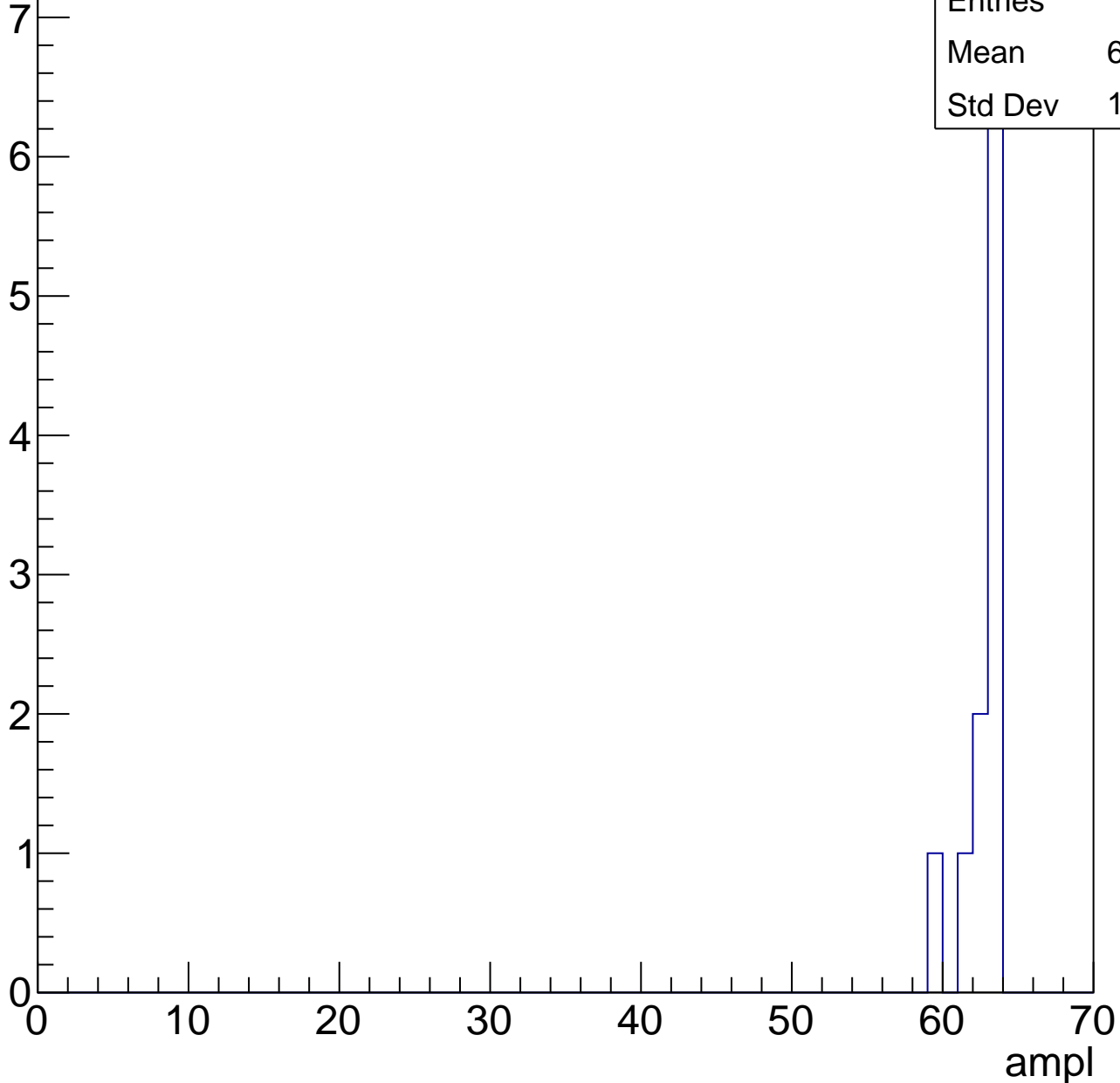


B0L001S, U7-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 11 |
| Mean | 62.27 |
| Std Dev | 1.213 |



B0L001S, U7-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch125, adc0

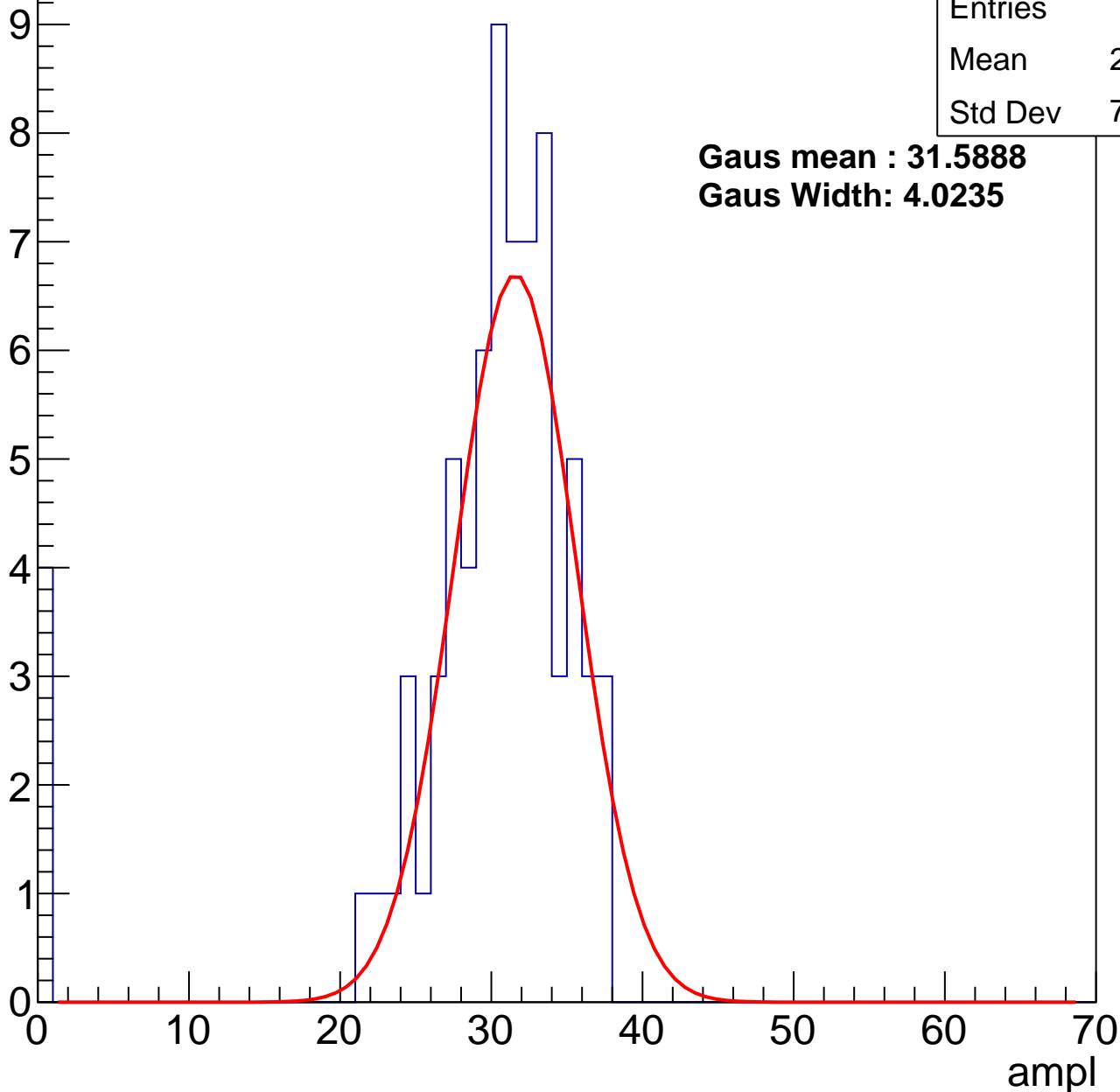
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.82 |
| Std Dev | 7.776 |

Gaus mean : 31.5888

Gaus Width: 4.0235



B0L001S, U7-ch125, adc1

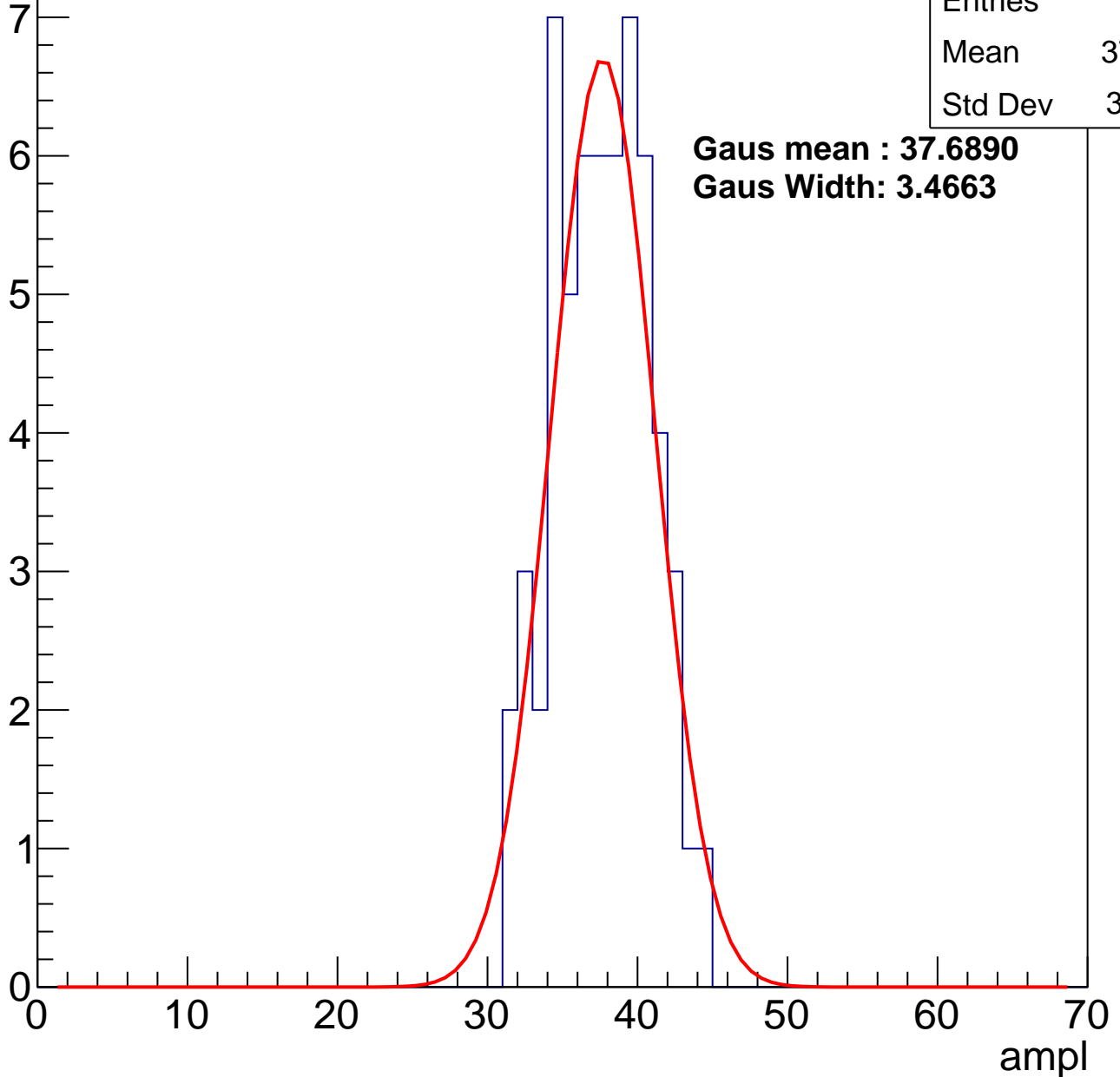
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 37.17 |
| Std Dev | 3.131 |

Gaus mean : 37.6890

Gaus Width: 3.4663



B0L001S, U7-ch125, adc2

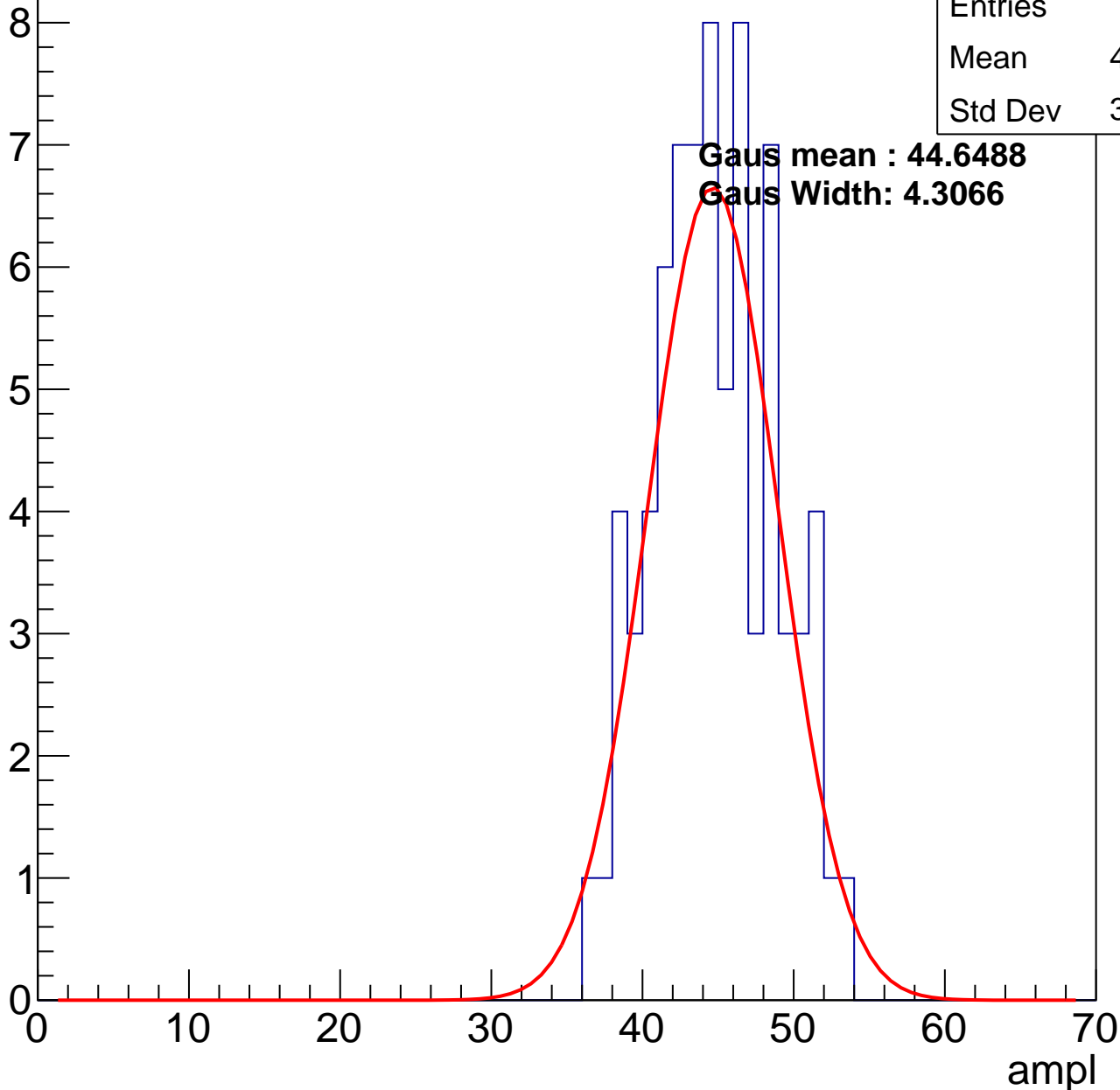
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 44.36 |
| Std Dev | 3.933 |

Gaus mean : 44.6488

Gaus Width: 4.3066

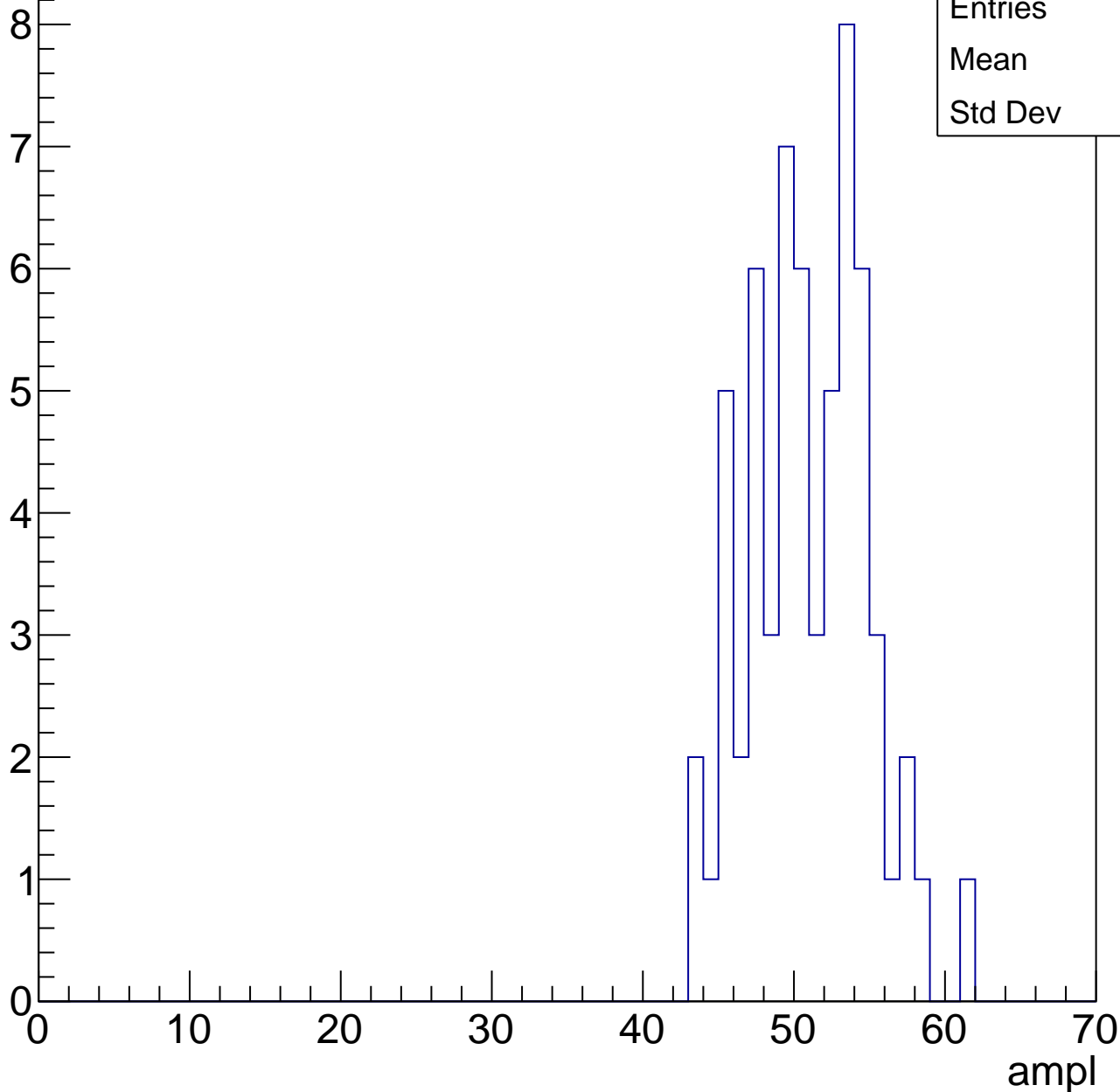


B0L001S, U7-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|------|
| Entries | 62 |
| Mean | 50.5 |
| Std Dev | 3.88 |

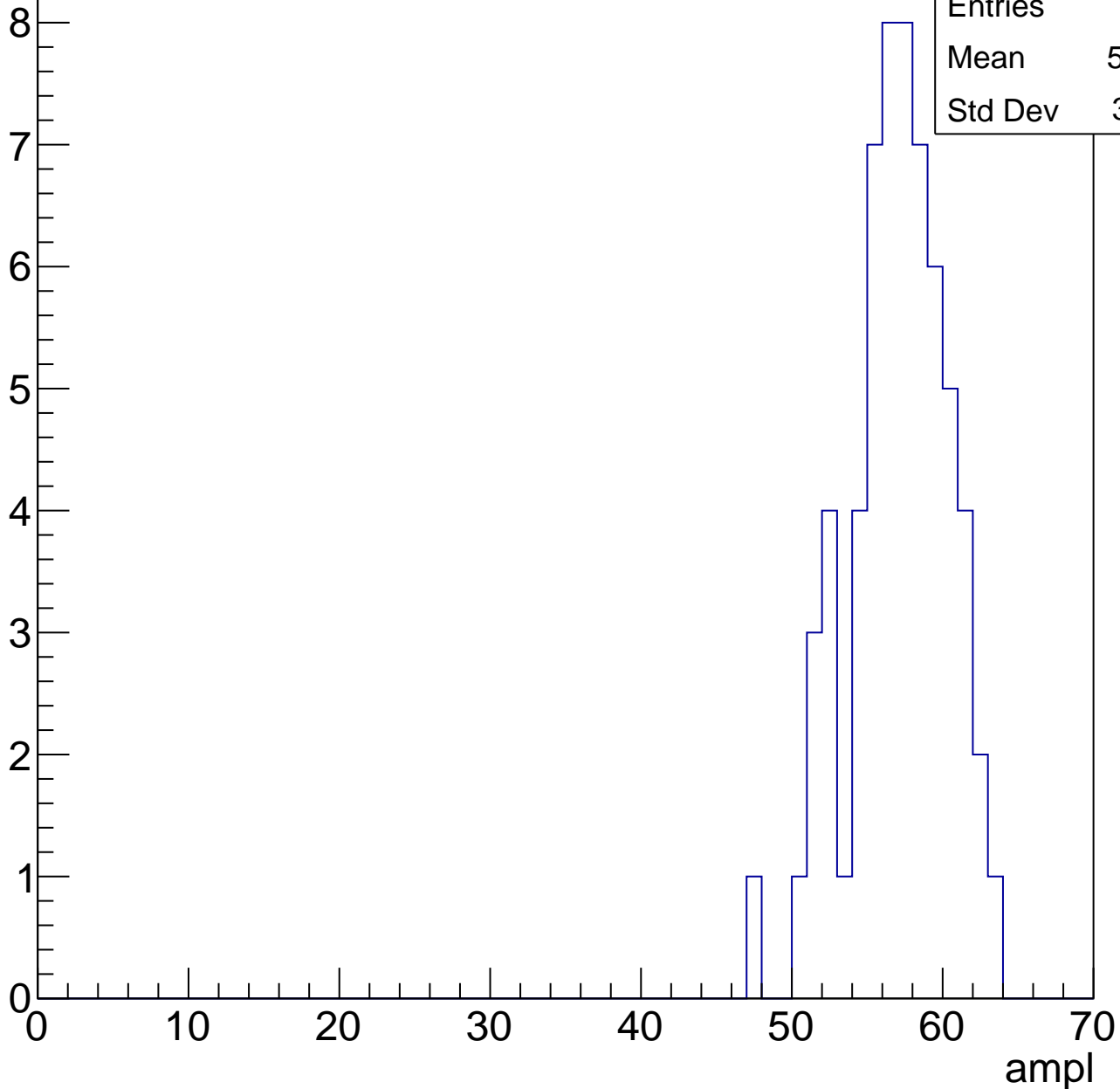


B0L001S, U7-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 56.56 |
| Std Dev | 3.261 |

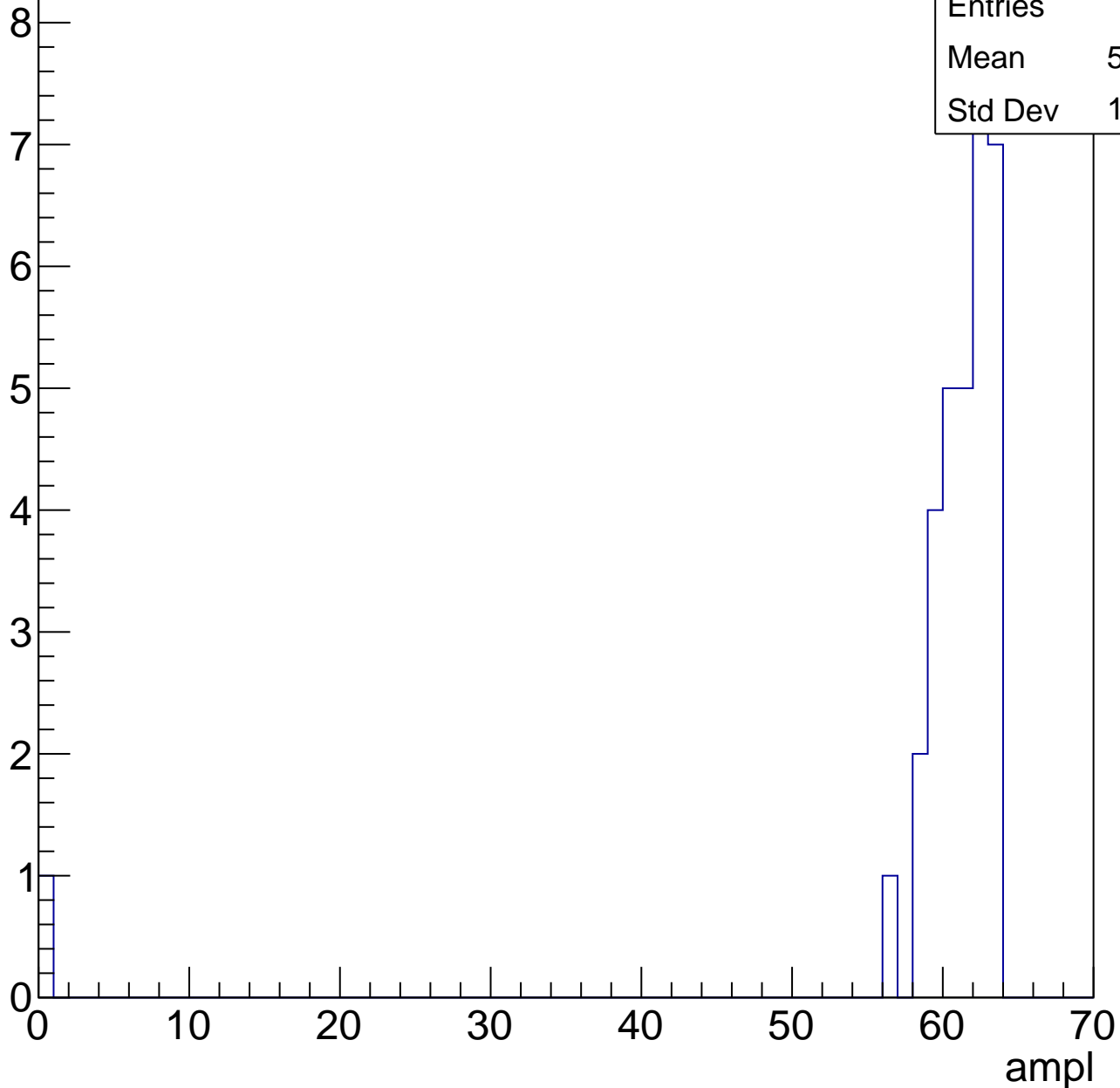


B0L001S, U7-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 33 |
| Mean | 59.09 |
| Std Dev | 10.59 |



B0L001S, U7-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B0L001S, U7-ch126, adc0

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 63 |
| Mean | 27.76 |
| Std Dev | 5.967 |

Gaus mean : 29.2426

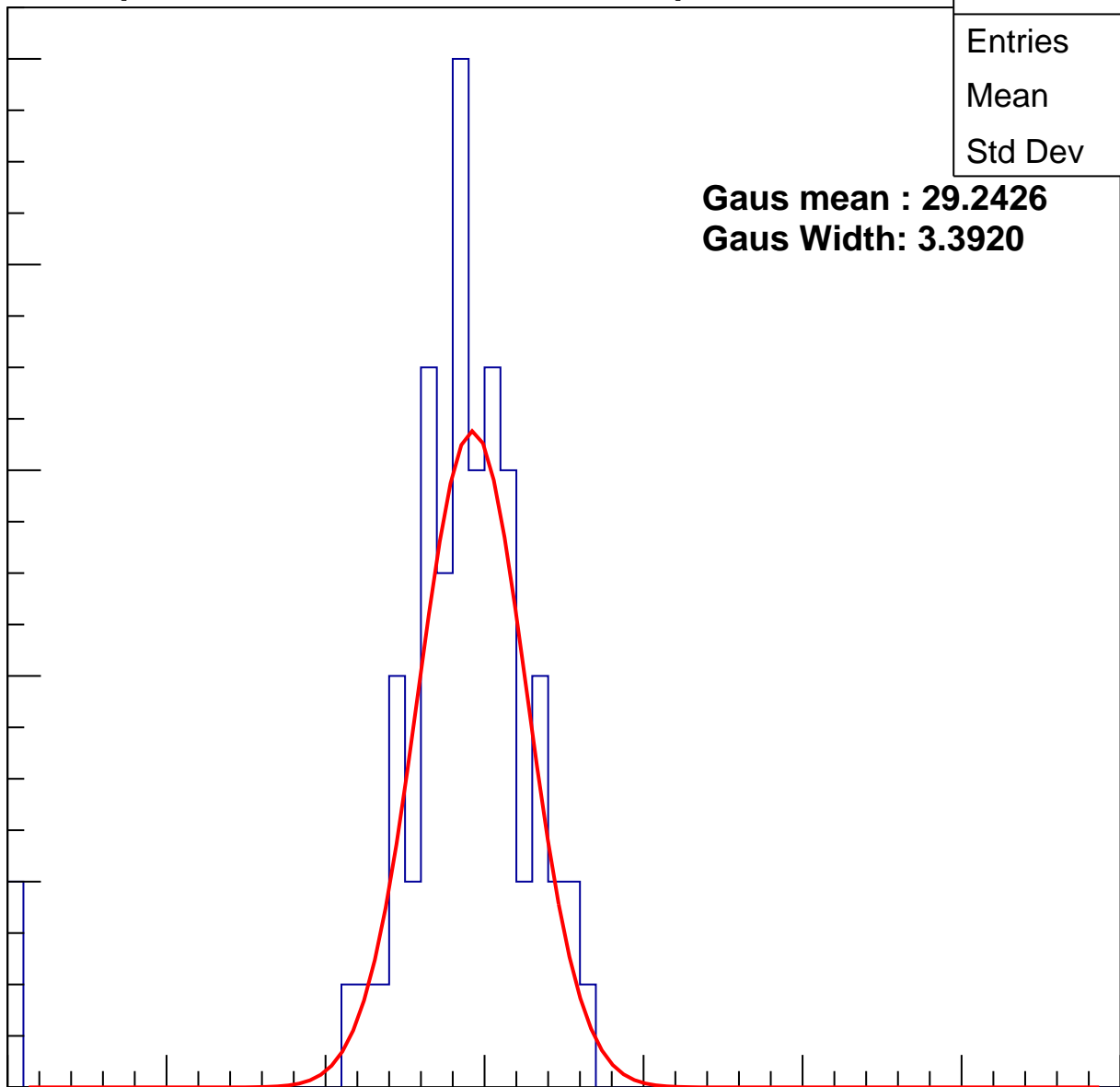
Gaus Width: 3.3920

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U7-ch126, adc1

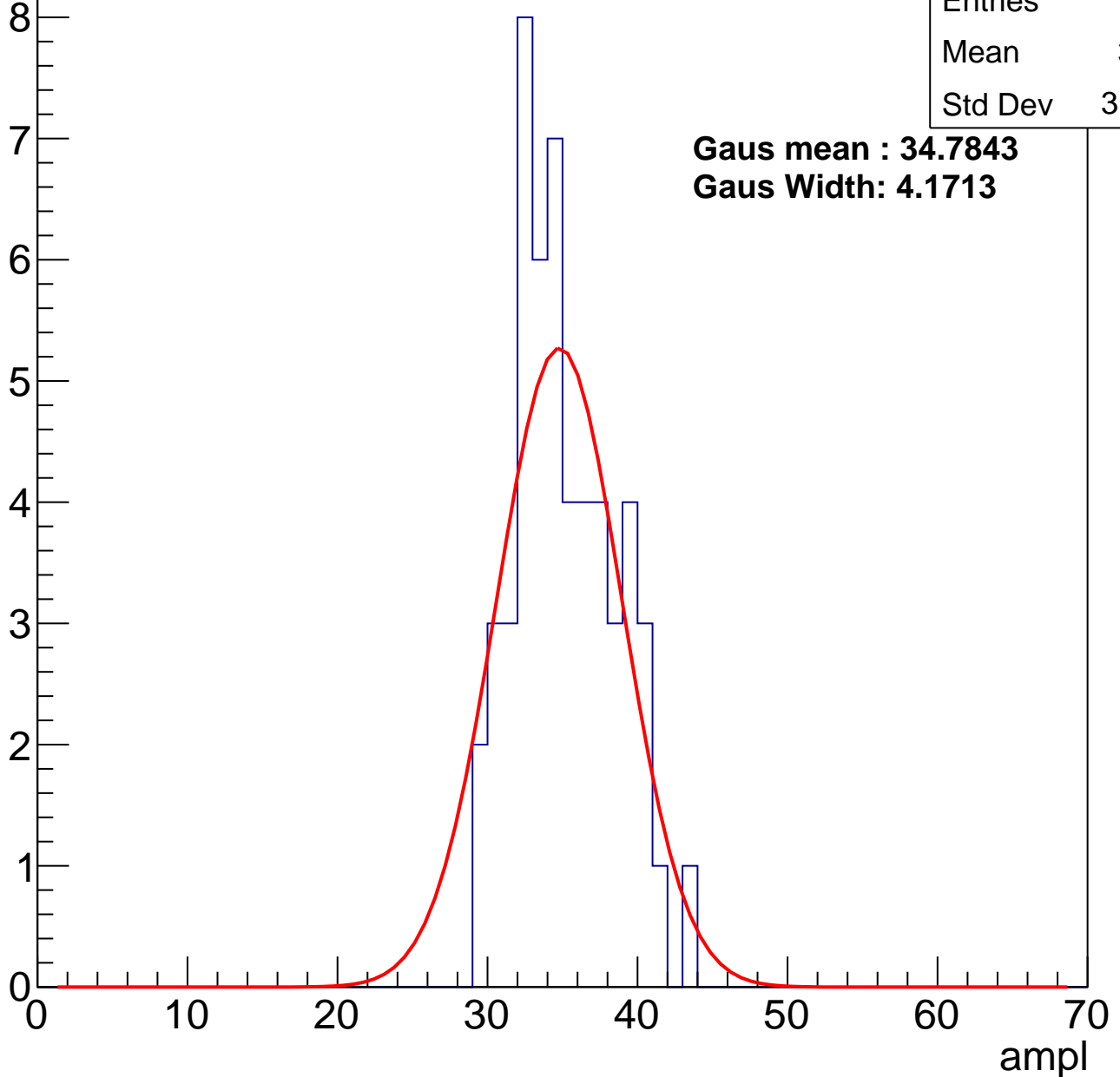
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 34.7 |
| Std Dev | 3.306 |

Gaus mean : 34.7843

Gaus Width: 4.1713



B0L001S, U7-ch126, adc2

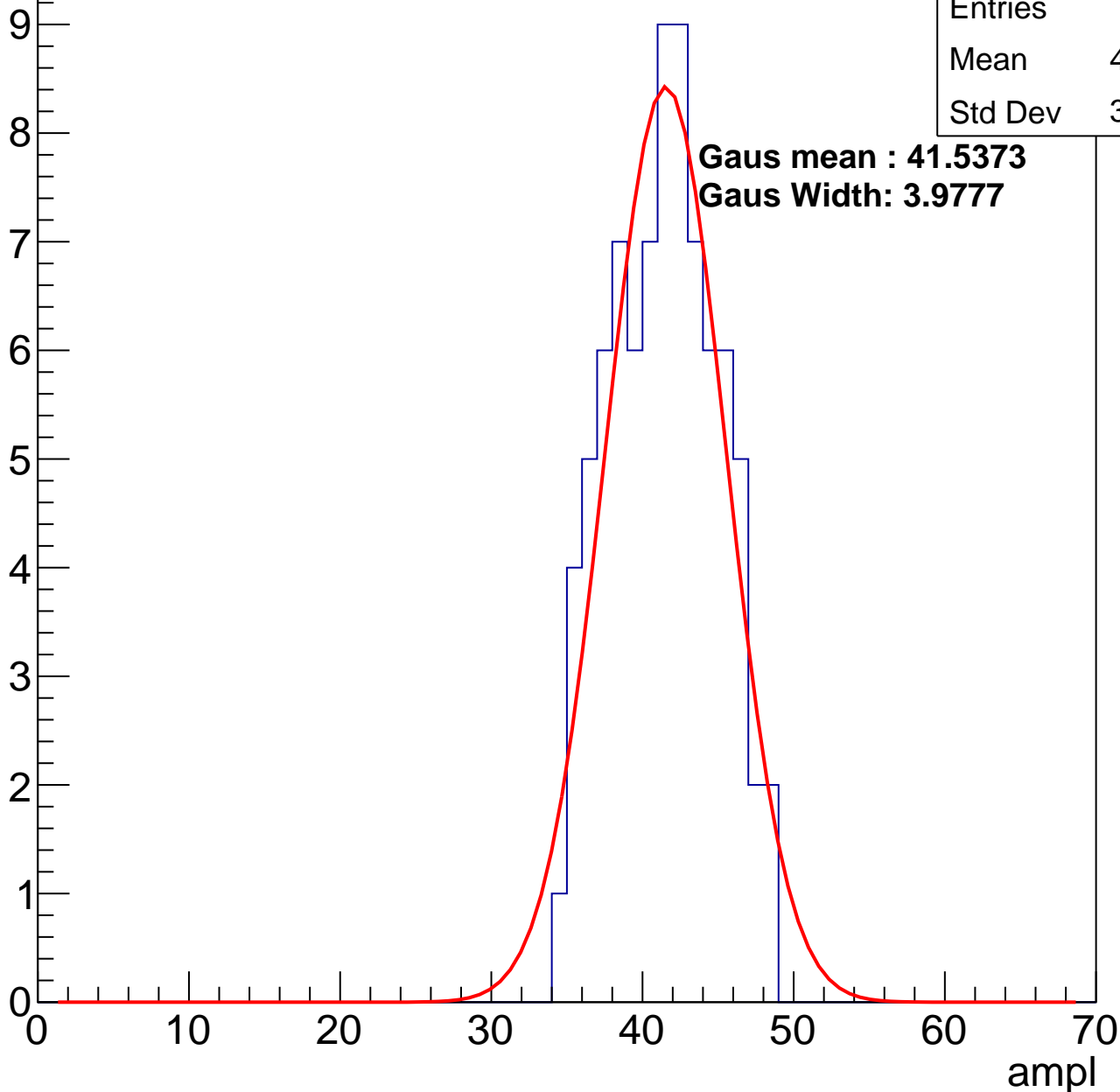
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 40.95 |
| Std Dev | 3.474 |

Gaus mean : 41.5373

Gaus Width: 3.9777

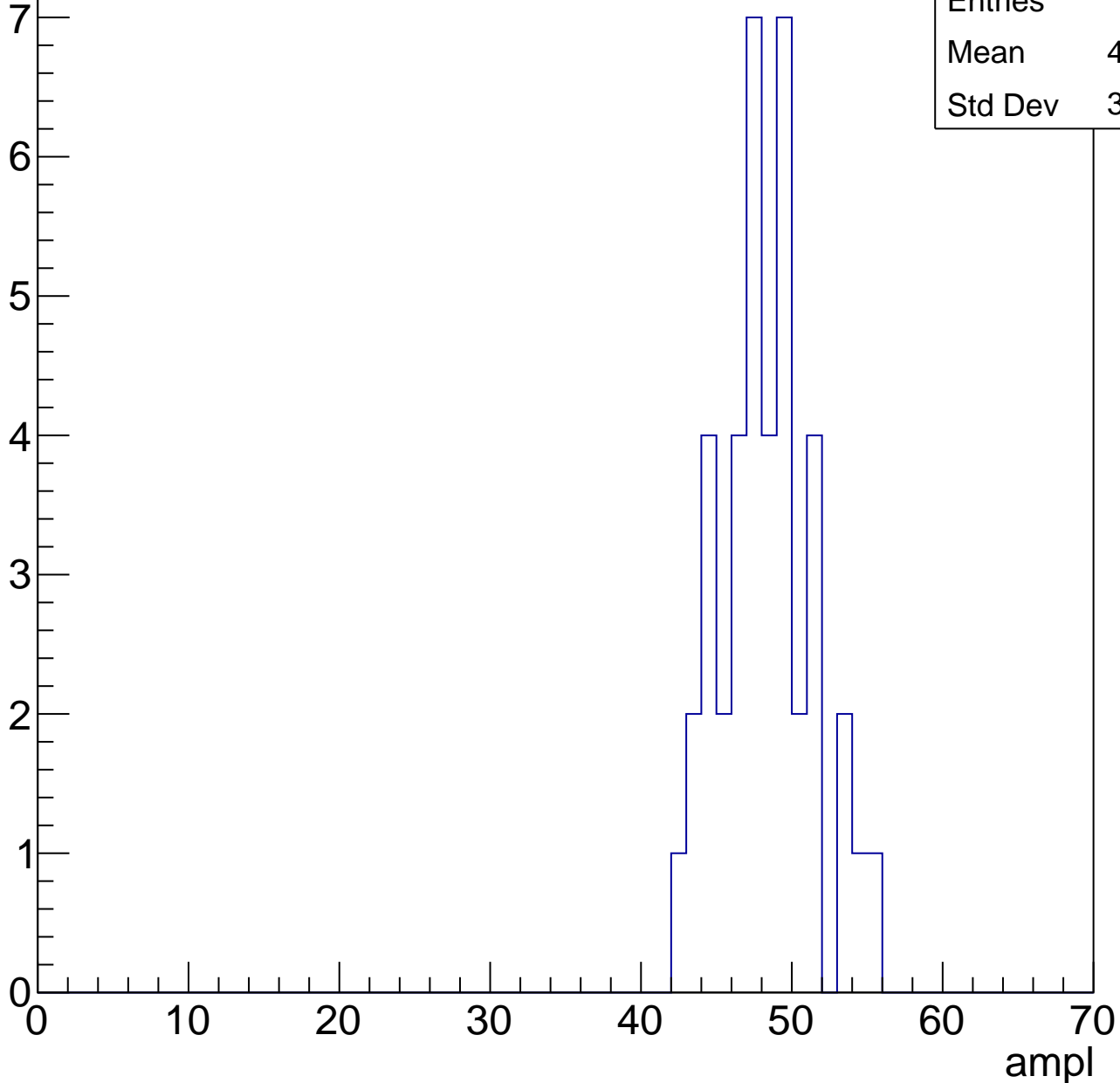


B0L001S, U7-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 47.83 |
| Std Dev | 3.028 |

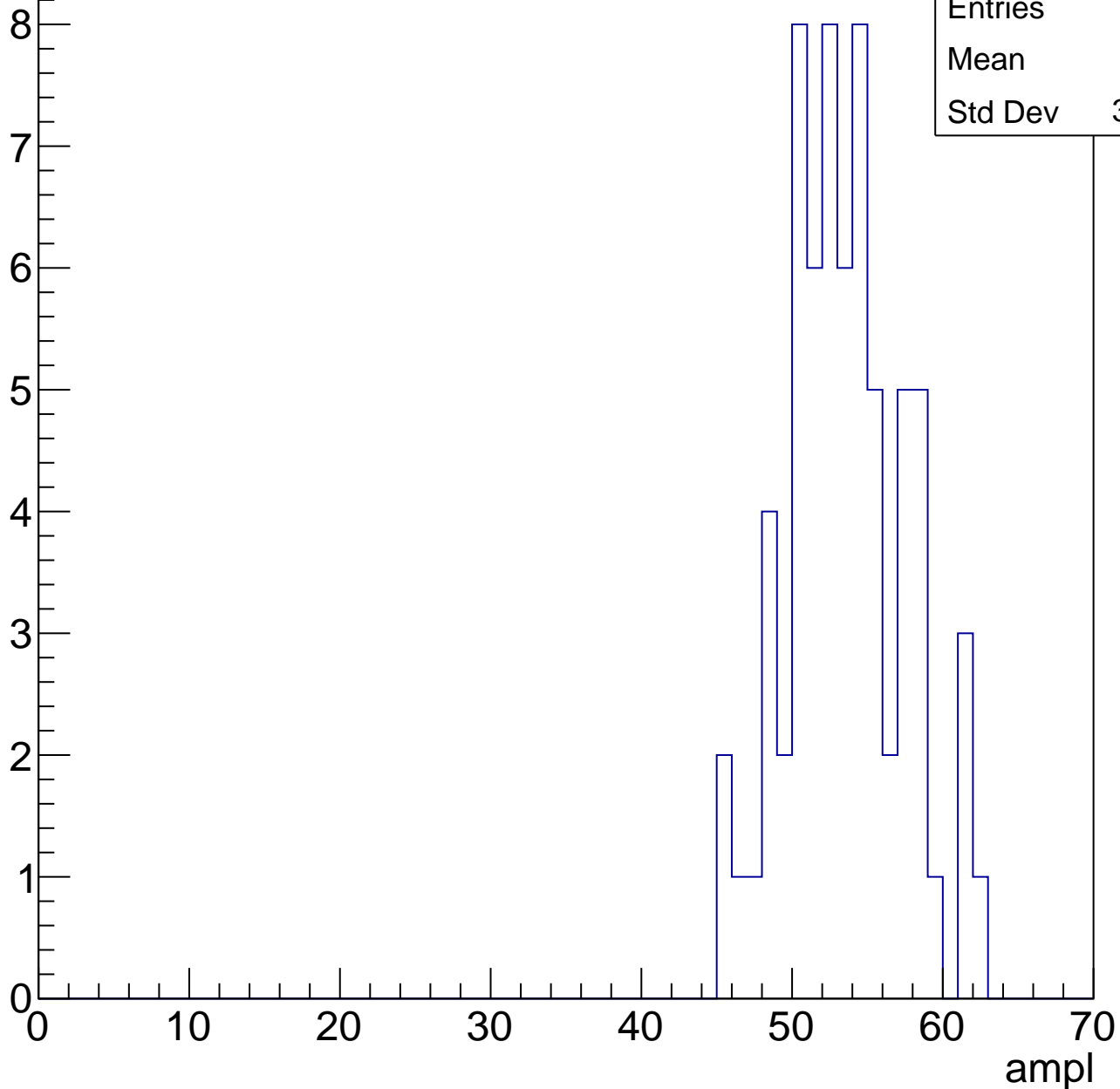


B0L001S, U7-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 53.1 |
| Std Dev | 3.851 |

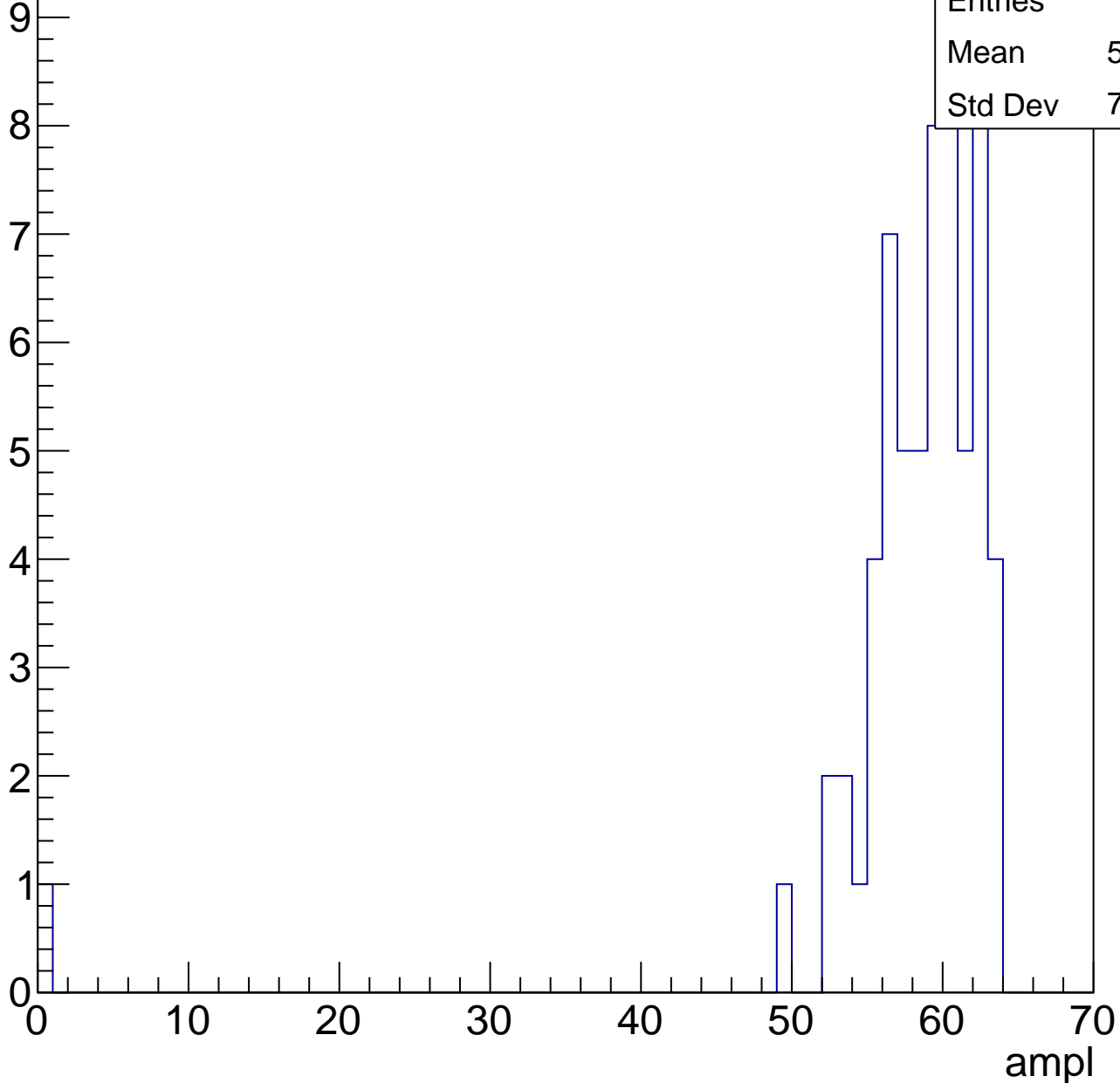


B0L001S, U7-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.57 |
| Std Dev | 7.942 |

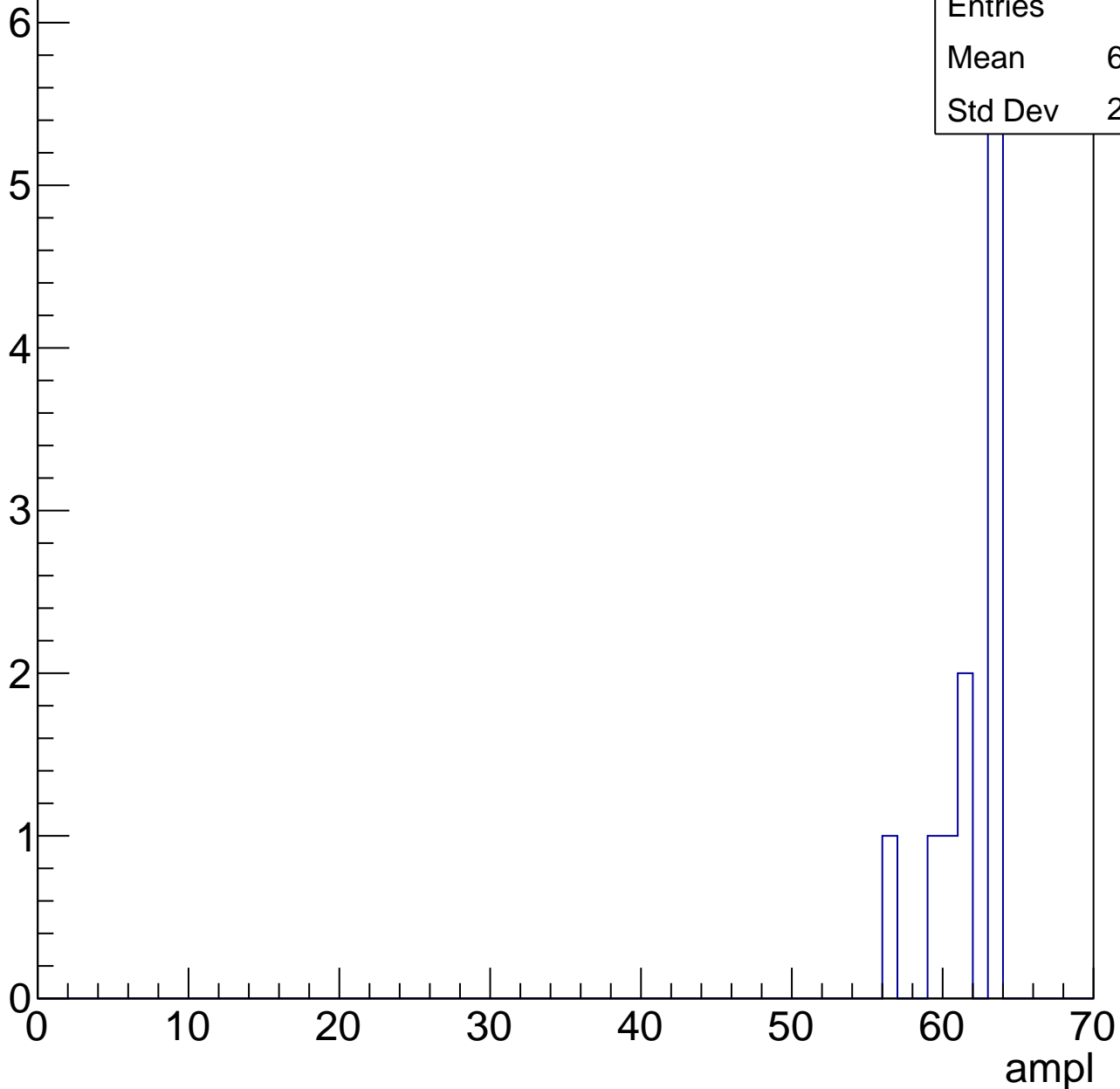


B0L001S, U7-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

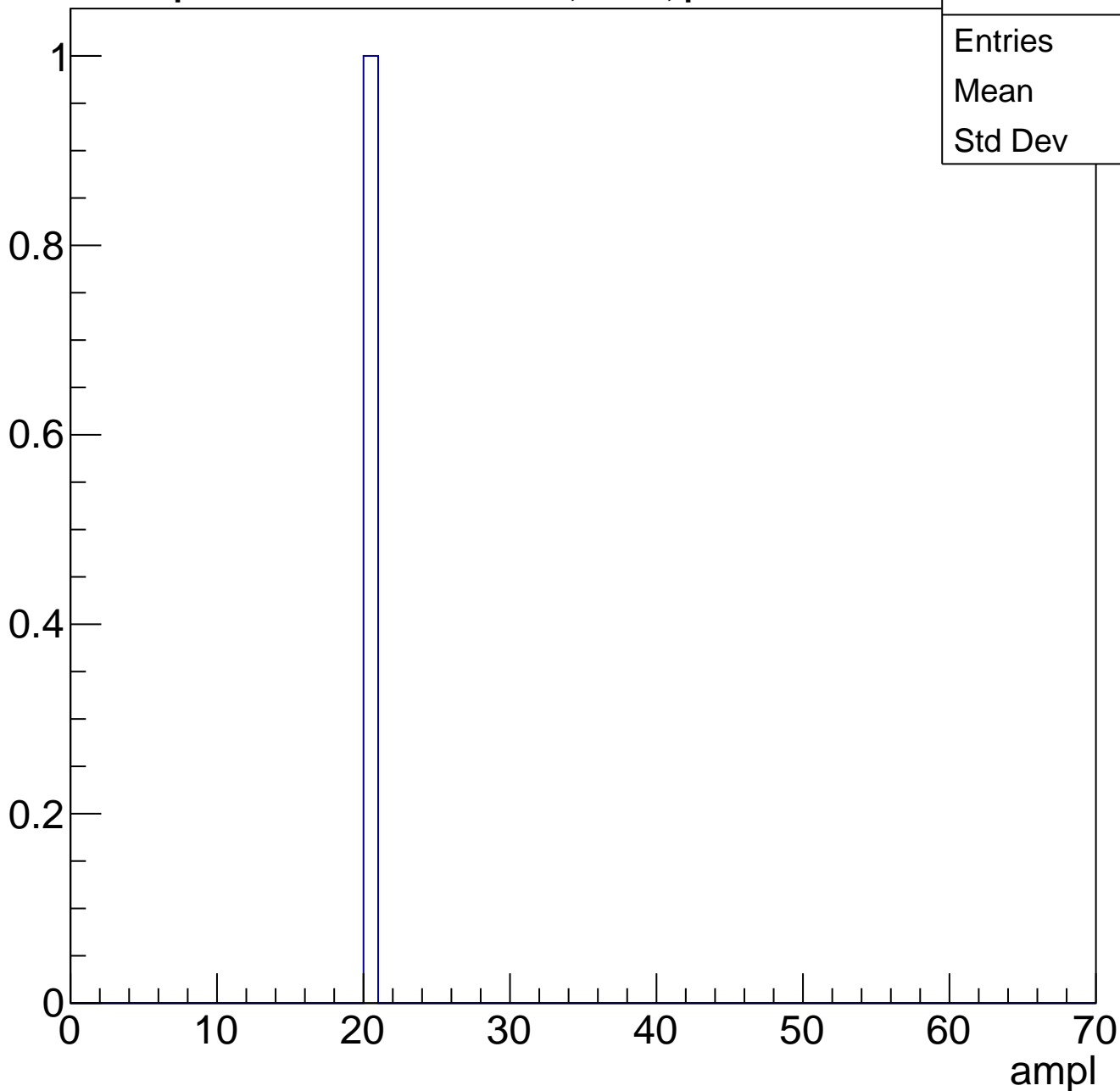
| | |
|---------|-------|
| Entries | 11 |
| Mean | 61.36 |
| Std Dev | 2.186 |



B0L001S, U7-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U7-ch127, adc0

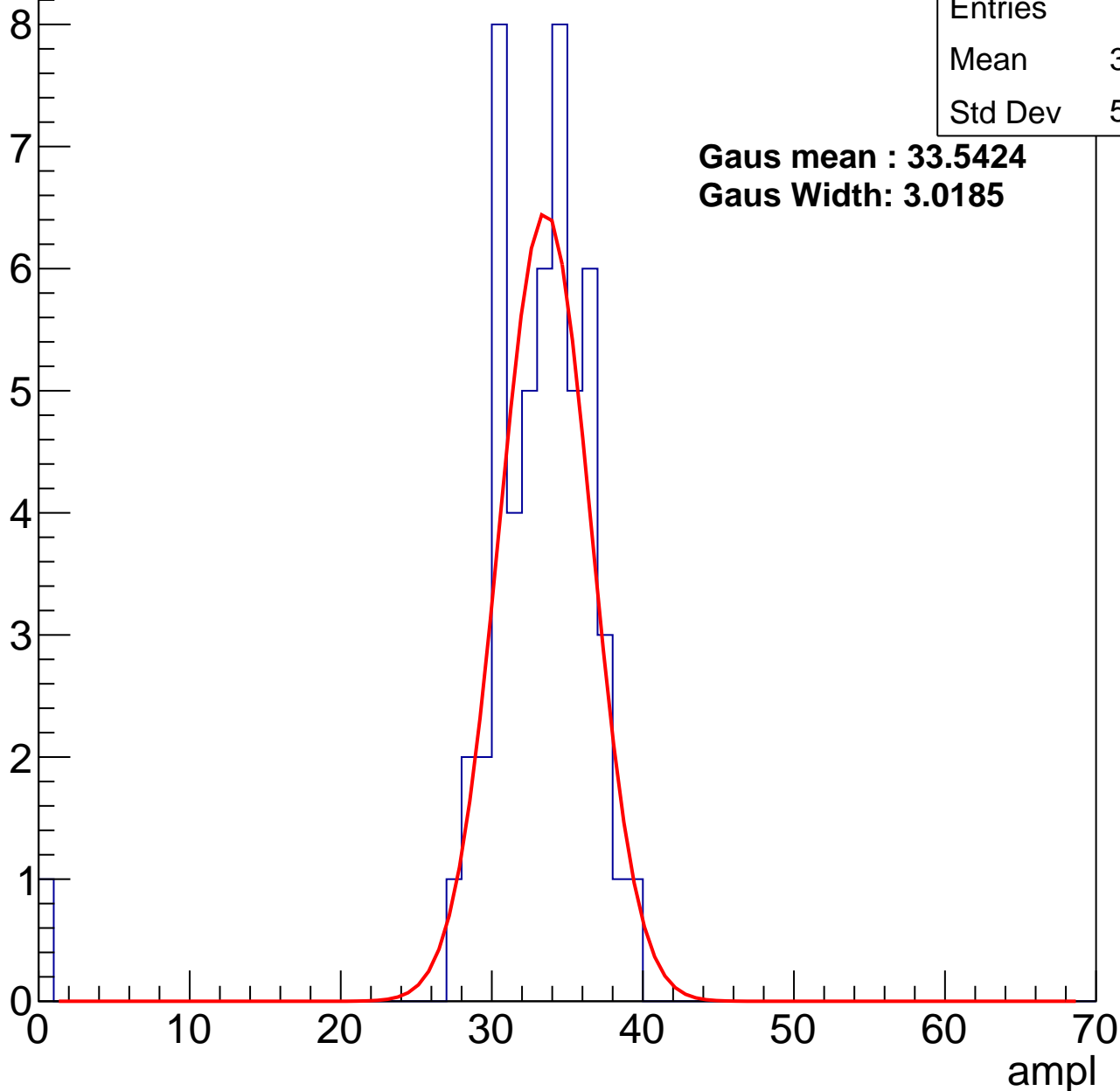
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 32.34 |
| Std Dev | 5.263 |

Gaus mean : 33.5424

Gaus Width: 3.0185



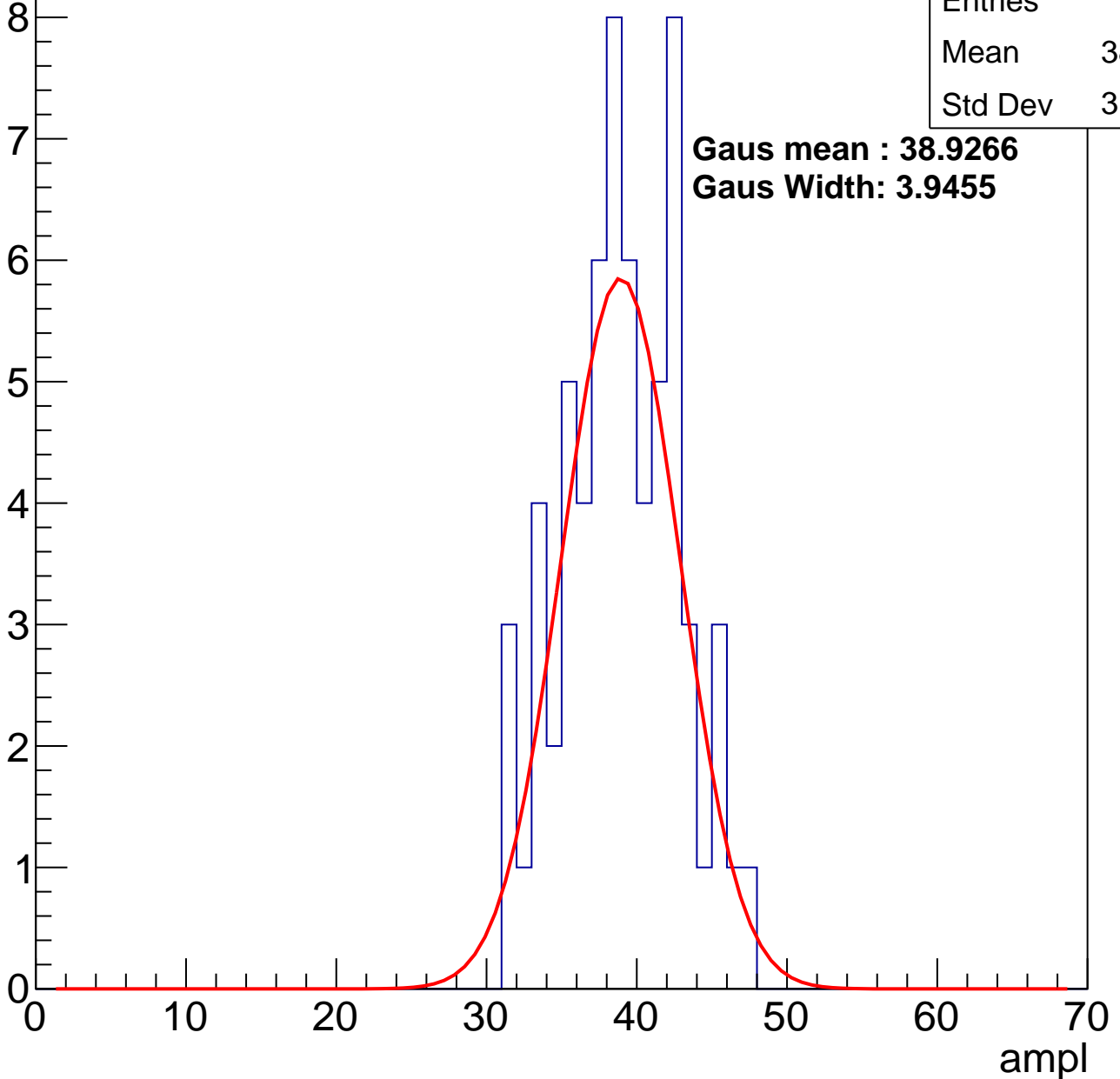
B0L001S, U7-ch127, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 38.55 |
| Std Dev | 3.843 |

Gaus mean : 38.9266
Gaus Width: 3.9455



B0L001S, U7-ch127, adc2

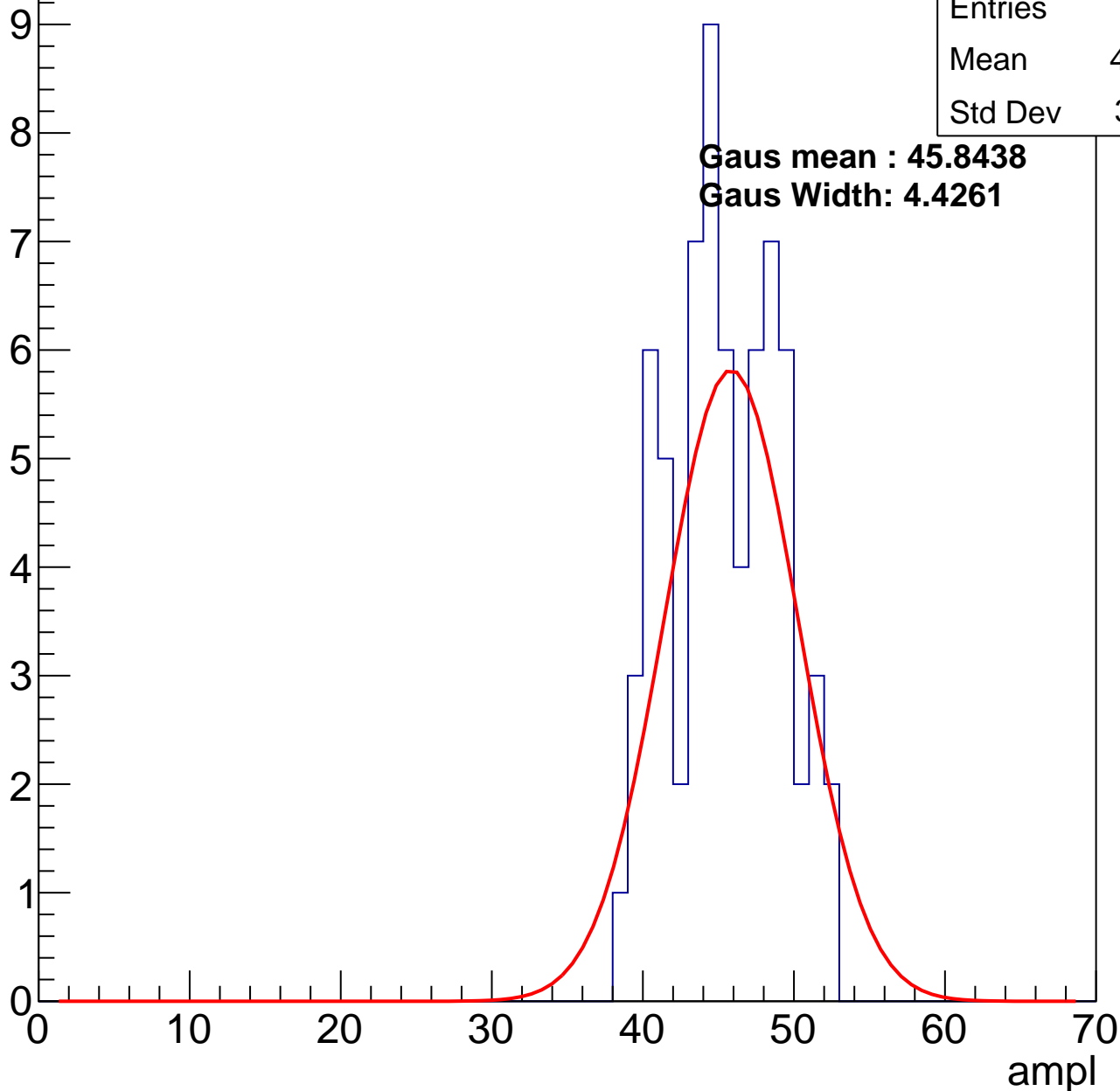
calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 44.99 |
| Std Dev | 3.581 |

Gaus mean : 45.8438

Gaus Width: 4.4261

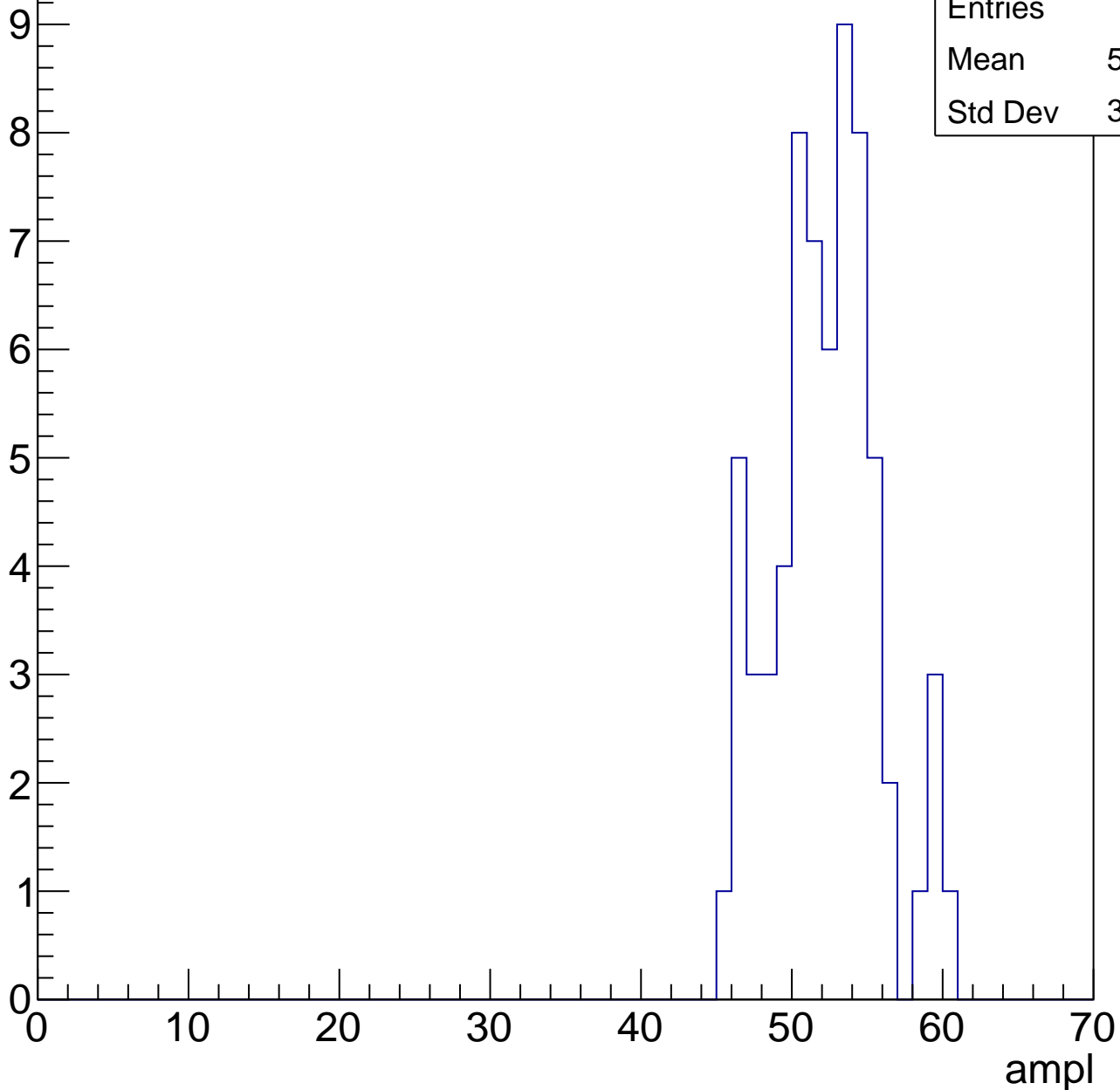


B0L001S, U7-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 51.76 |
| Std Dev | 3.456 |



B0L001S, U7-ch127, adc4

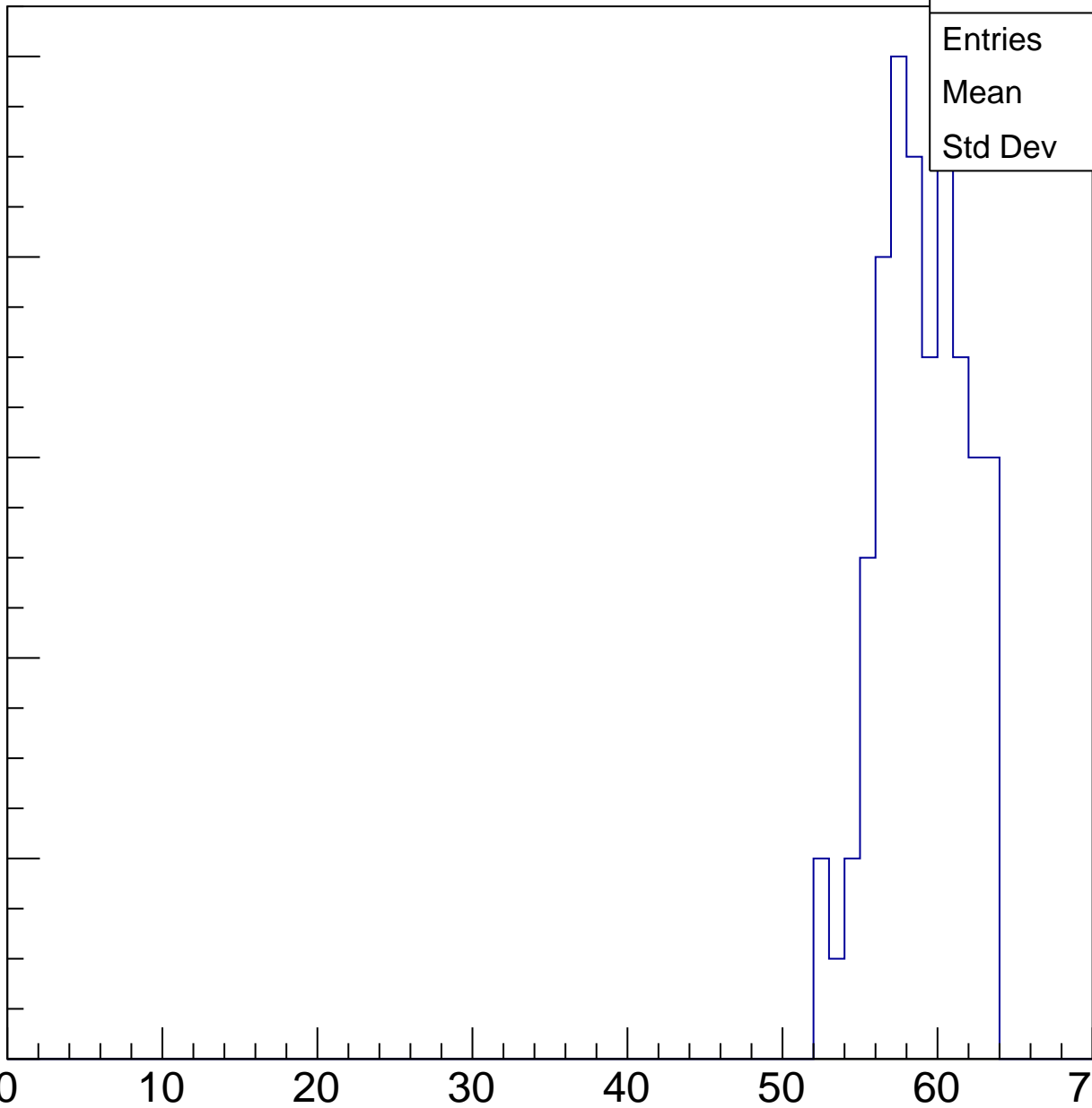
calib_packv5_042523_0143.root, FC#9, port A1

Entry

10
8
6
4
2
0

| | |
|---------|-------|
| Entries | 72 |
| Mean | 58.47 |
| Std Dev | 2.769 |

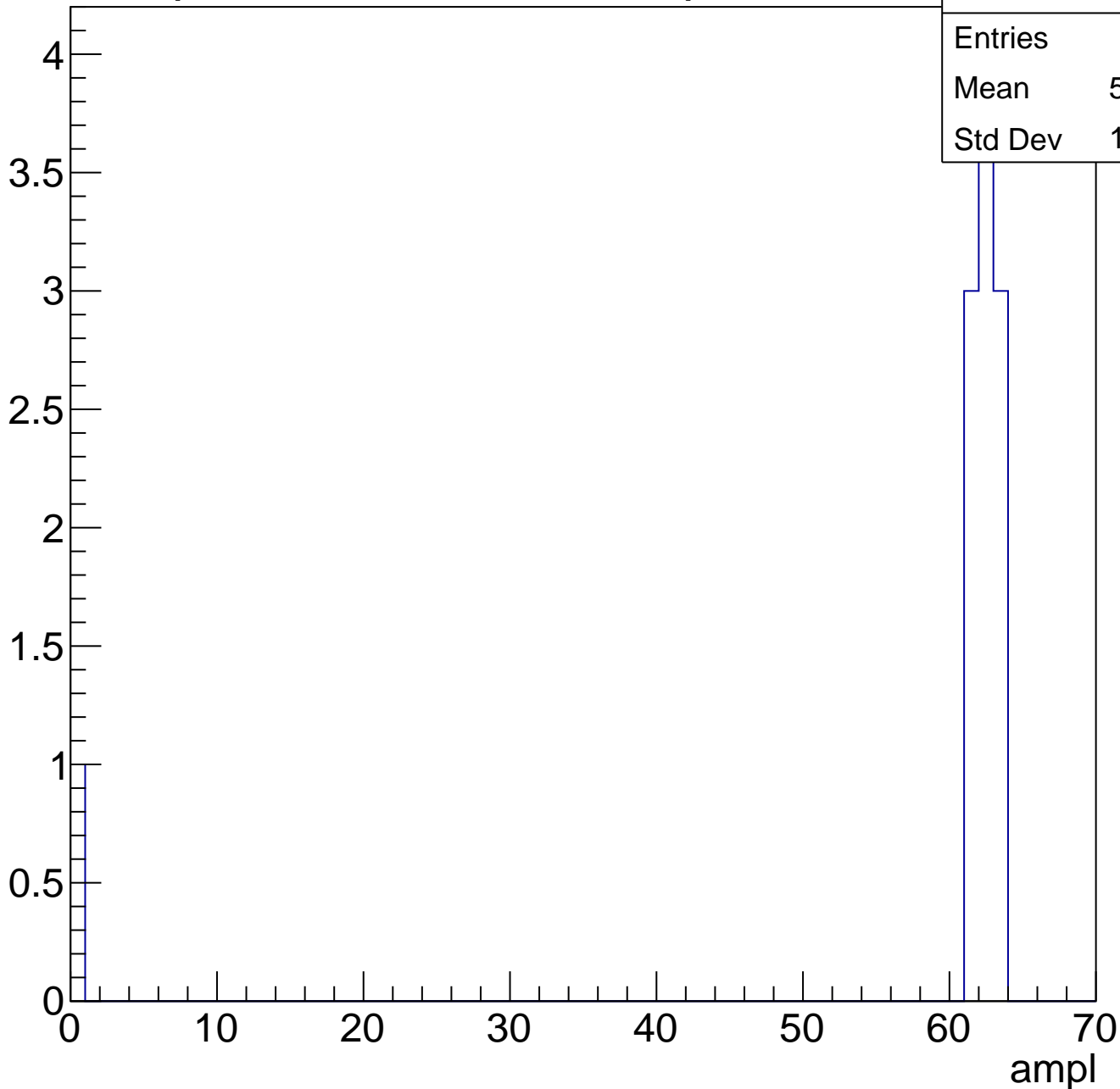
ampl



B0L001S, U7-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.36 |
| Std Dev | 17.84 |

B0L001S, U7-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

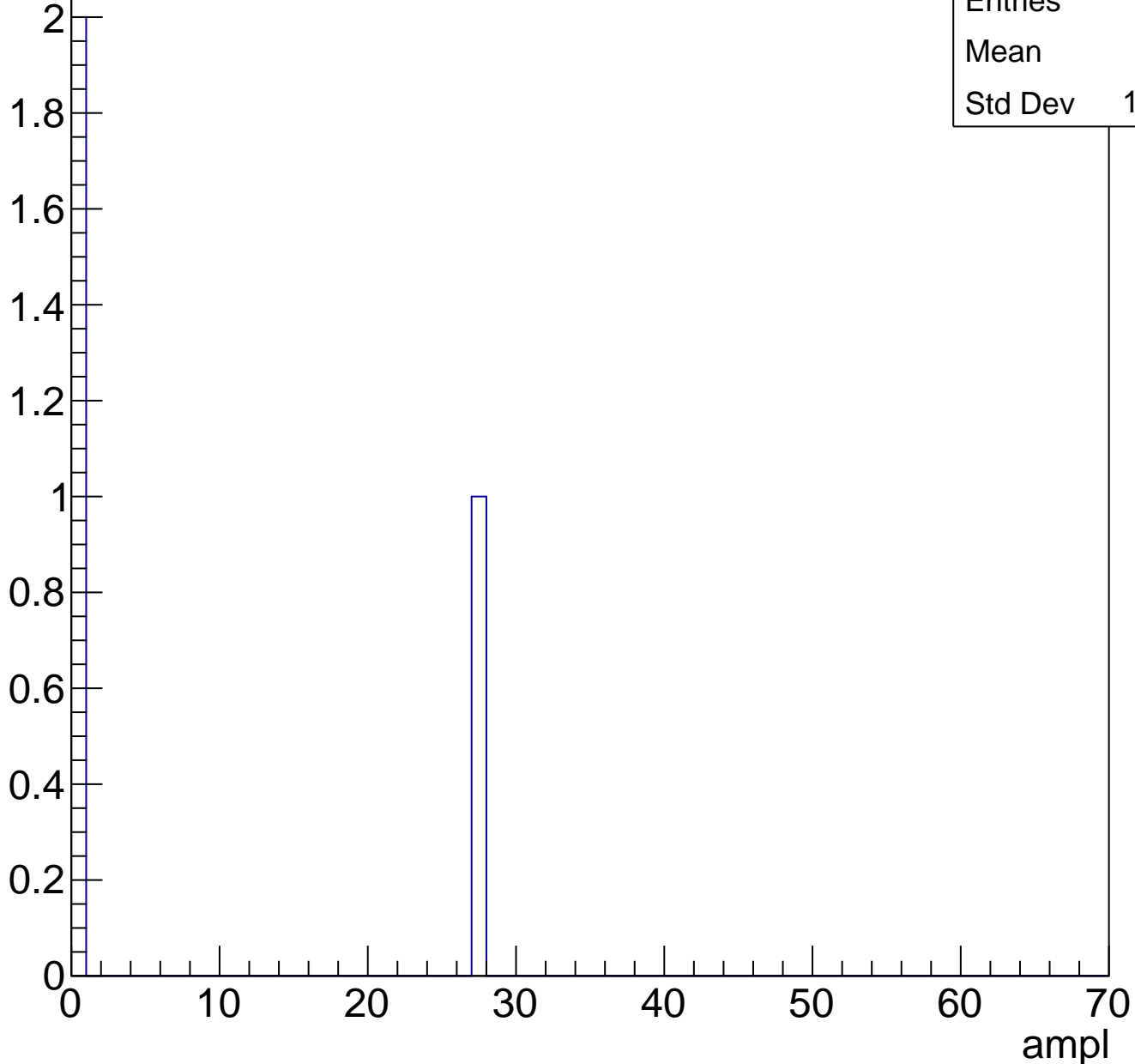
Entry



B0L001S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

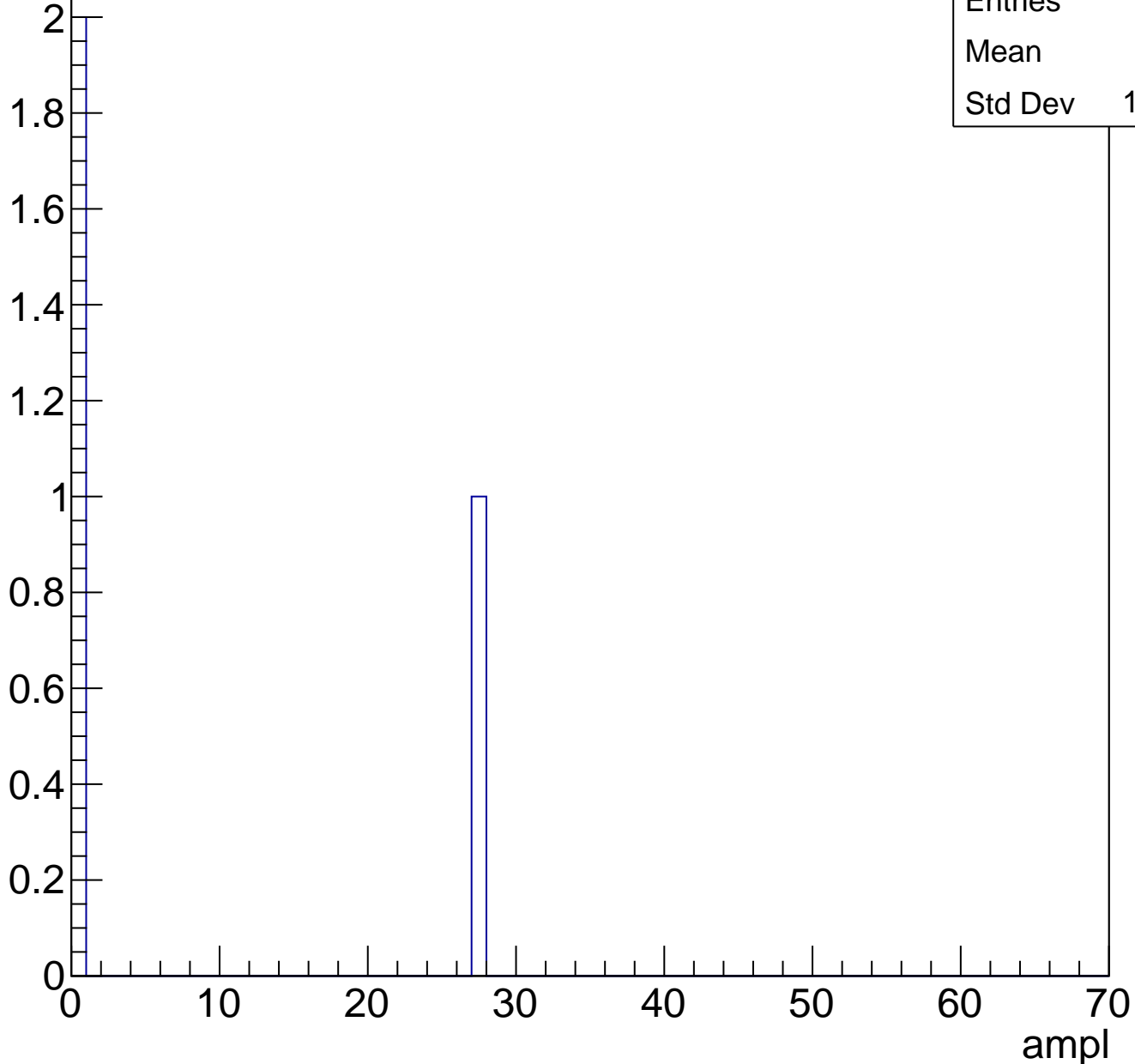


| | |
|---------|-------|
| Entries | 3 |
| Mean | 9 |
| Std Dev | 12.73 |

B0L001S, U7-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 9 |
| Std Dev | 12.73 |