

B1L001S, U16-ch0, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	30.49
Std Dev	4.006

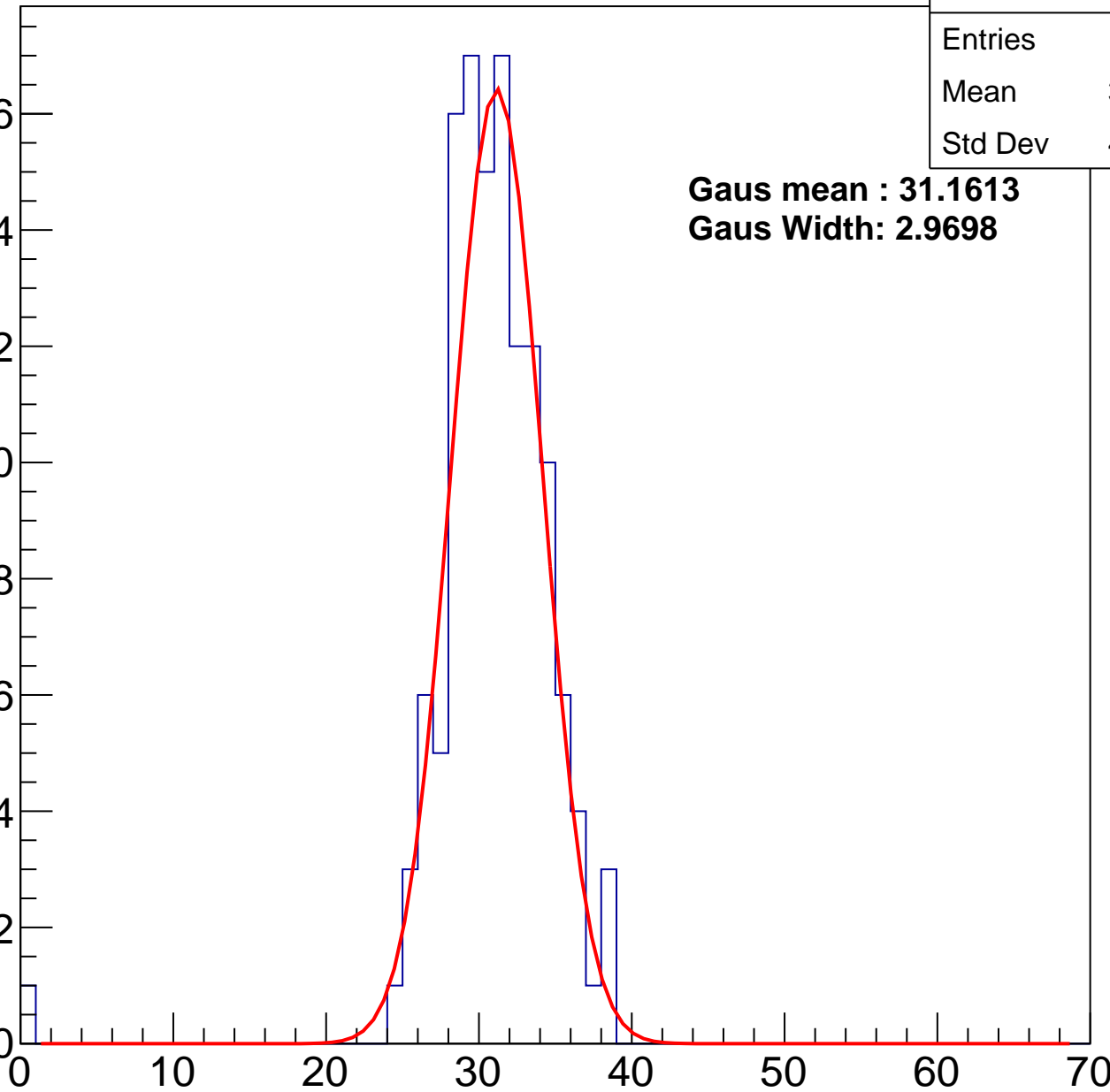
Gaus mean : 31.1613

Gaus Width: 2.9698

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.59
Std Dev	3.348

Gaus mean : 37.9912

Gaus Width: 3.4004

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

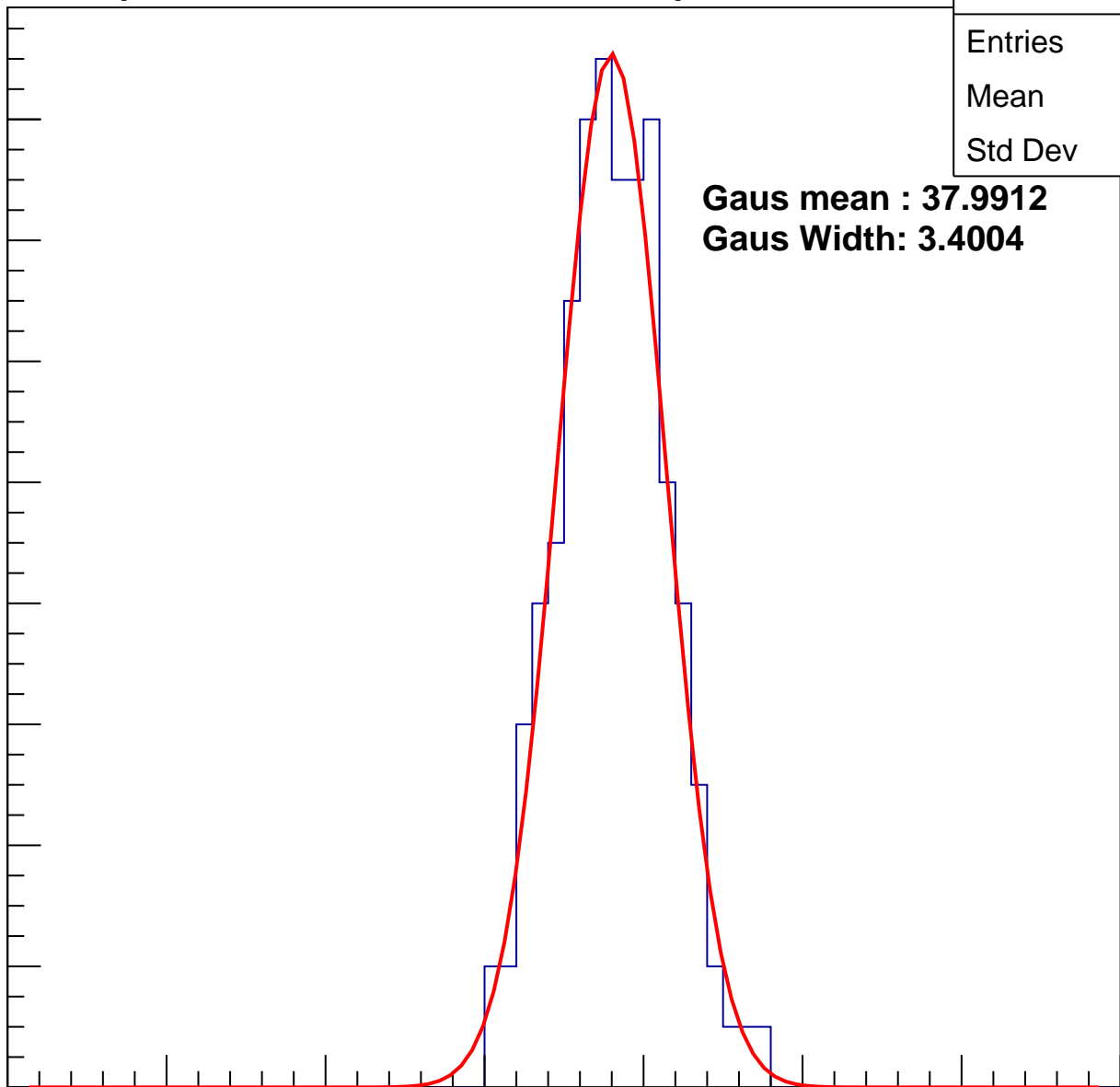
40

50

60

70

ampl



B1L001S, U16-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	44.14
Std Dev	3.236

Gaus mean : 44.7321

Gaus Width: 3.3388

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

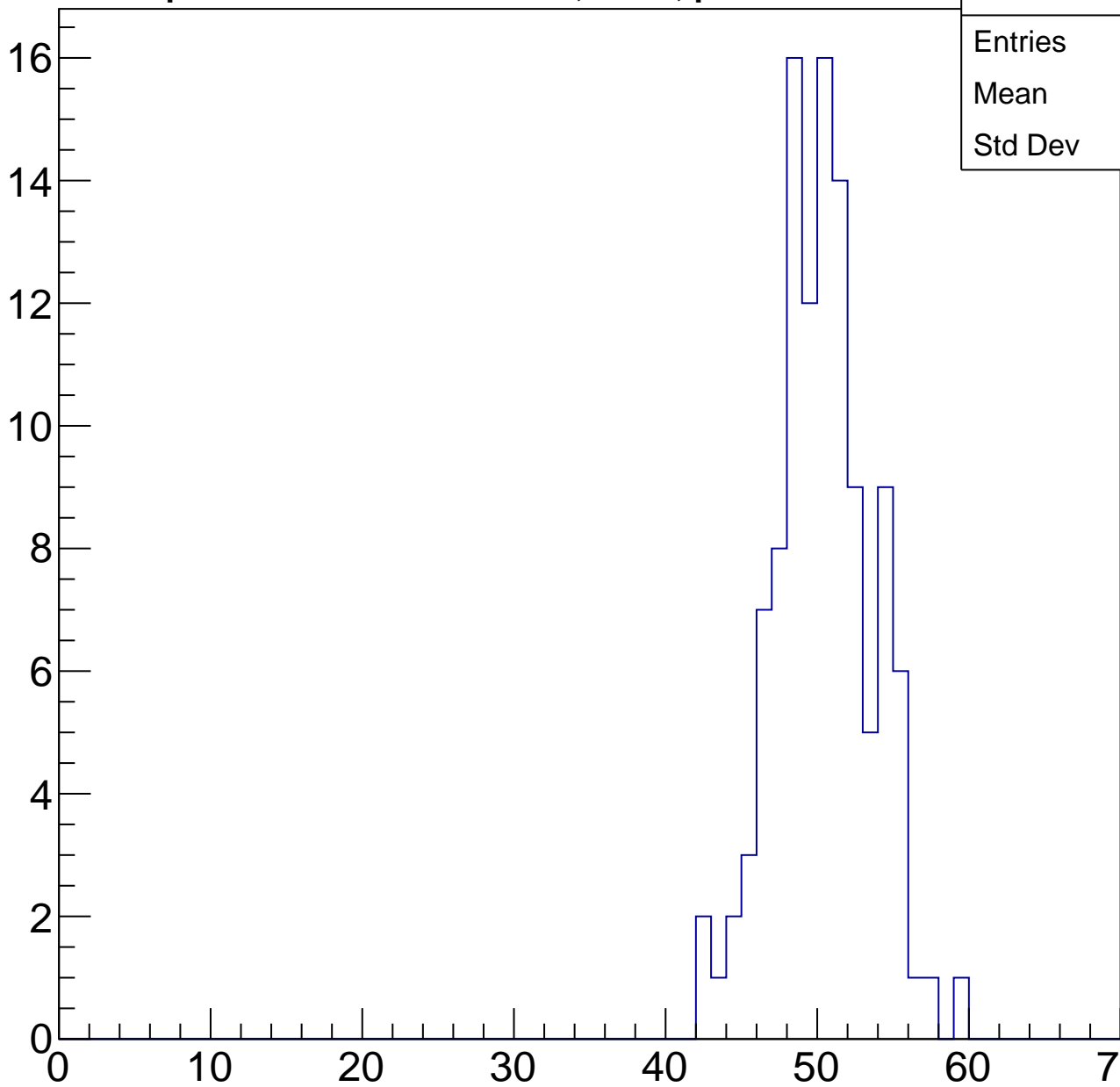
Entries	113
Mean	49.9
Std Dev	3.173

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

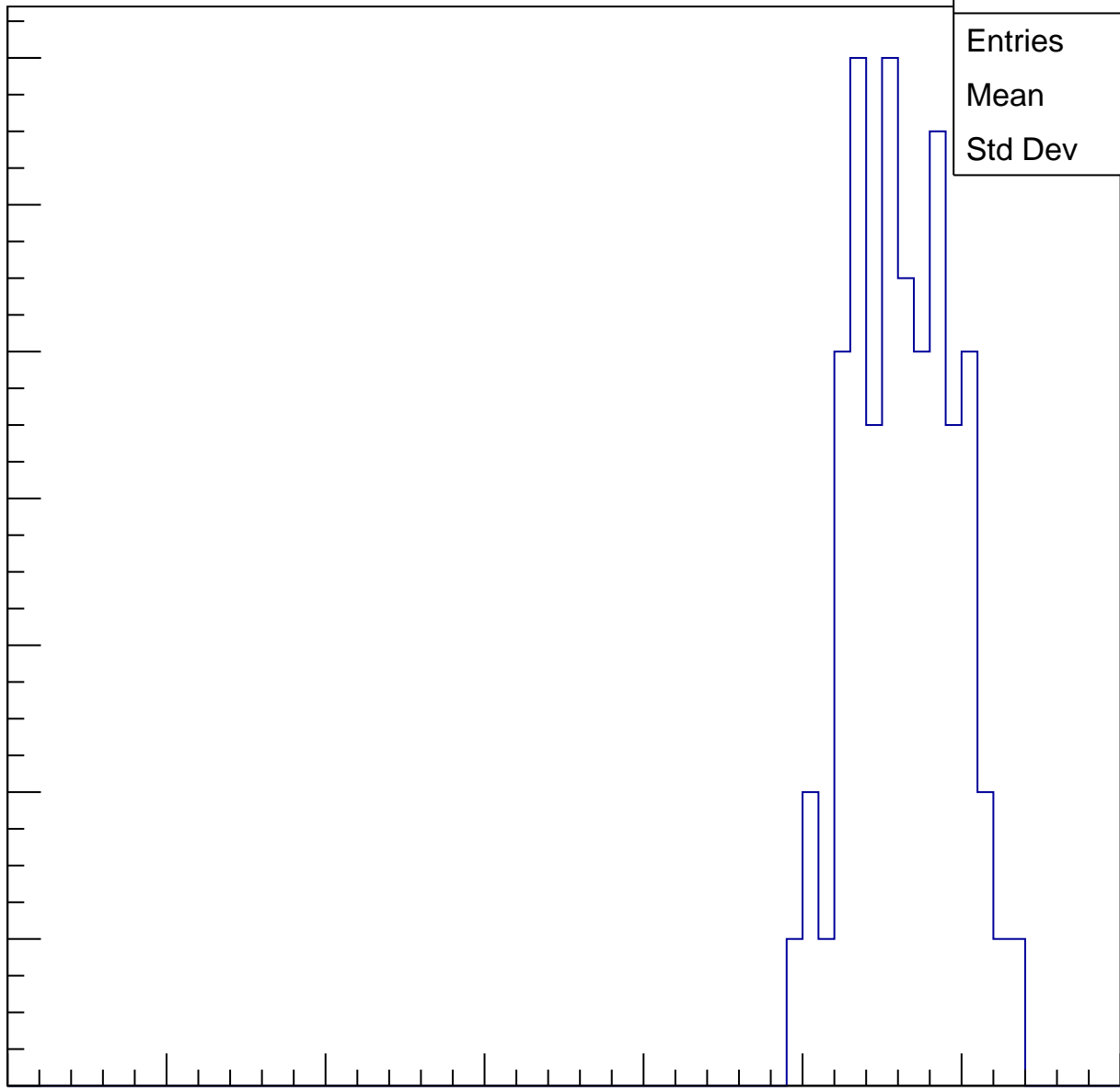
Entries	116
Mean	55.89
Std Dev	3.216

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch0, adc5

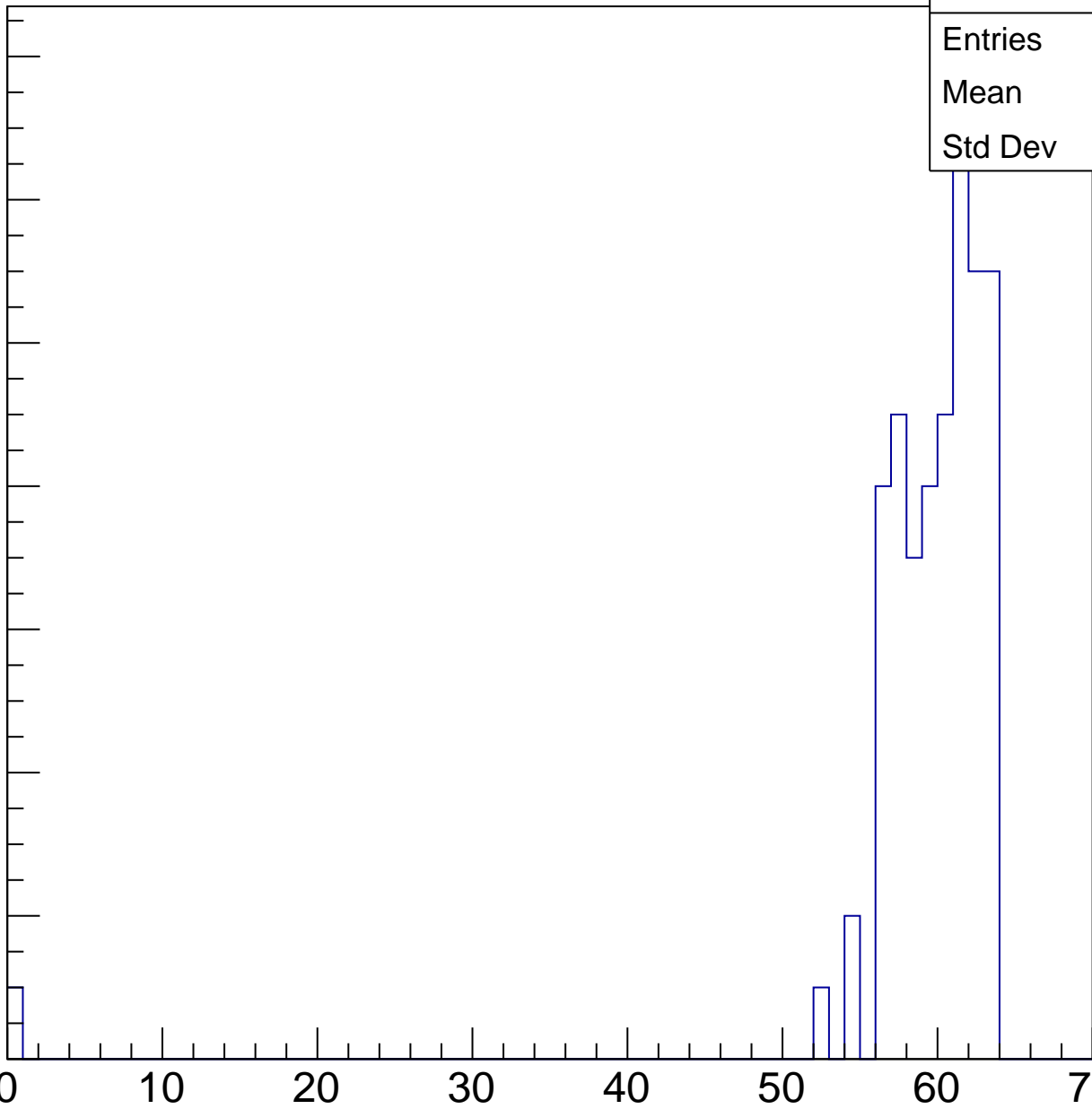
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.86
Std Dev	7.057

ampl

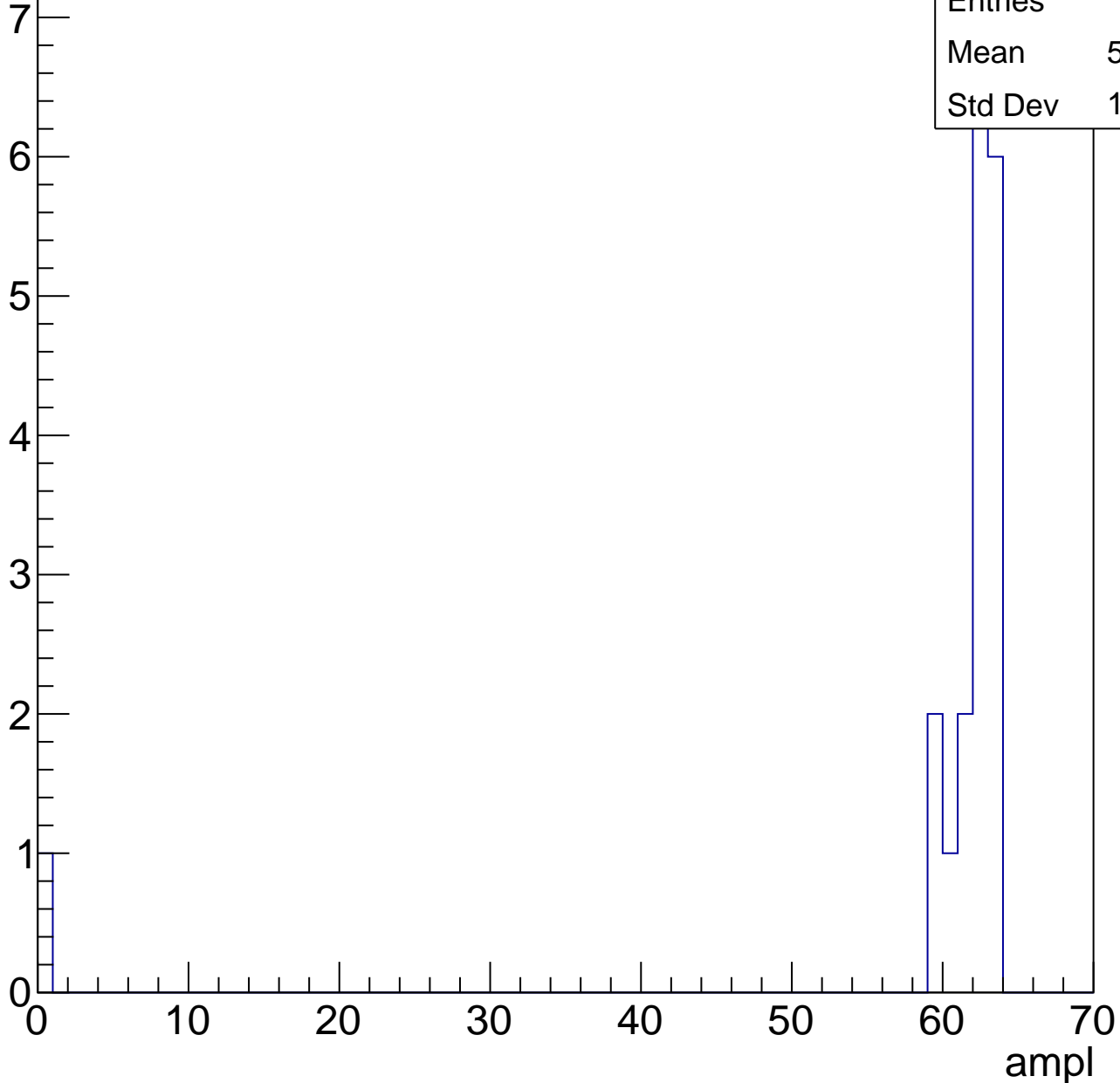


B1L001S, U16-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

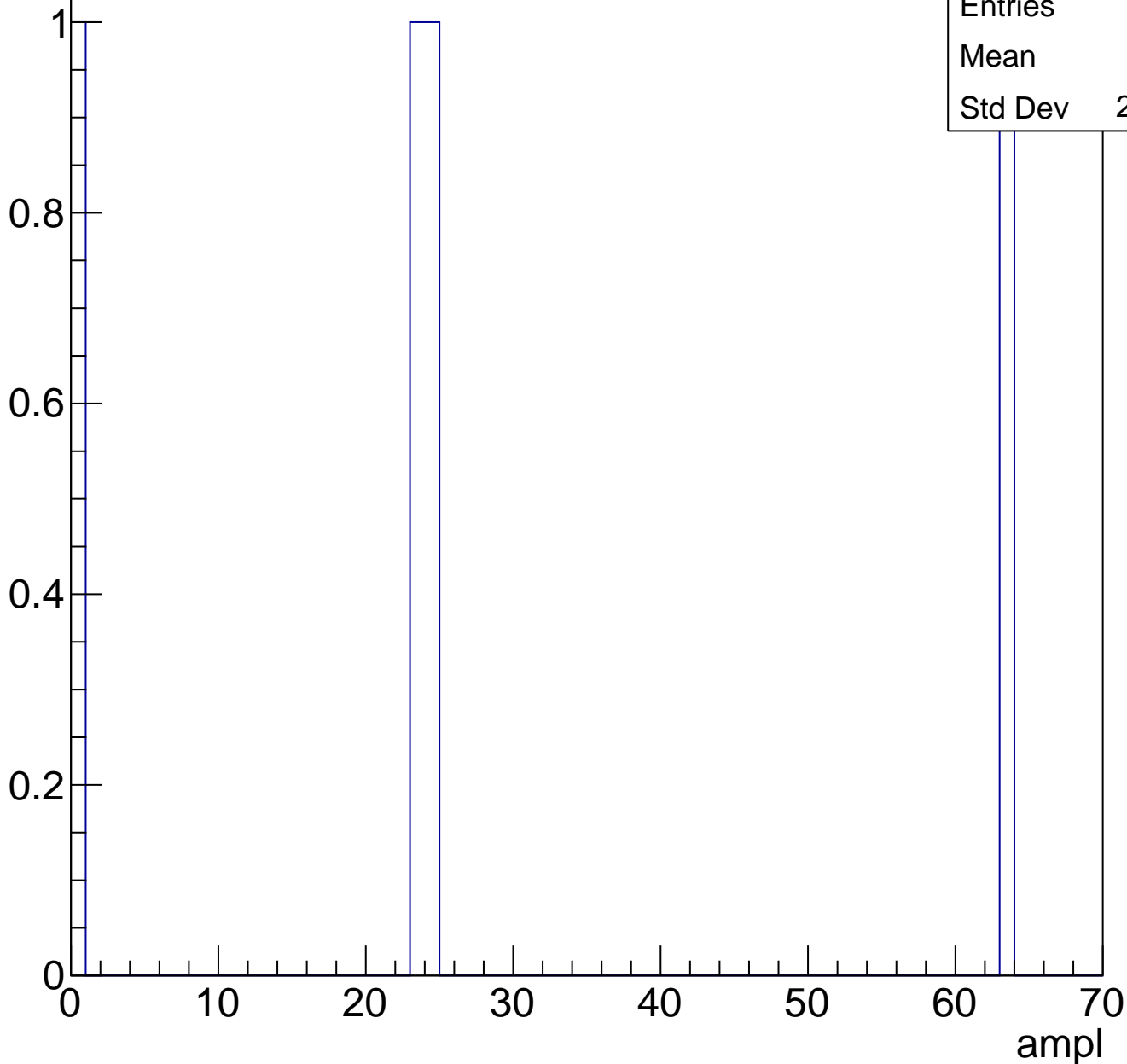
Entries	19
Mean	58.53
Std Dev	13.85



B1L001S, U16-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	27.5
Std Dev	22.63

B1L001S, U16-ch1, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	29.31
Std Dev	3.663

Gaus mean : 30.1157

Gaus Width: 3.5980

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

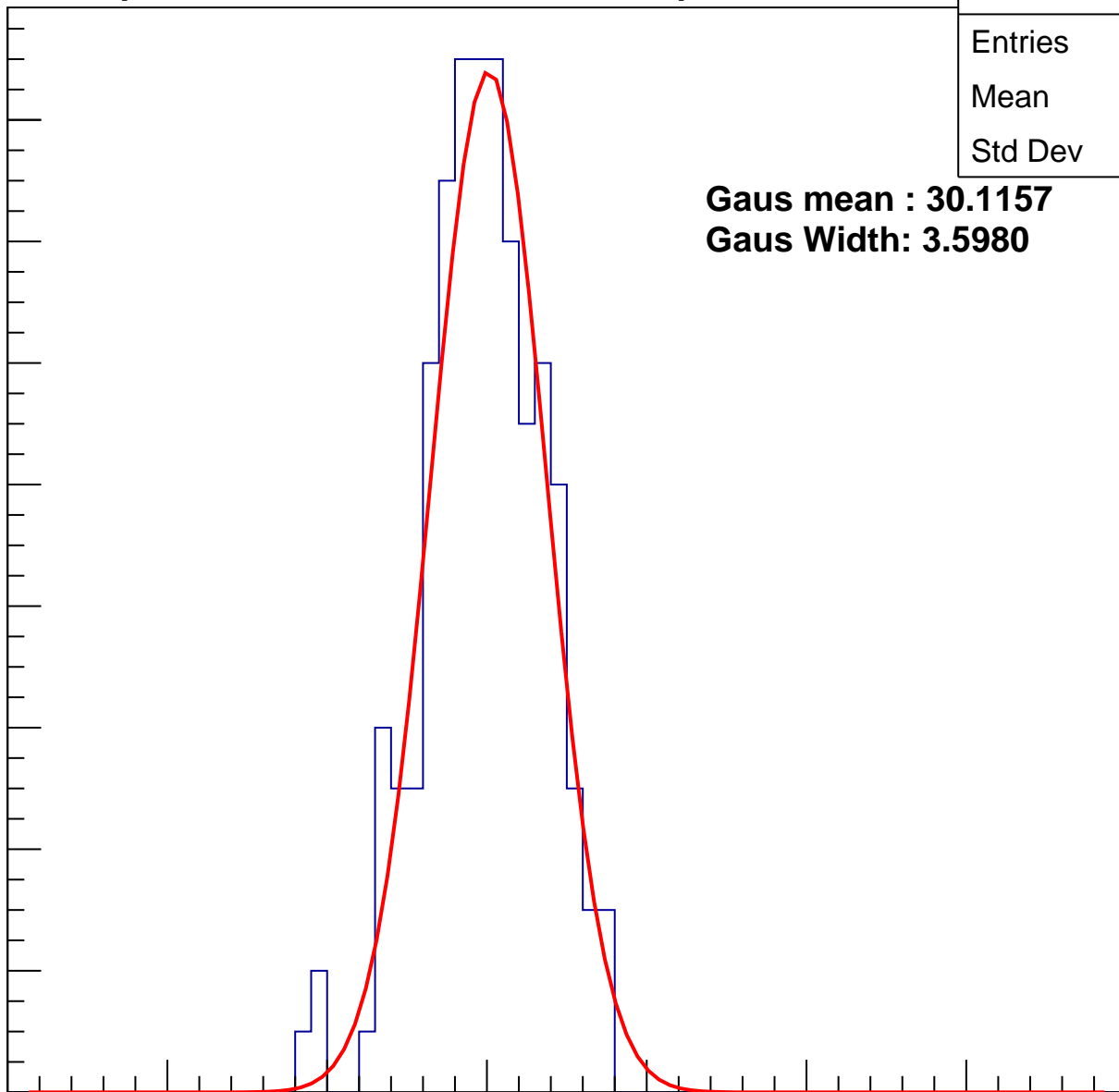
40

50

60

70

ampl



B1L001S, U16-ch1, adc1

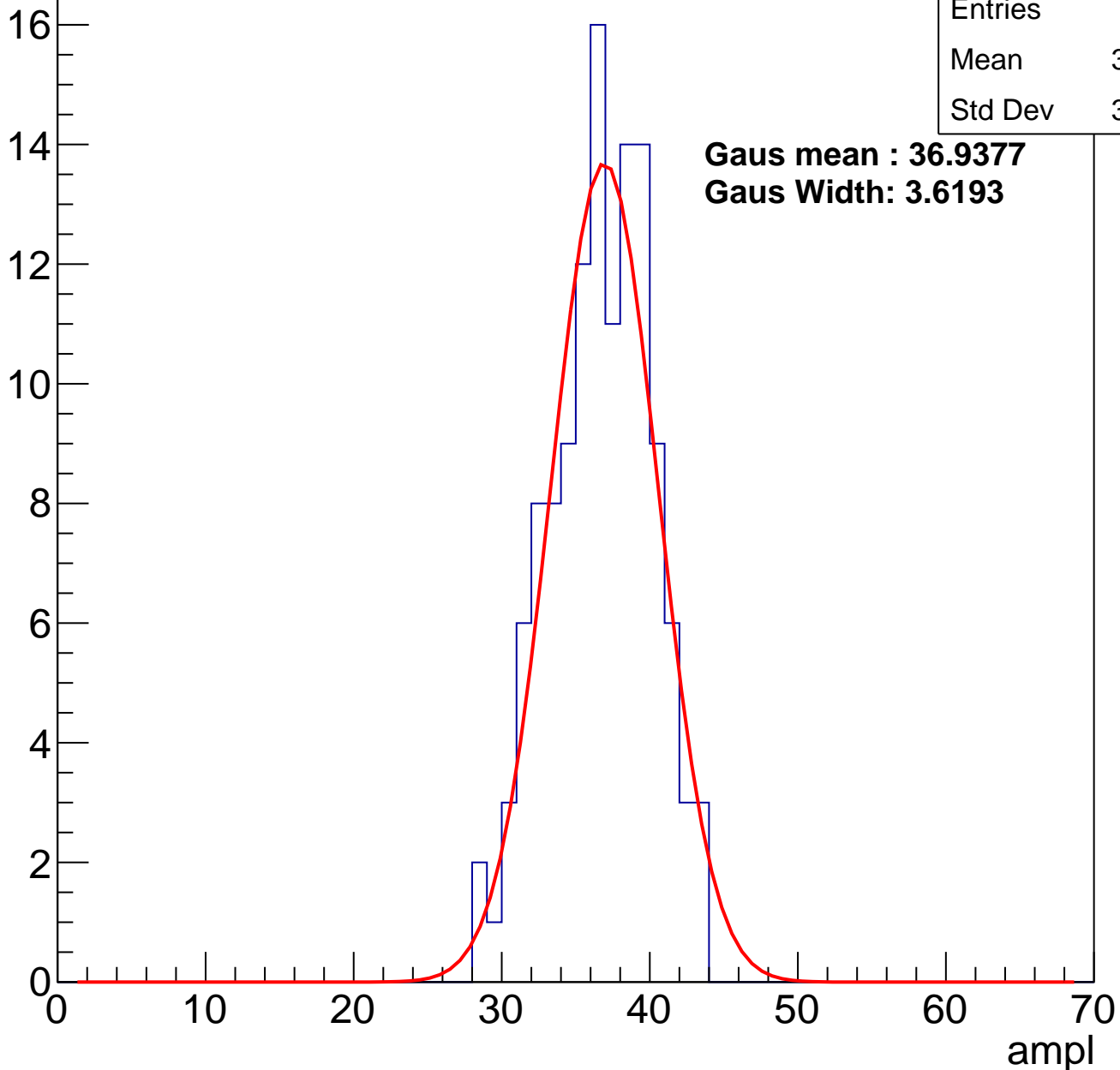
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.23
Std Dev	3.368

Gaus mean : 36.9377

Gaus Width: 3.6193

Entry



B1L001S, U16-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

42.84

Std Dev

3.635

Gaus mean : 43.4988

Gaus Width: 3.4804

0

10

20

30

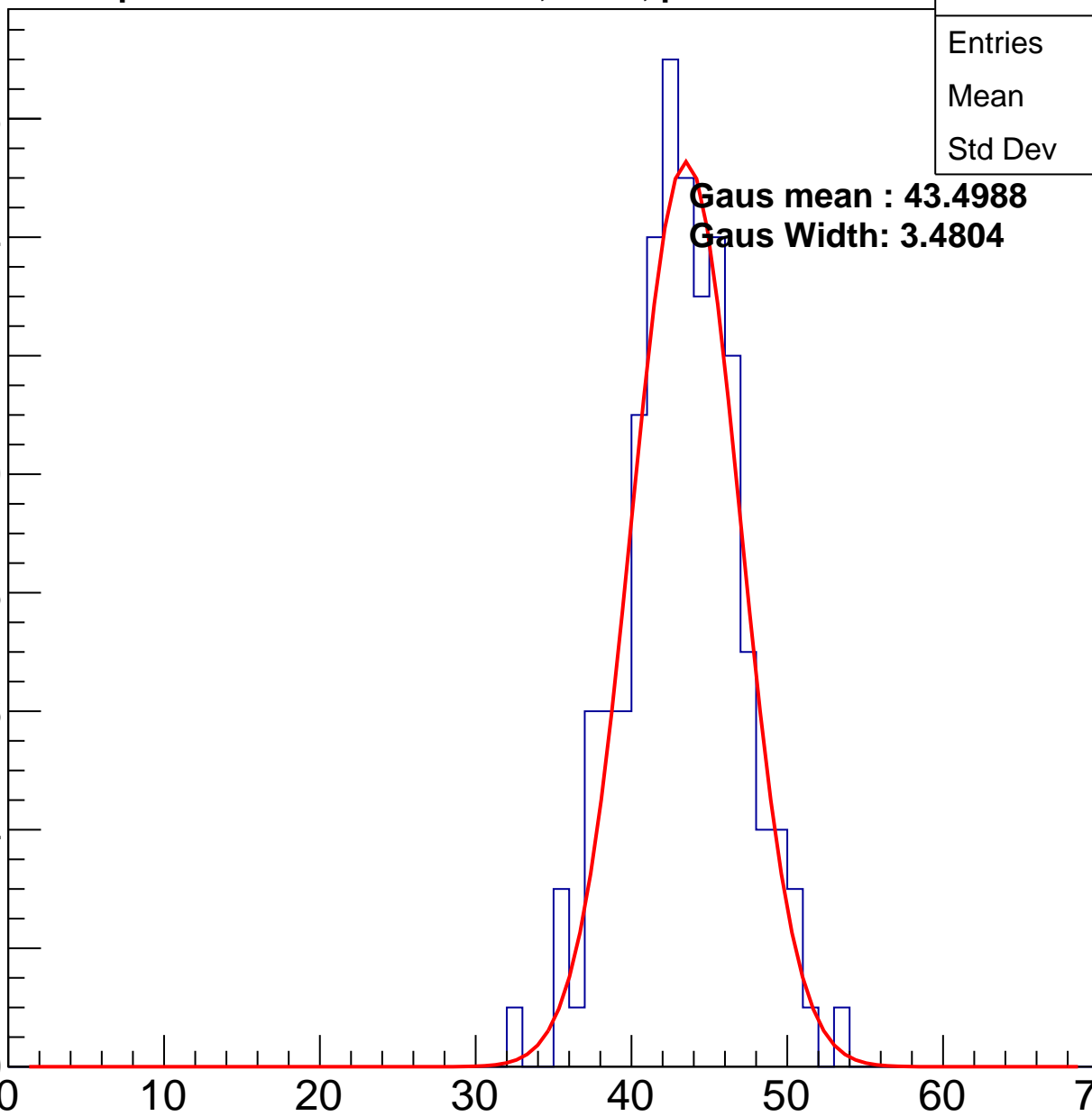
40

50

60

70

ampl



B1L001S, U16-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	48.99
Std Dev	3.075

Entry

12

10

8

6

4

2

0

0

10

20

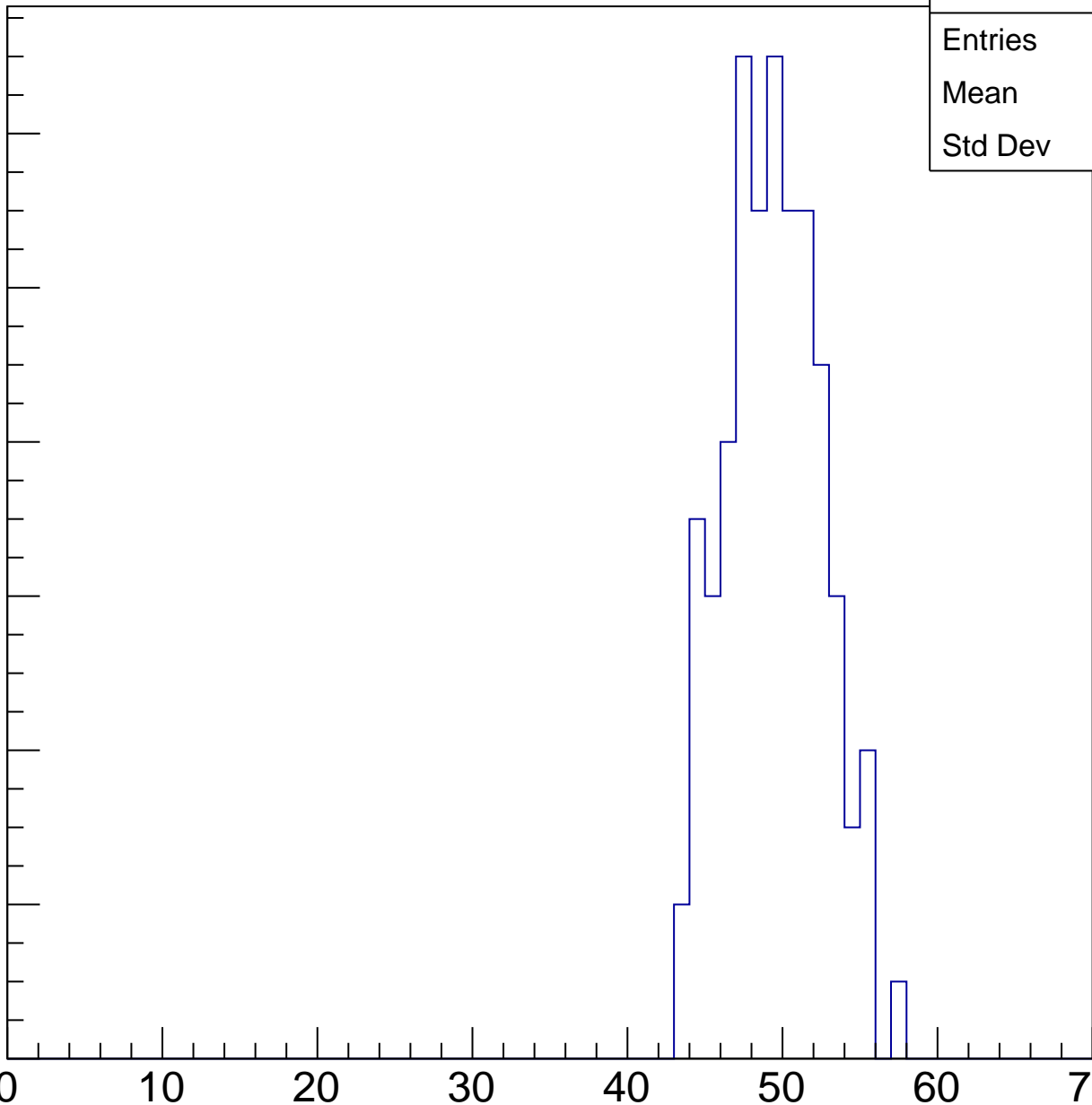
30

40

50

60

ampl



B1L001S, U16-ch1, adc4

calib_packv5_042523_0143.root, FC#2, port C2

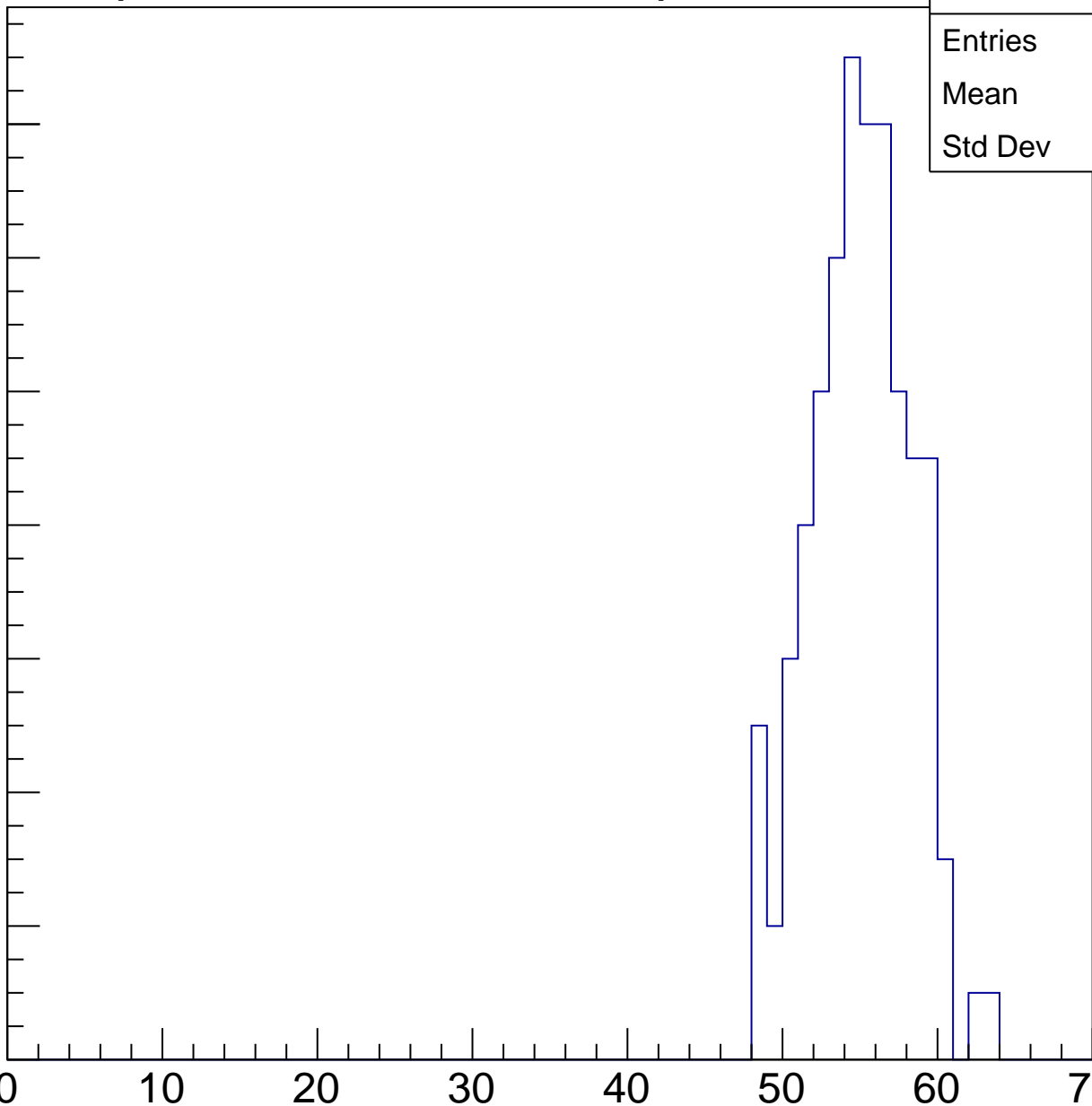
Entries	119
Mean	54.57
Std Dev	3.166

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch1, adc5

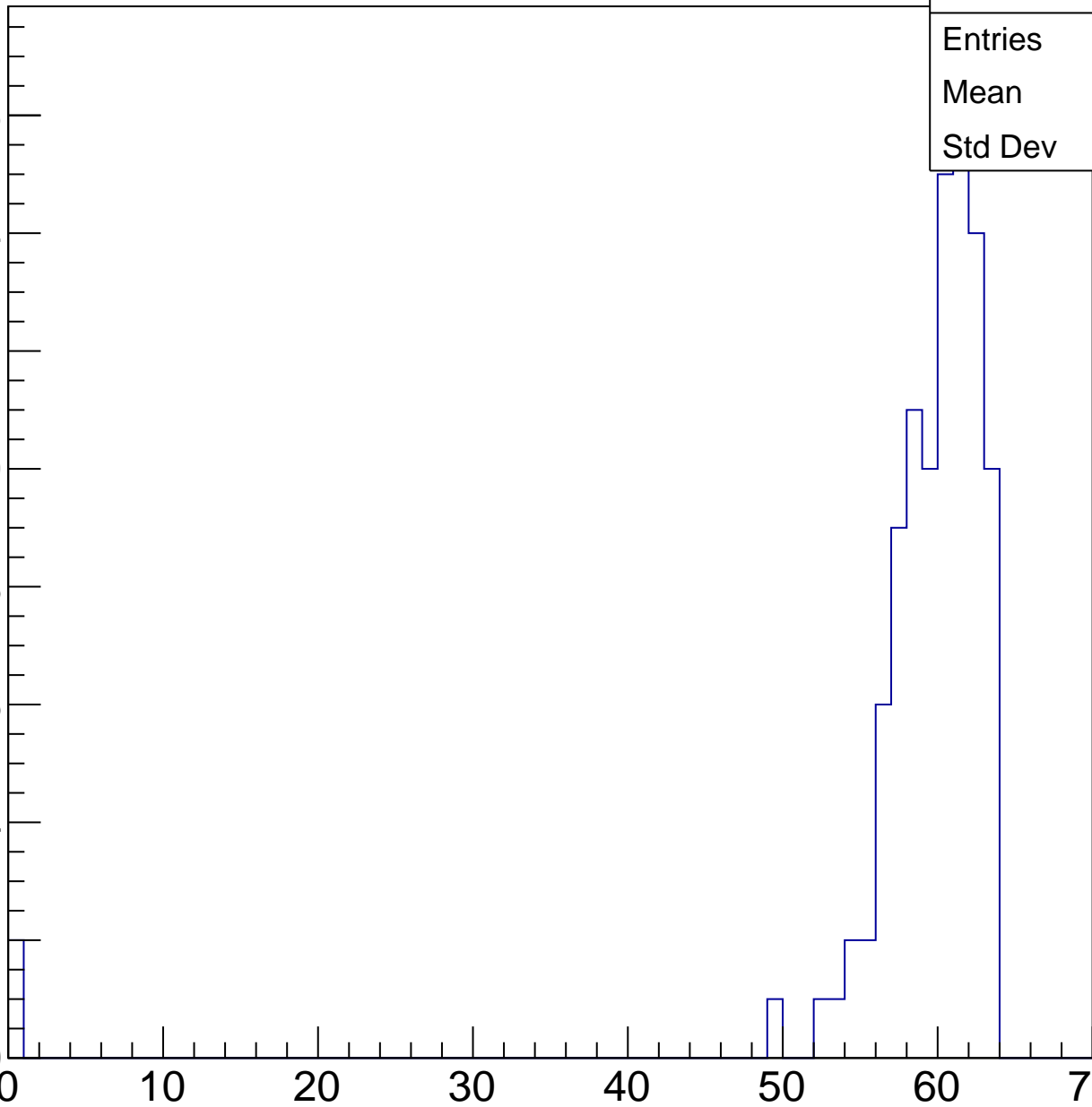
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.26
Std Dev	8.699

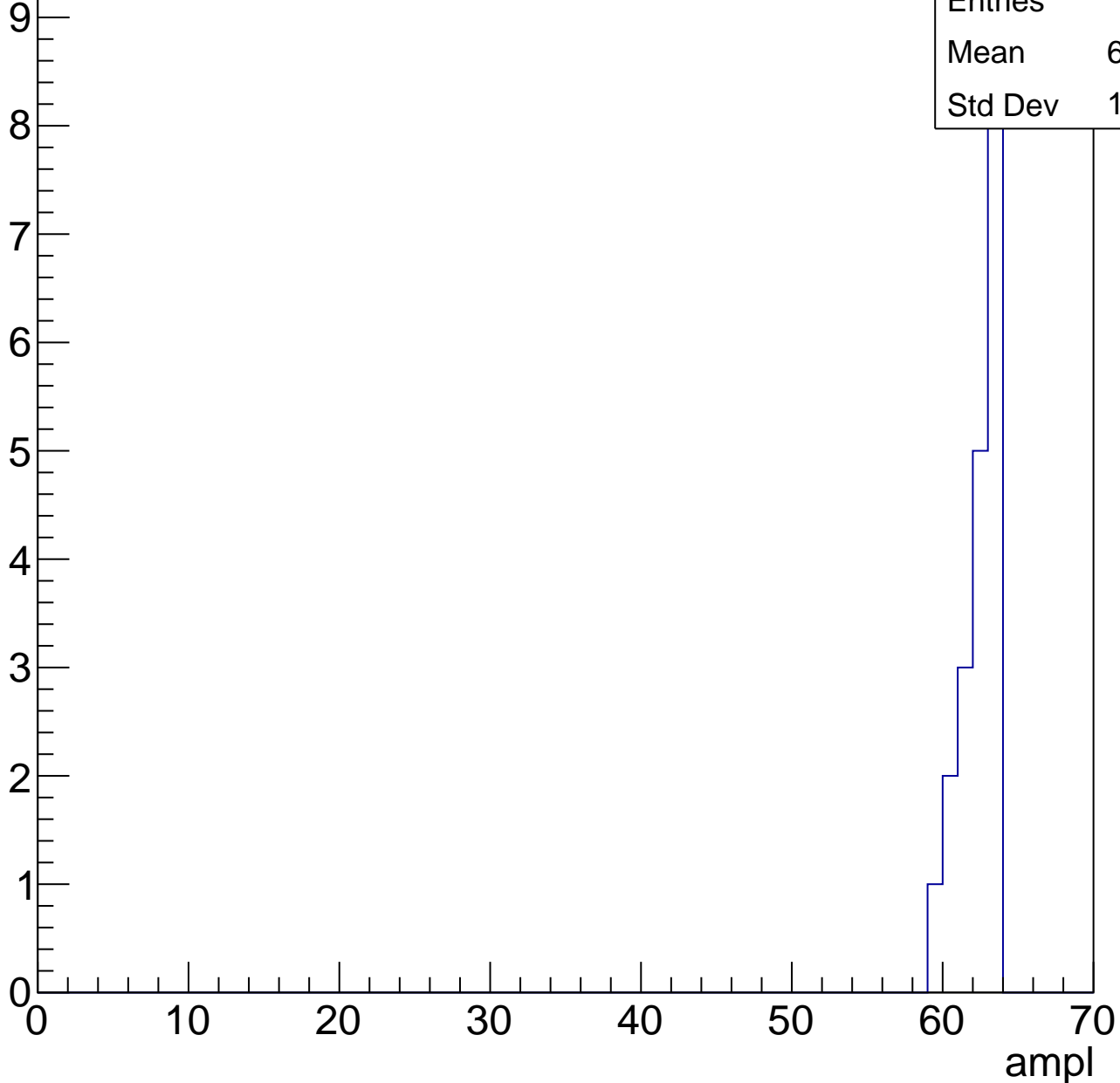
ampl



B1L001S, U16-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch2, adc0

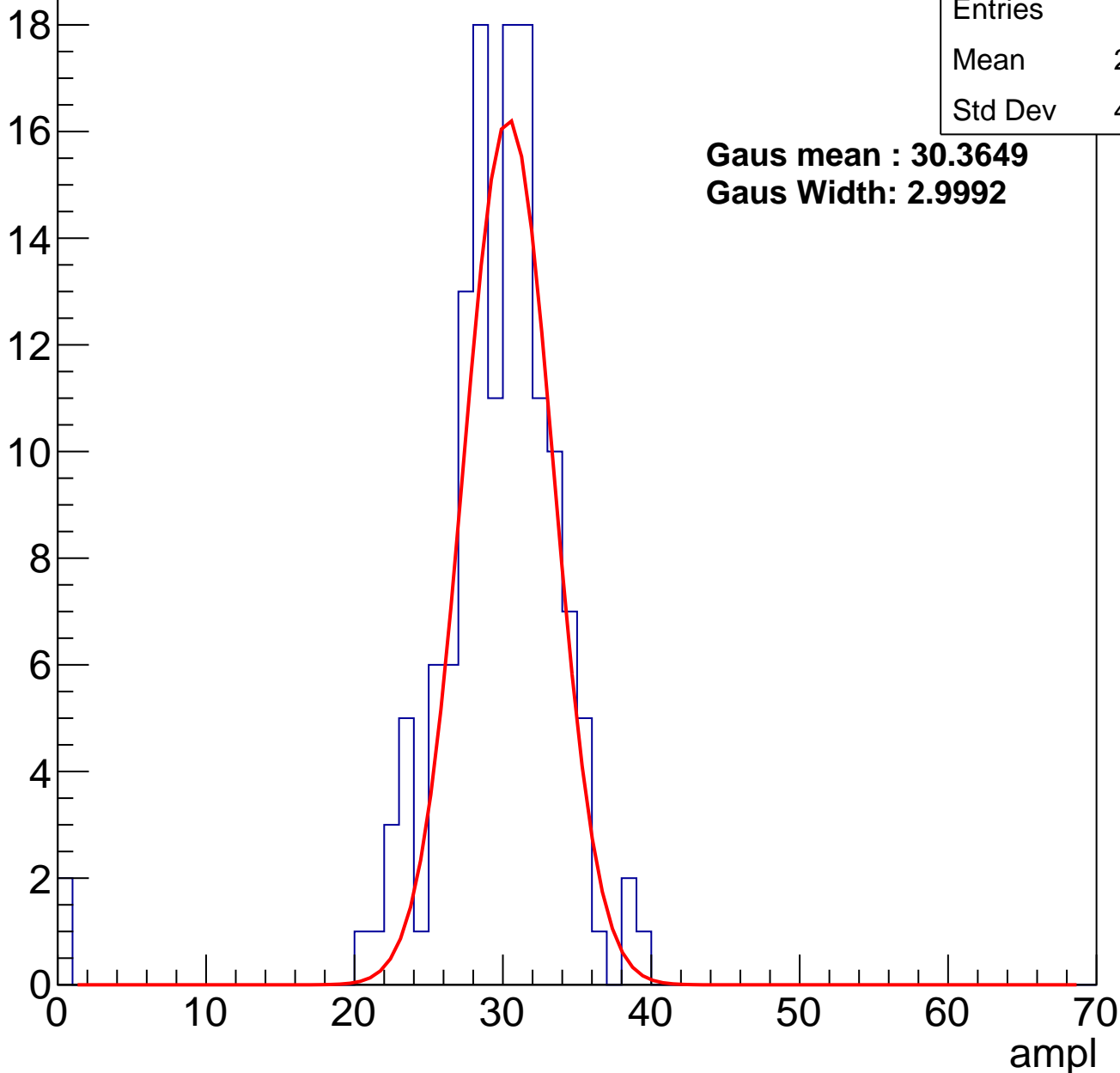
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.07
Std Dev	4.938

Gaus mean : 30.3649

Gaus Width: 2.9992

Entry



B1L001S, U16-ch2, adc1

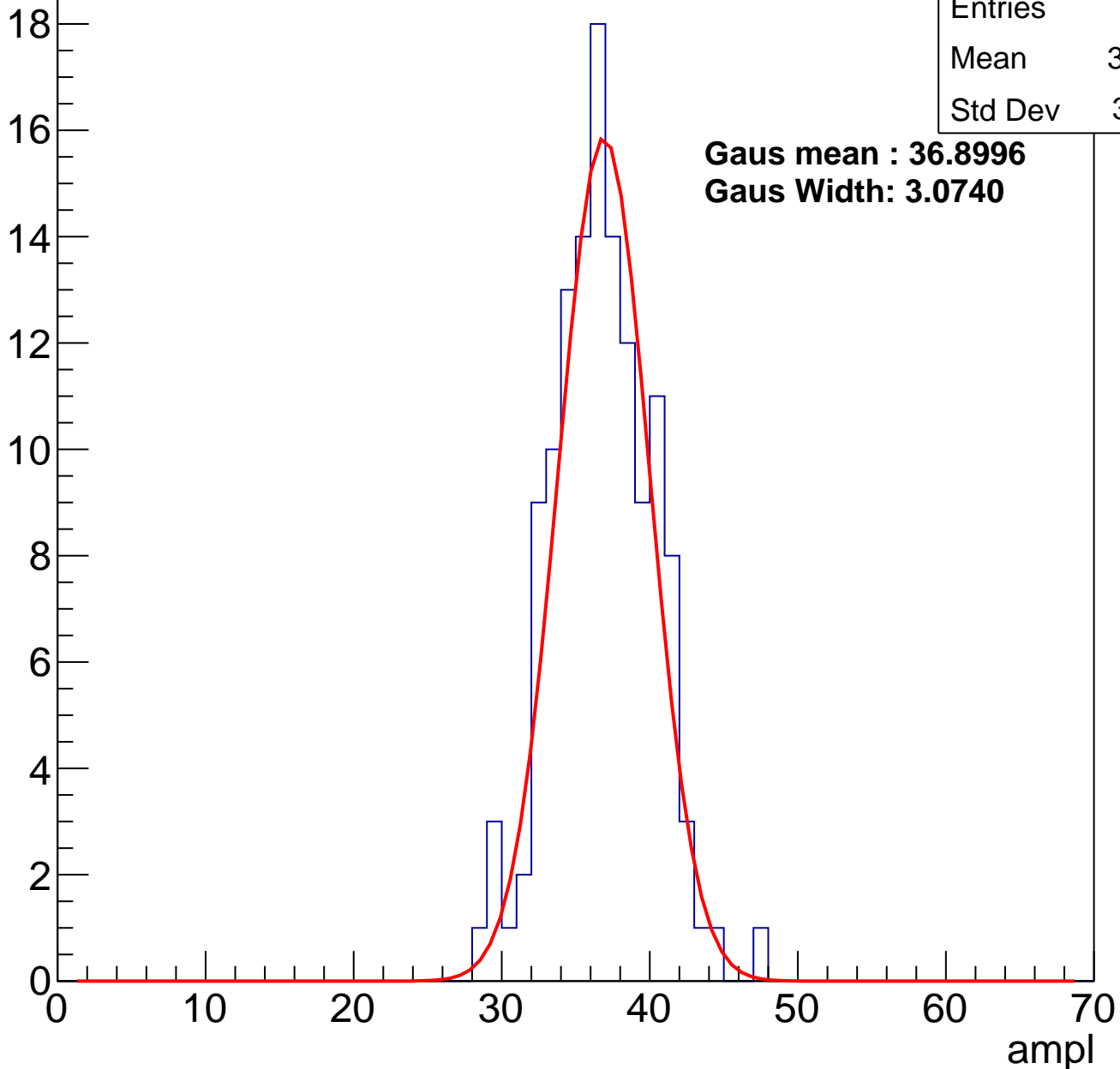
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.32
Std Dev	3.321

Gaus mean : 36.8996

Gaus Width: 3.0740

Entry



B1L001S, U16-ch2, adc2

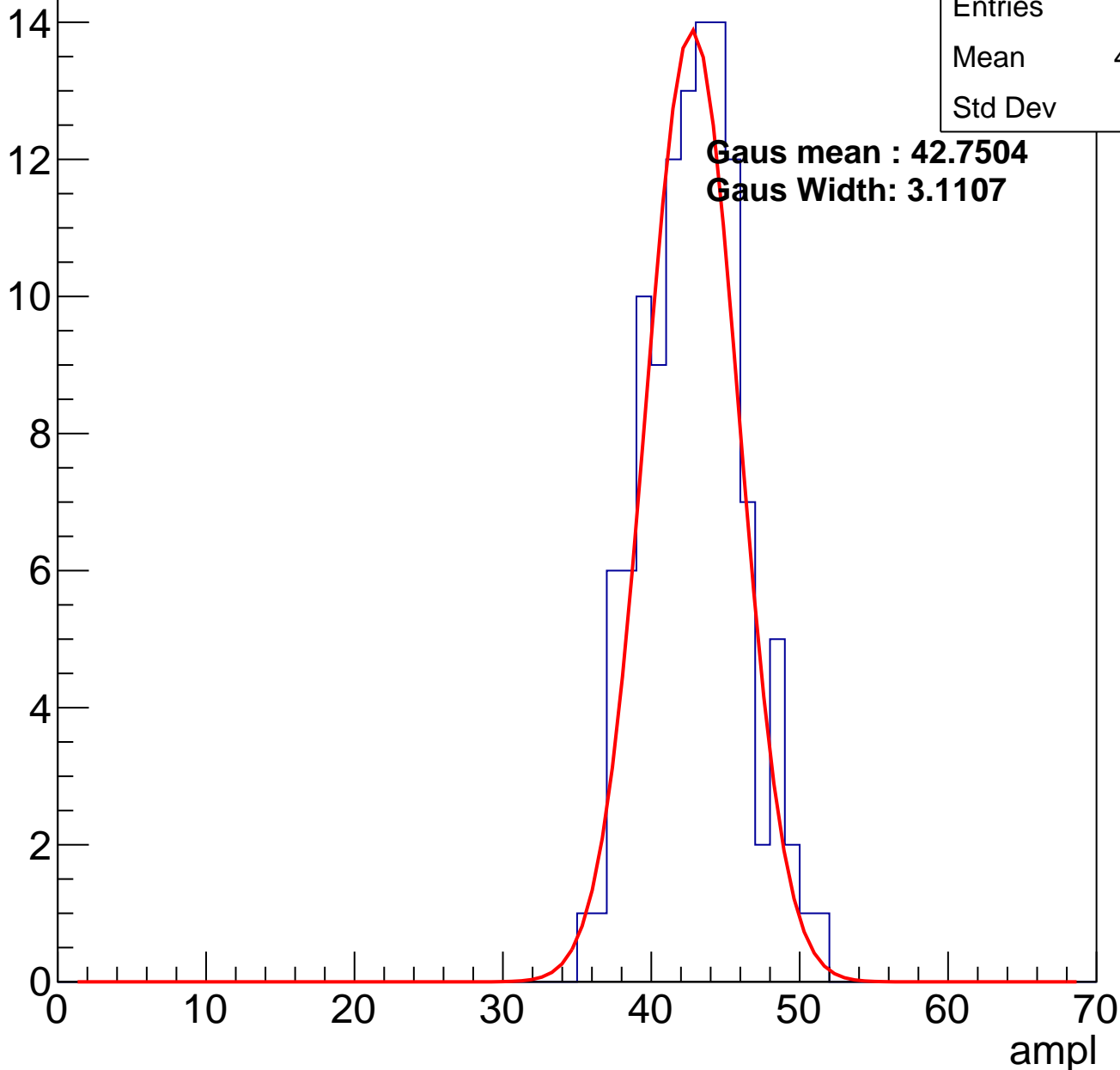
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	42.43
Std Dev	3.23

Gaus mean : 42.7504

Gaus Width: 3.1107

Entry



B1L001S, U16-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

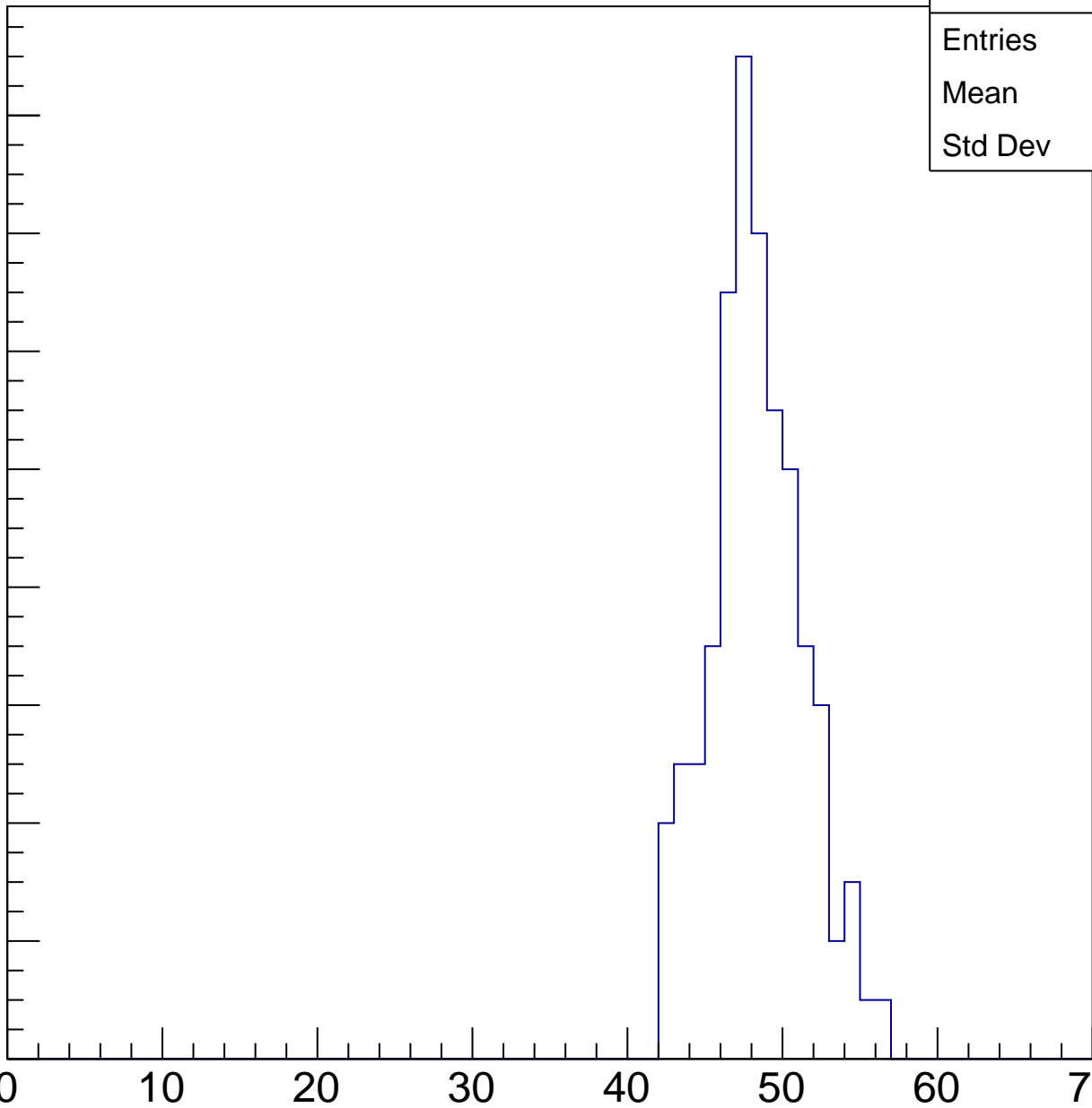
Entries	106
Mean	47.87
Std Dev	3.01

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	54.08
Std Dev	3.172

Entry

16

14

12

10

8

6

4

2

0

0

10

20

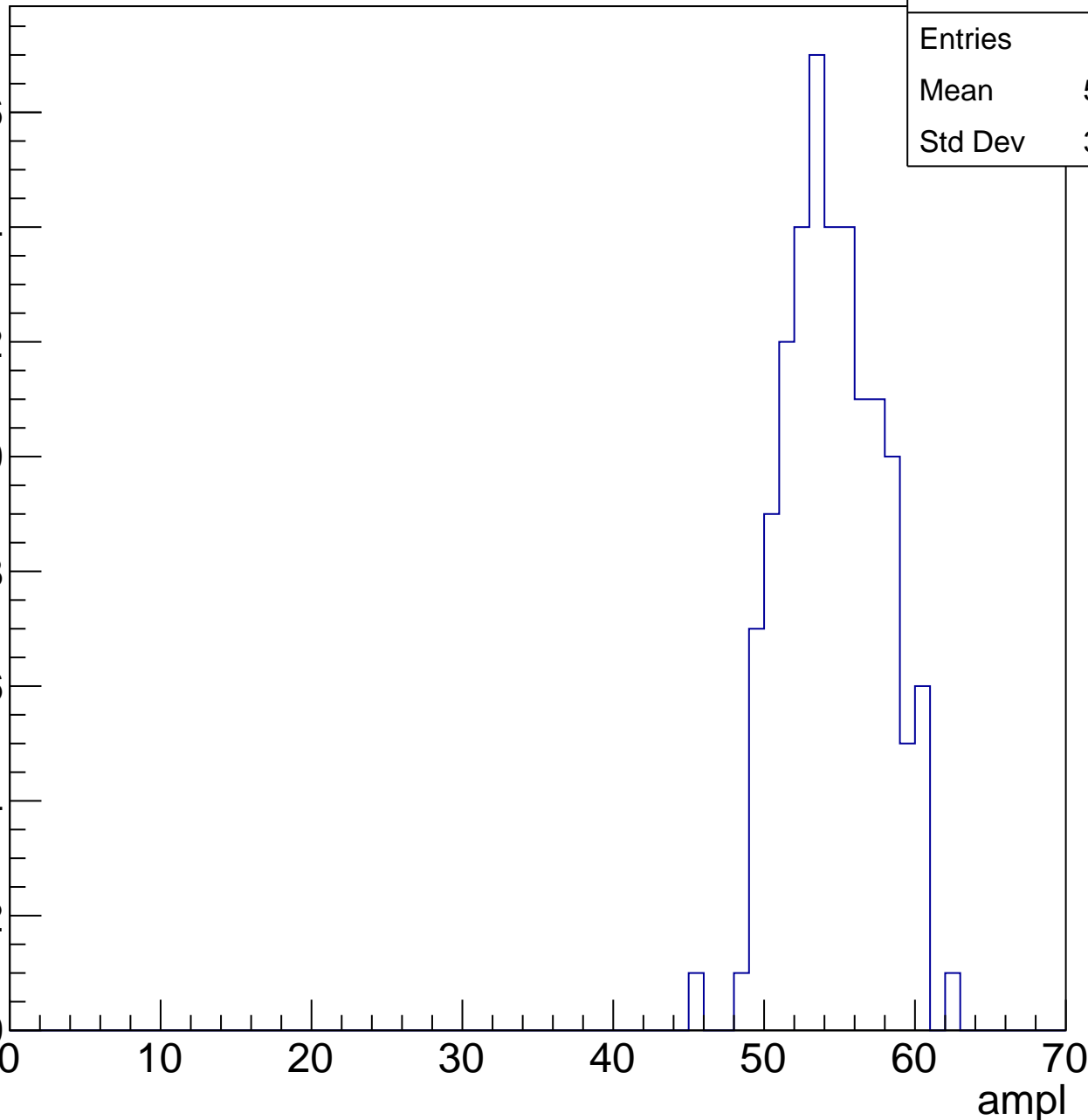
30

40

50

60

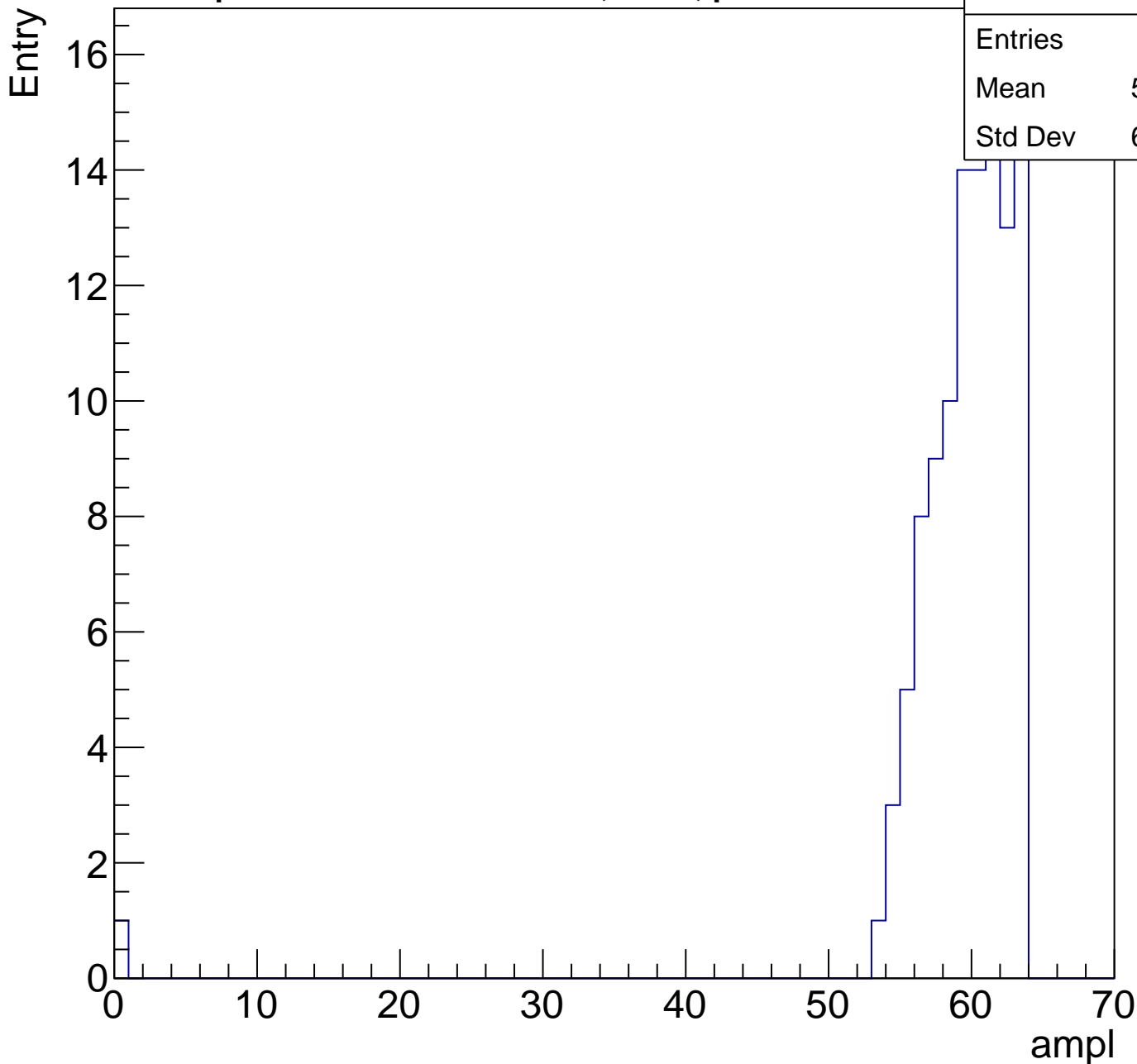
ampl



B1L001S, U16-ch2, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	58.97
Std Dev	6.209

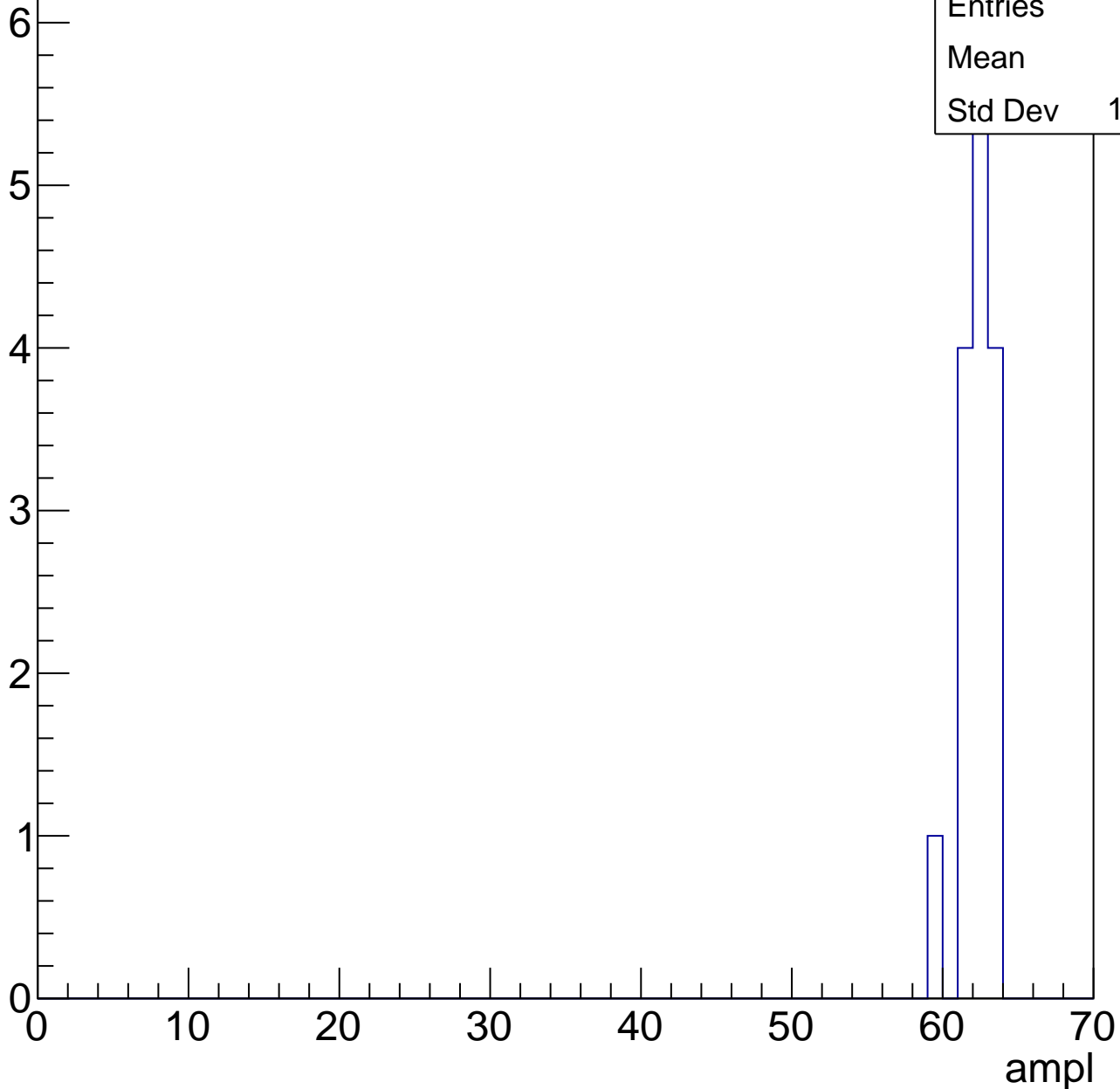


B1L001S, U16-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	61.8
Std Dev	1.046



B1L001S, U16-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



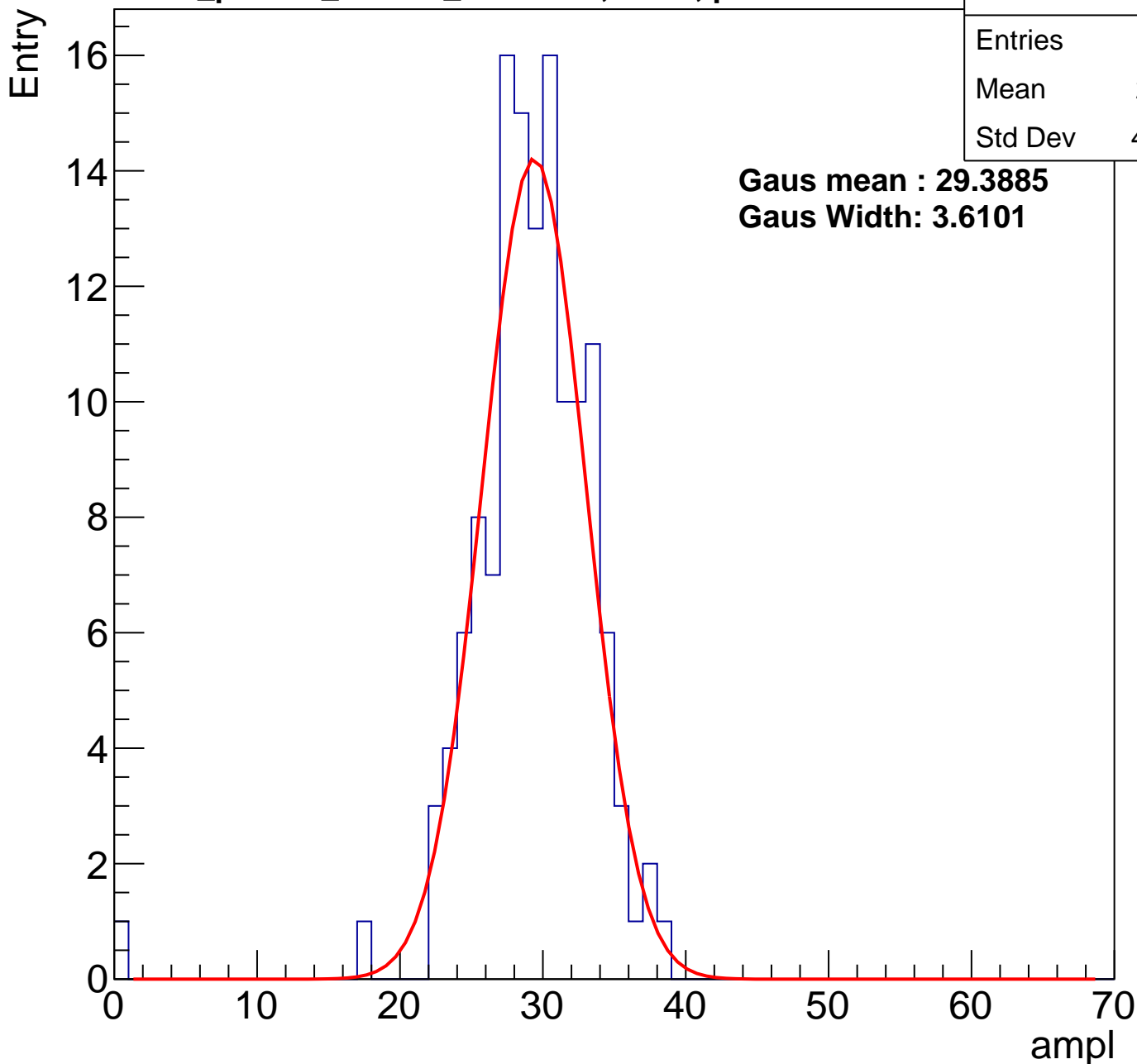
B1L001S, U16-ch3, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.81
Std Dev	4.344

Gaus mean : 29.3885

Gaus Width: 3.6101



B1L001S, U16-ch3, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.94
Std Dev	3.534

Gaus mean : 36.5776

Gaus Width: 3.4812

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

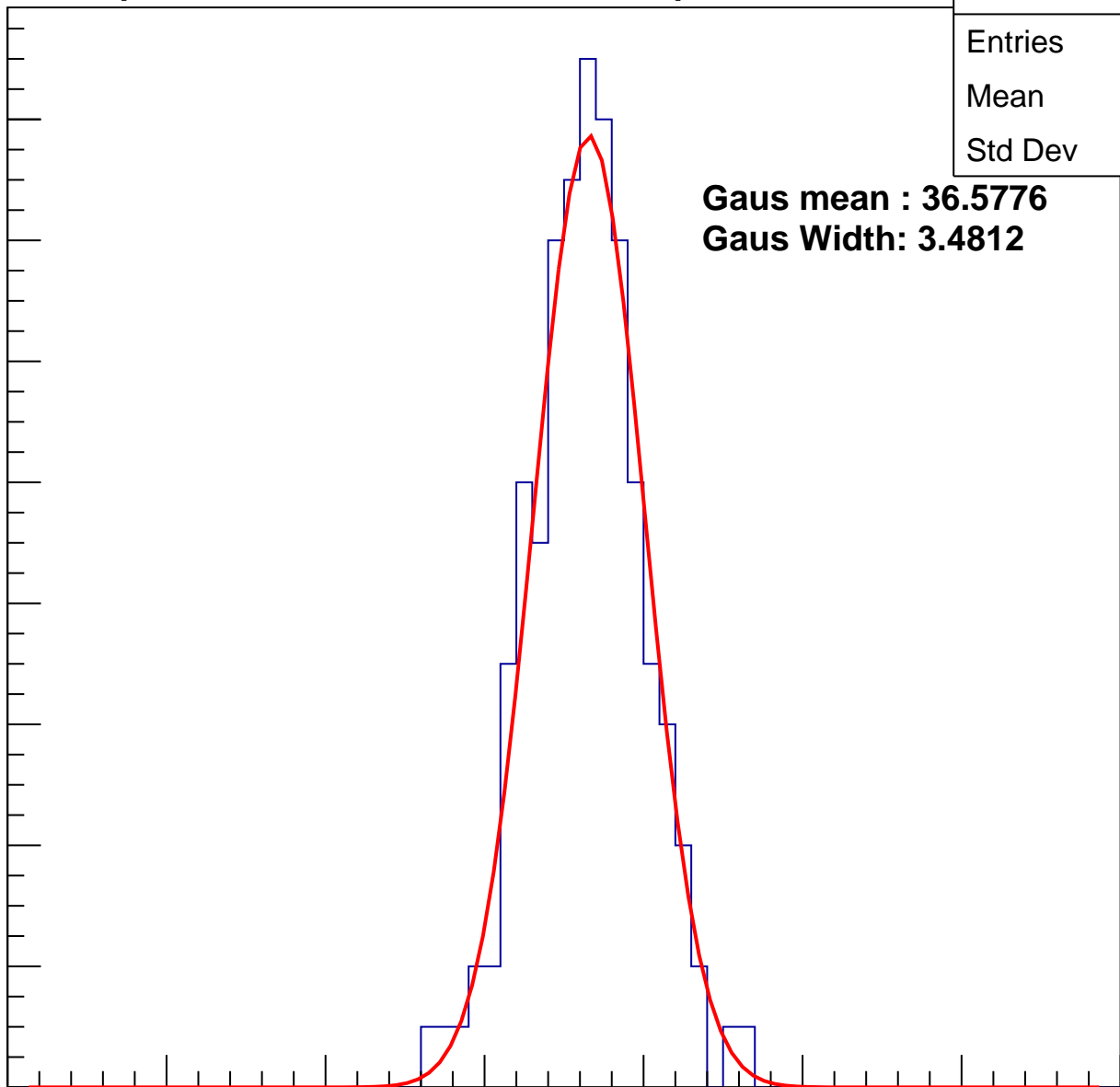
40

50

60

70

ampl



B1L001S, U16-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.65
Std Dev	3.262

Gaus mean : 43.0984

Gaus Width: 3.2263

Entry

12

10

8

6

4

2

0

0

10

20

30

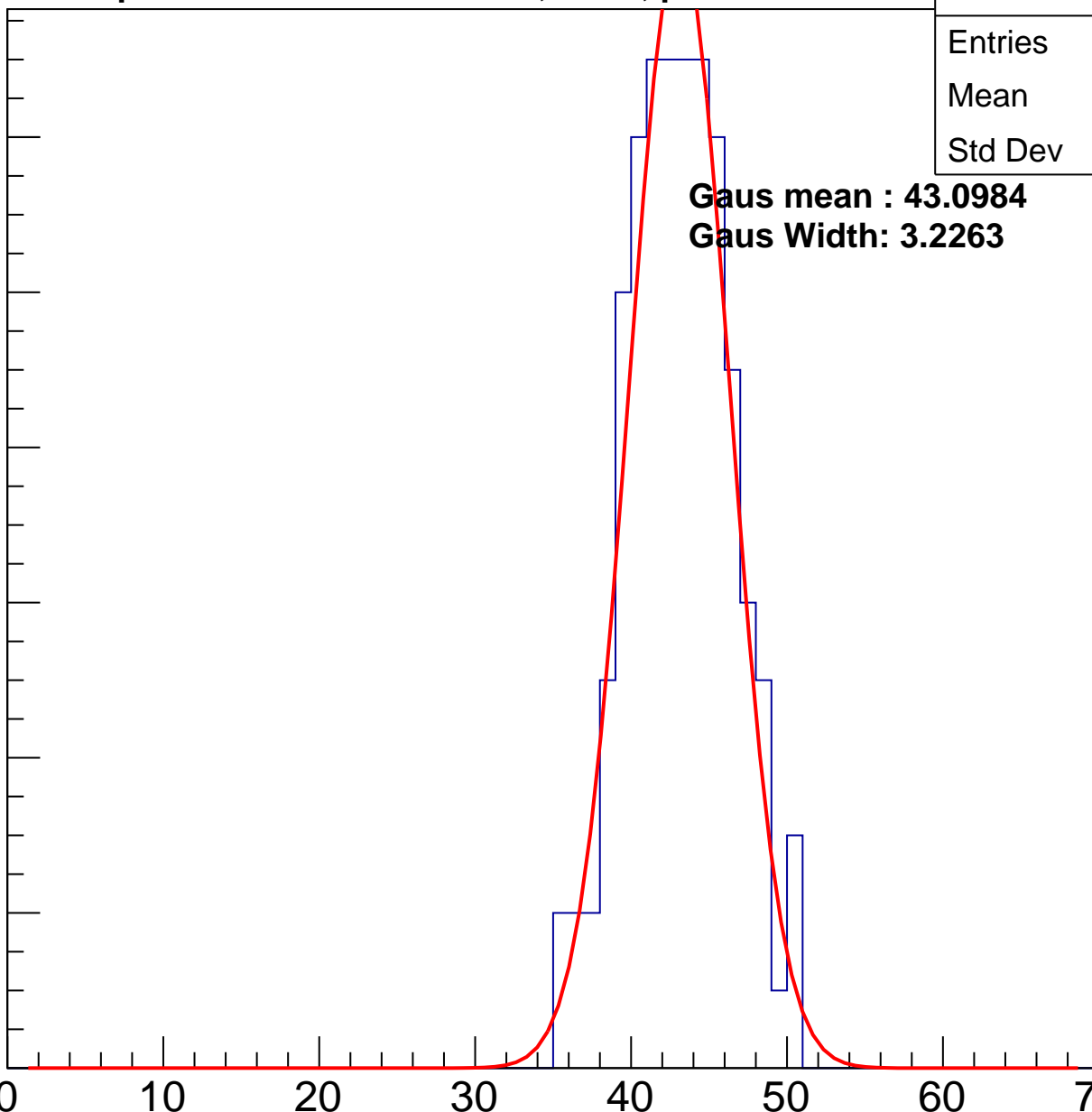
40

50

60

70

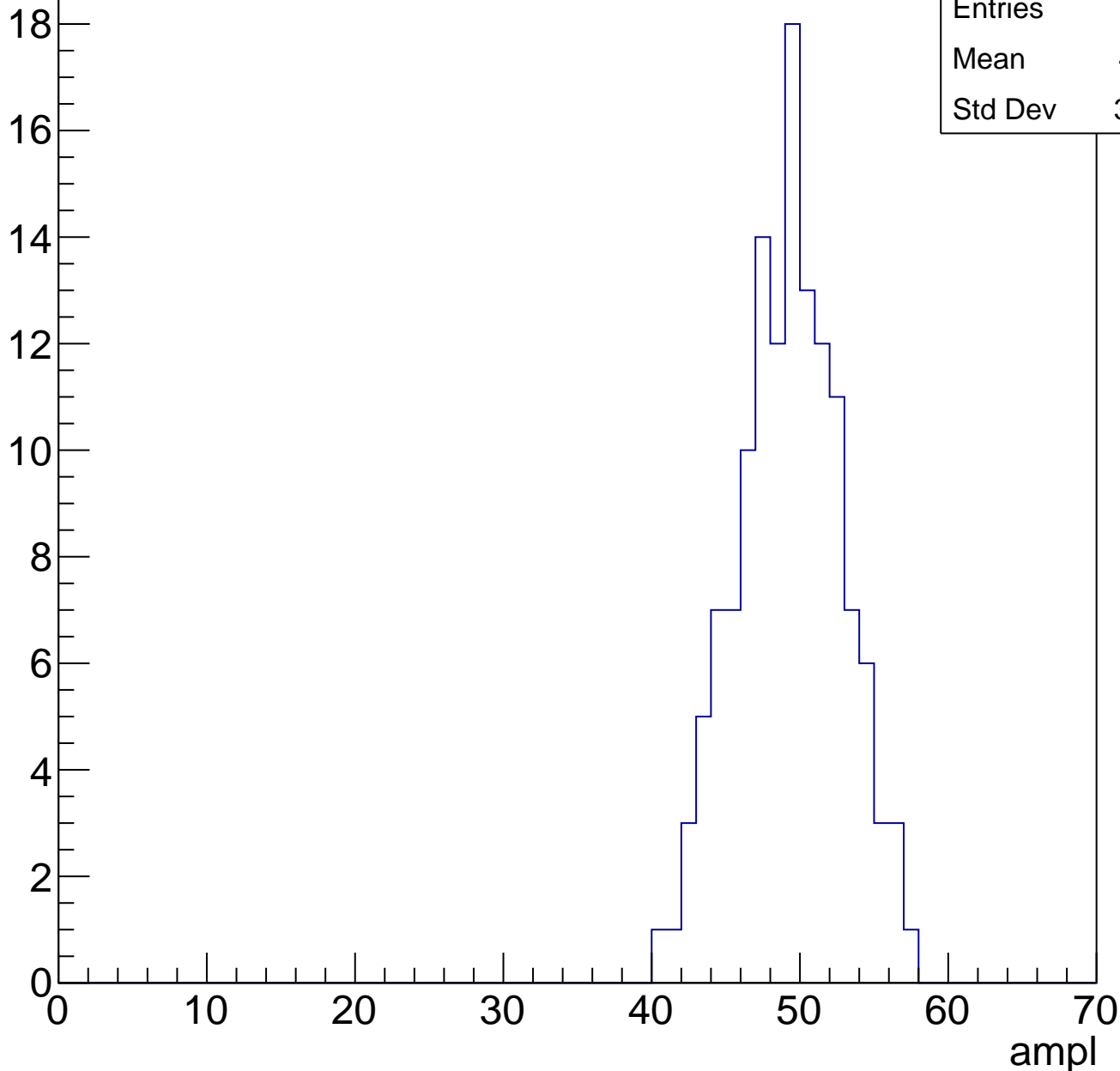
ampl



B1L001S, U16-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch3, adc4

calib_packv5_042523_0143.root, FC#2, port C2

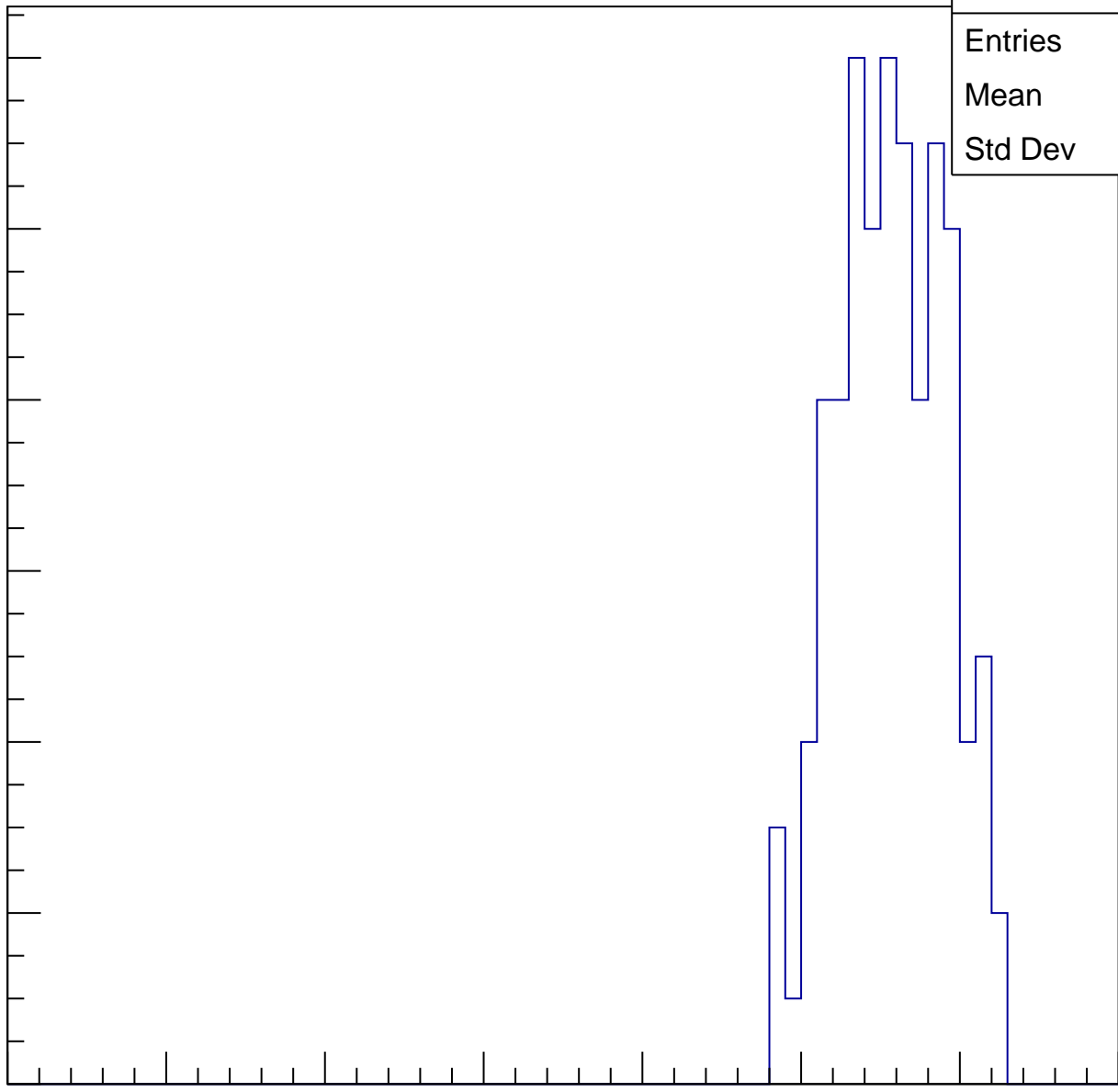
Entries	109
Mean	55.25
Std Dev	3.338

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

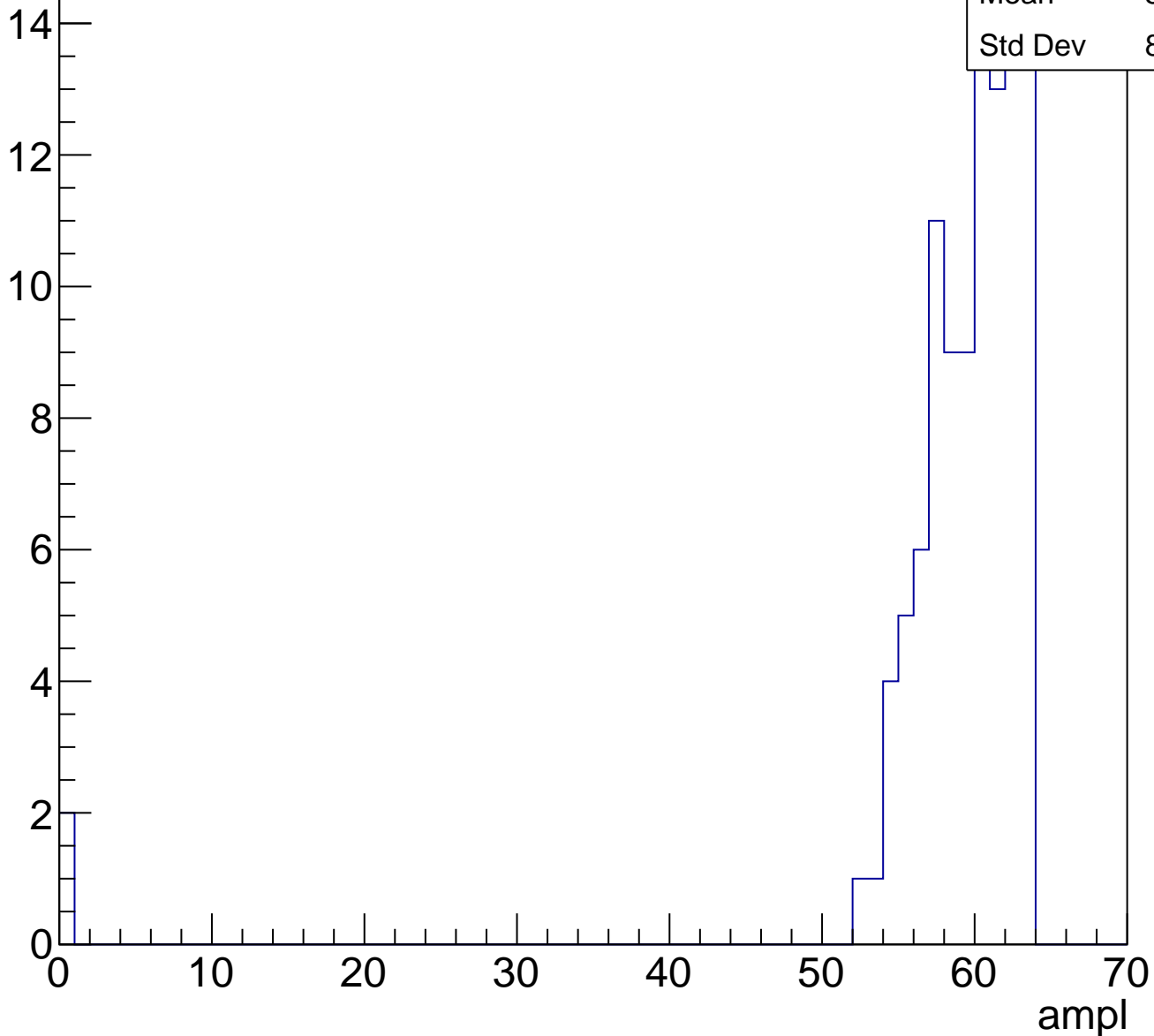
ampl



B1L001S, U16-ch3, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

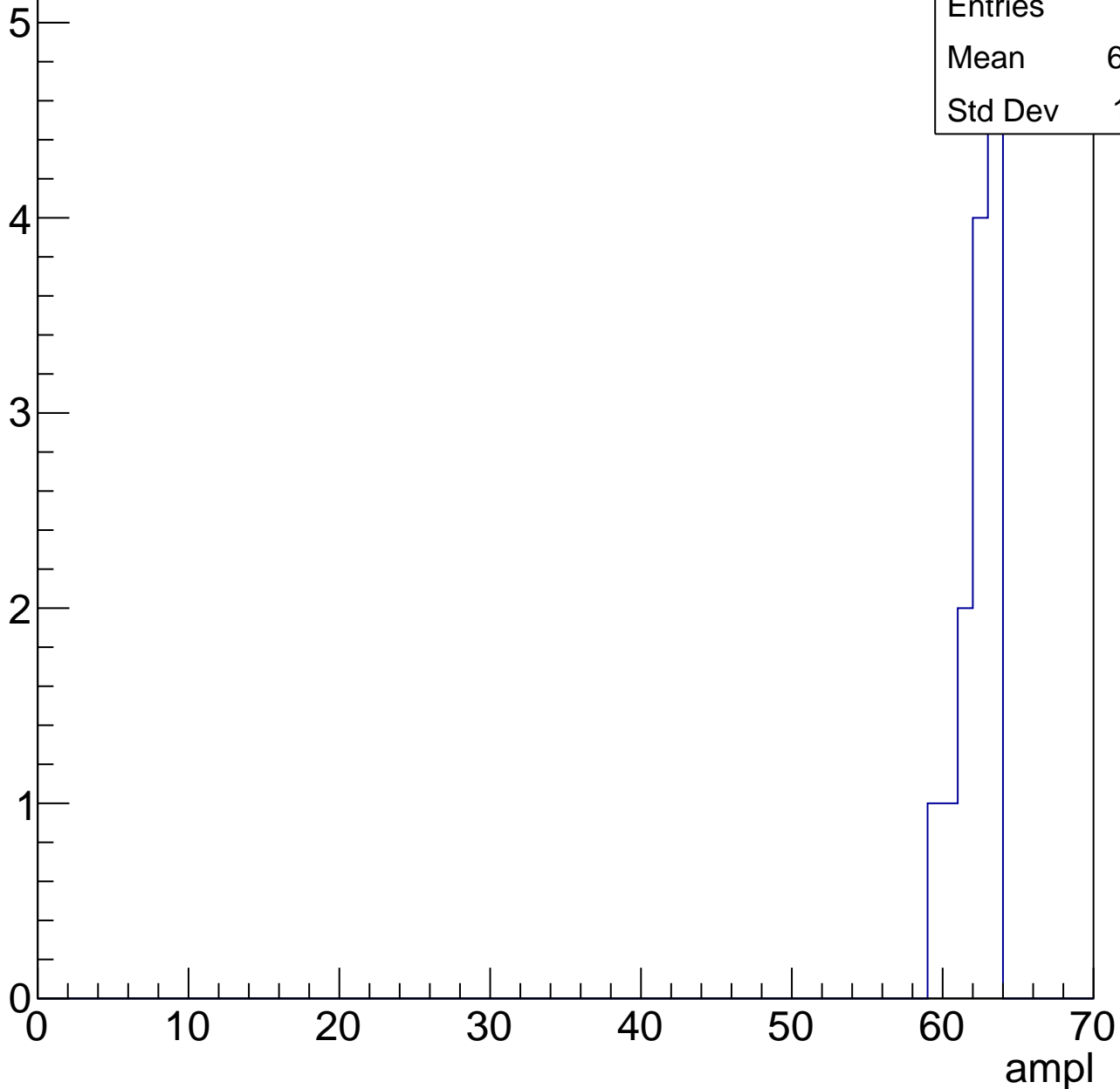


B1L001S, U16-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	61.85
Std Dev	1.231



B1L001S, U16-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

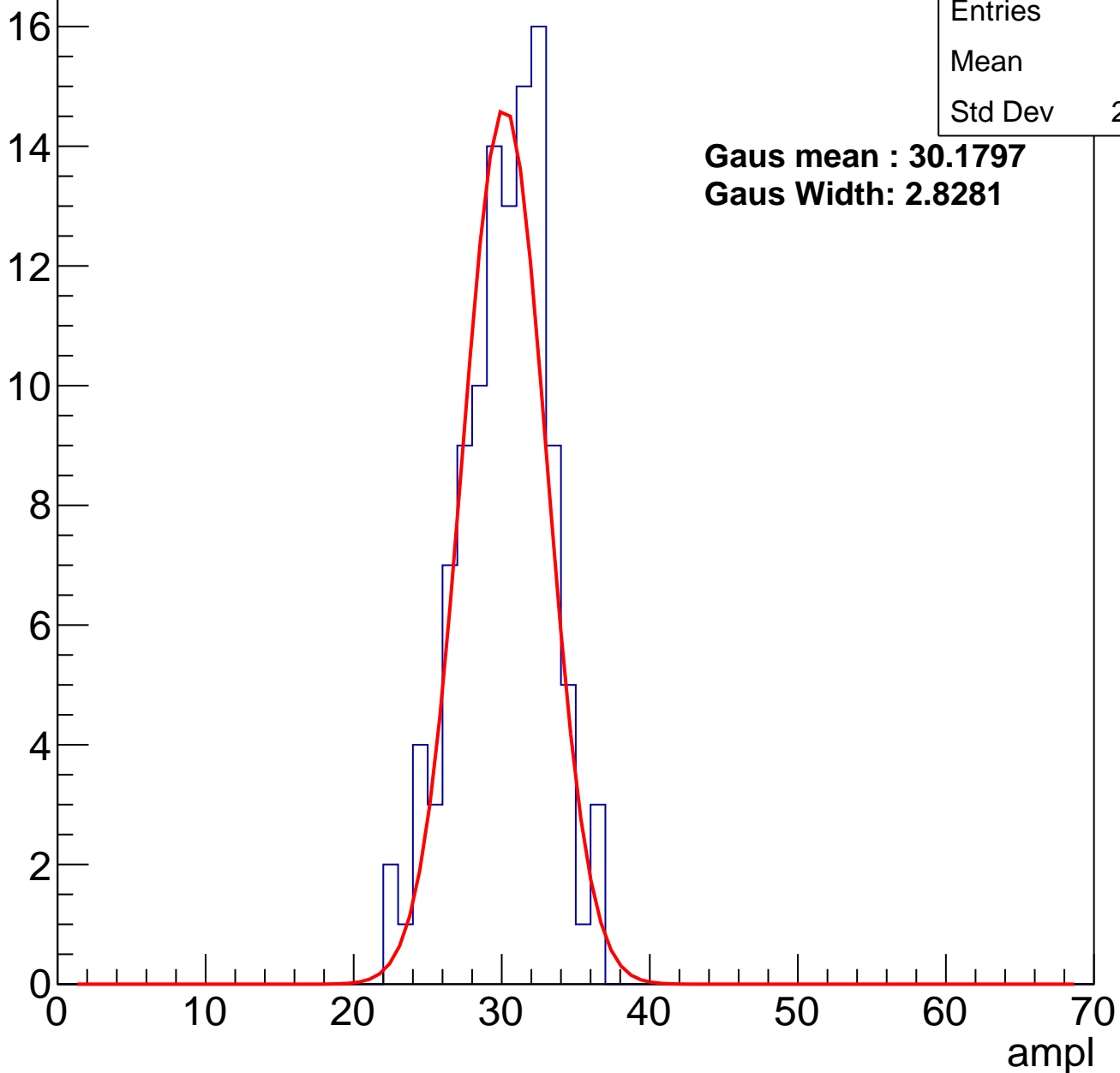


Entries	1
Mean	0
Std Dev	0

B1L001S, U16-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch4, adc1

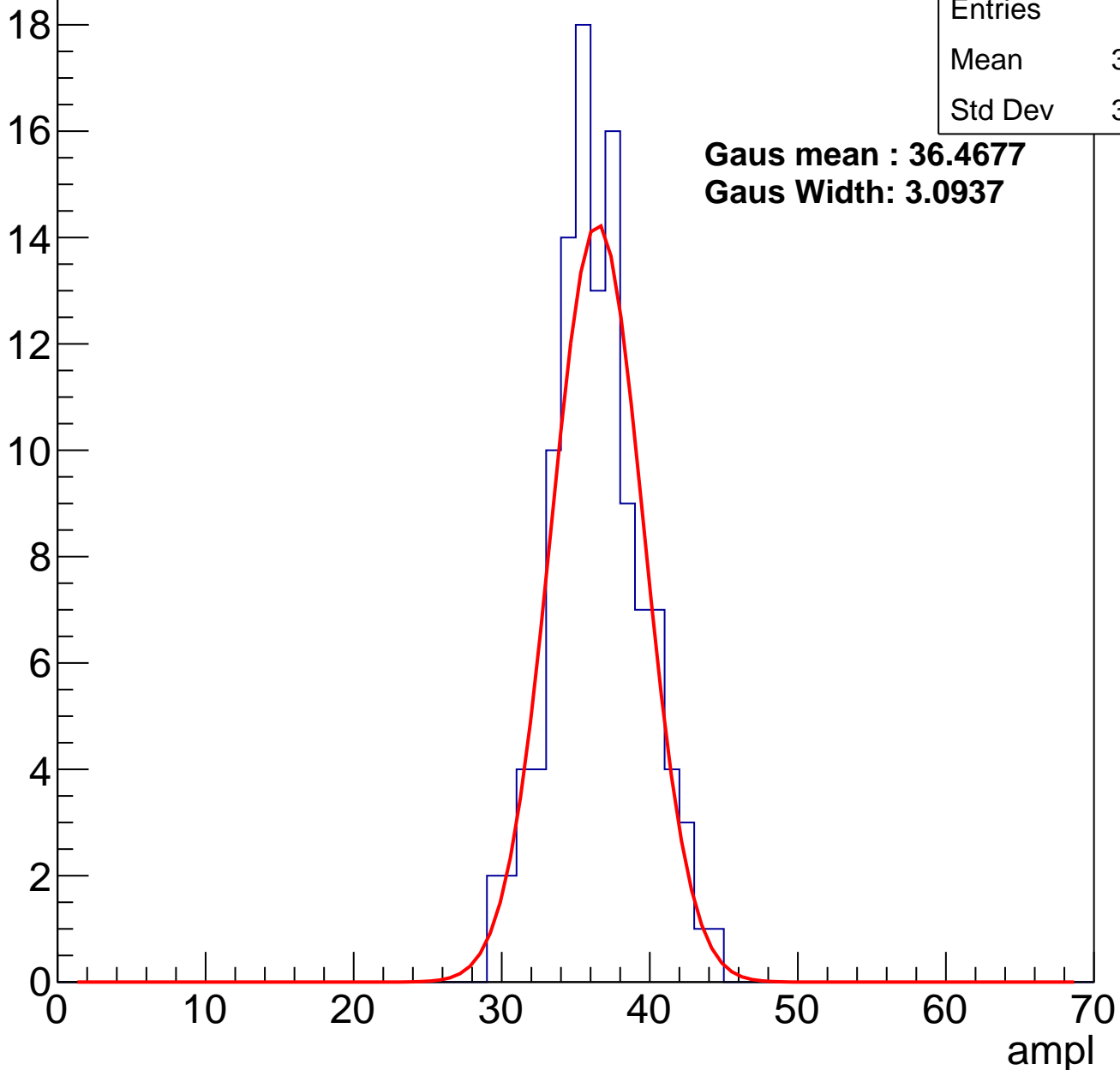
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	35.98
Std Dev	3.016

Gaus mean : 36.4677

Gaus Width: 3.0937

Entry



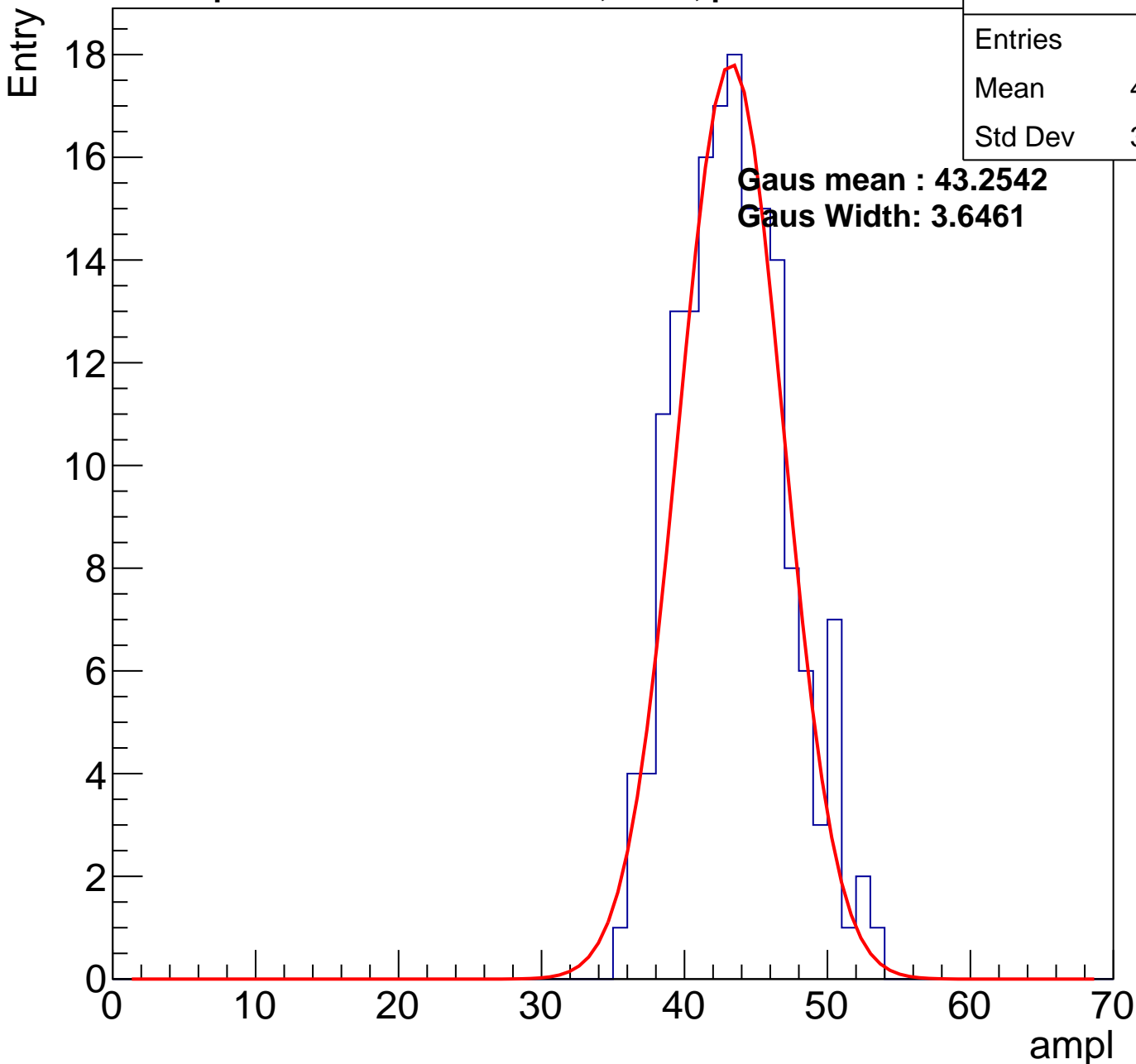
B1L001S, U16-ch4, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	42.98
Std Dev	3.722

Gaus mean : 43.2542

Gaus Width: 3.6461



B1L001S, U16-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	49.75
Std Dev	3.053

Entry

12

10

8

6

4

2

0

0

10

20

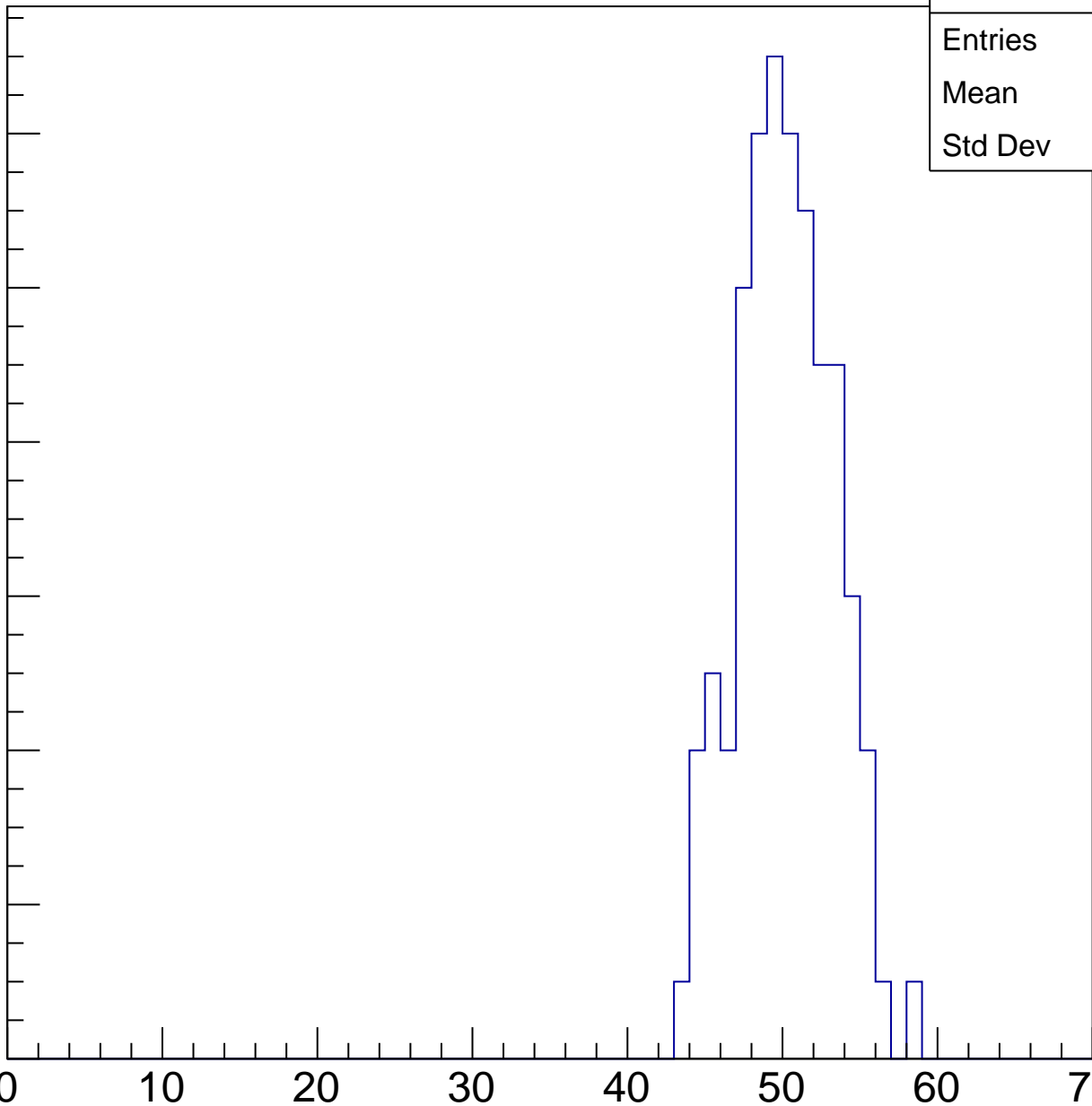
30

40

50

60

ampl



B1L001S, U16-ch4, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

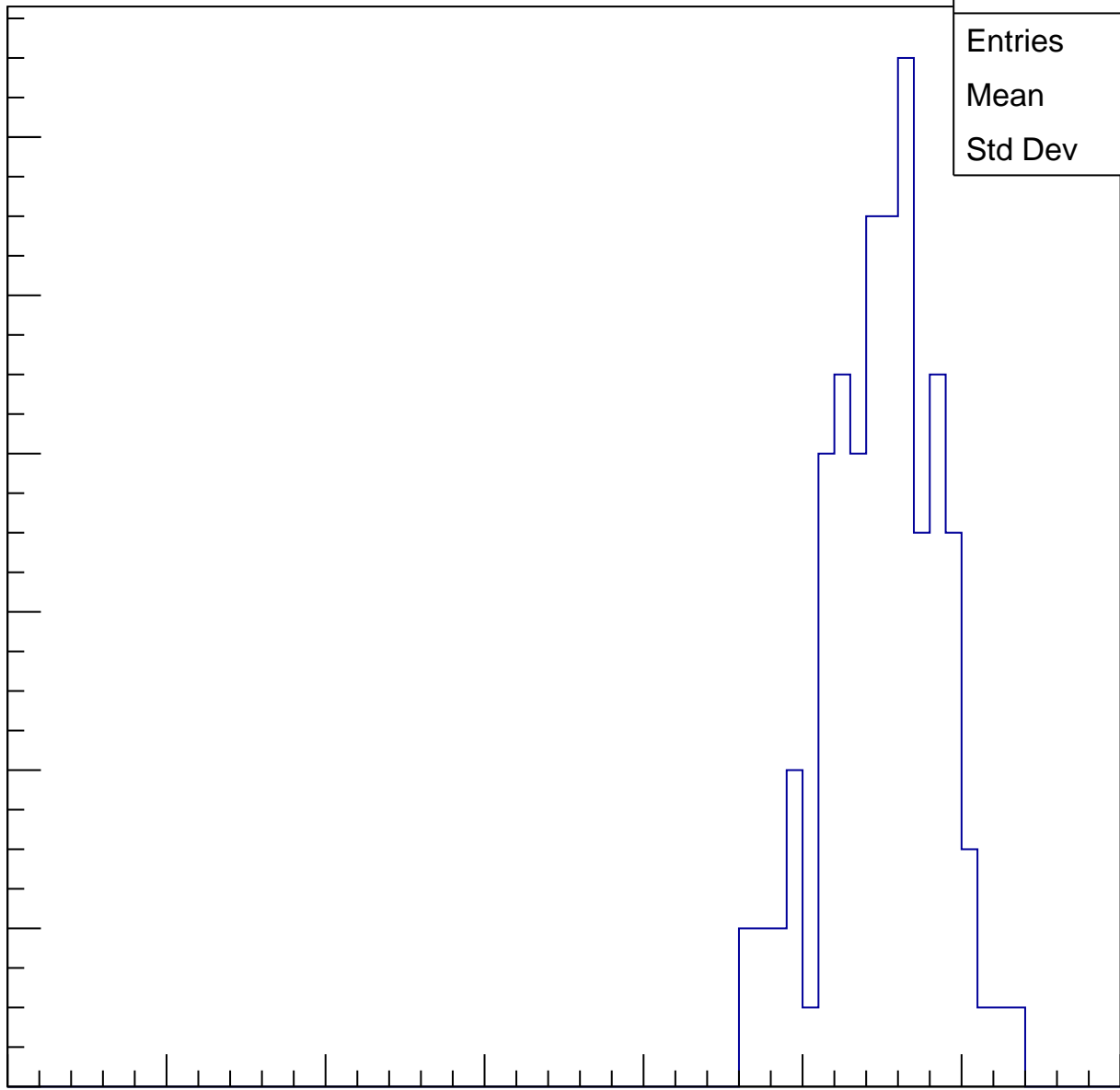
2

0

Entries	100
Mean	54.55
Std Dev	3.531

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch4, adc5

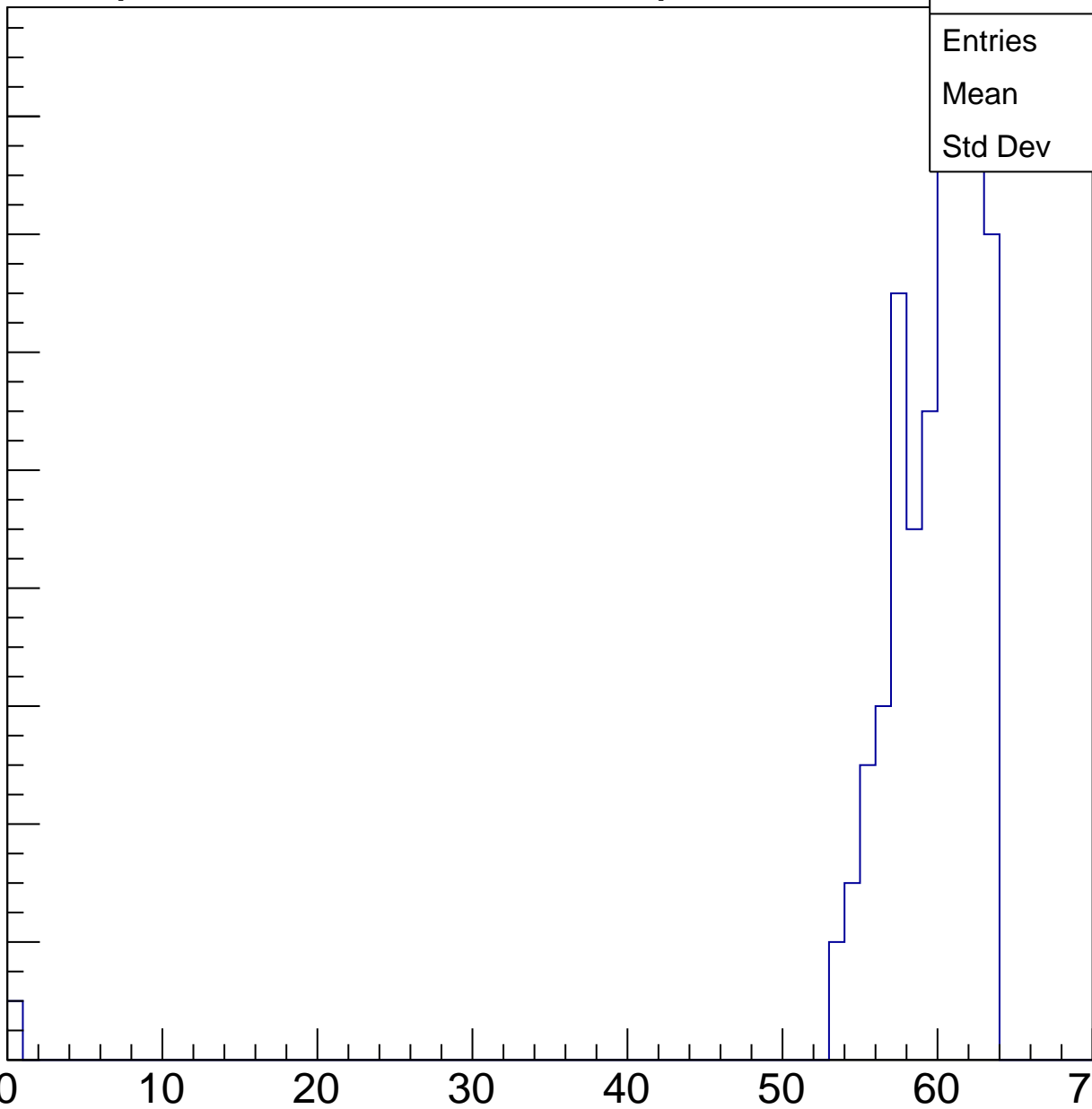
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	113
Mean	58.96
Std Dev	6.154

ampl

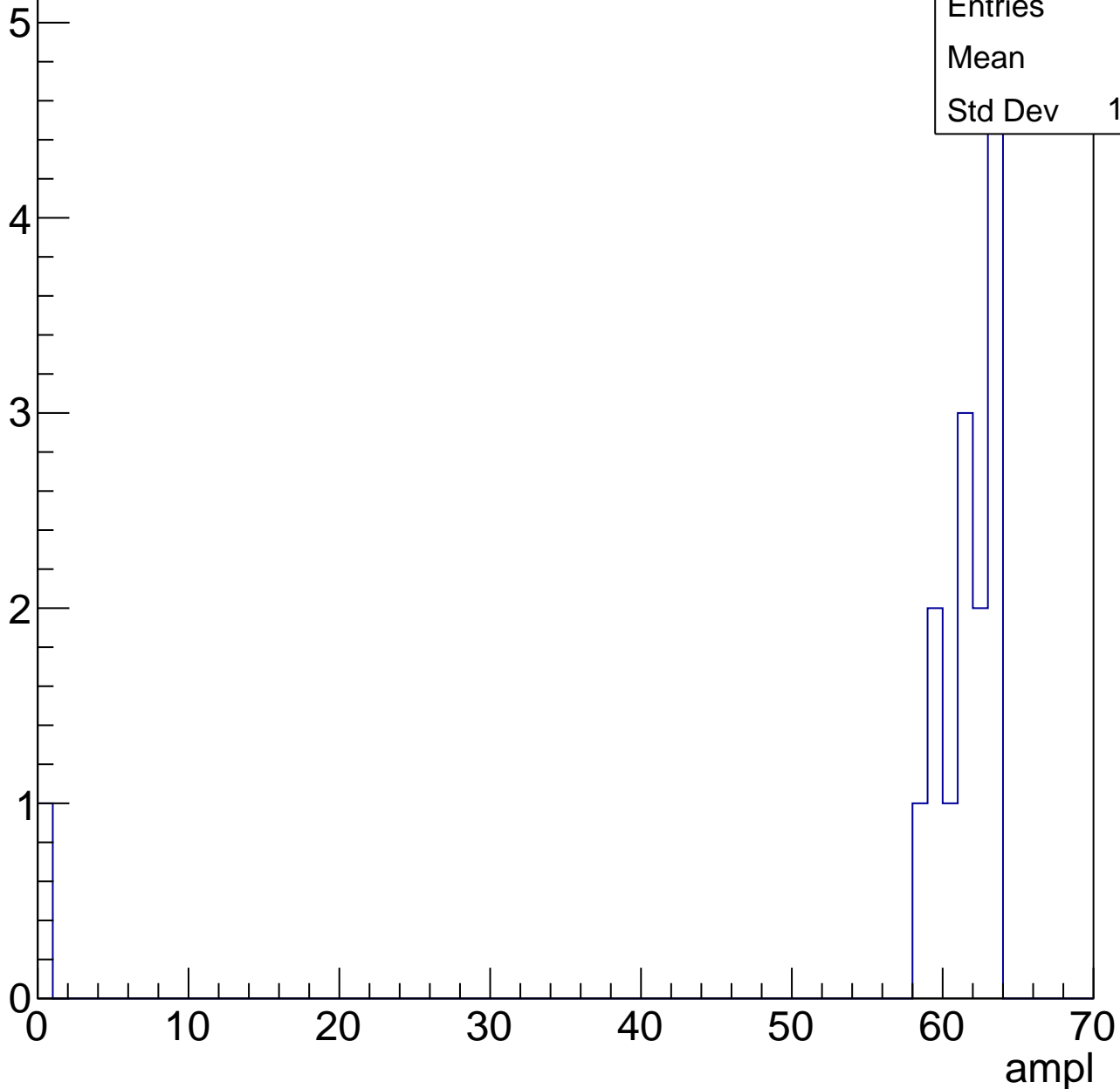


B1L001S, U16-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	57.2
Std Dev	15.37



B1L001S, U16-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch5, adc0

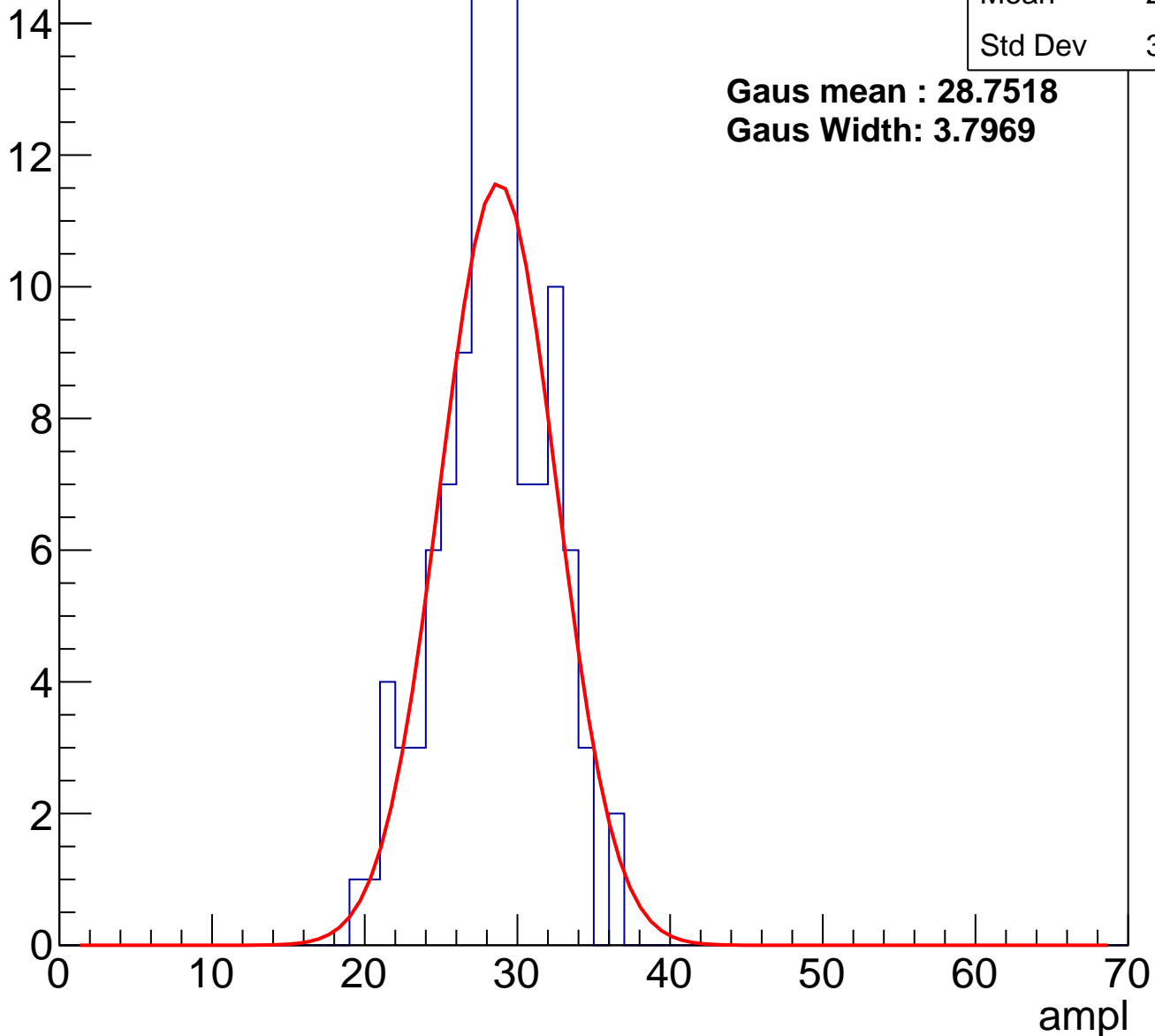
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	27.98
Std Dev	3.487

Gaus mean : 28.7518

Gaus Width: 3.7969

Entry



B1L001S, U16-ch5, adc1

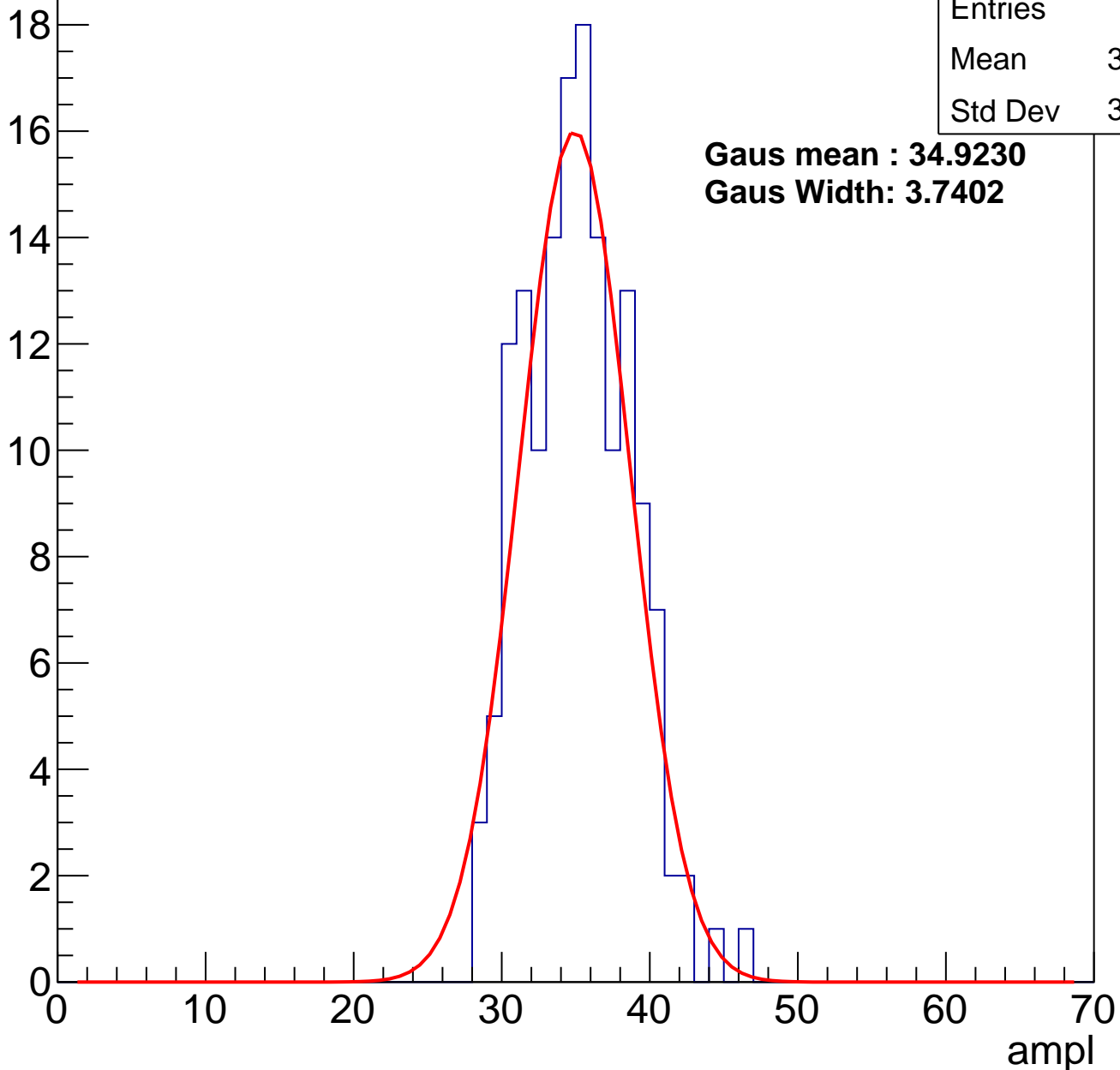
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	34.68
Std Dev	3.495

Gaus mean : 34.9230

Gaus Width: 3.7402

Entry



B1L001S, U16-ch5, adc2

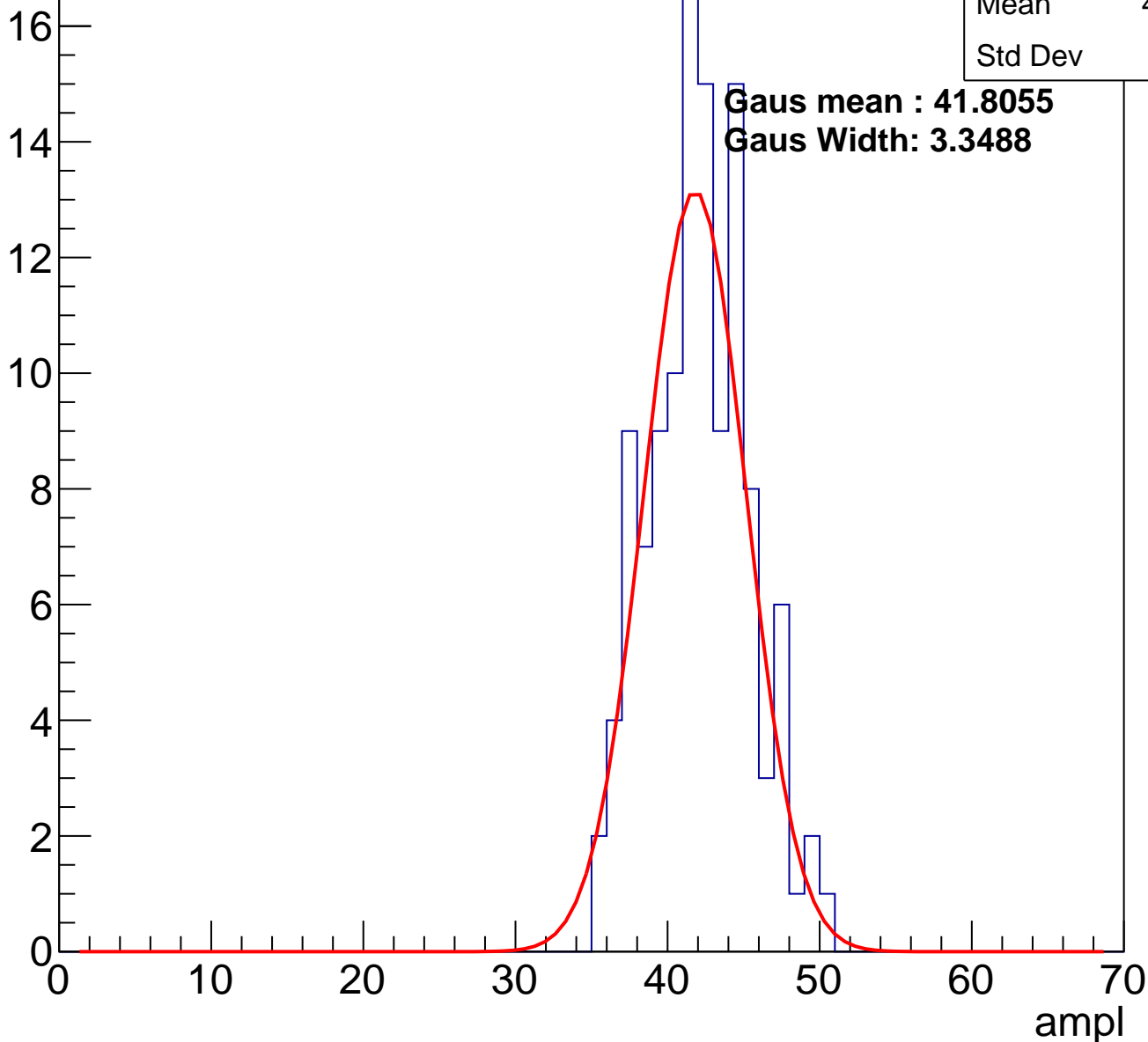
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	118
Mean	41.64
Std Dev	3.24

Gaus mean : 41.8055

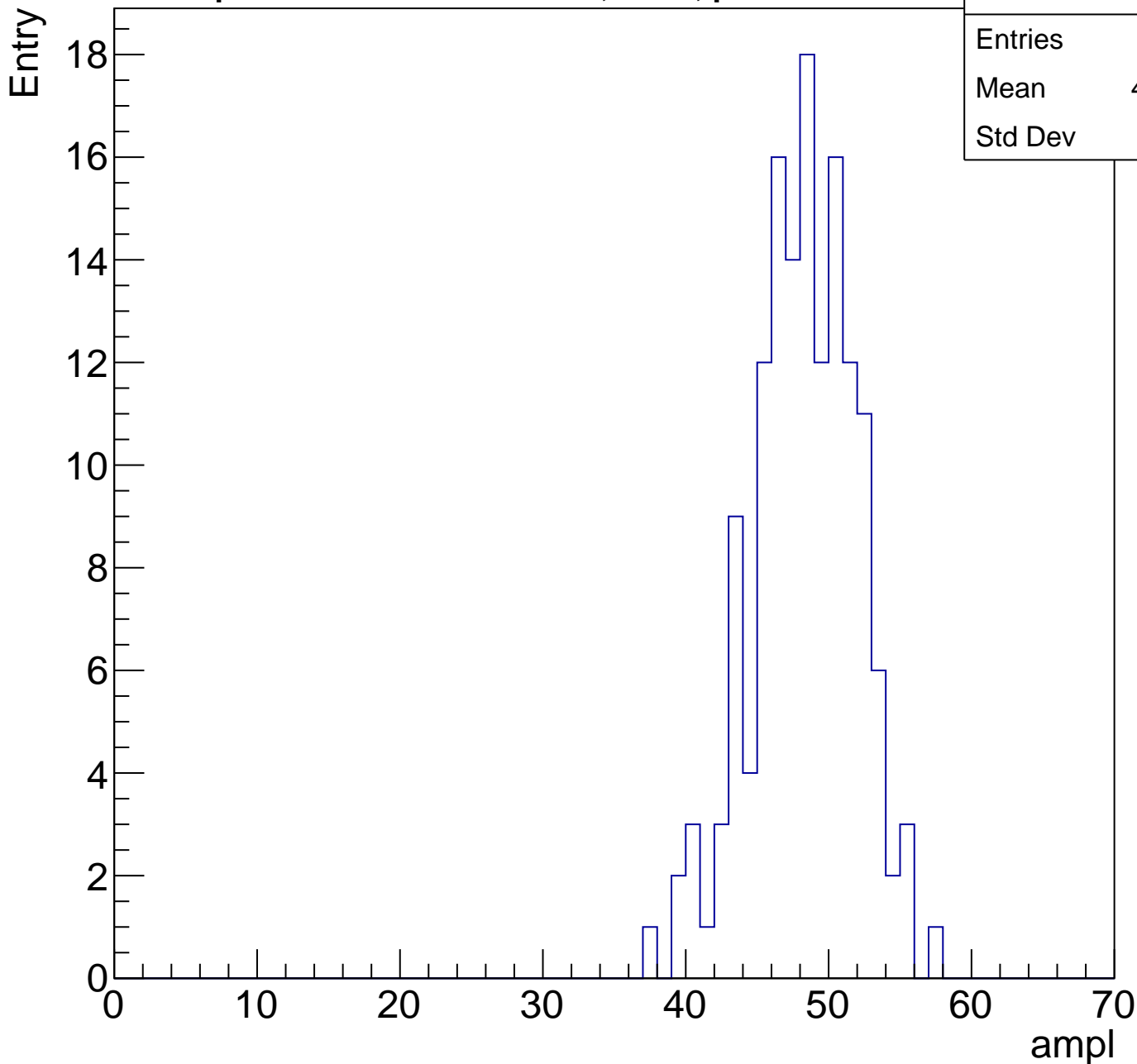
Gaus Width: 3.3488



B1L001S, U16-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	47.83
Std Dev	3.62



B1L001S, U16-ch5, adc4

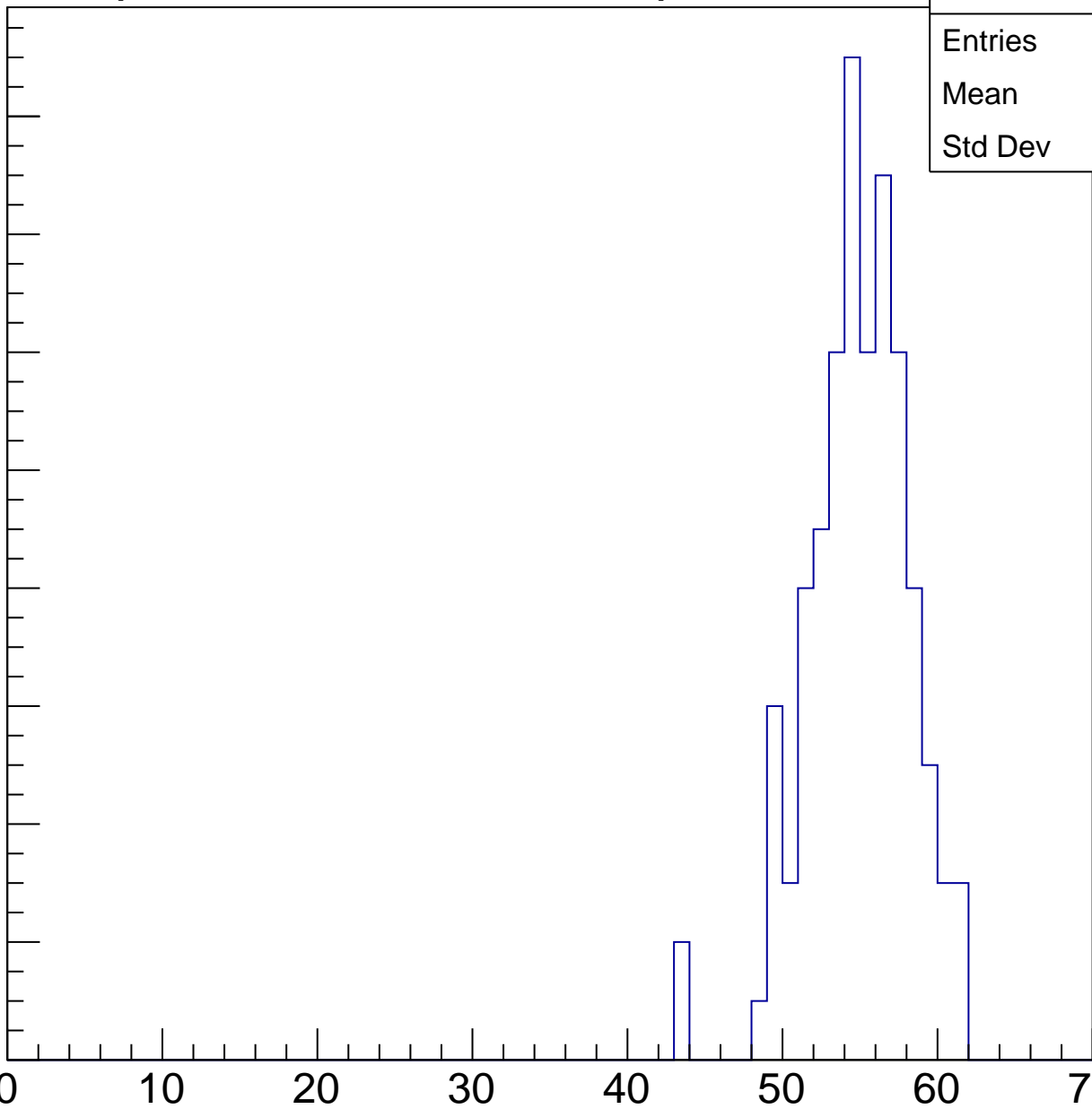
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	54.43
Std Dev	3.299

ampl



B1L001S, U16-ch5, adc5

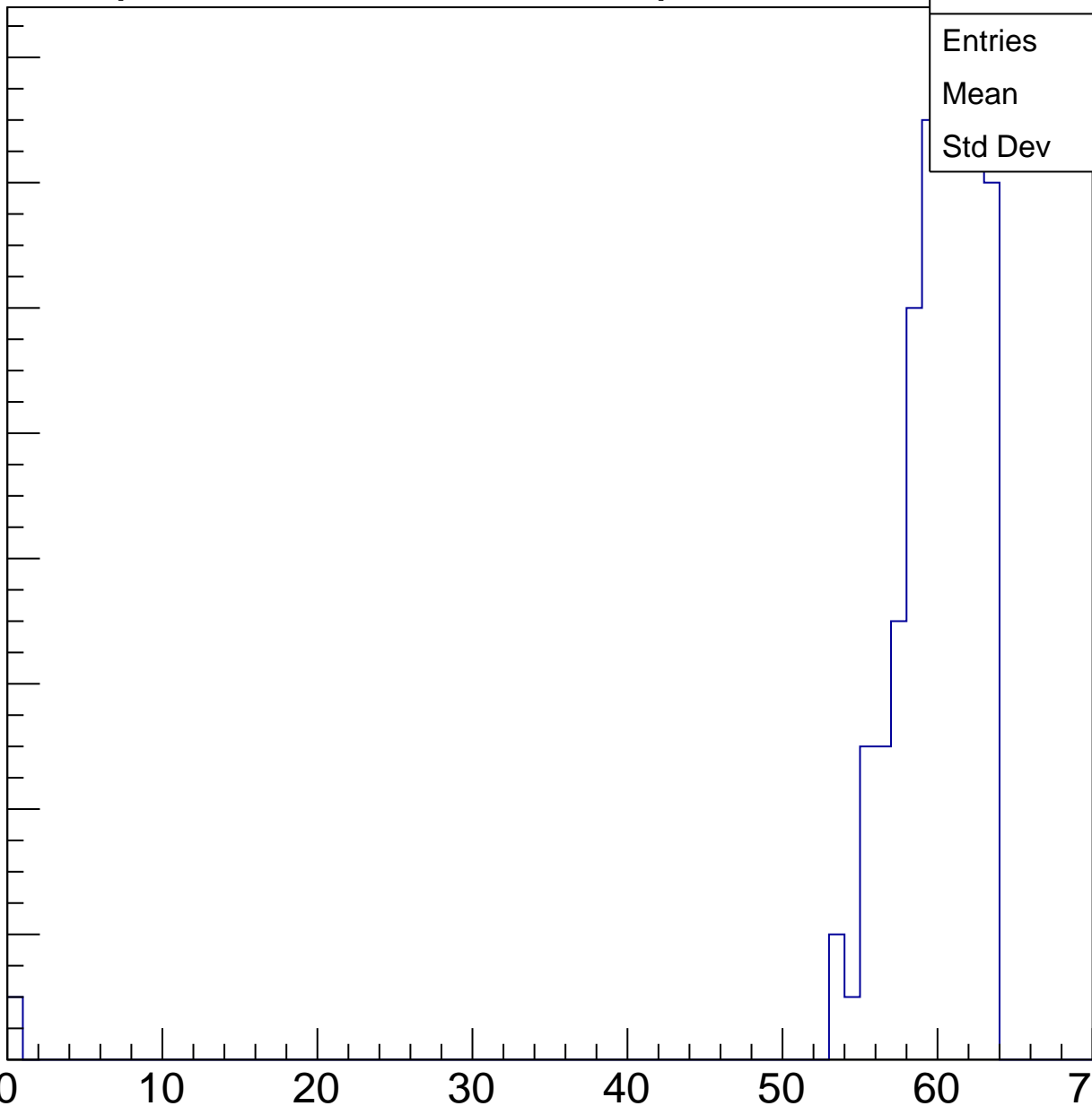
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	59.14
Std Dev	6.171

ampl

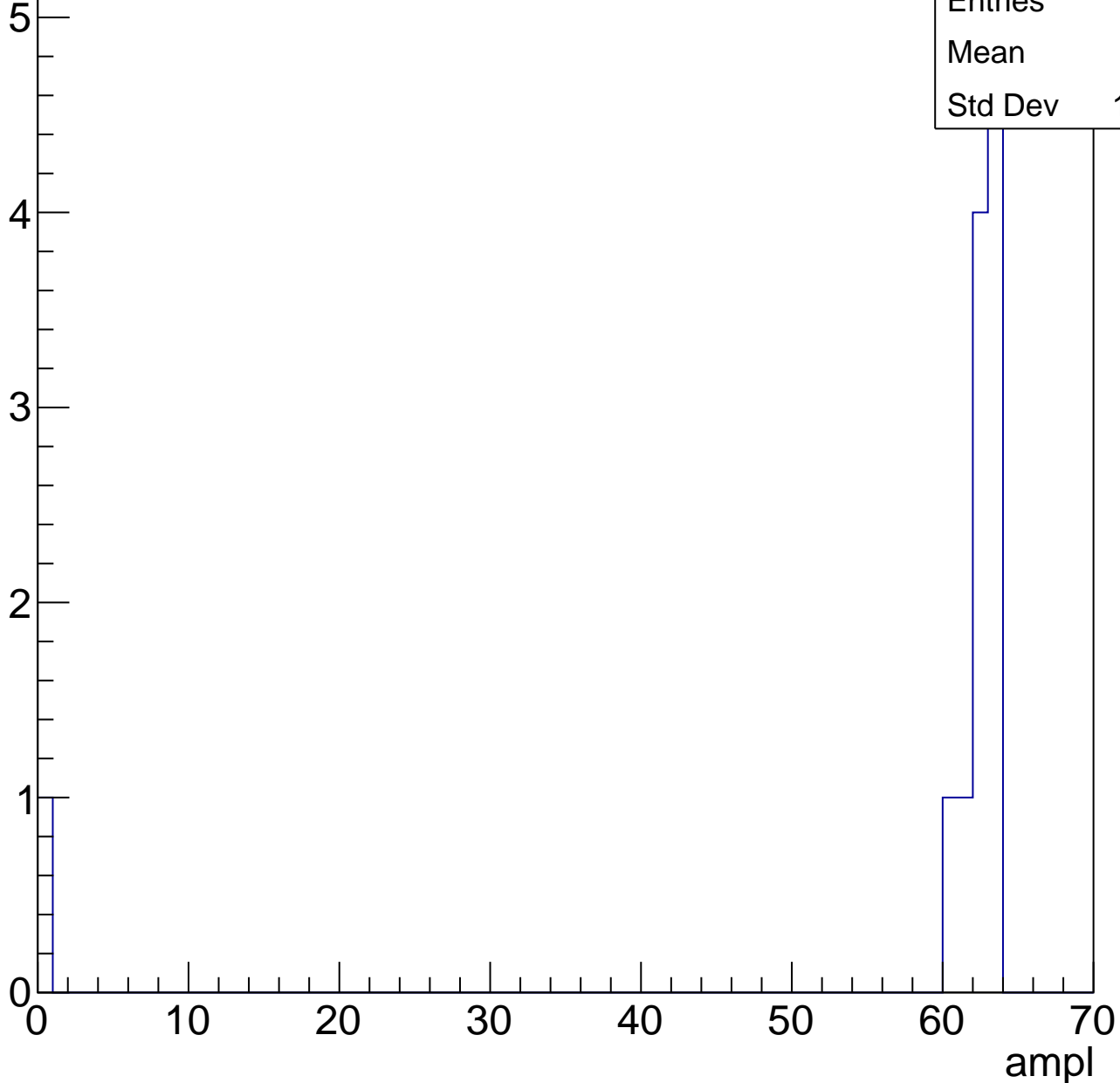


B1L001S, U16-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

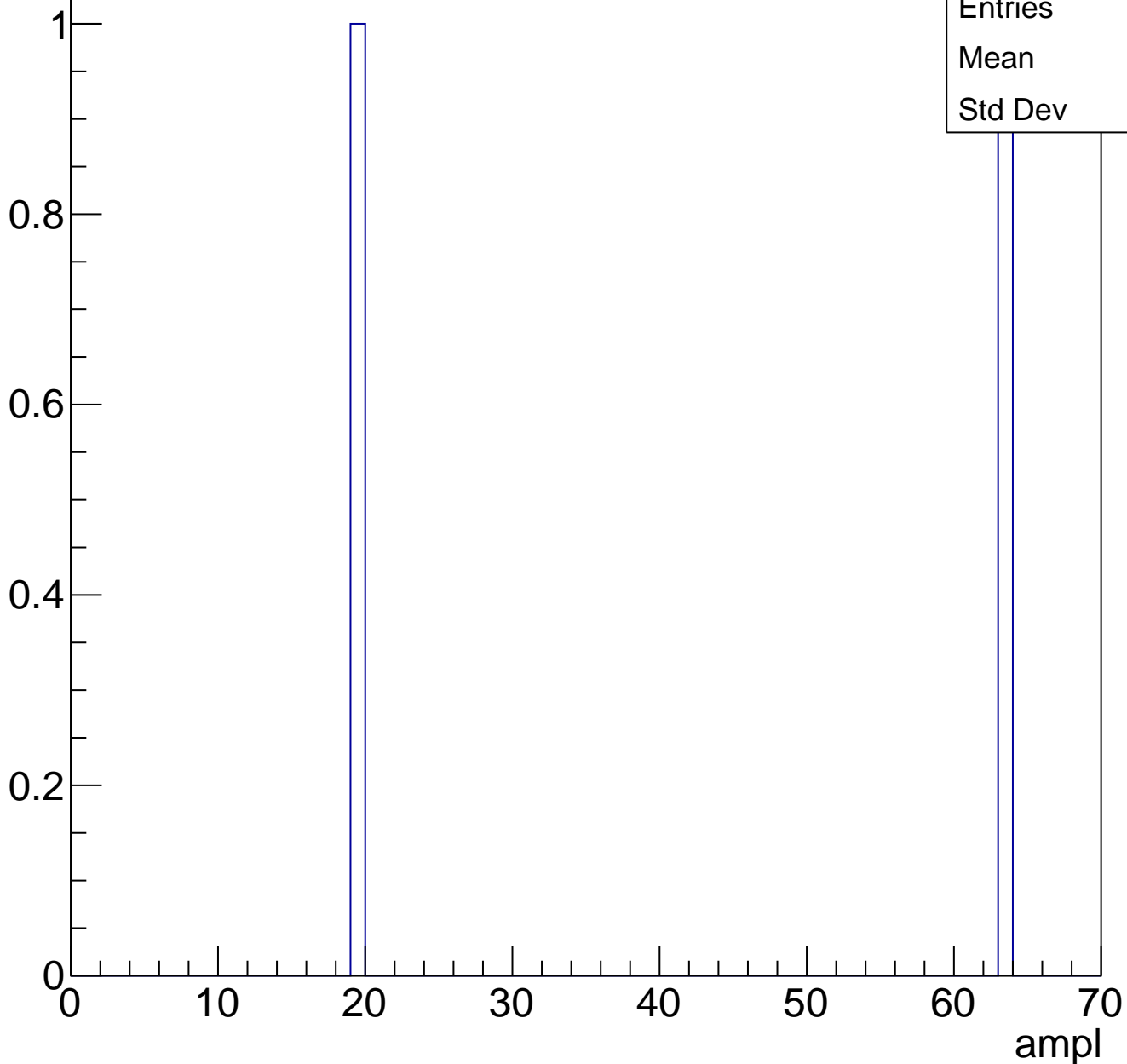
Entries	12
Mean	57
Std Dev	17.21



B1L001S, U16-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	41
Std Dev	22

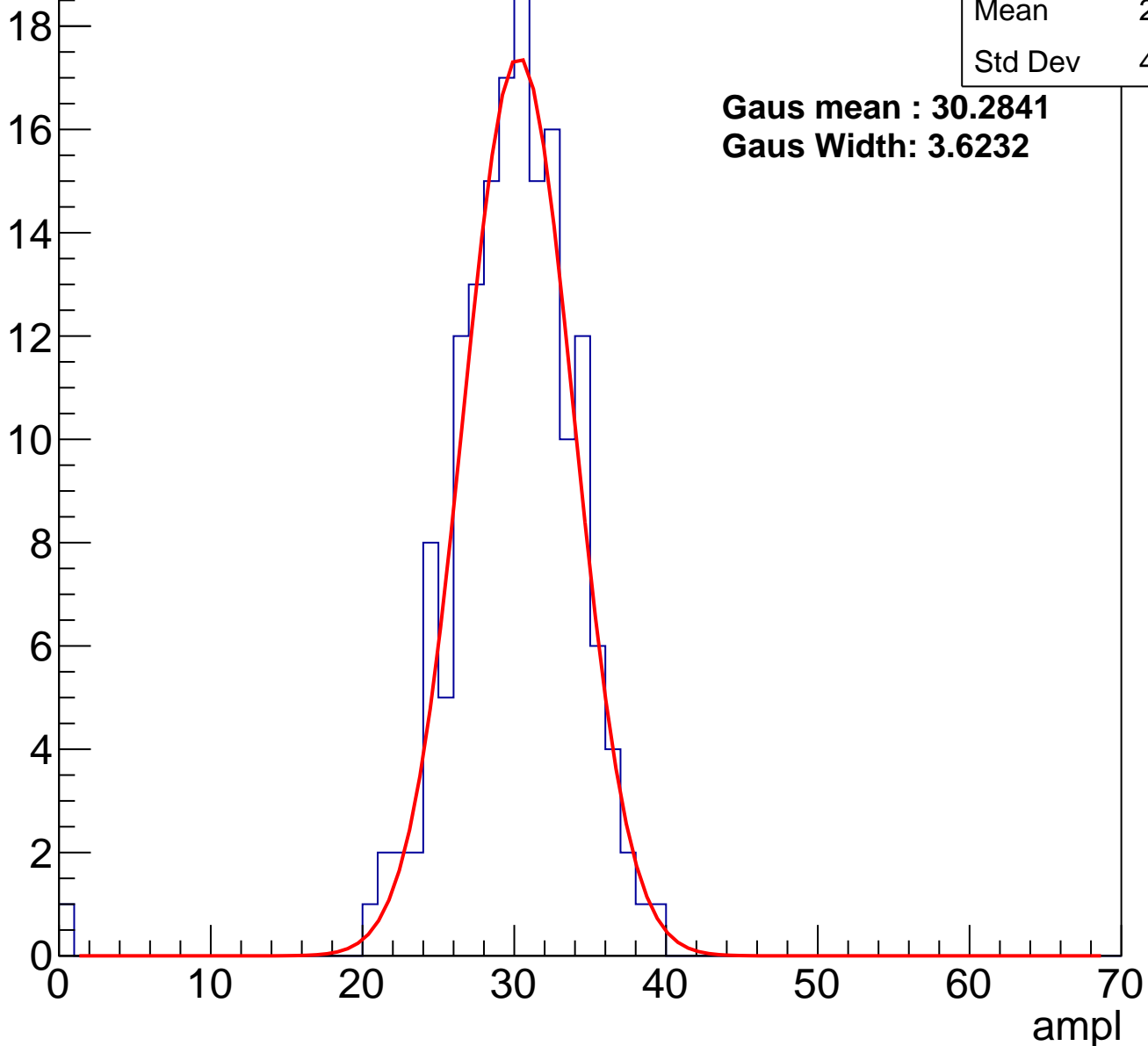
B1L001S, U16-ch6, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	29.48
Std Dev	4.306

Gaus mean : 30.2841
Gaus Width: 3.6232

Entry



B1L001S, U16-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	37.67
Std Dev	3.395

Gaus mean : 38.0869

Gaus Width: 3.6459

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

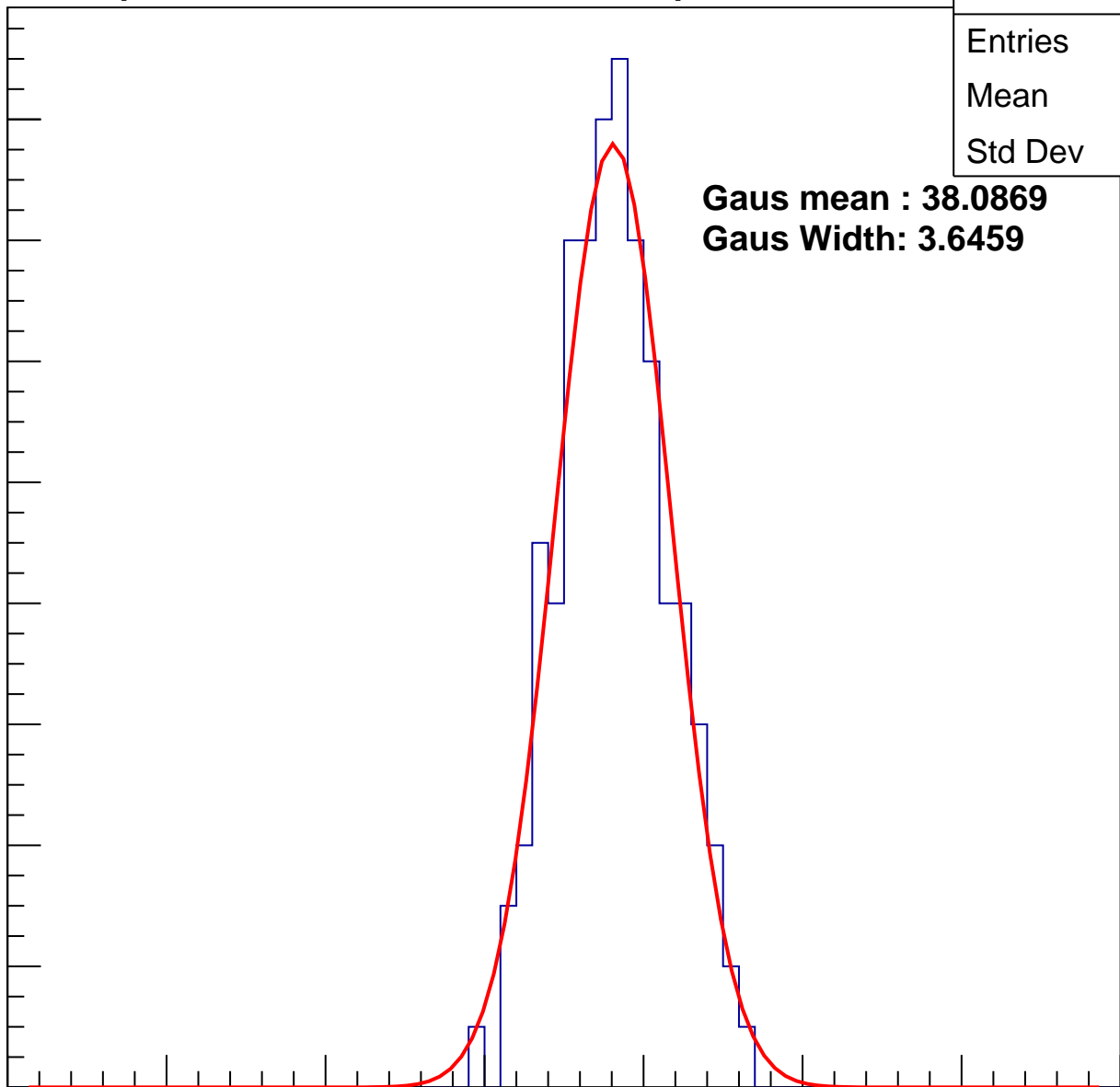
40

50

60

70

ampl



B1L001S, U16-ch6, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

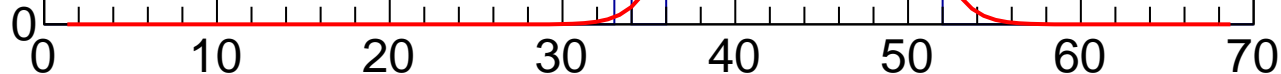
70

ampl

Entries	97
Mean	43.16
Std Dev	3.373

Gaus mean : 43.7820

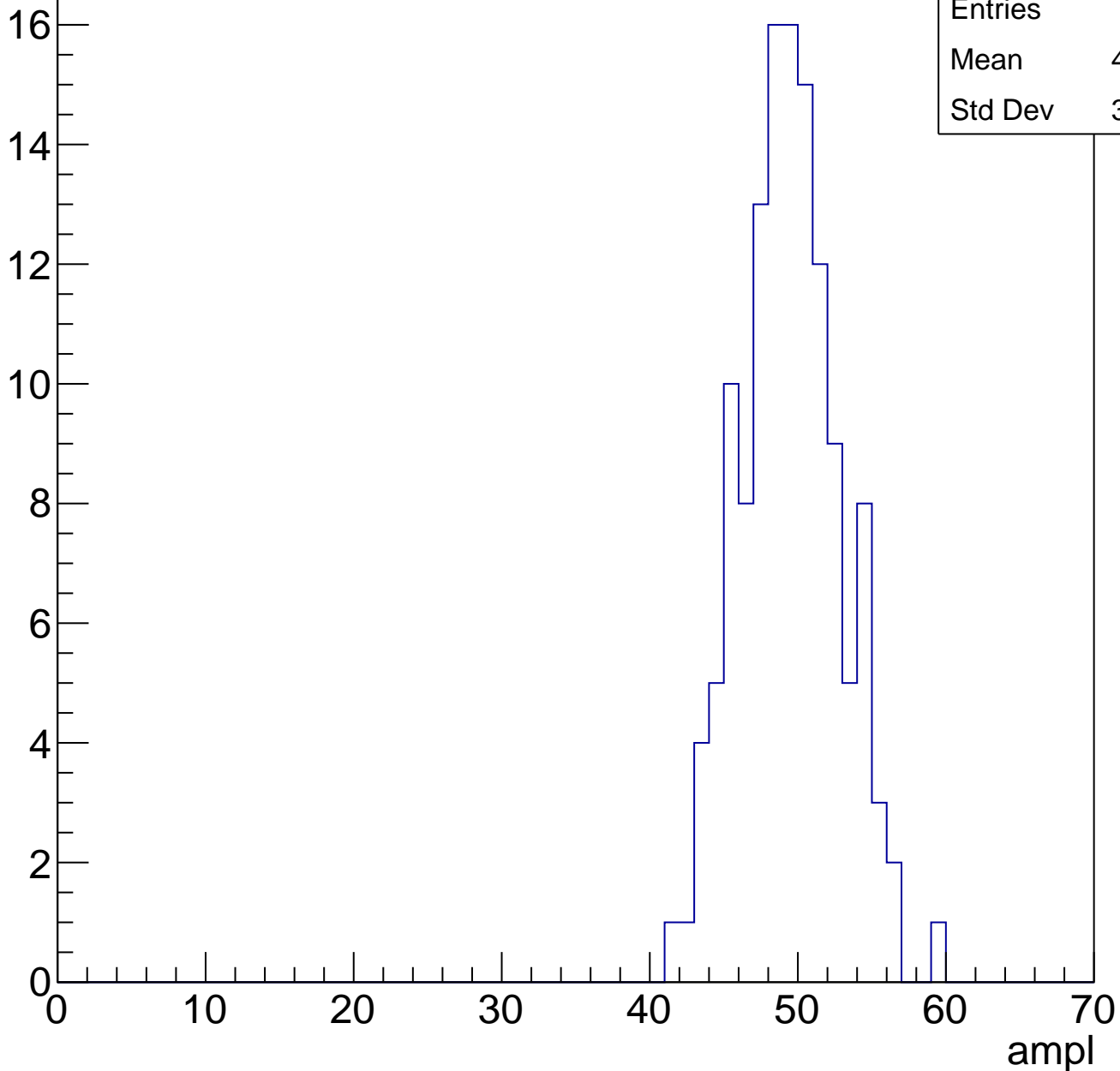
Gaus Width: 3.3898



B1L001S, U16-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

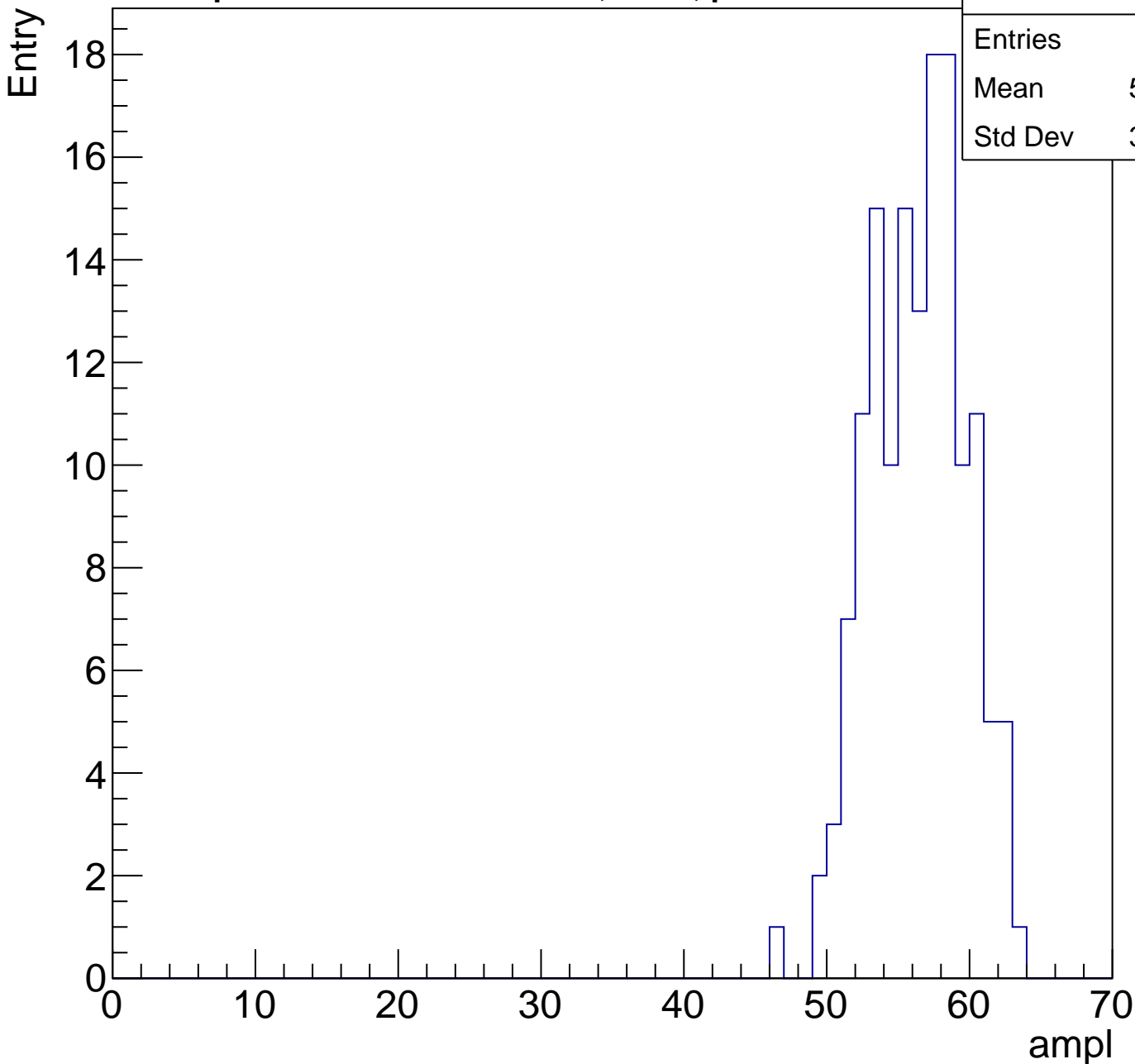


Entries	129
Mean	48.98
Std Dev	3.308

B1L001S, U16-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	55.92
Std Dev	3.276



B1L001S, U16-ch6, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

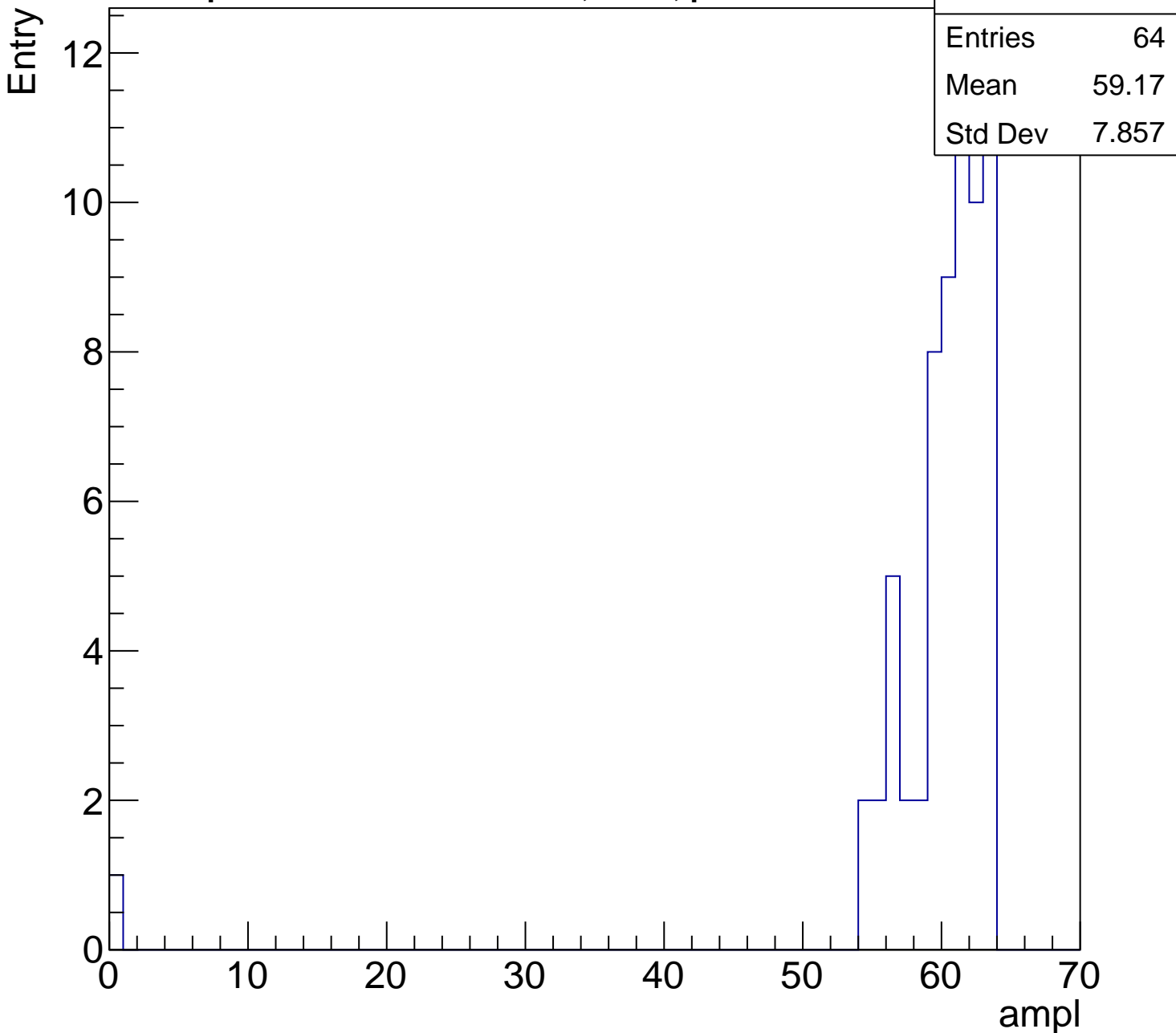
2

0

Entries	64
Mean	59.17
Std Dev	7.857

ampl

0 10 20 30 40 50 60 70

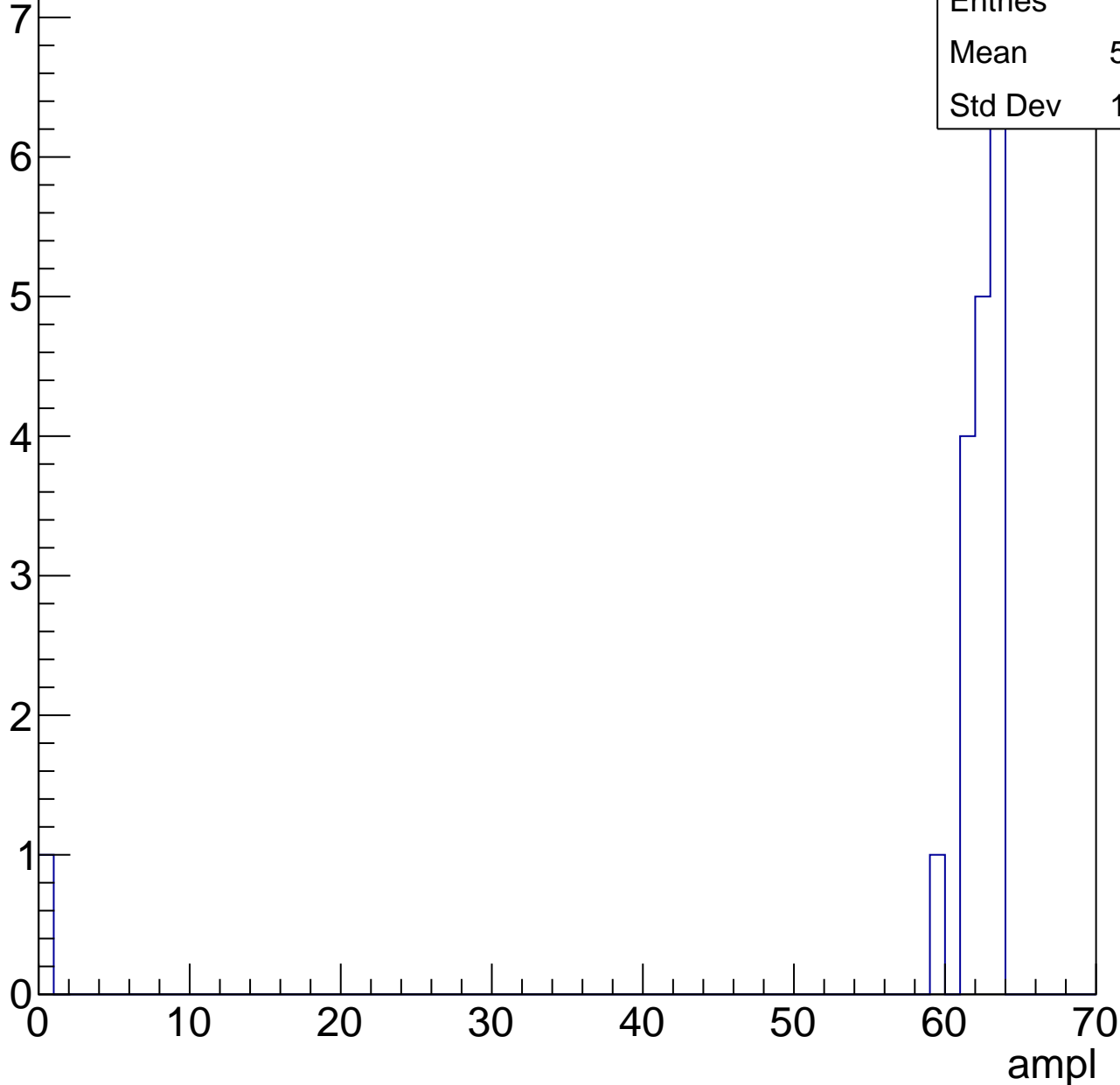


B1L001S, U16-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.56
Std Dev	14.24



B1L001S, U16-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch7, adc0

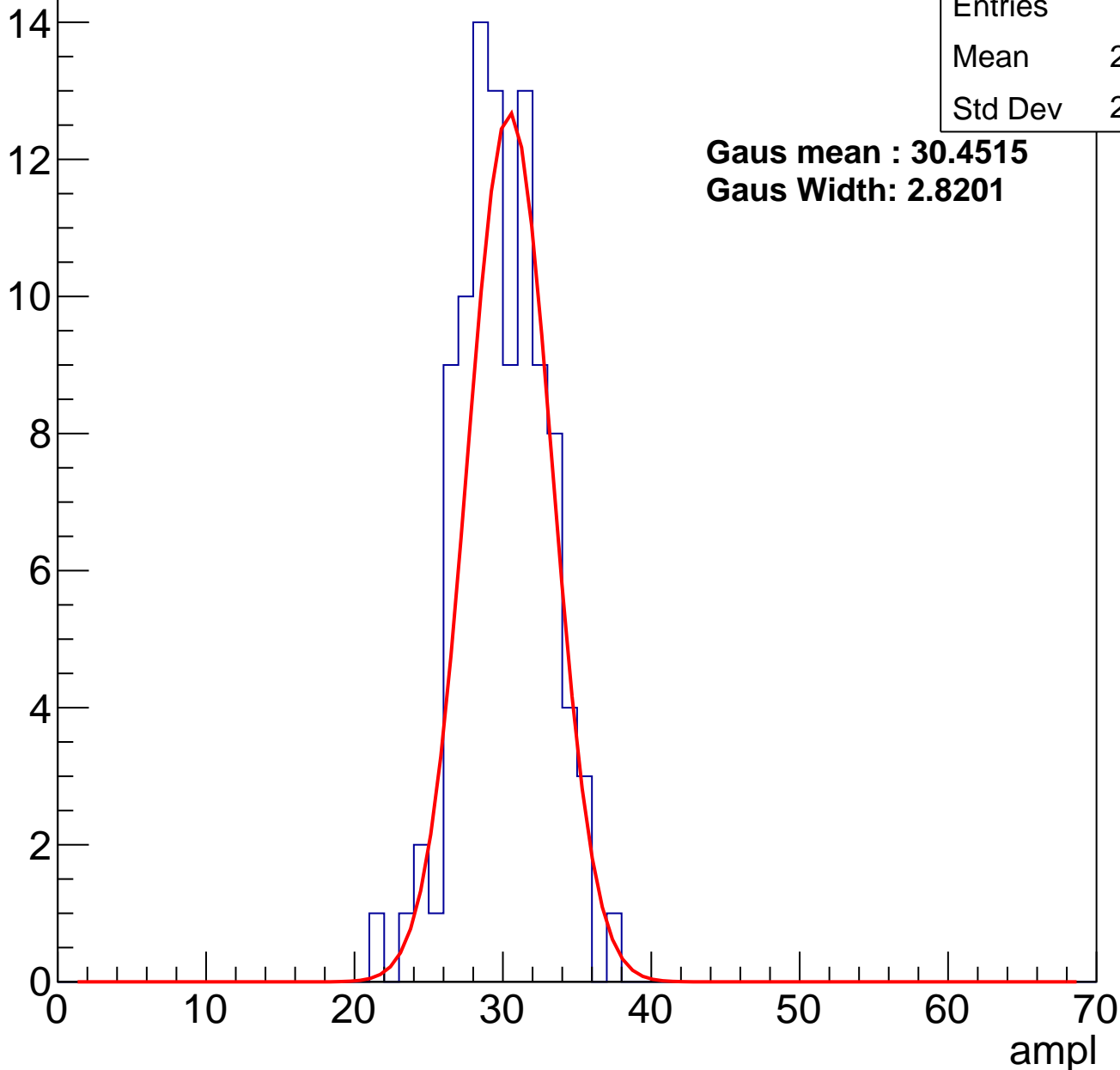
calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	29.52
Std Dev	2.886

Gaus mean : 30.4515

Gaus Width: 2.8201

Entry



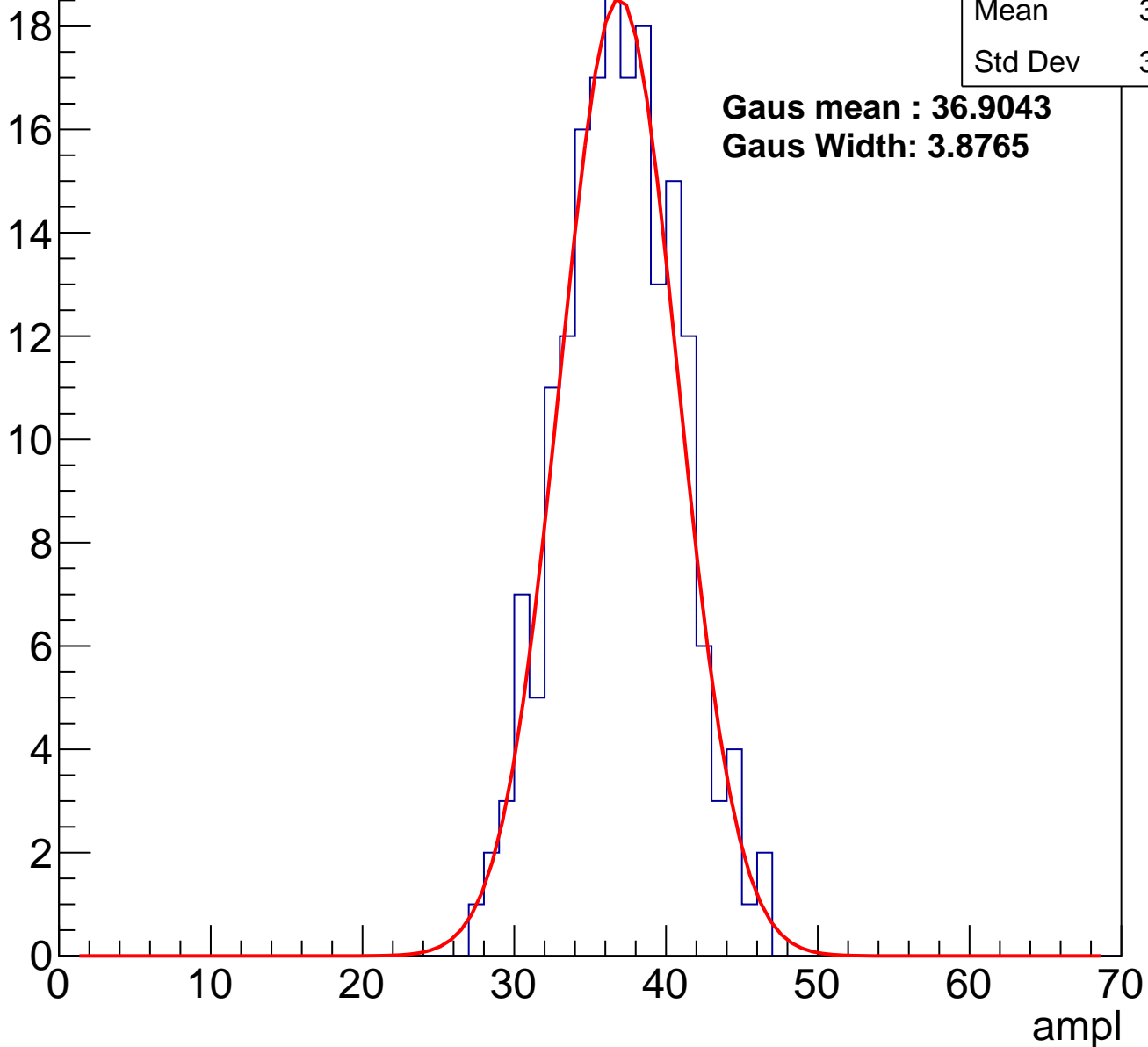
B1L001S, U16-ch7, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	184
Mean	36.48
Std Dev	3.832

Gaus mean : 36.9043
Gaus Width: 3.8765

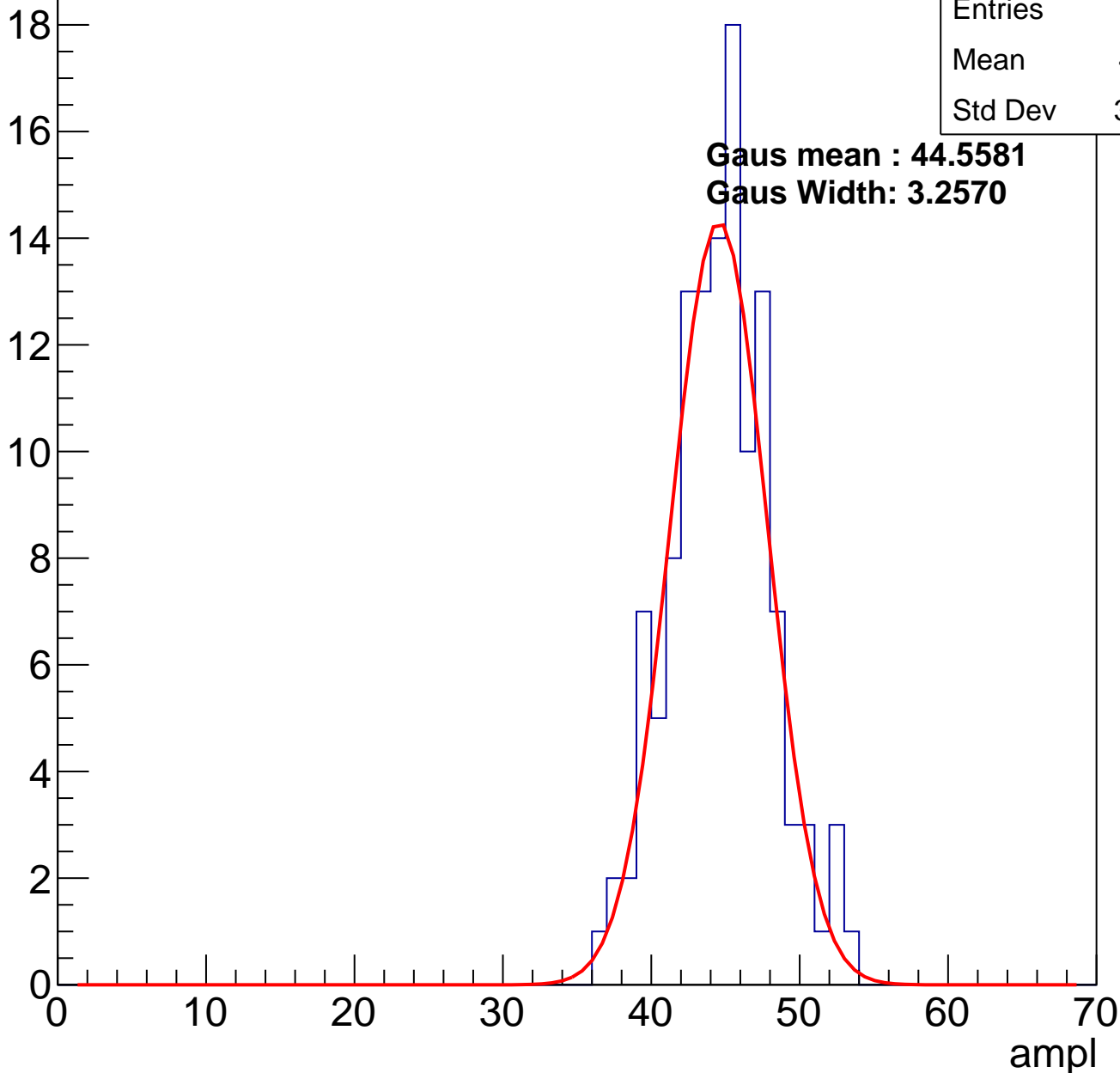
Entry



B1L001S, U16-ch7, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

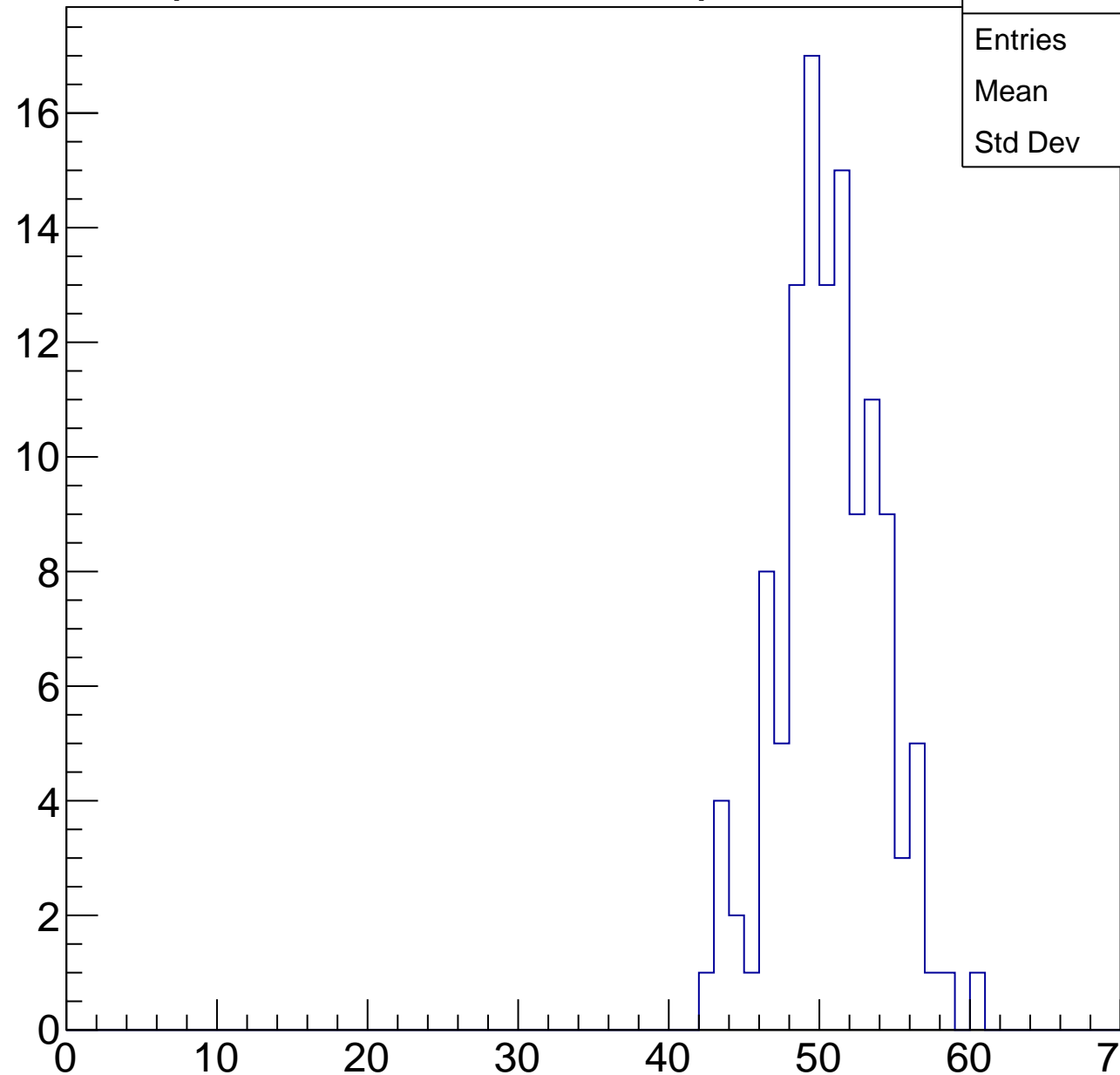
Entries	119
Mean	50.24
Std Dev	3.373

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch7, adc4

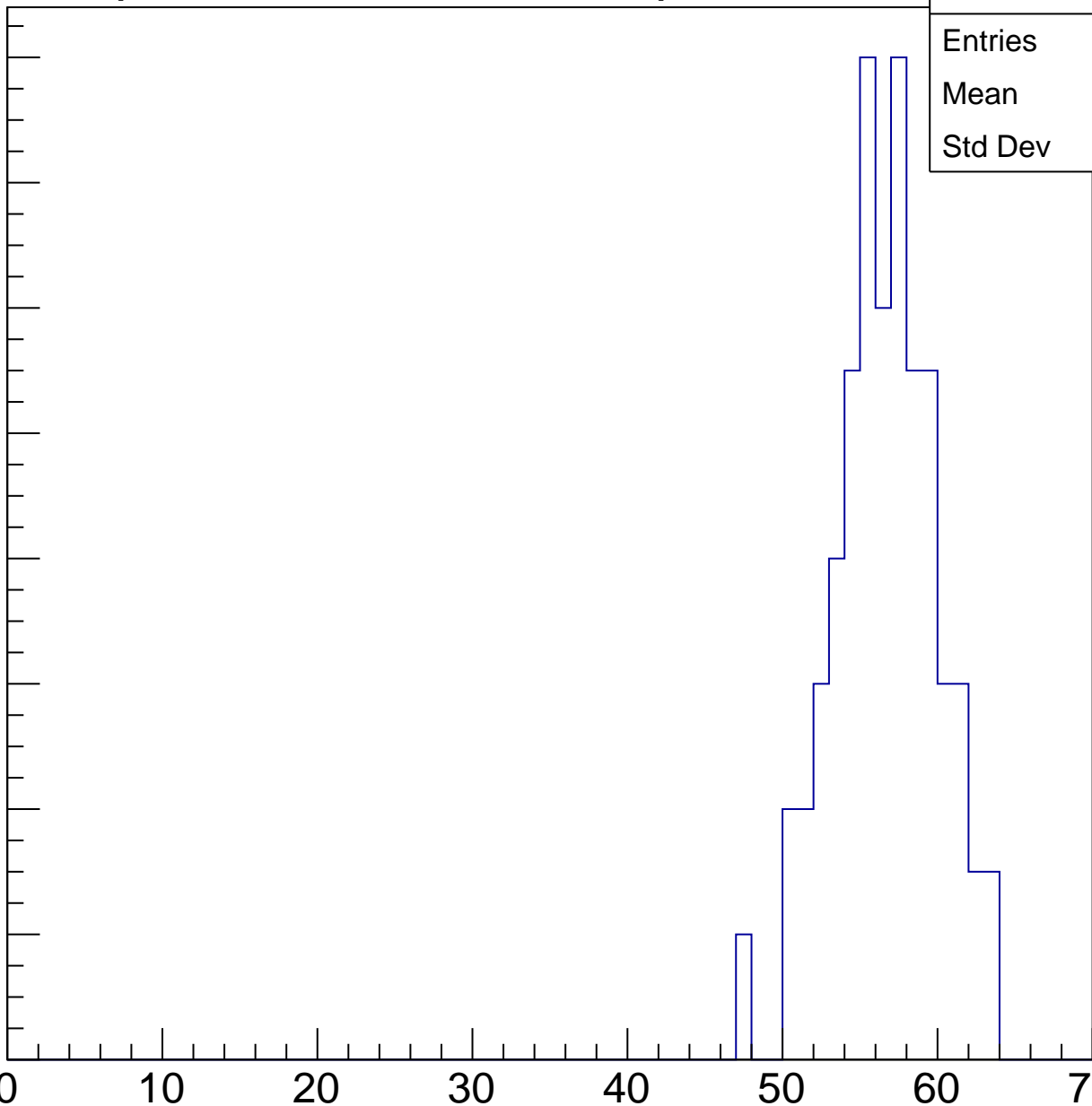
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	56.13
Std Dev	3.295

ampl



B1L001S, U16-ch7, adc5

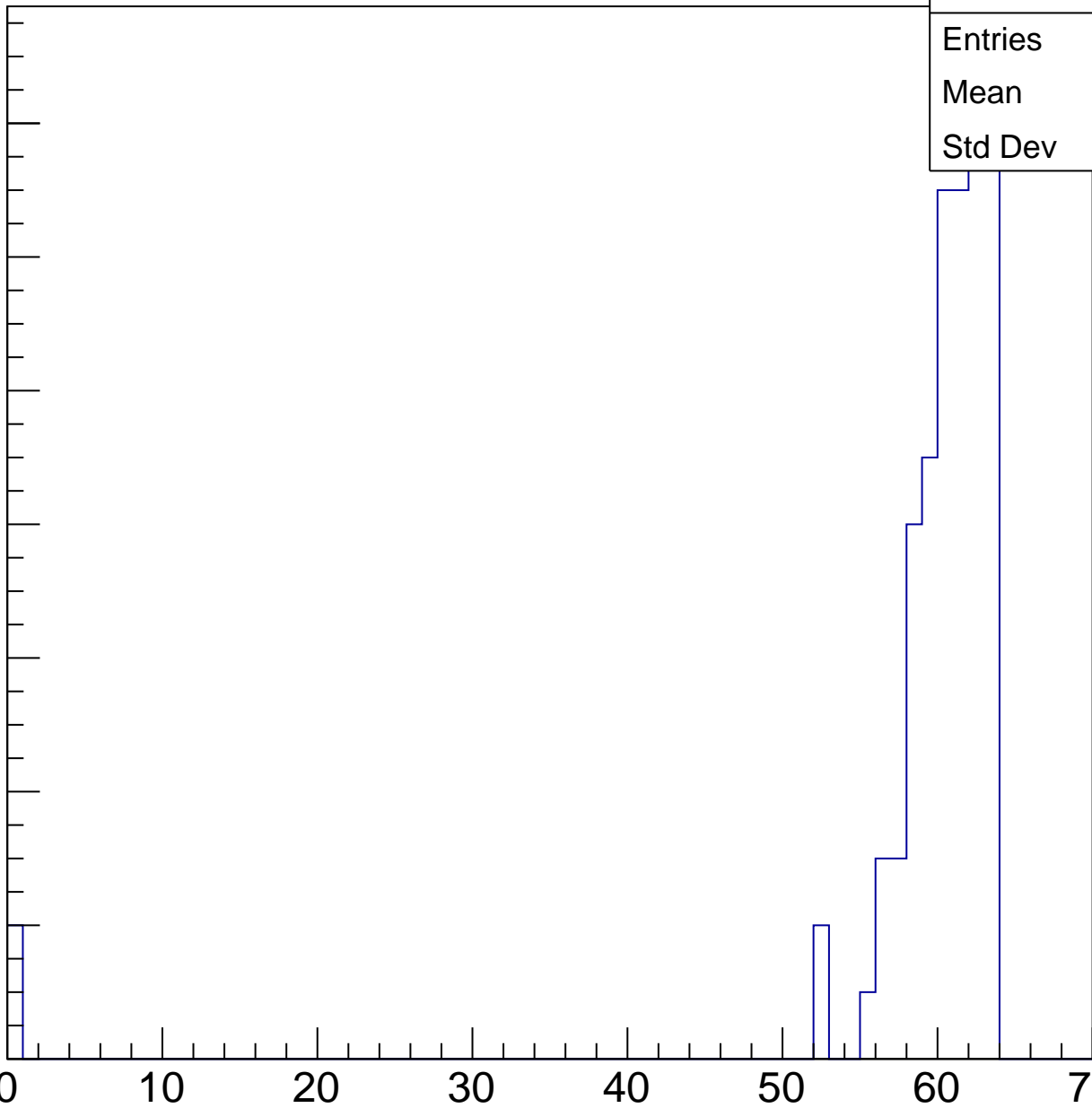
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	58.78
Std Dev	9.538

ampl



B1L001S, U16-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

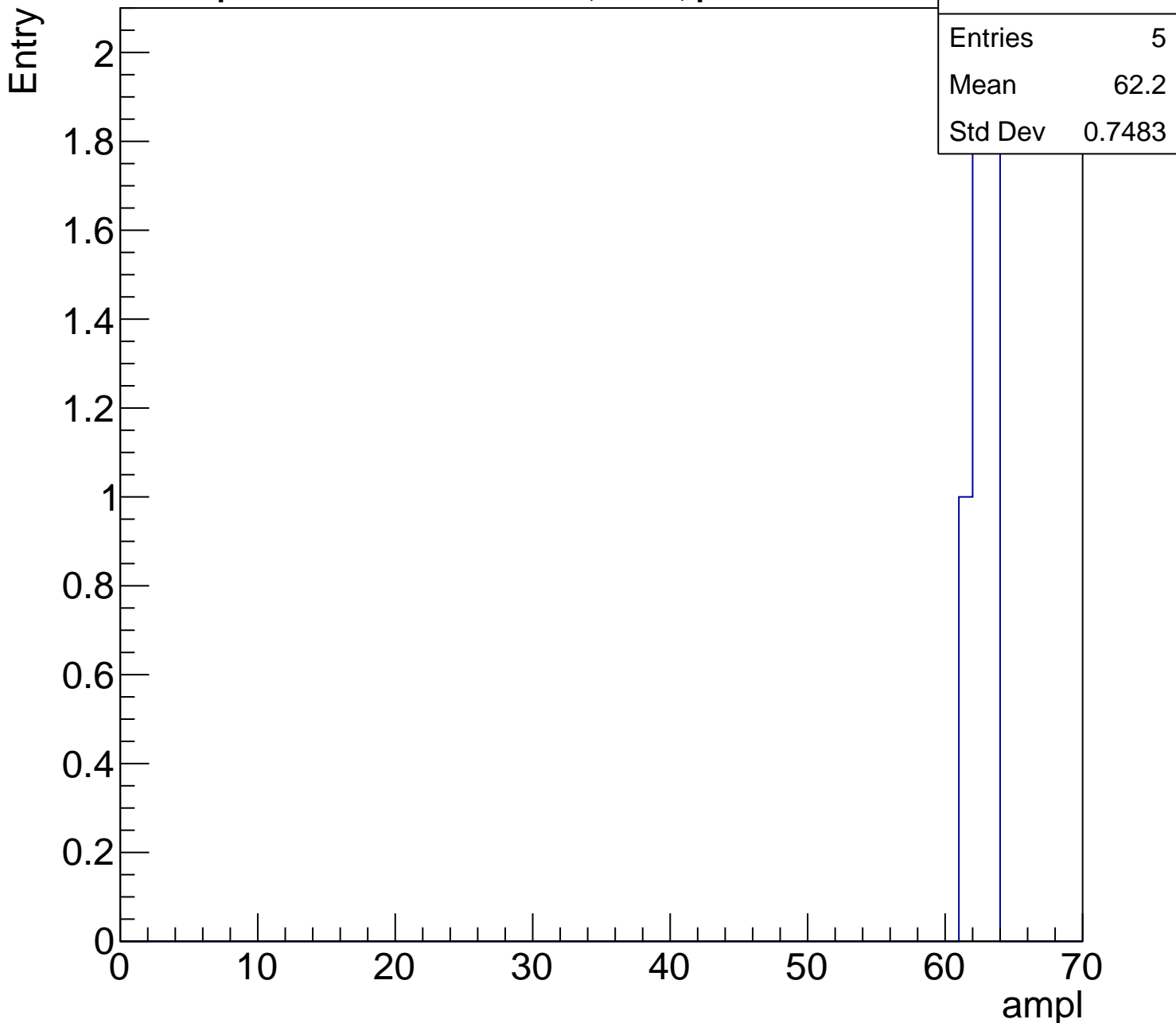
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch7, adc7

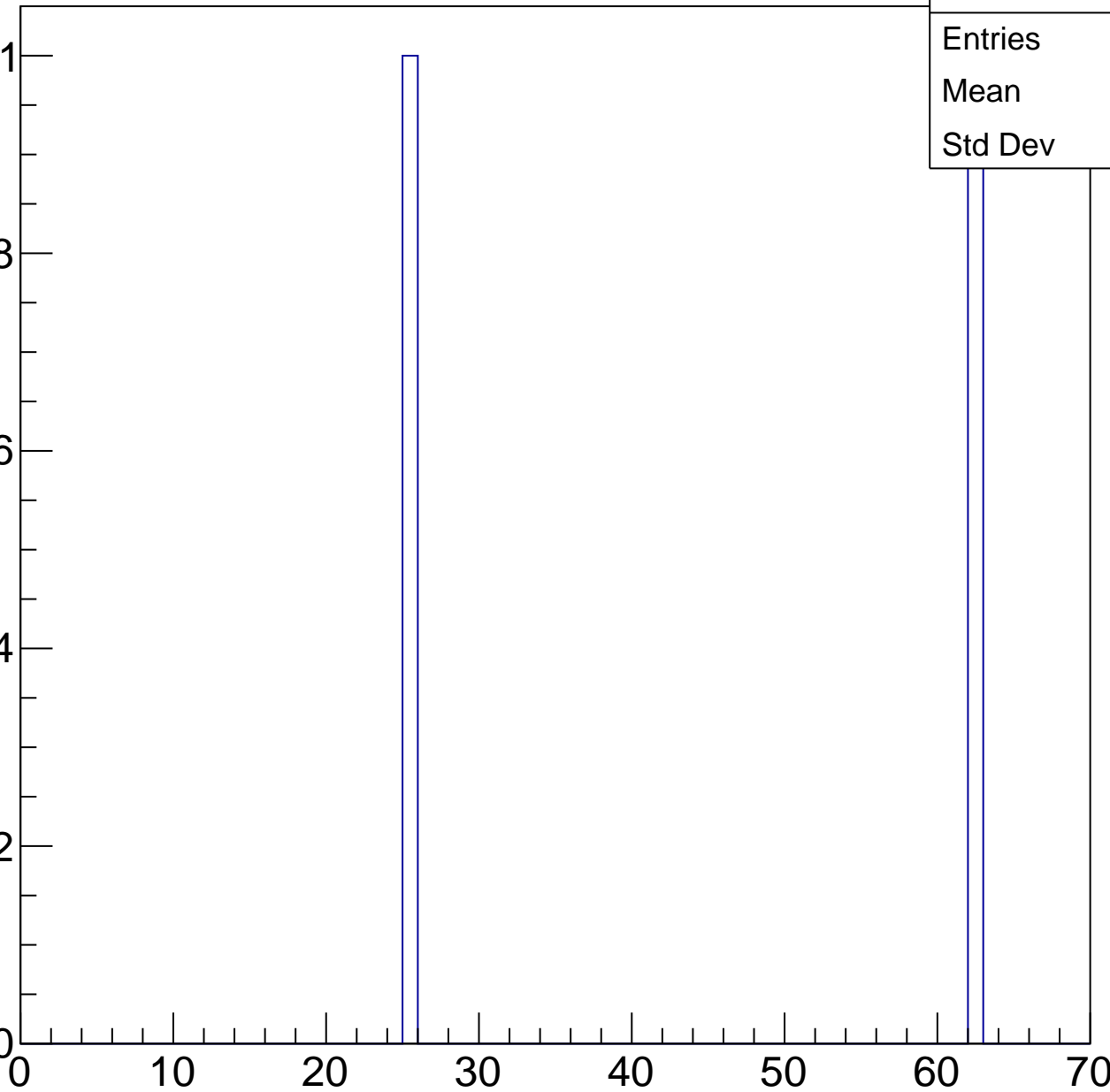
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43.5
Std Dev	18.5

ampl



B1L001S, U16-ch8, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.65
Std Dev	5.126

Gaus mean : 30.6957

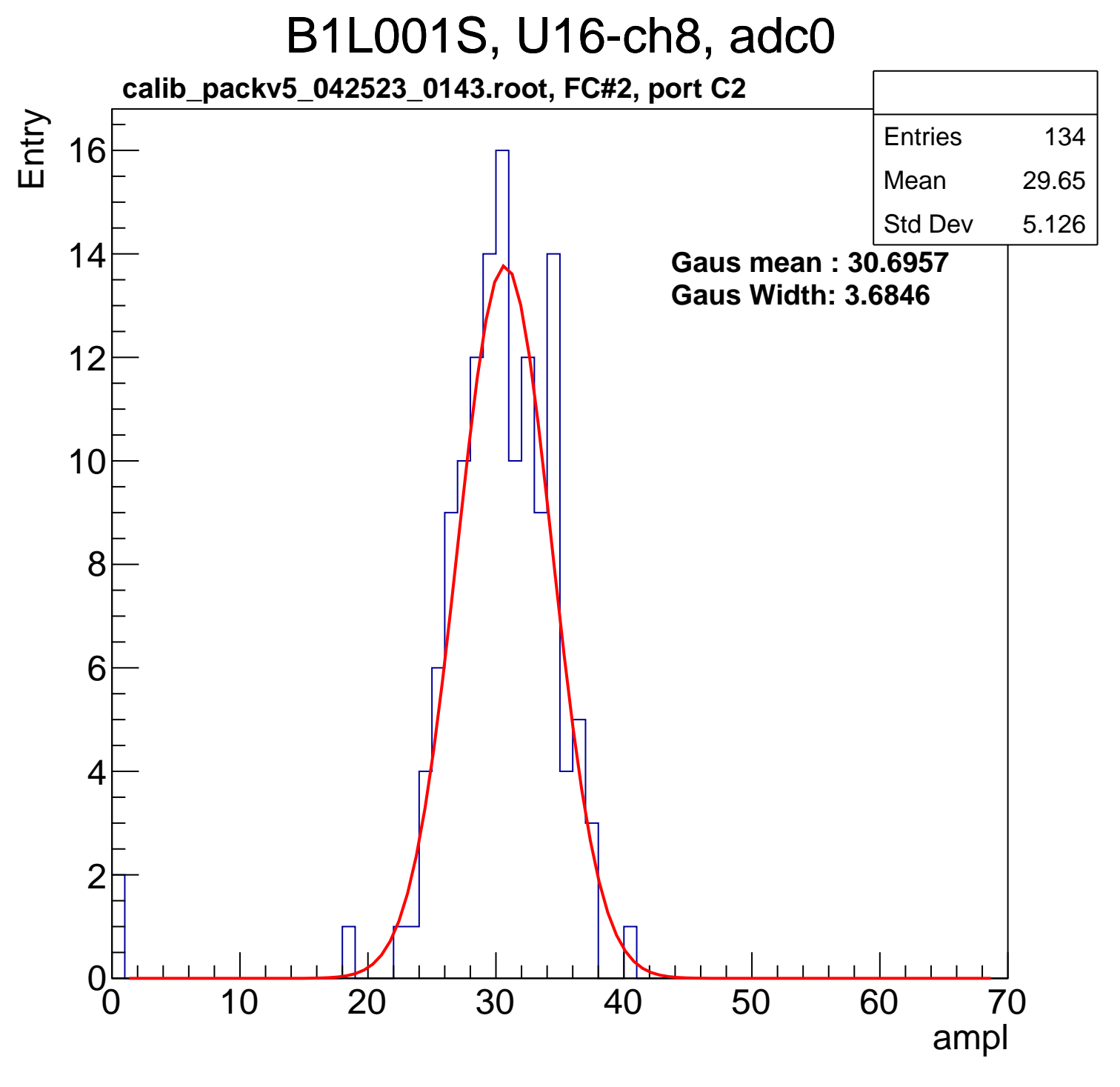
Gaus Width: 3.6846

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch8, adc1

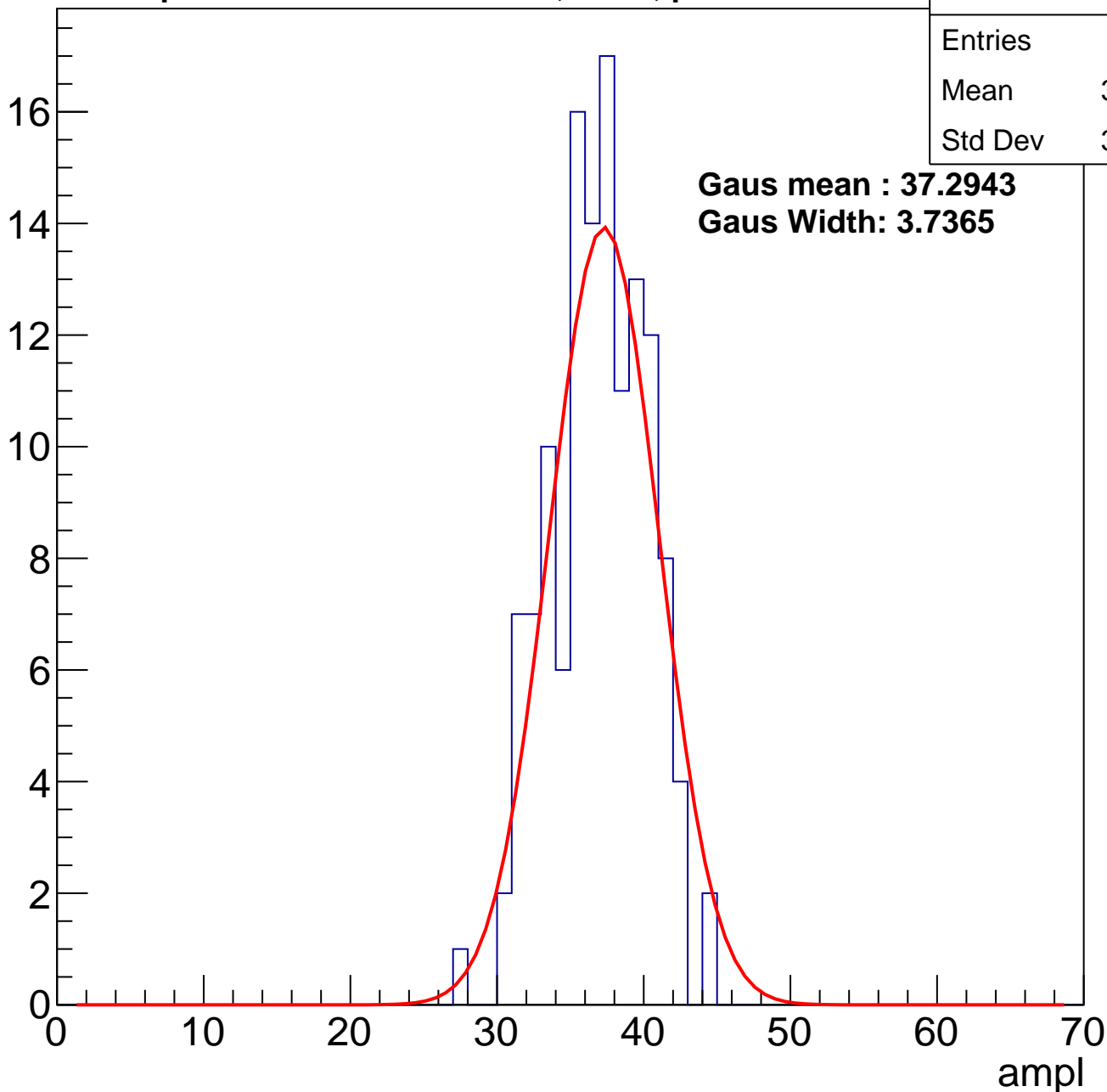
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36.49
Std Dev	3.259

Gaus mean : 37.2943

Gaus Width: 3.7365

Entry



B1L001S, U16-ch8, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	43.42
Std Dev	3.04

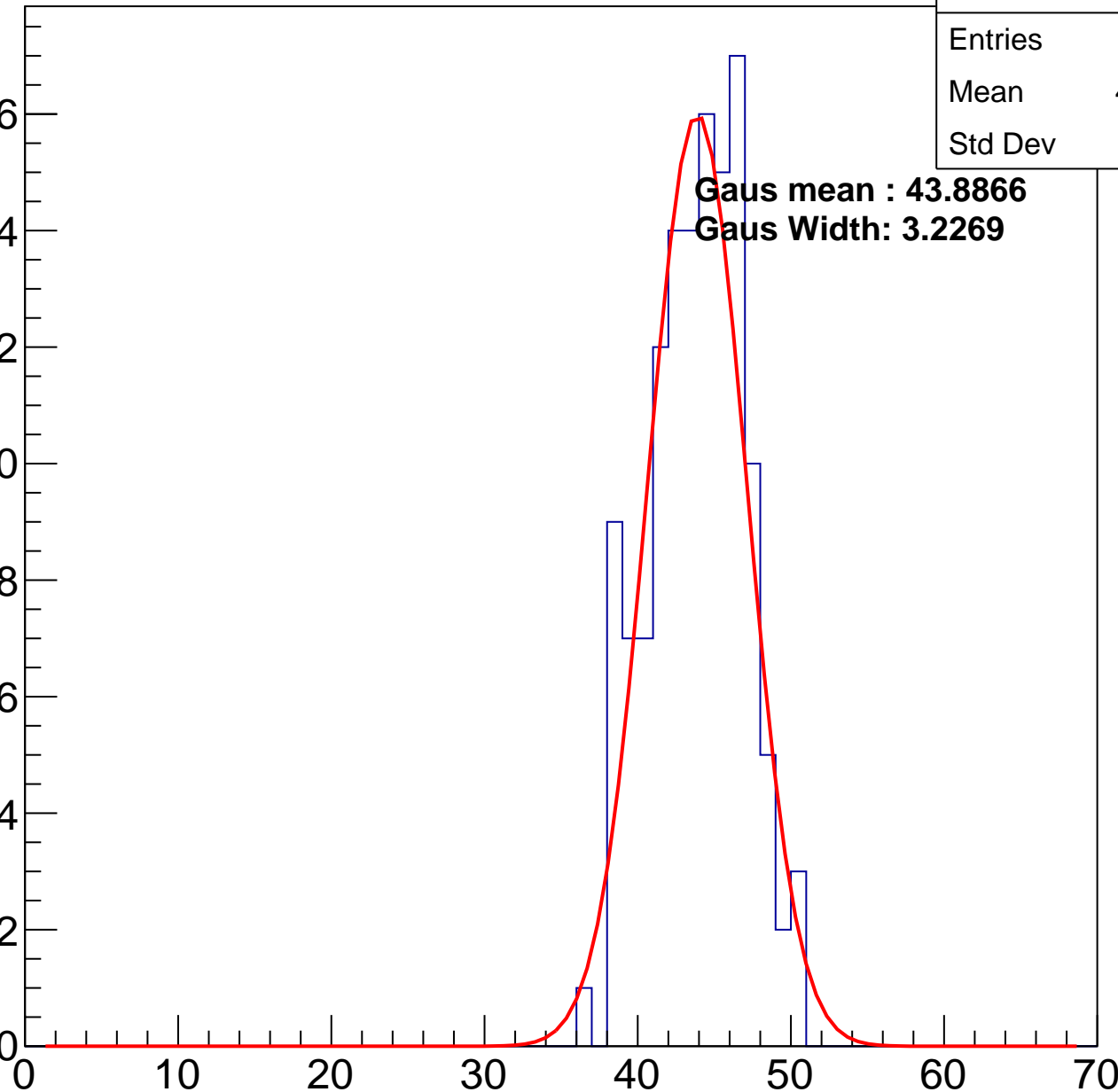
Gaus mean : 43.8866

Gaus Width: 3.2269

Entry

16
14
12
10
8
6
4
2
0

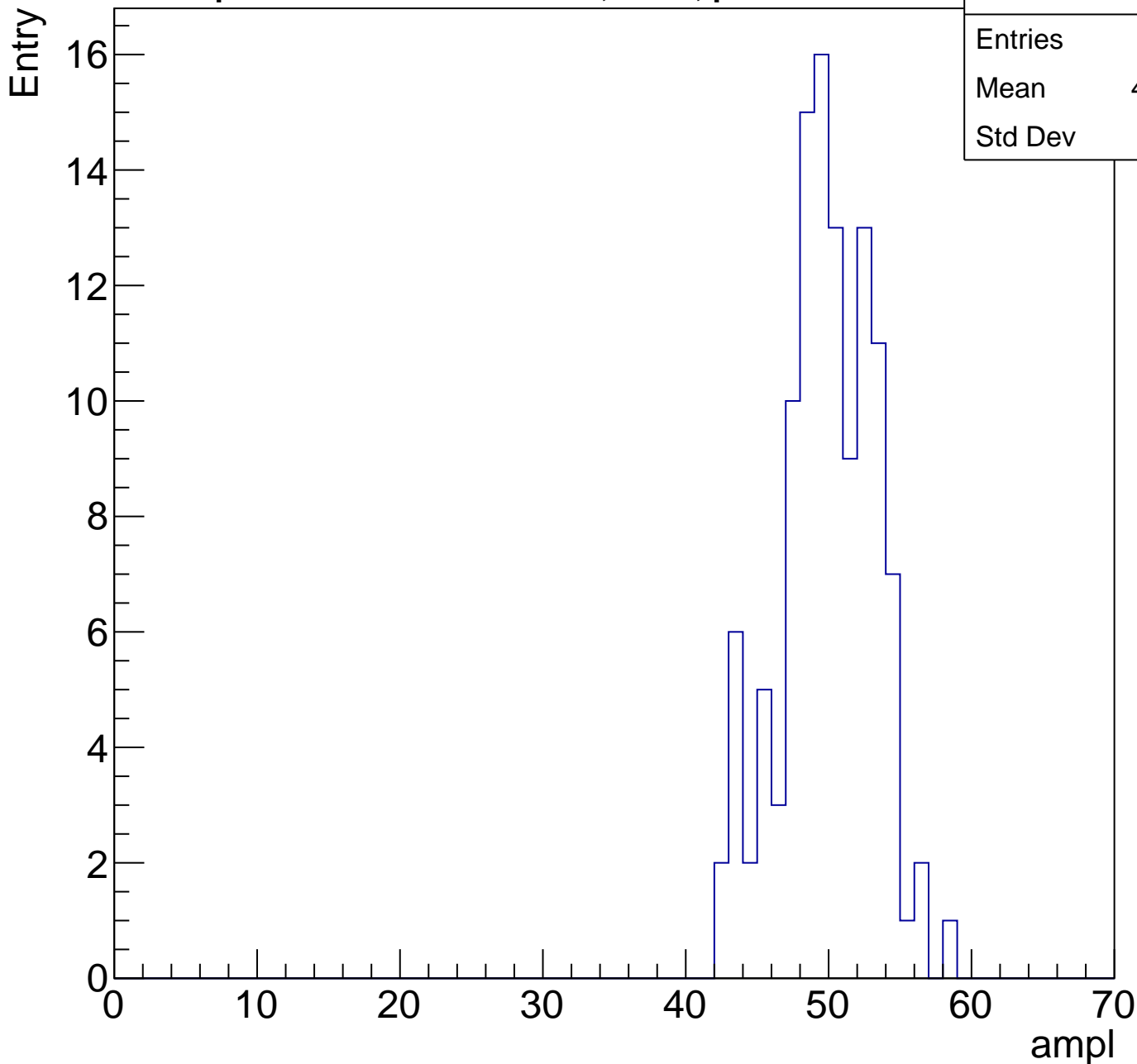
ampl



B1L001S, U16-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

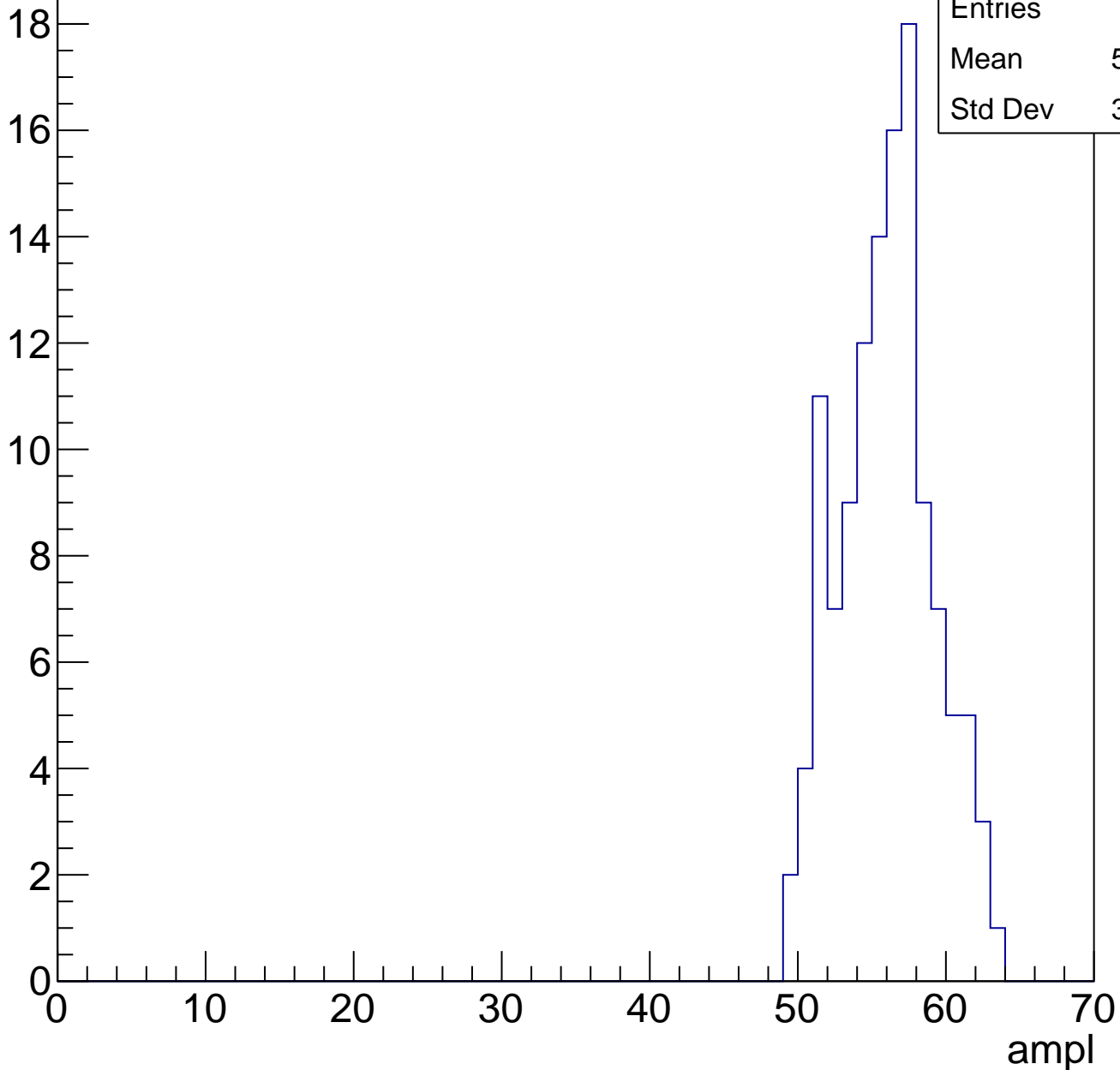
Entries	116
Mean	49.47
Std Dev	3.26



B1L001S, U16-ch8, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch8, adc5

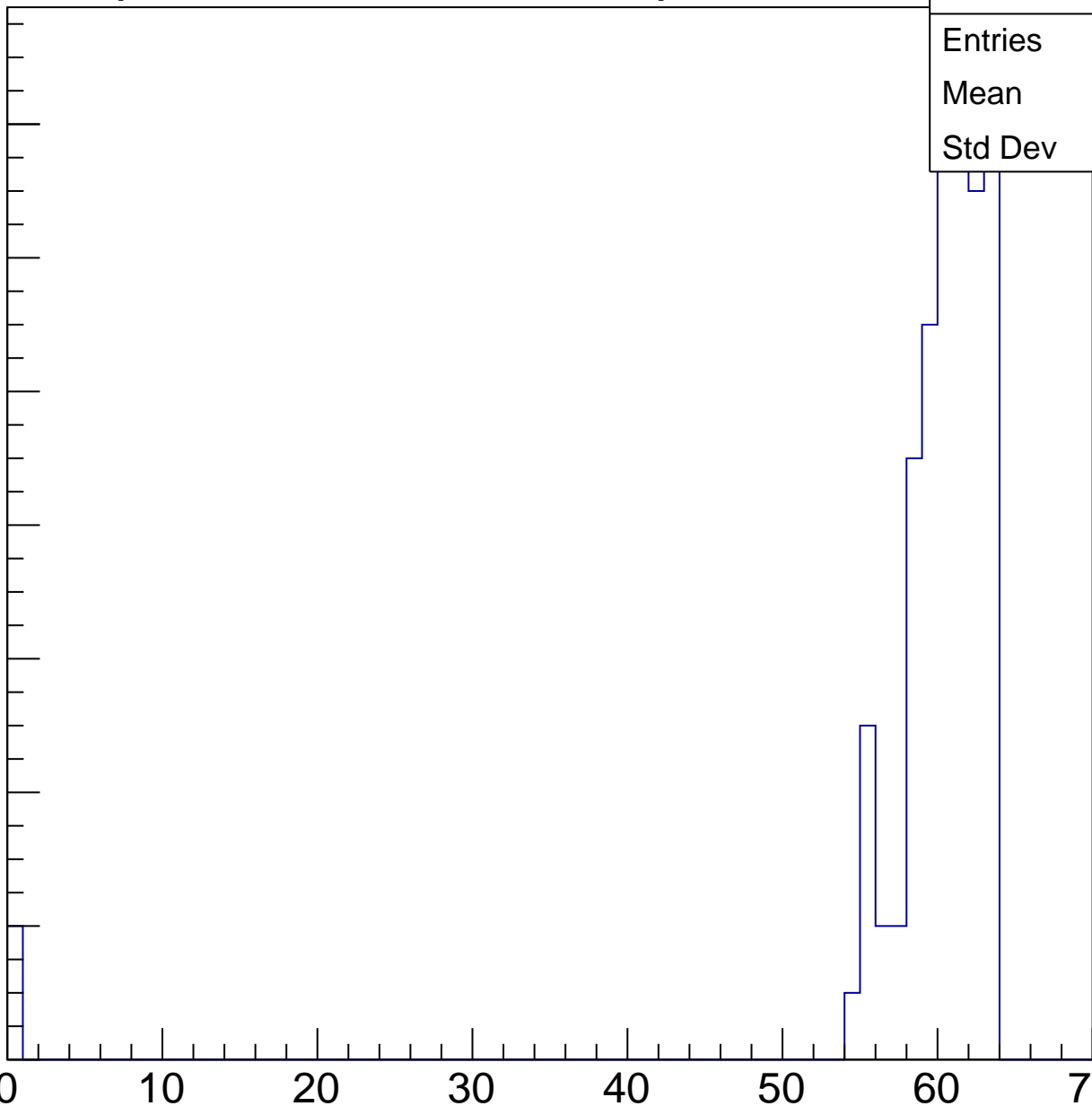
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.75
Std Dev	9.188

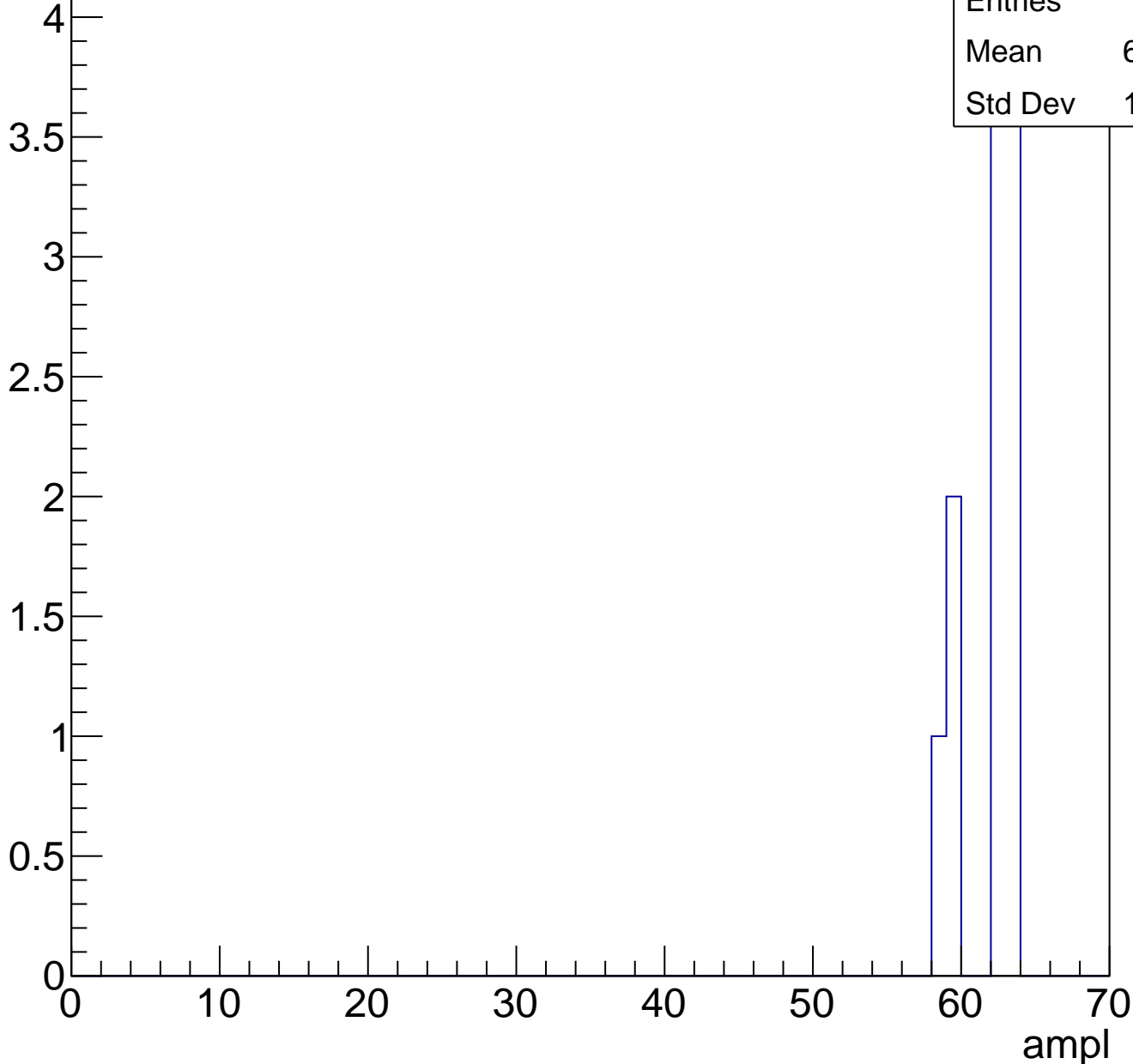
ampl



B1L001S, U16-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

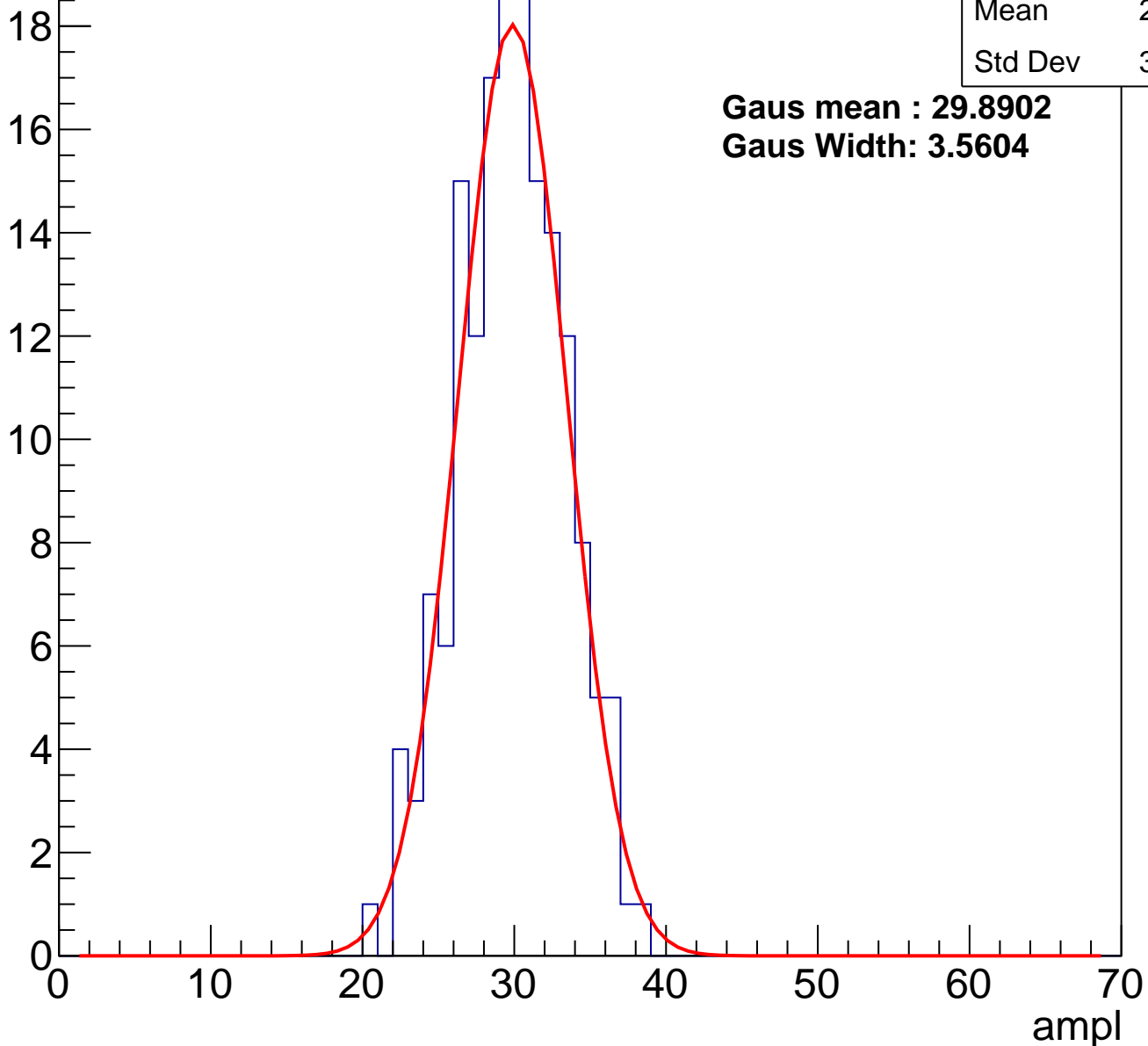
Entry



B1L001S, U16-ch9, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	37.1
Std Dev	3.301

Gaus mean : 37.4021

Gaus Width: 3.1675

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

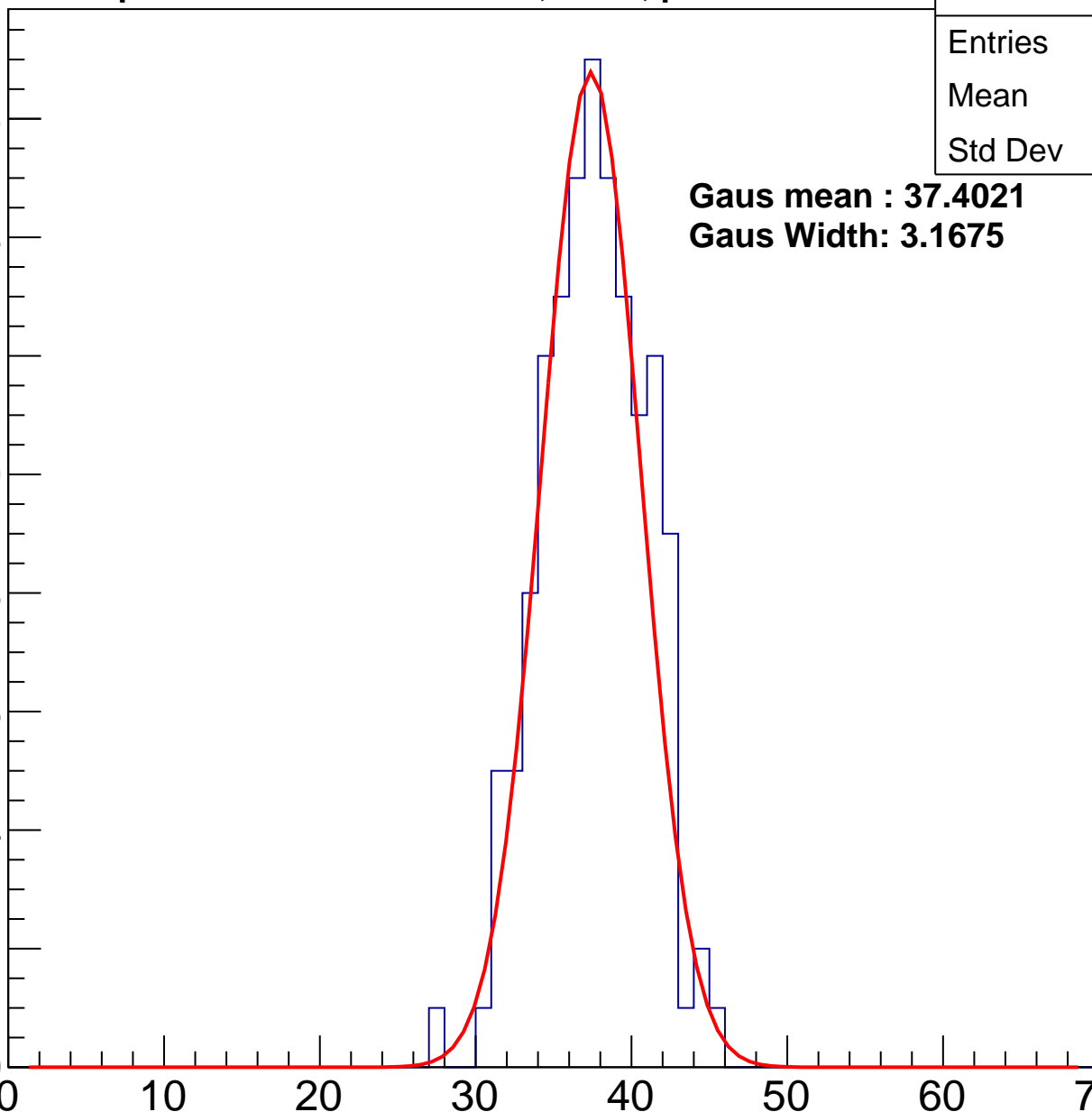
40

50

60

70

ampl



B1L001S, U16-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	43.04
Std Dev	3.129

Gaus mean : 43.2150

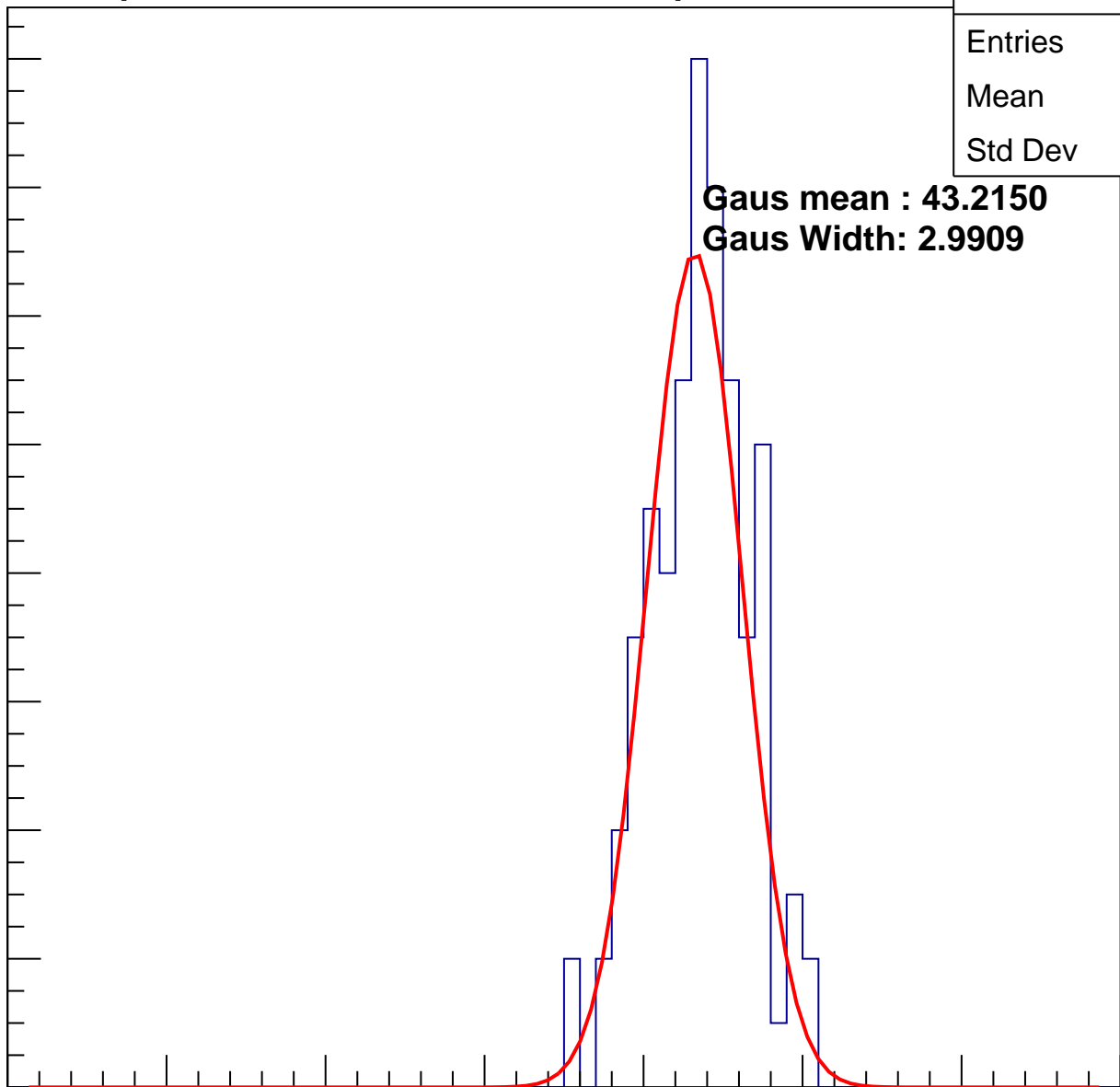
Gaus Width: 2.9909

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

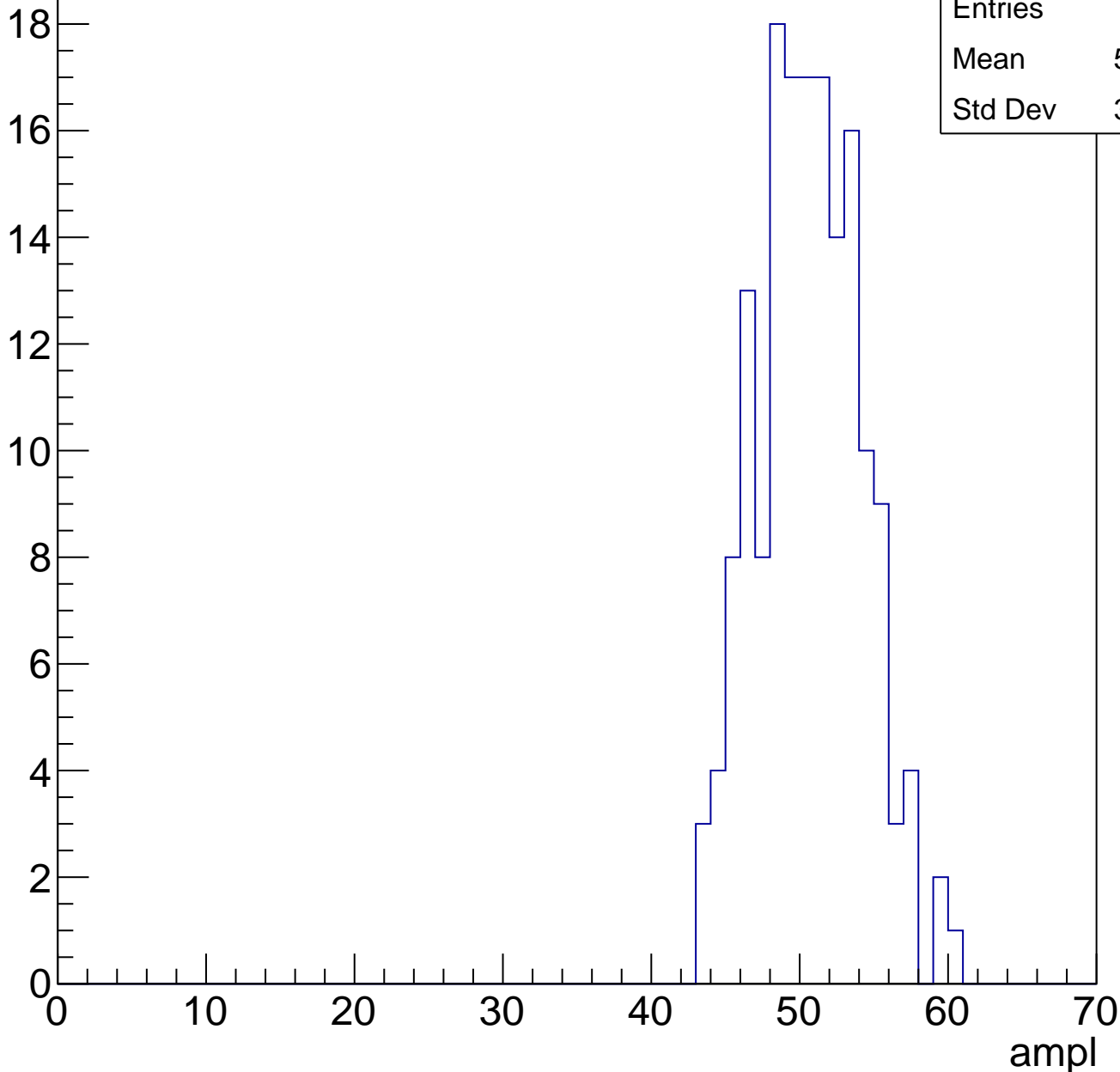


B1L001S, U16-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	50.23
Std Dev	3.509

Entry



B1L001S, U16-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

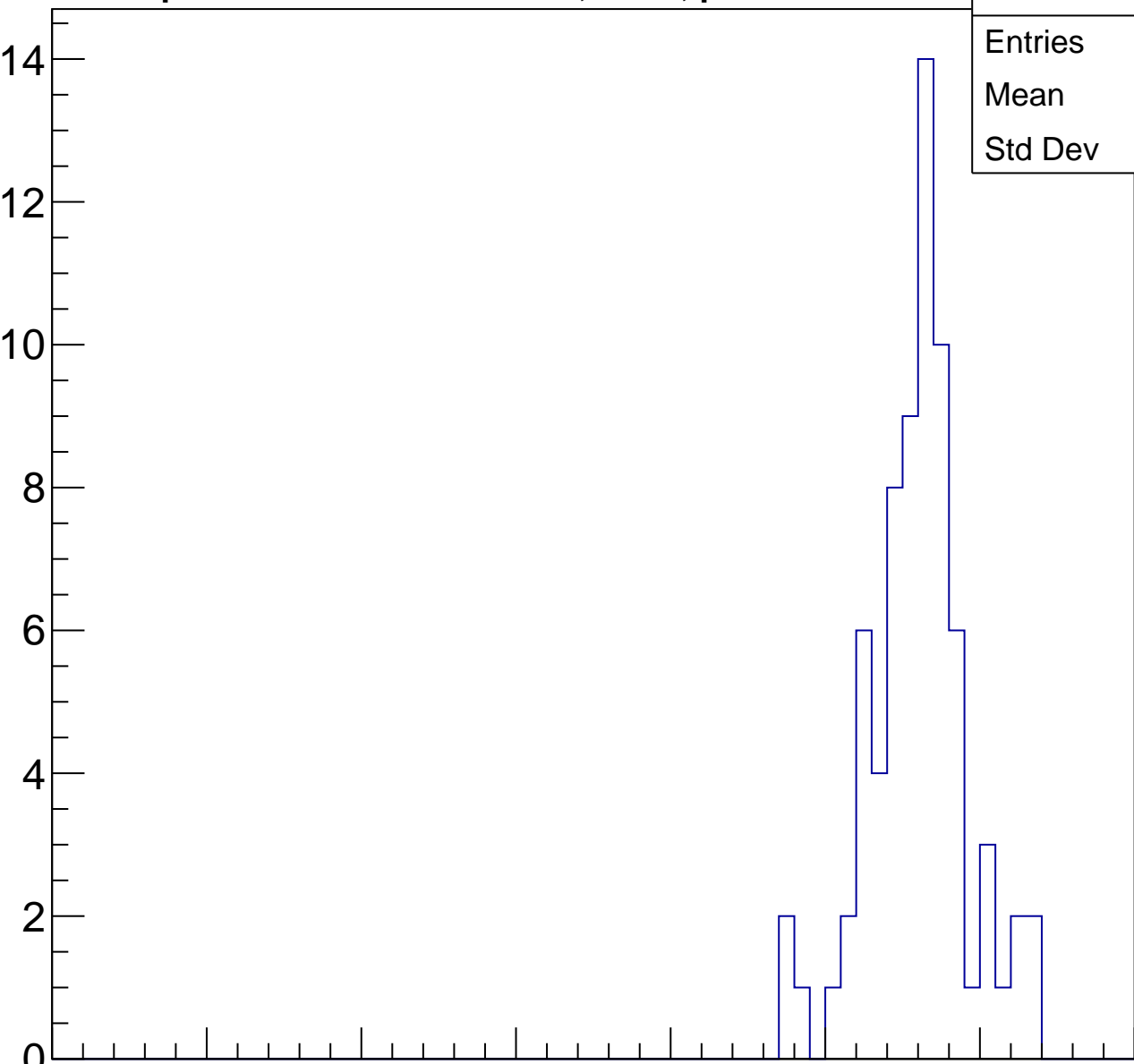
Entries	72
Mean	55.51
Std Dev	3.236

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch9, adc5

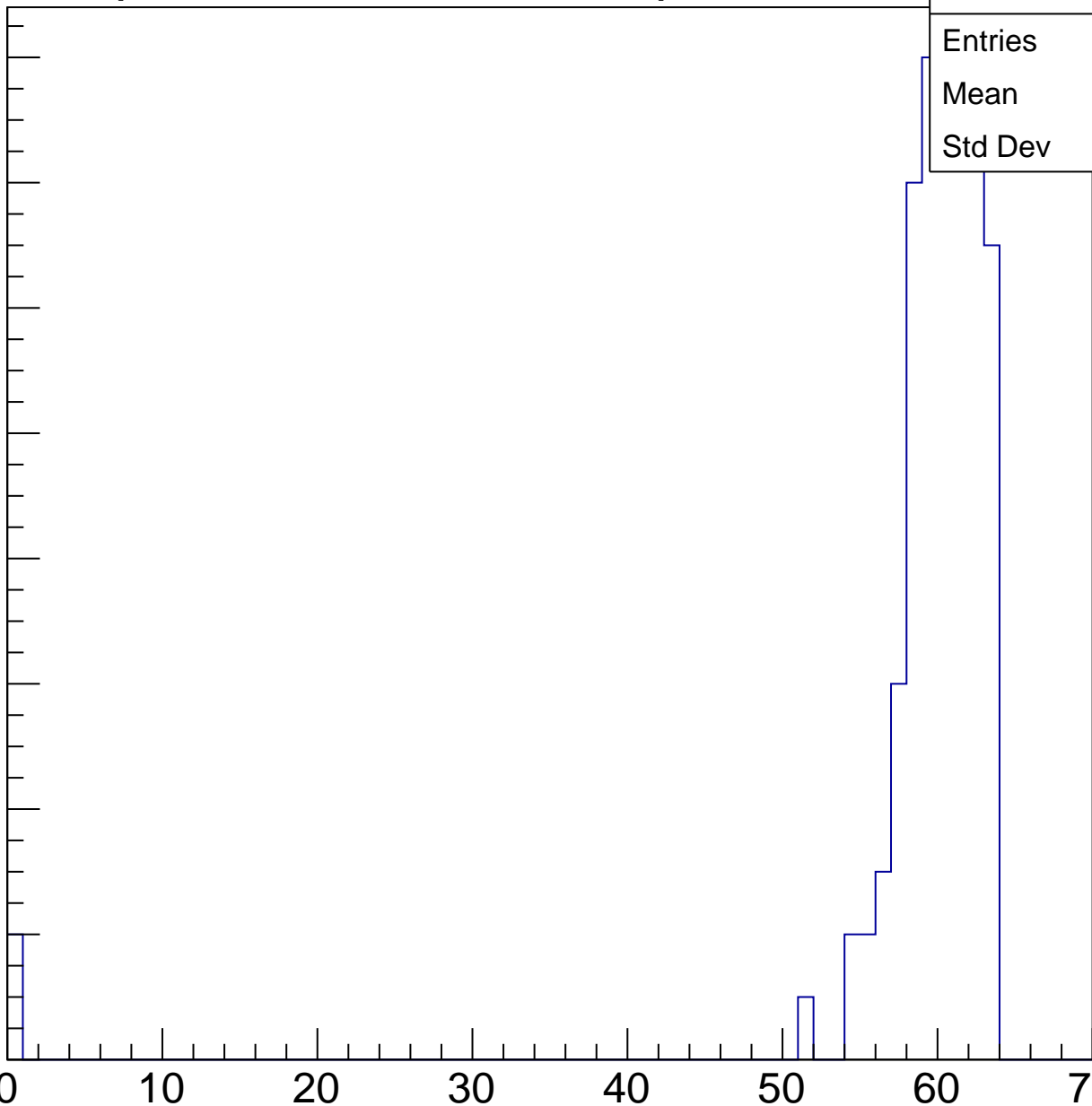
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	58.7
Std Dev	8.464

ampl

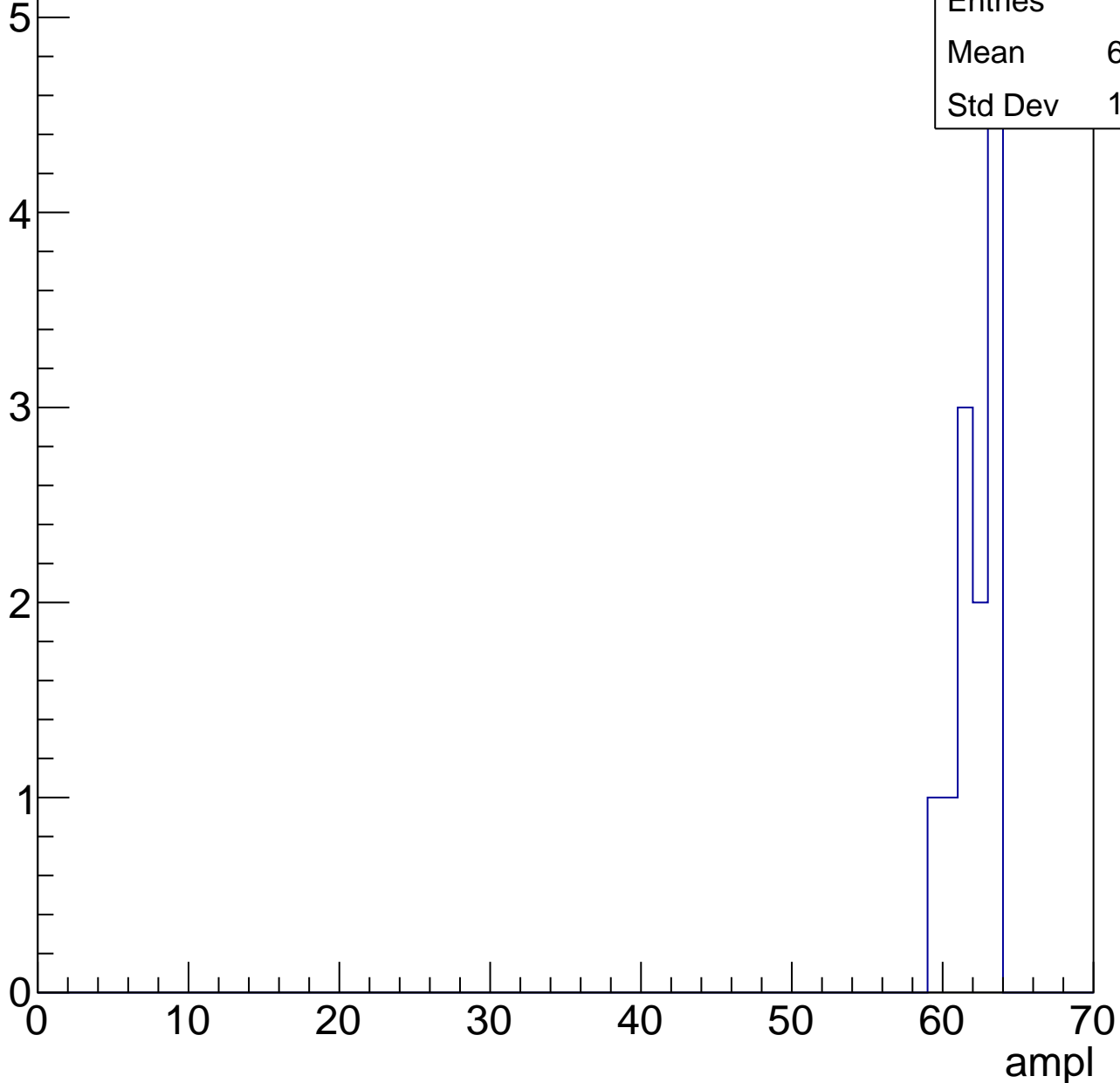


B1L001S, U16-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.299



B1L001S, U16-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch10, adc0

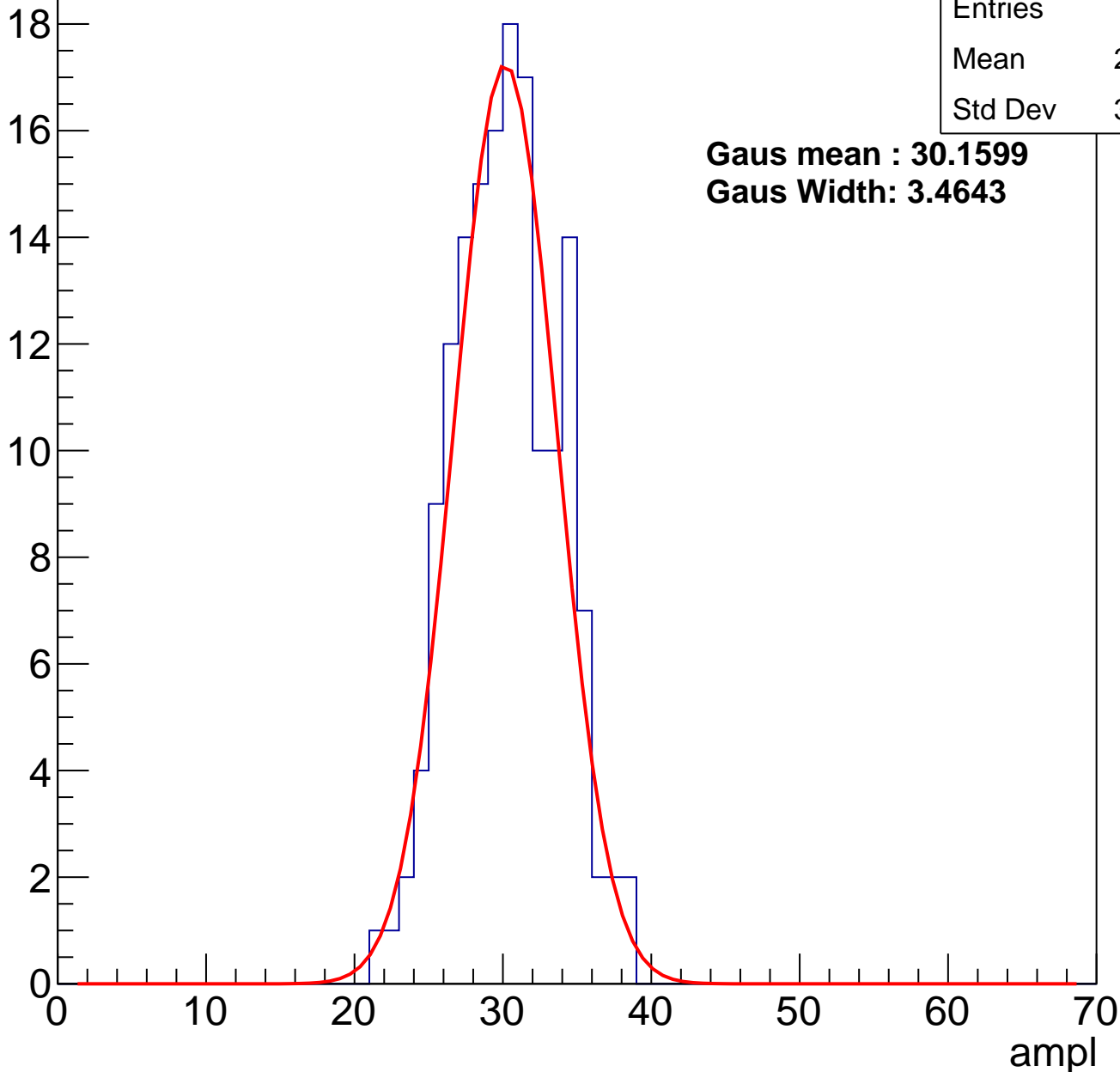
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	29.77
Std Dev	3.436

Gaus mean : 30.1599

Gaus Width: 3.4643

Entry



B1L001S, U16-ch10, adc1

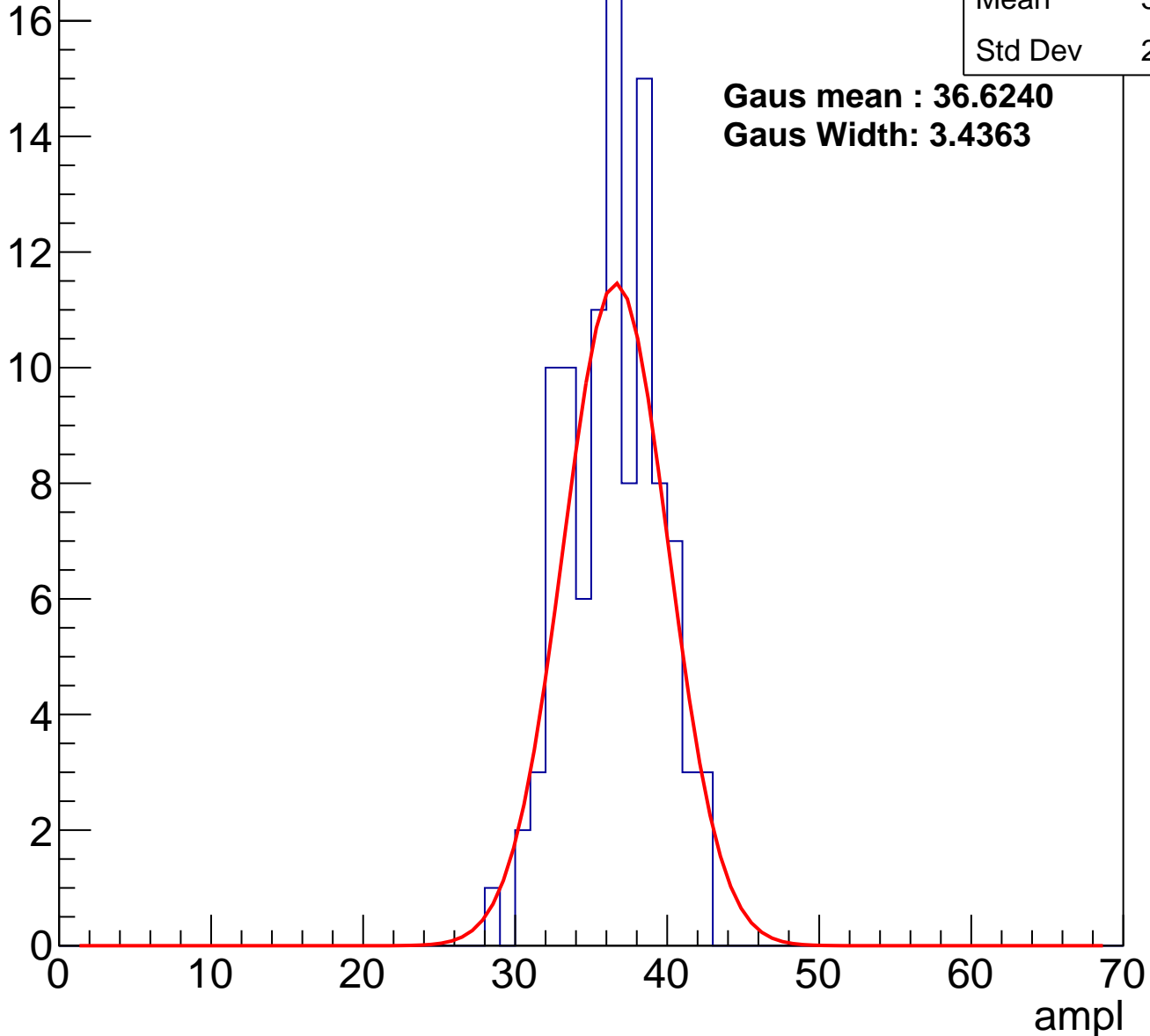
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	35.95
Std Dev	2.992

Gaus mean : 36.6240

Gaus Width: 3.4363

Entry



B1L001S, U16-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.11
Std Dev	3.236

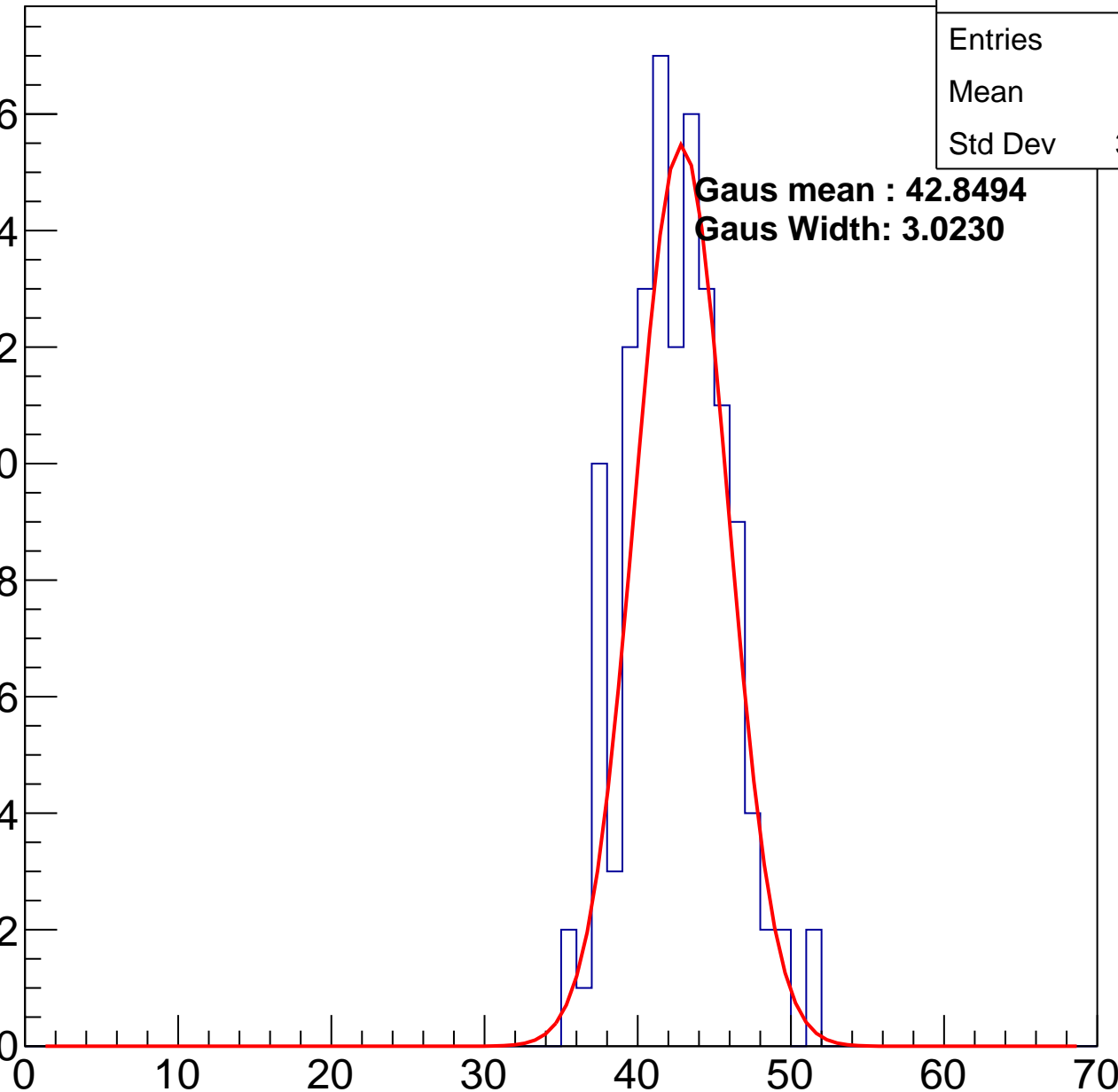
Gaus mean : 42.8494

Gaus Width: 3.0230

Entry

16
14
12
10
8
6
4
2
0

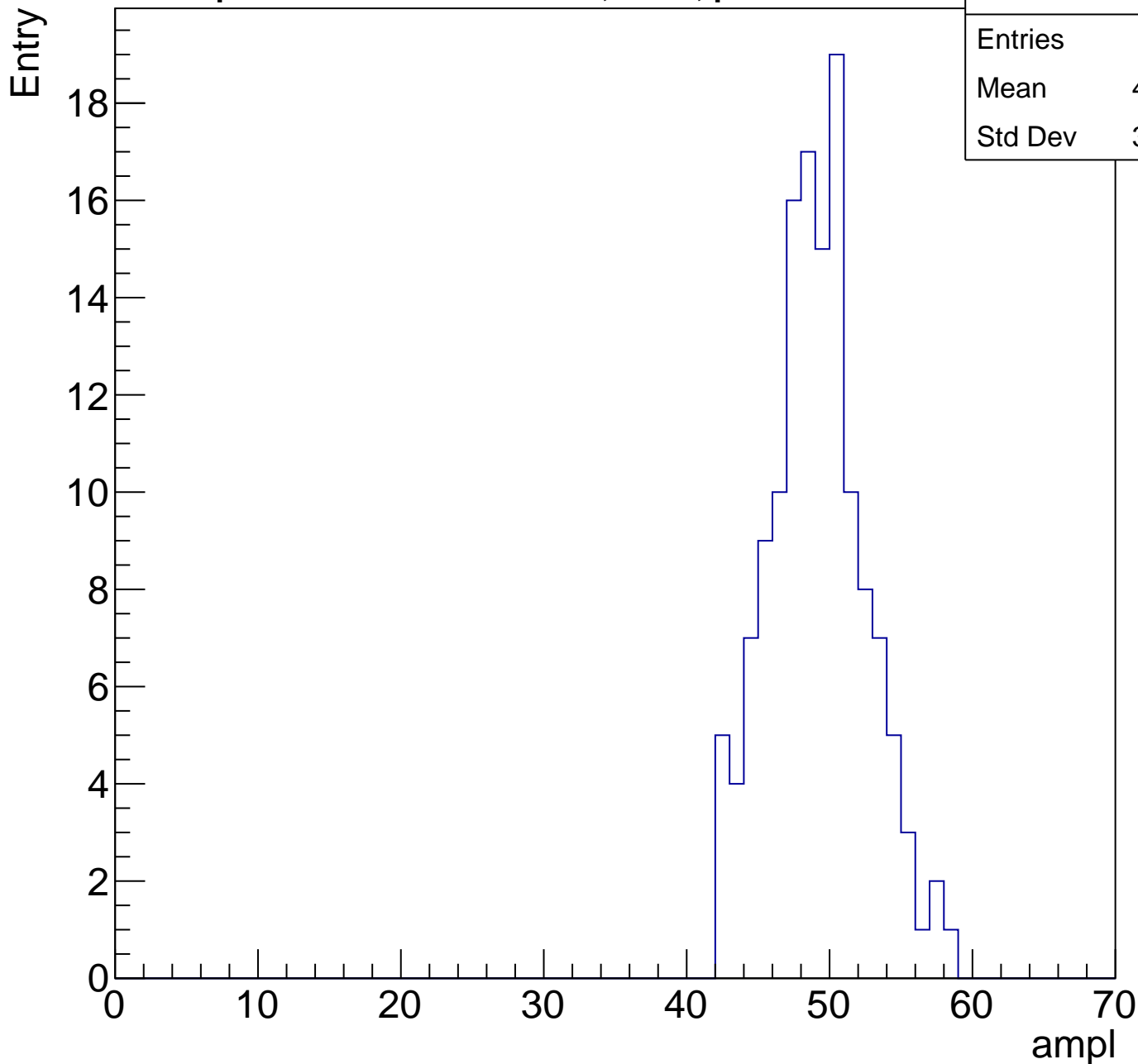
ampl



B1L001S, U16-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.69
Std Dev	3.392



B1L001S, U16-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

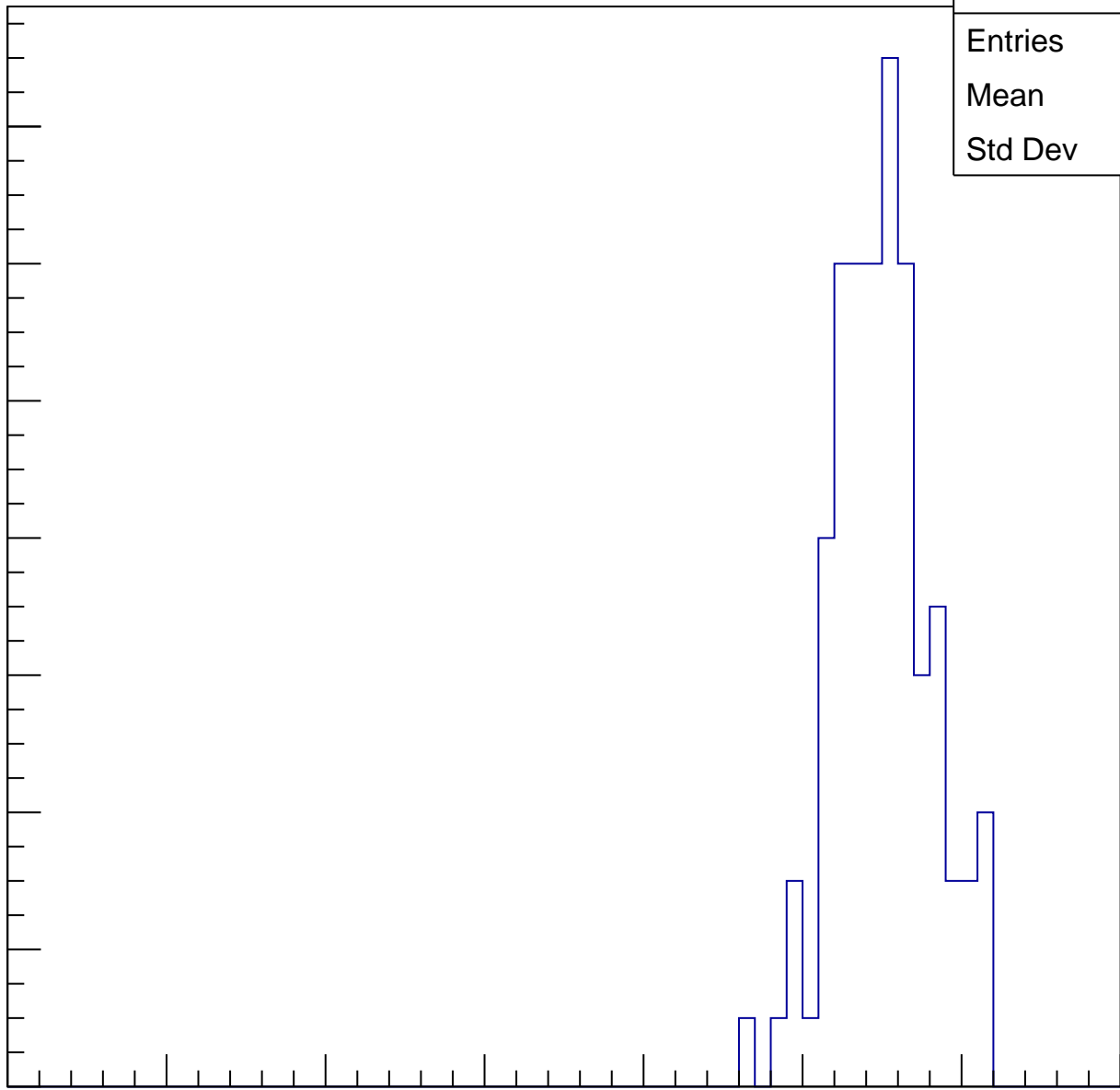
2

0

Entries	100
Mean	54.53
Std Dev	3.005

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch10, adc5

calib_packv5_042523_0143.root, FC#2, port C2

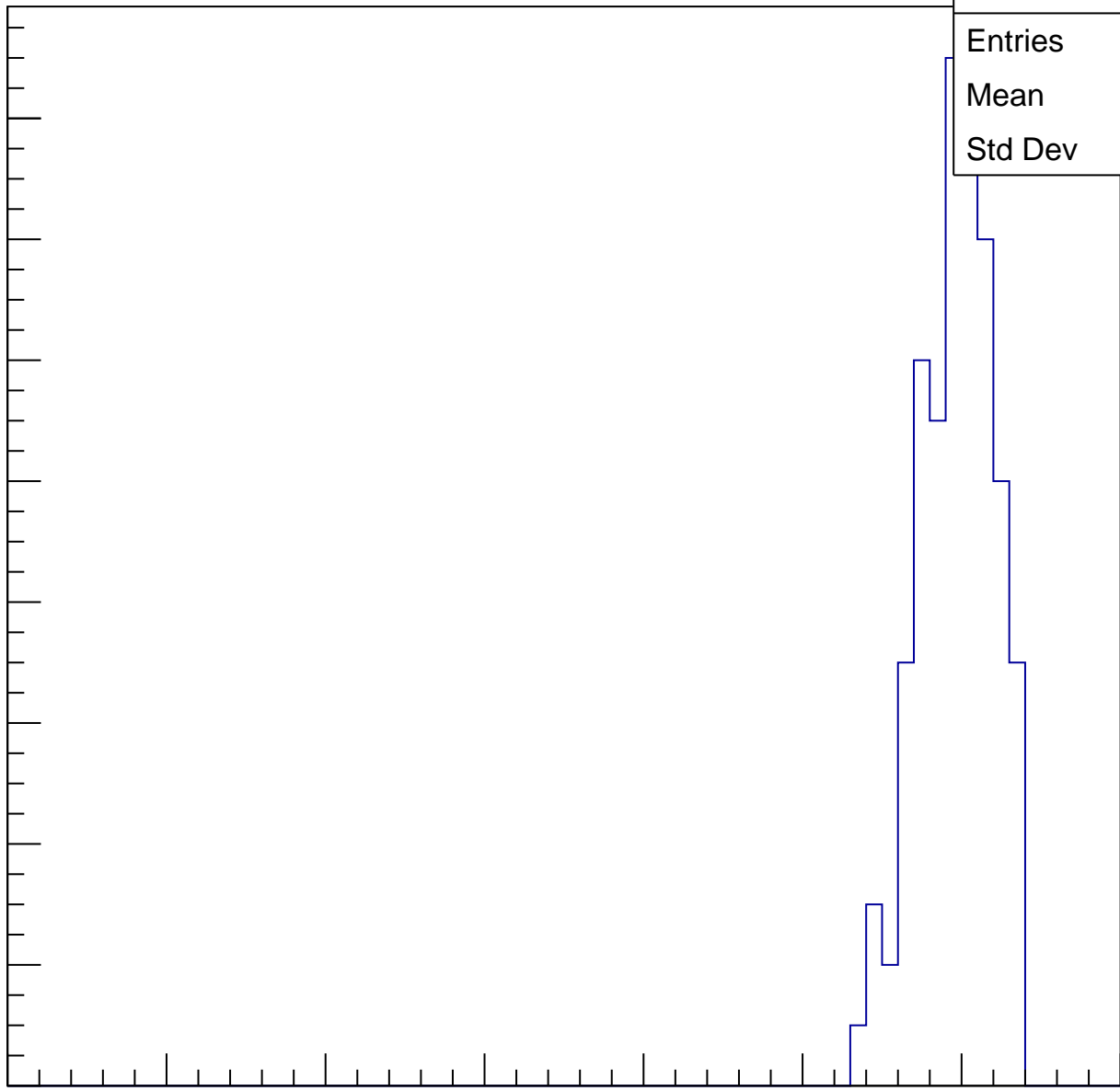
Entries	100
Mean	59.17
Std Dev	2.315

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	28
Mean	57.71
Std Dev	16.04

0

B1L001S, U16-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

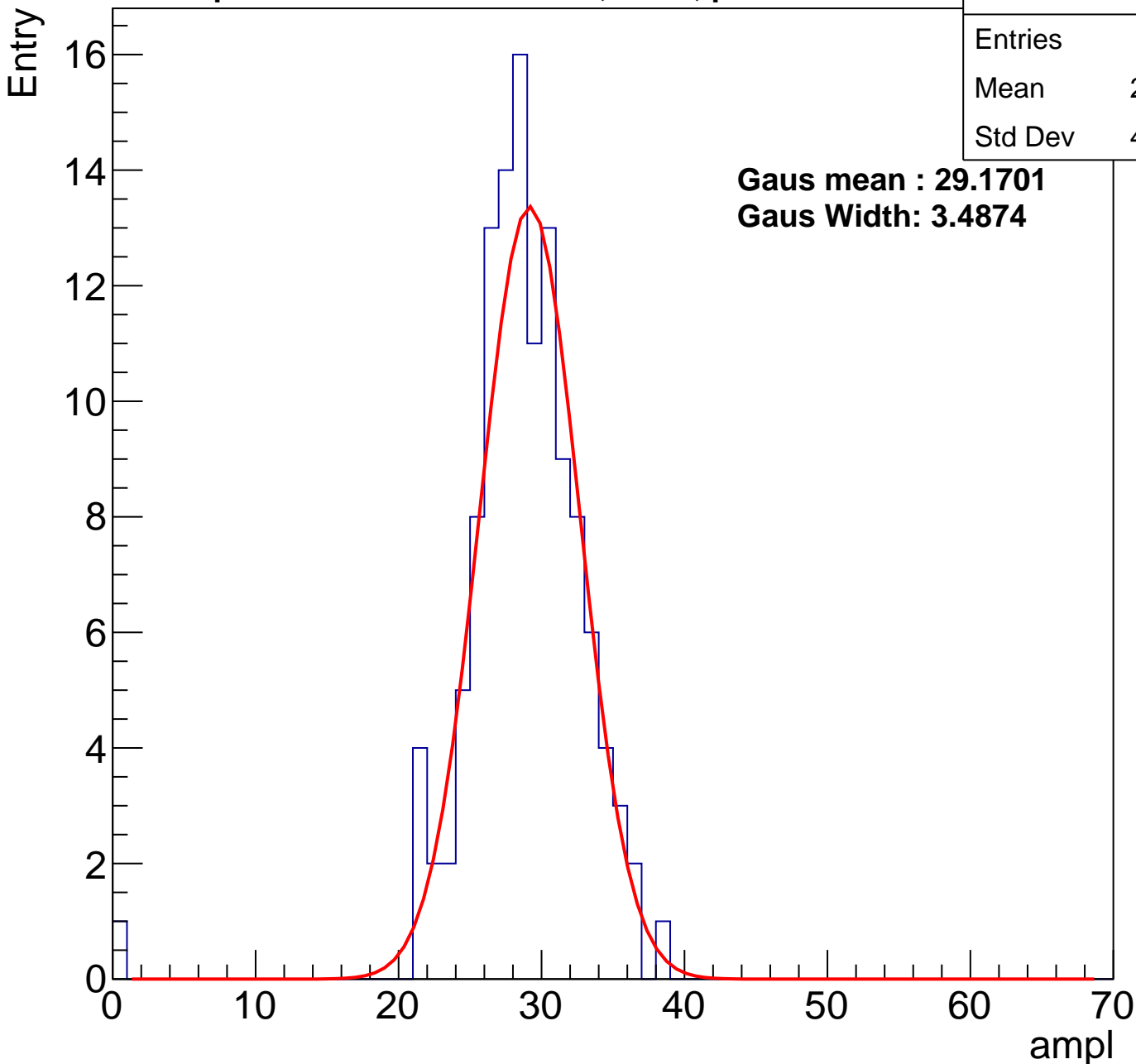
B1L001S, U16-ch11, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.29
Std Dev	4.295

Gaus mean : 29.1701

Gaus Width: 3.4874

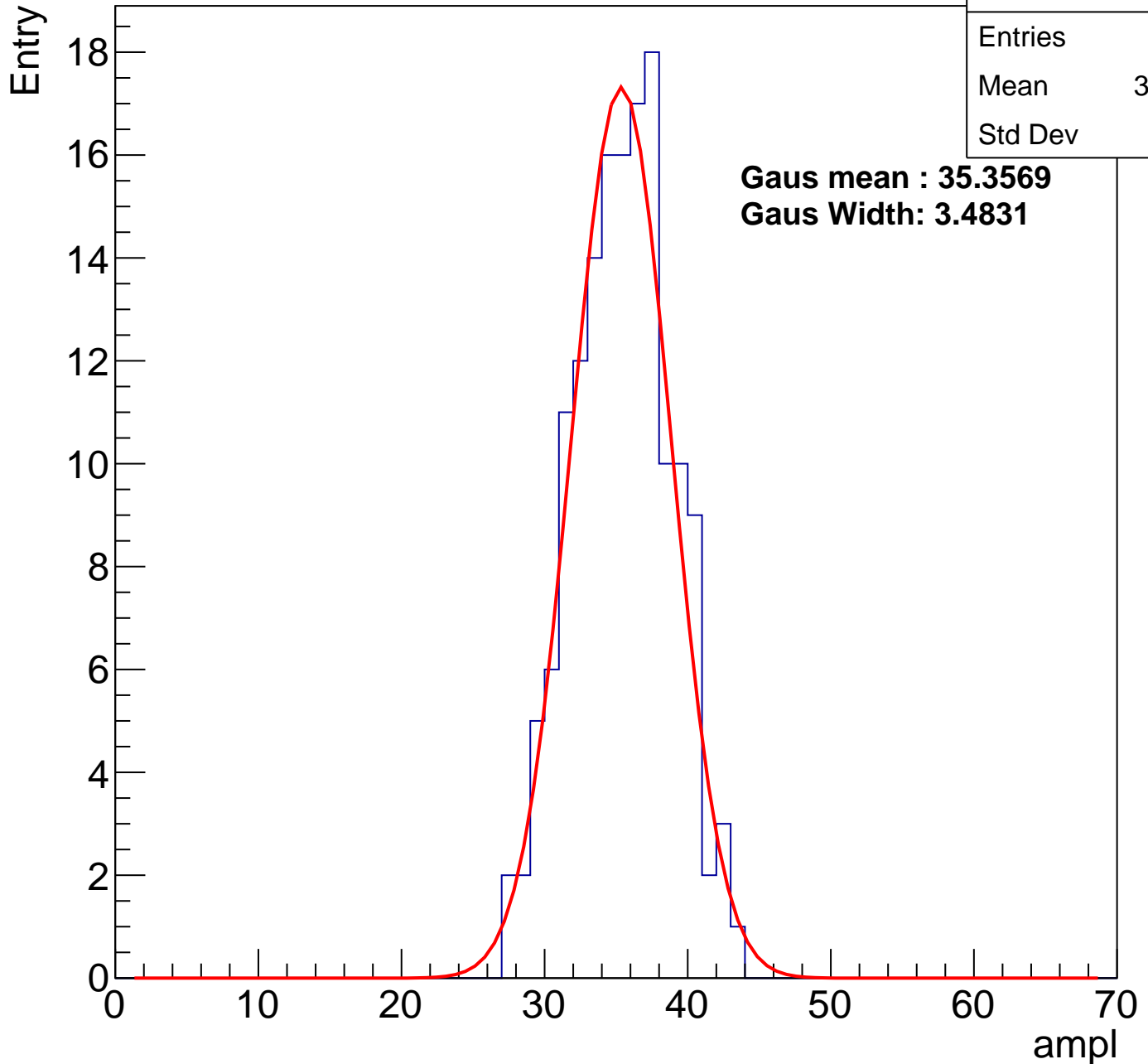


B1L001S, U16-ch11, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	34.97
Std Dev	3.38

Gaus mean : 35.3569
Gaus Width: 3.4831



B1L001S, U16-ch11, adc2

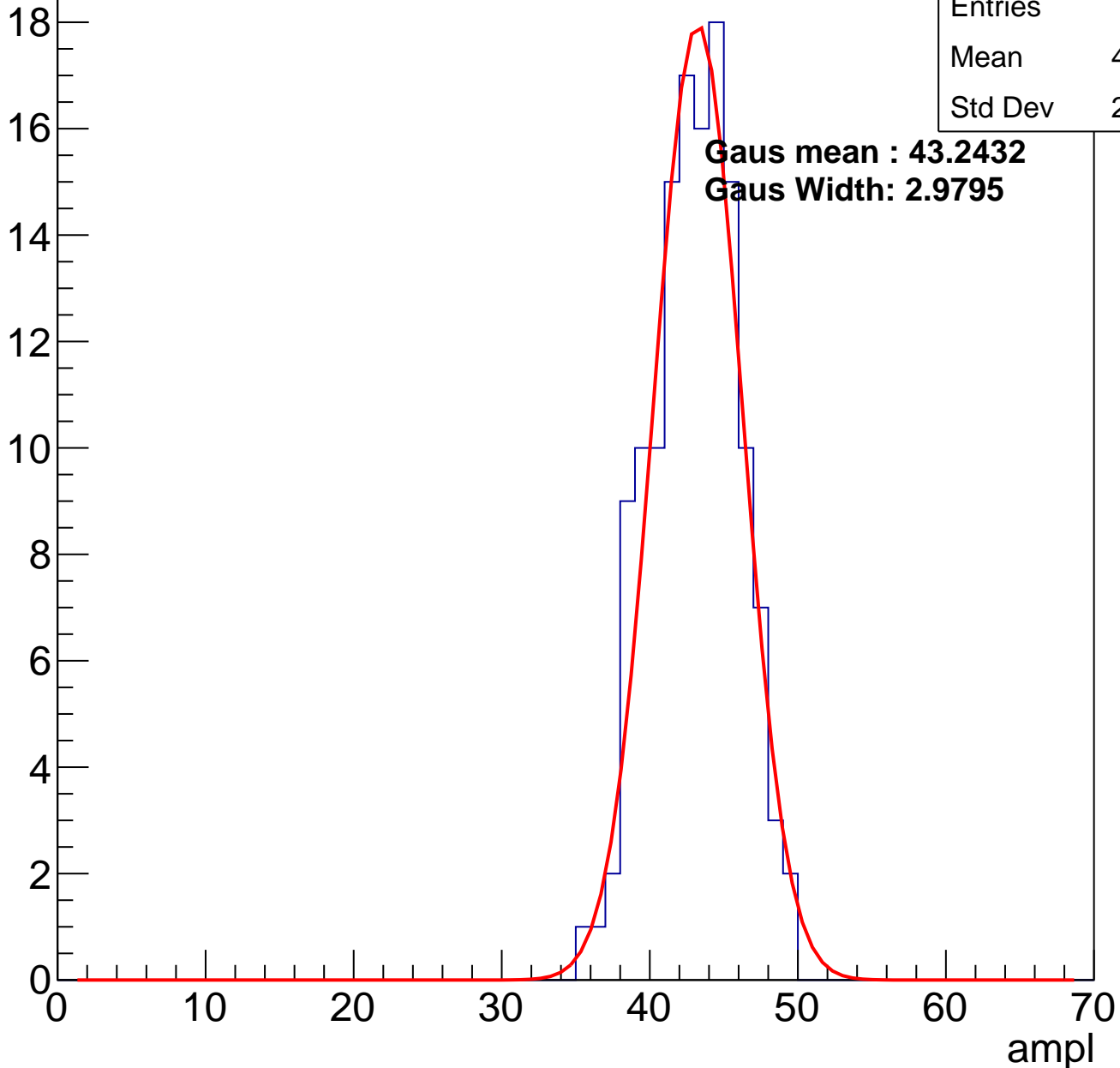
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	42.59
Std Dev	2.896

Gaus mean : 43.2432

Gaus Width: 2.9795

Entry



B1L001S, U16-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

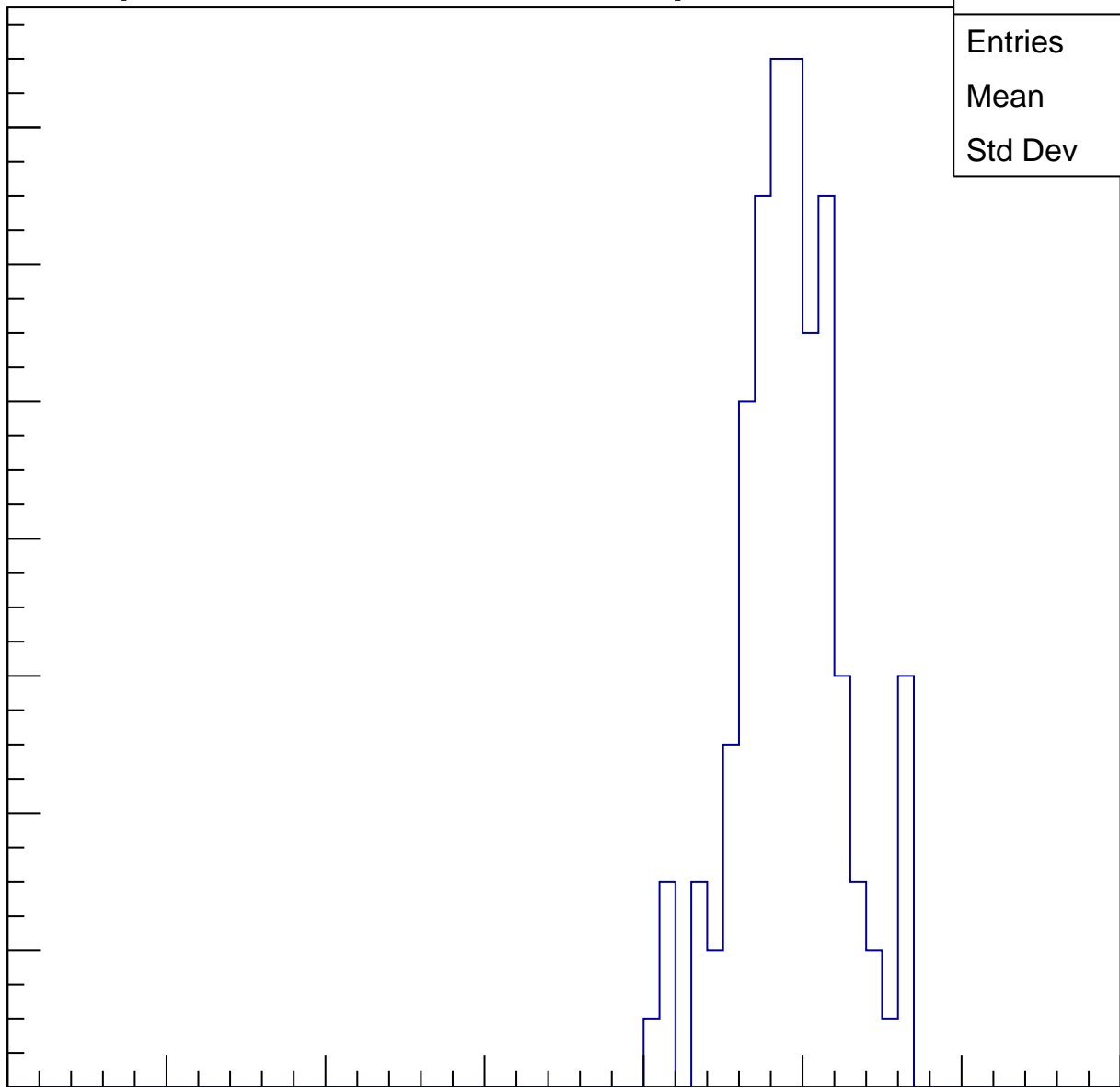
Entries	109
Mean	48.75
Std Dev	3.327

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

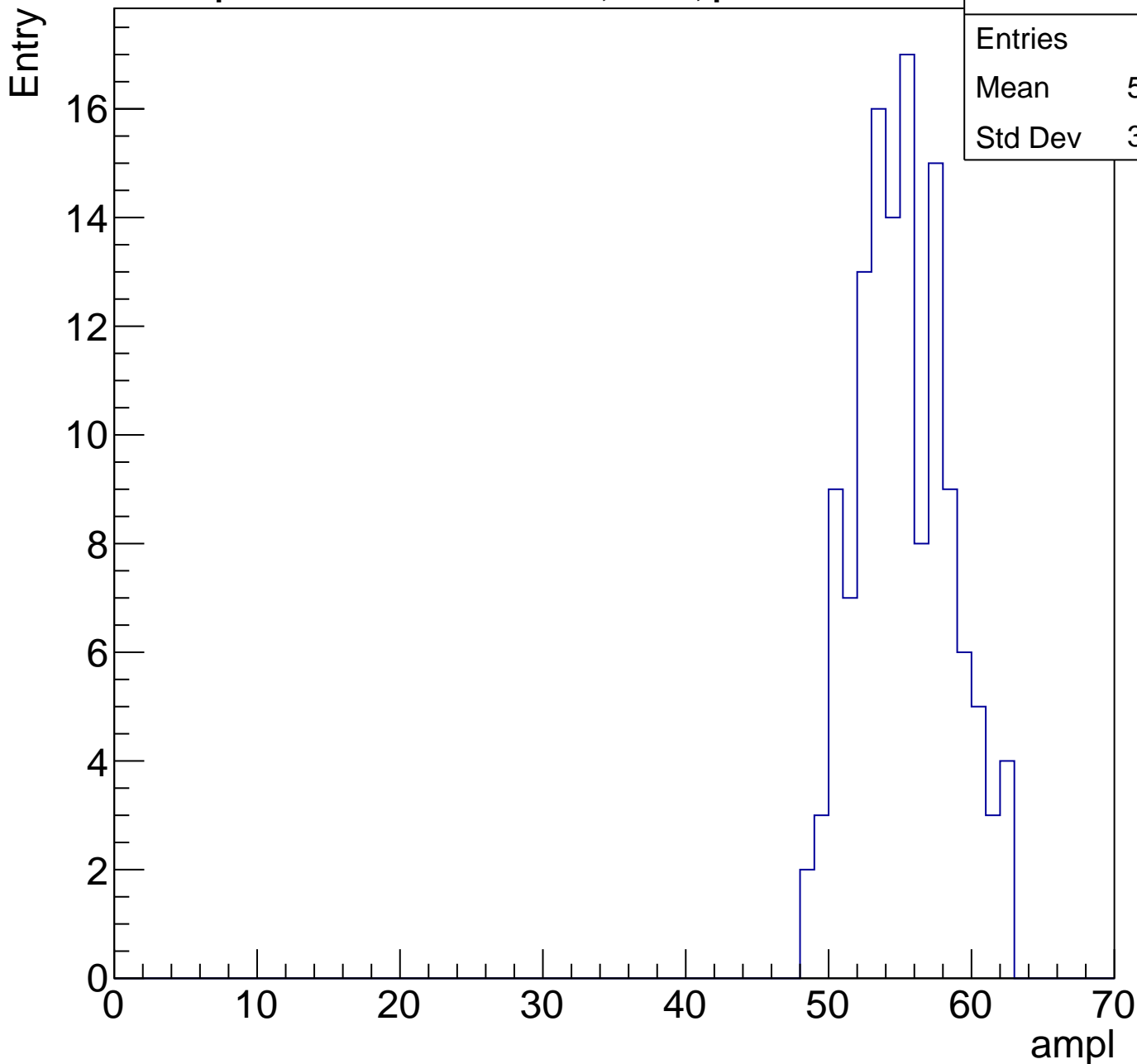
ampl



B1L001S, U16-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	54.77
Std Dev	3.275



B1L001S, U16-ch11, adc5

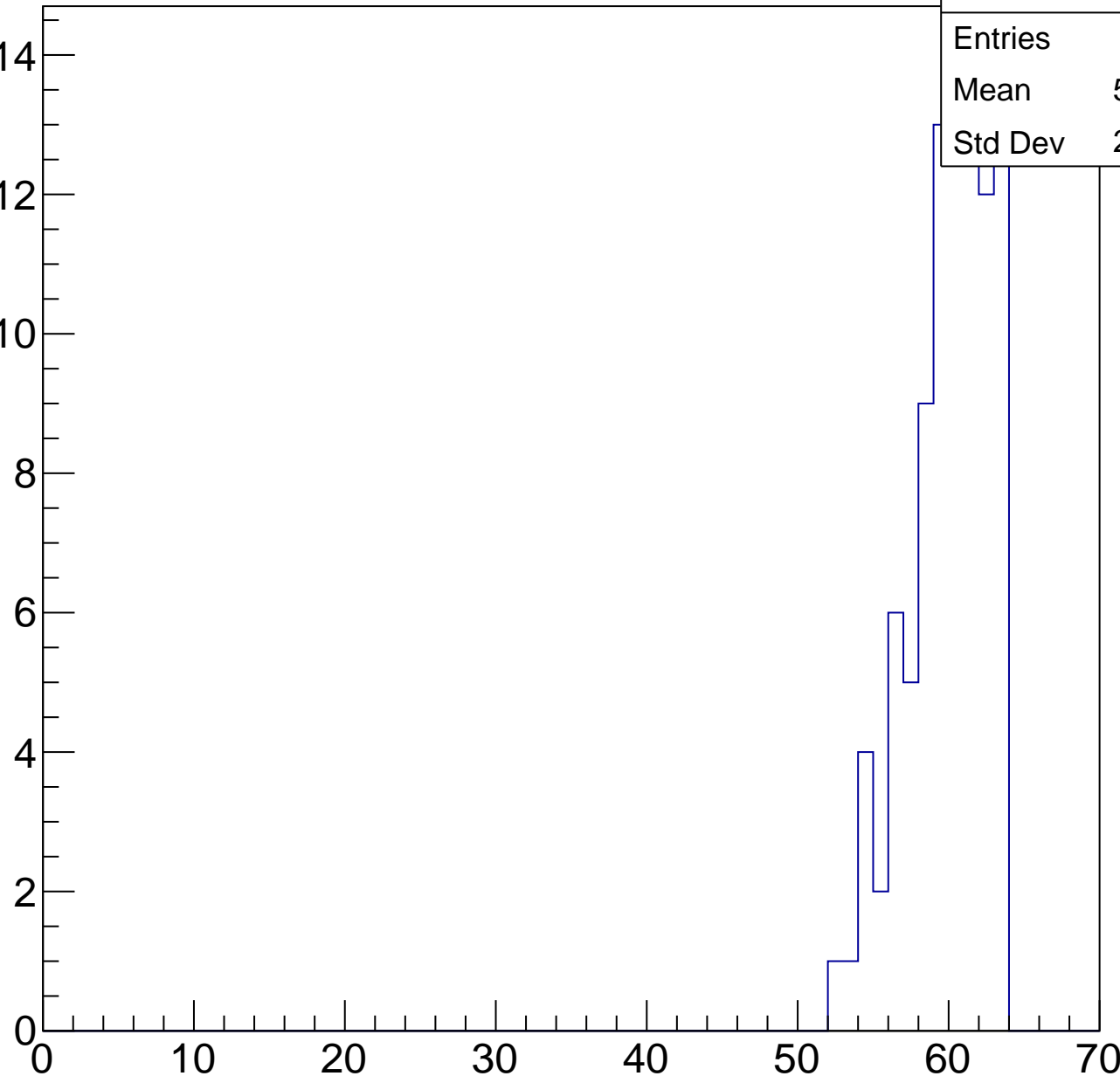
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	59.59
Std Dev	2.667

ampl

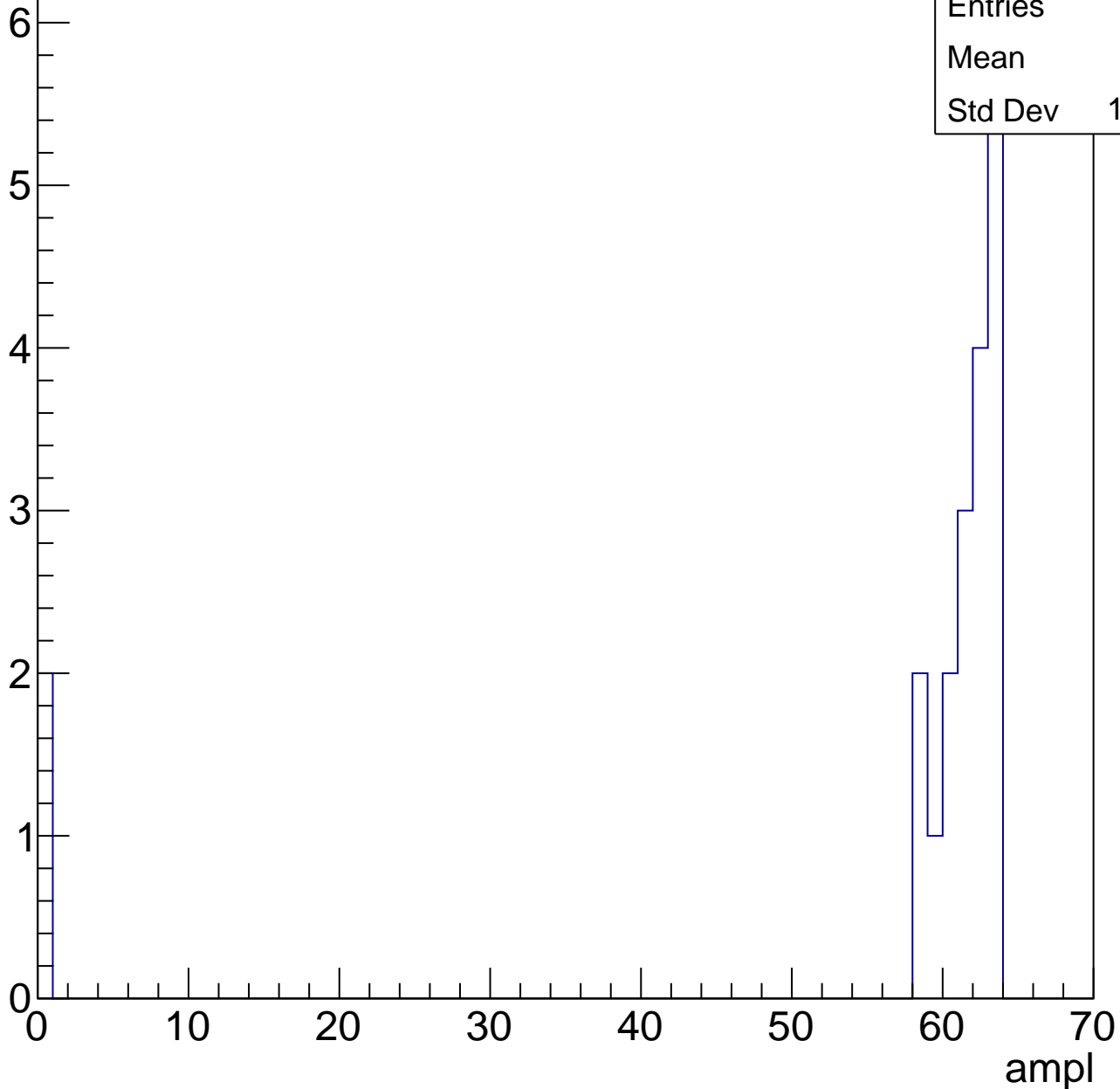


B1L001S, U16-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	55.2
Std Dev	18.47



B1L001S, U16-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch12, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.93
Std Dev	3.153

Gaus mean : 30.1987

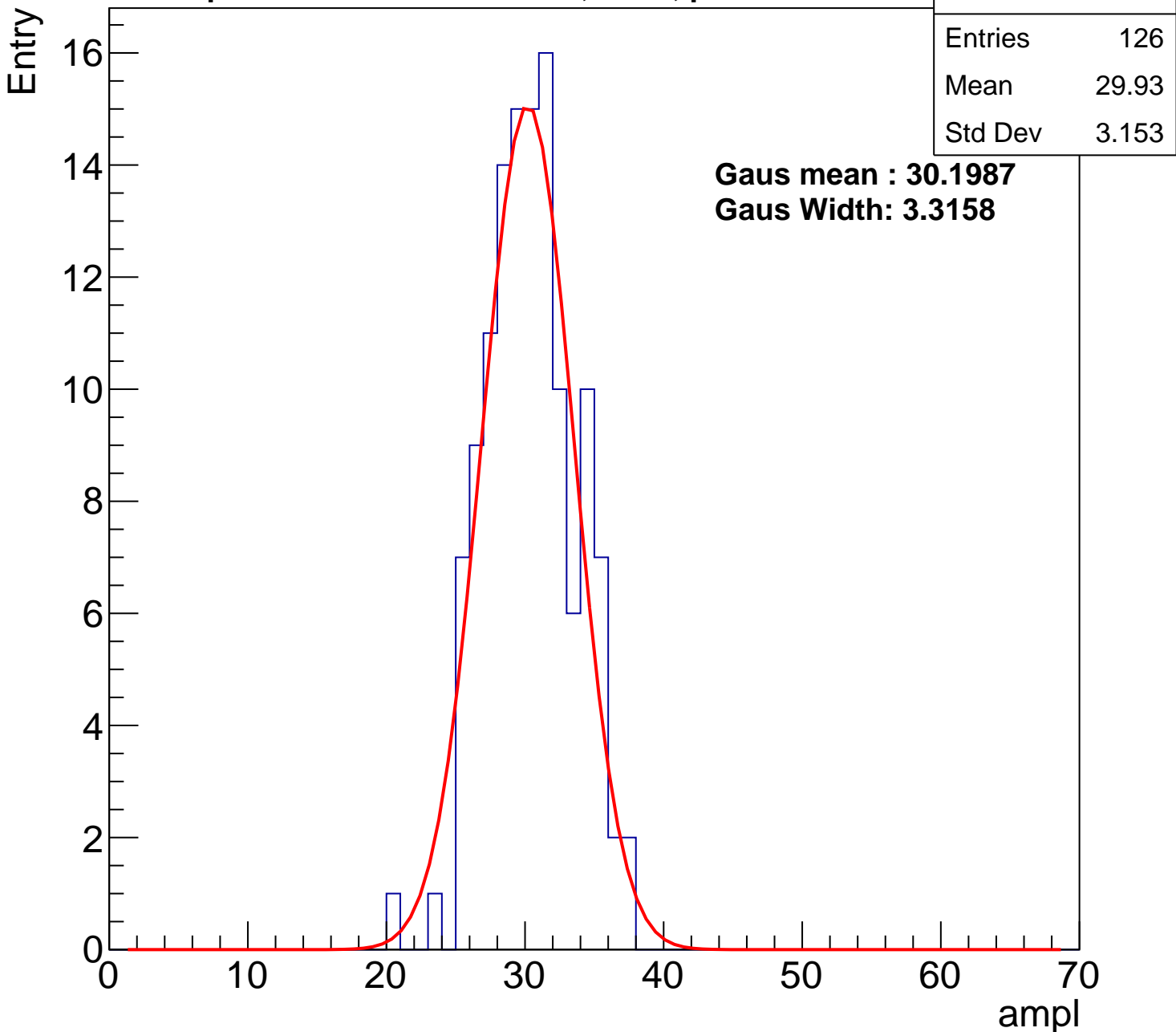
Gaus Width: 3.3158

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch12, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	36.03
Std Dev	3.228

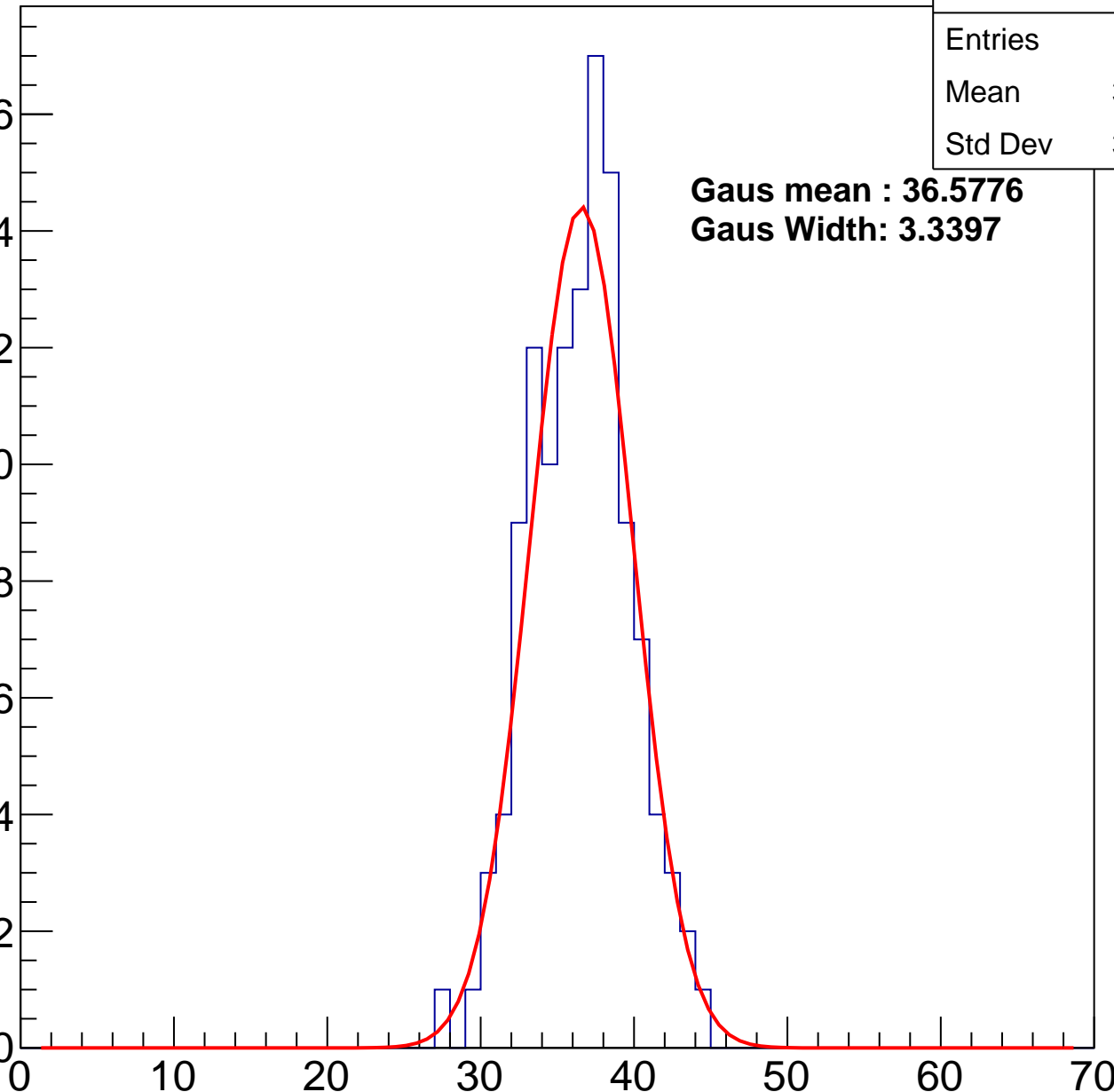
Gaus mean : 36.5776

Gaus Width: 3.3397

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	42.93
Std Dev	3.467

Gaus mean : 43.7881

Gaus Width: 3.3501

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

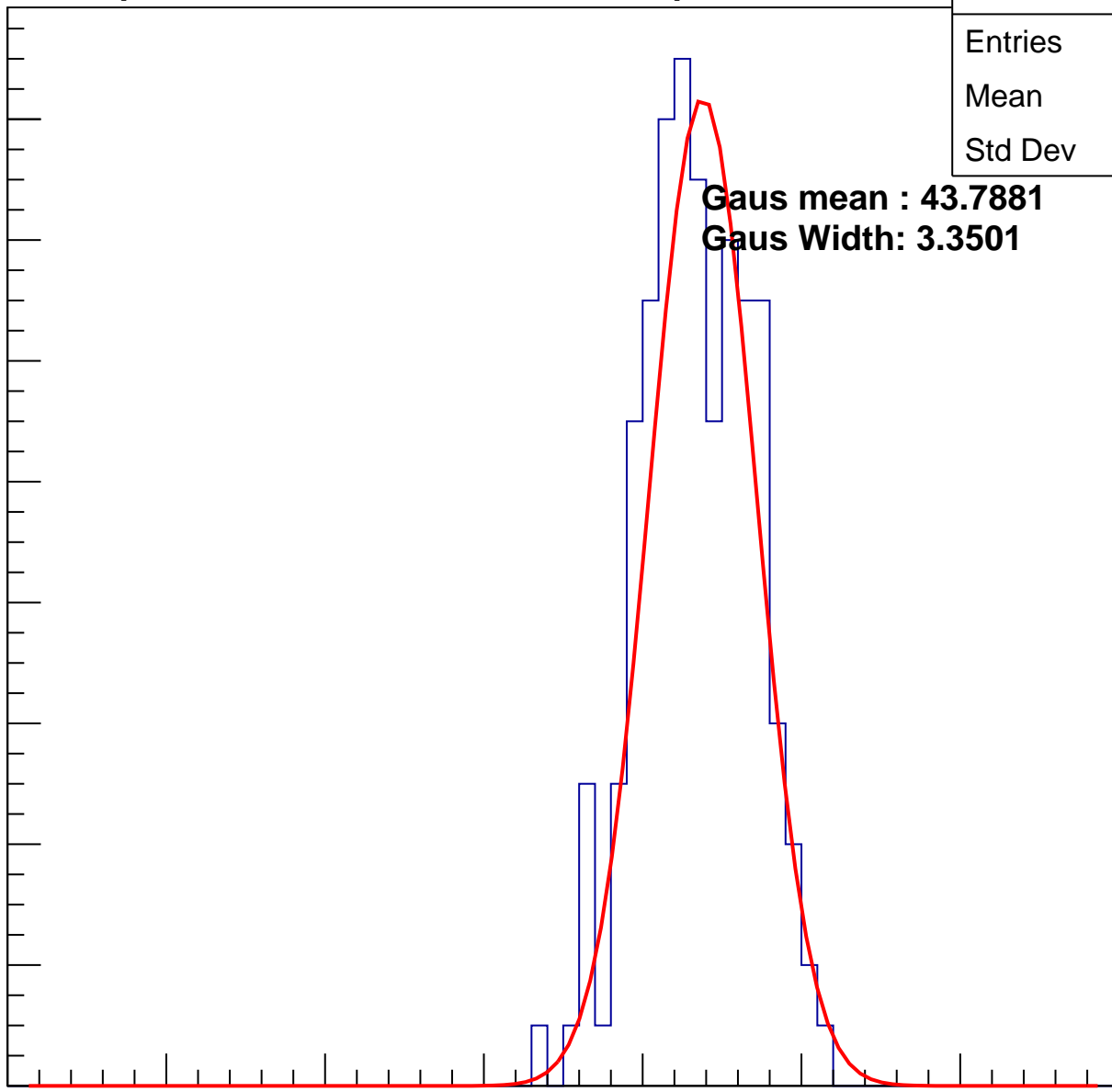
40

50

60

70

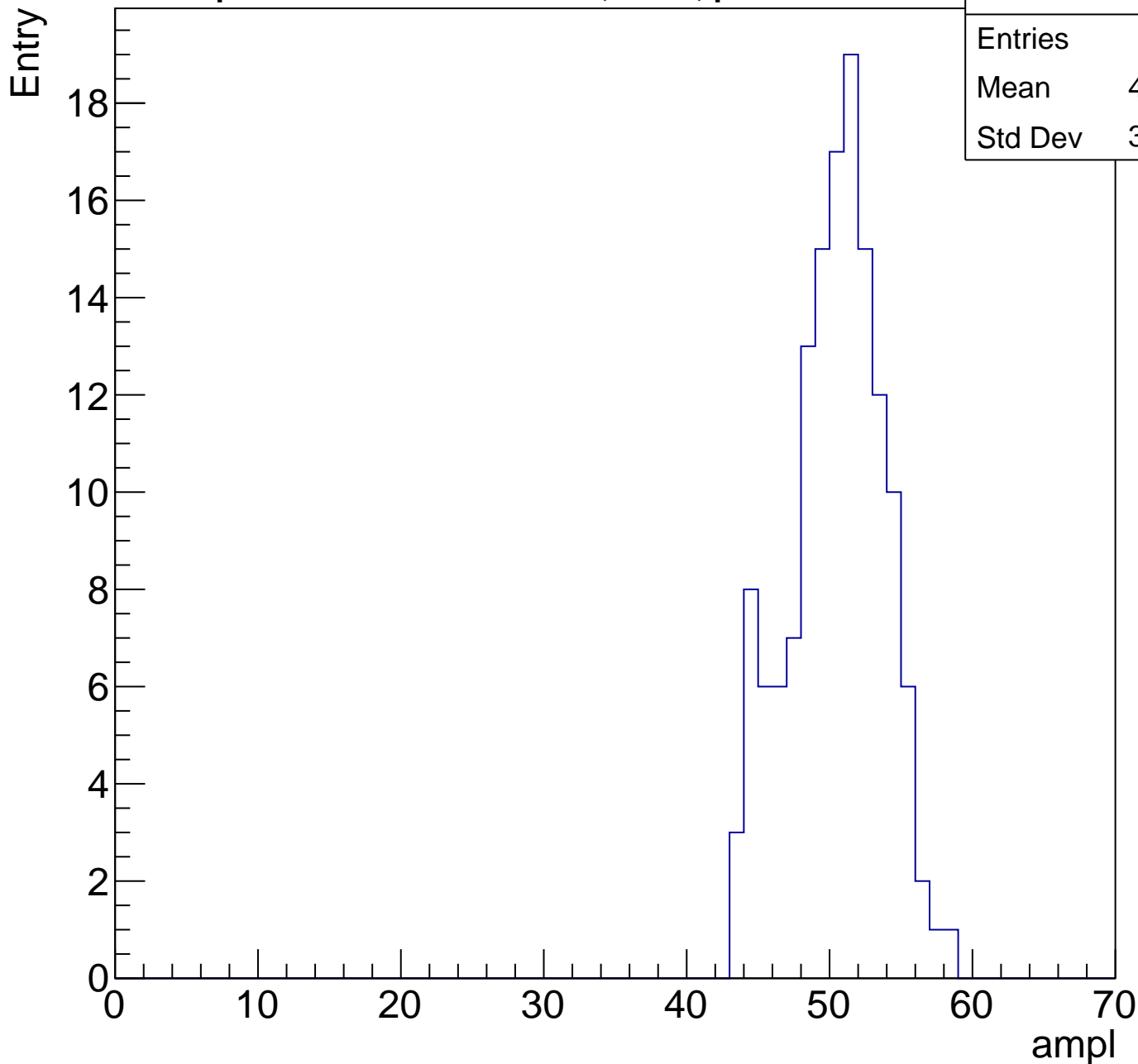
ampl



B1L001S, U16-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.98
Std Dev	3.254



B1L001S, U16-ch12, adc4

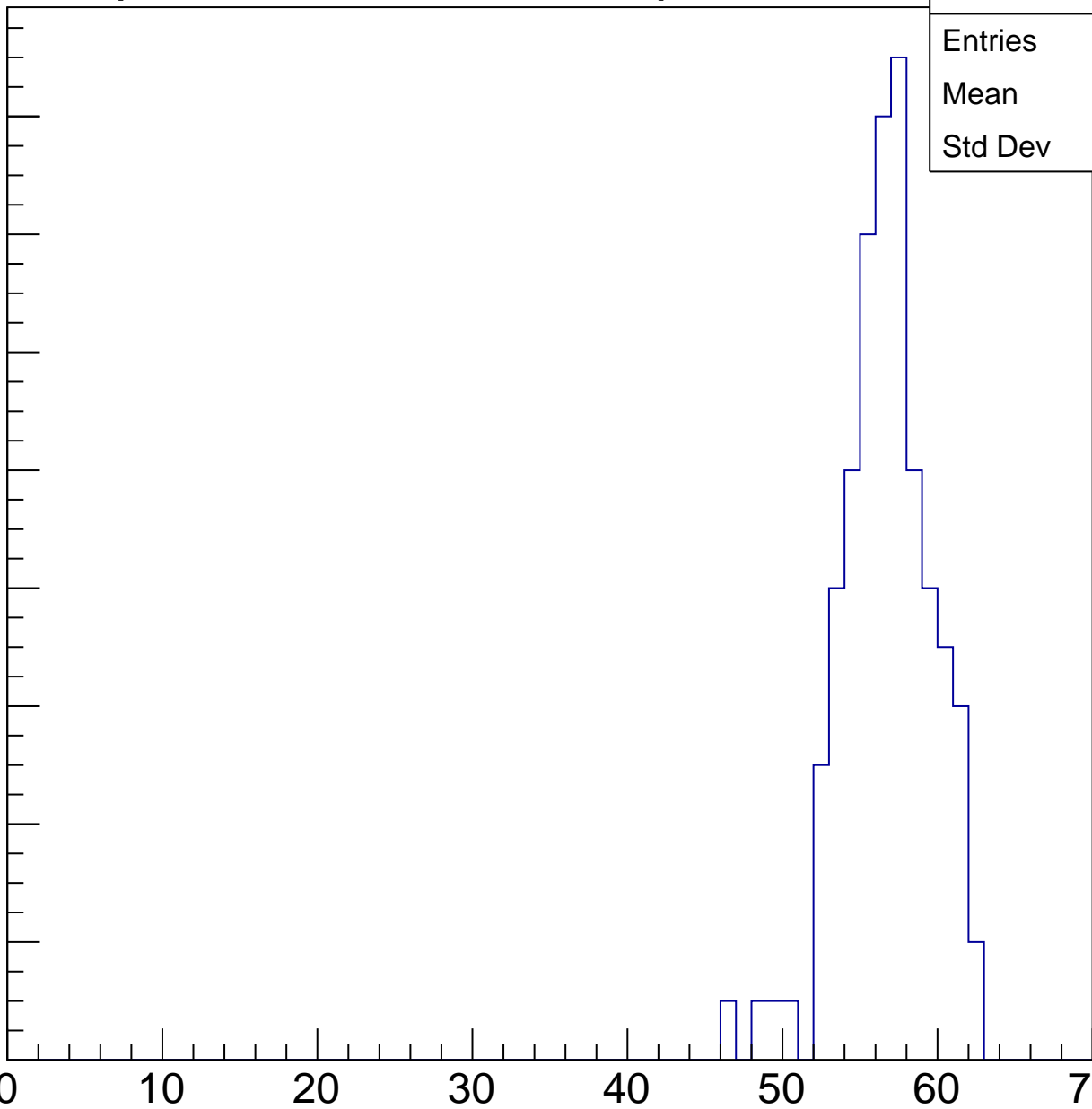
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	56.21
Std Dev	2.922

ampl



B1L001S, U16-ch12, adc5

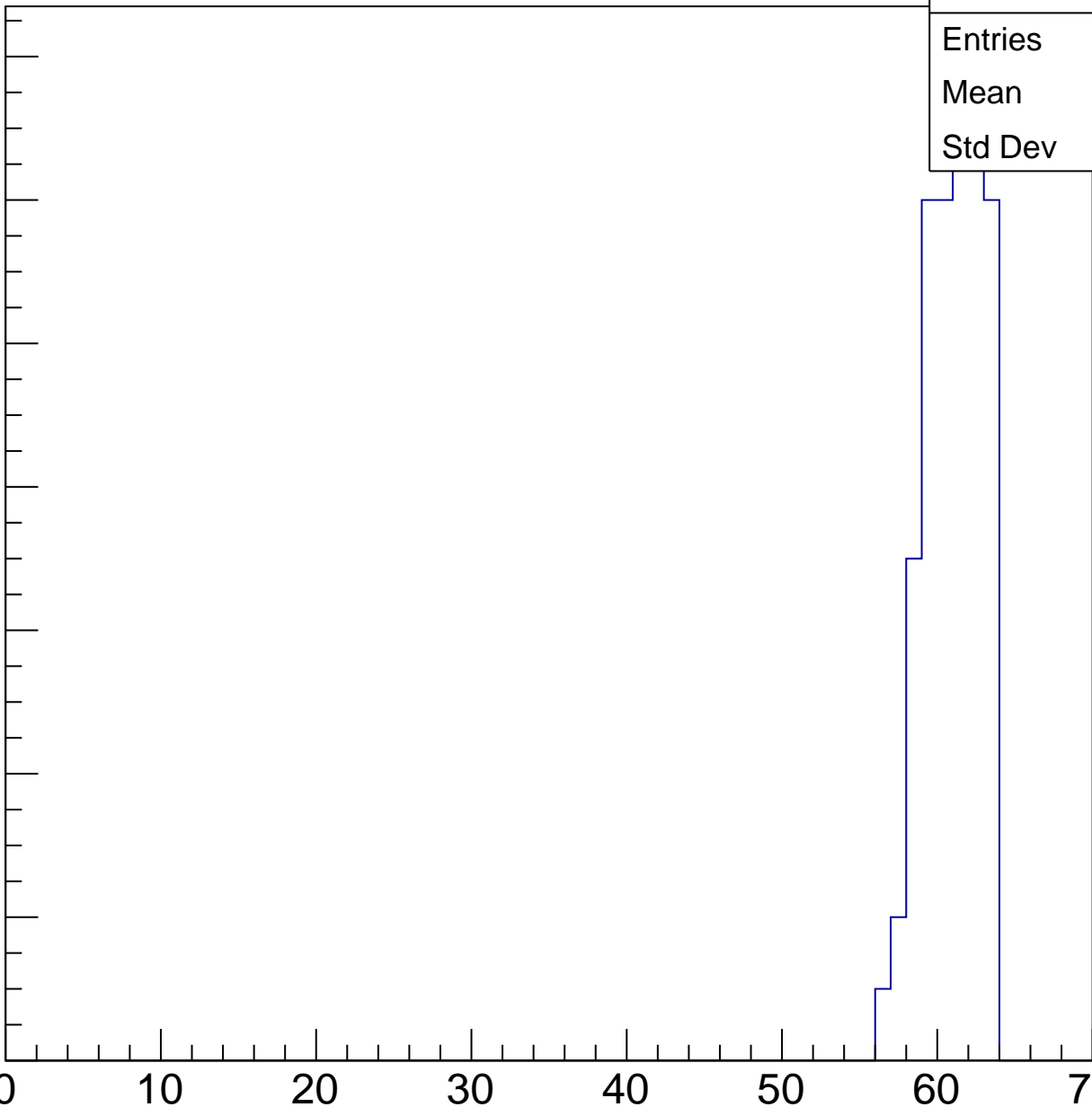
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	60.56
Std Dev	1.767

ampl

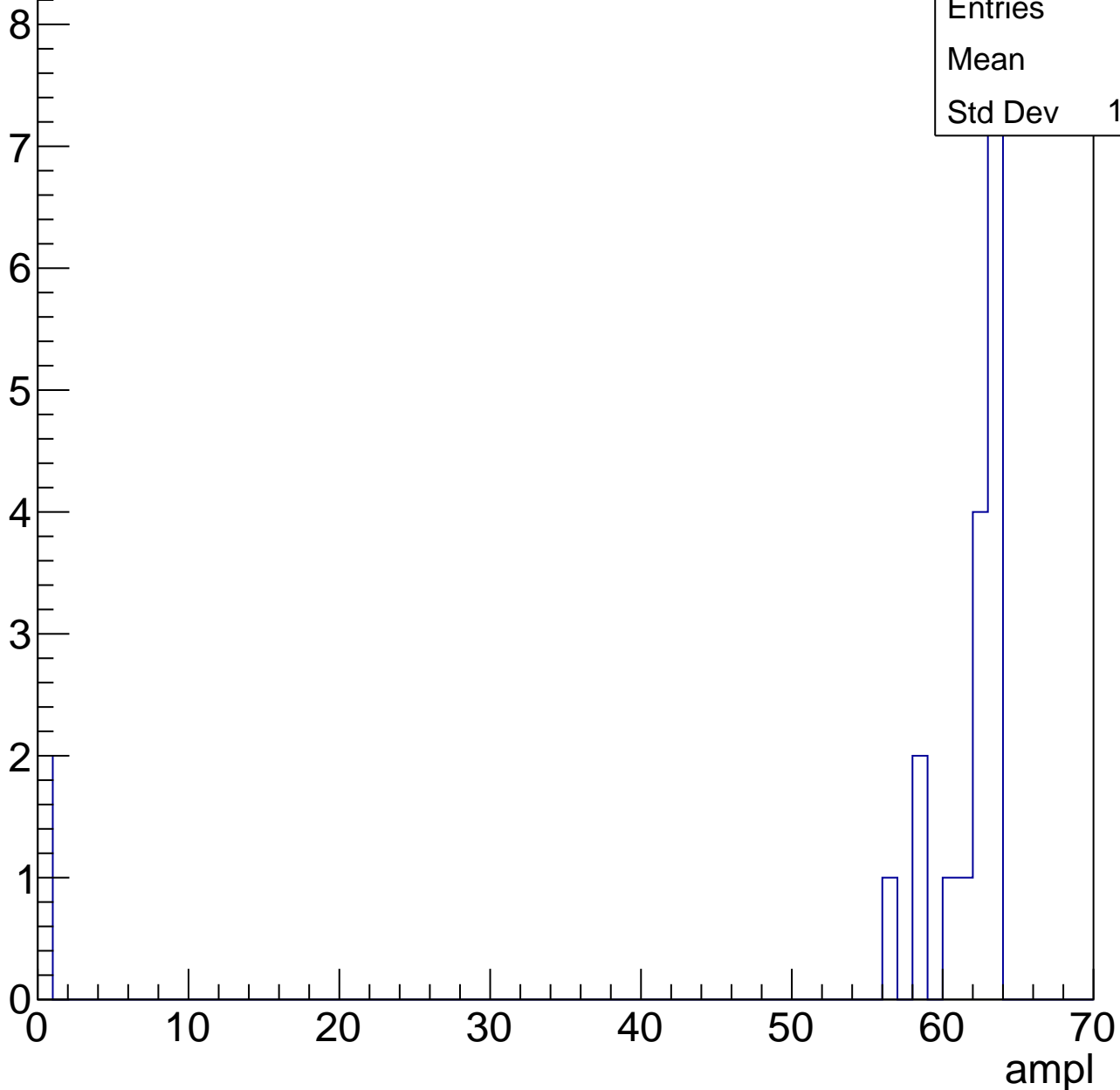


B1L001S, U16-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	55
Std Dev	18.97



B1L001S, U16-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L001S, U16-ch13, adc0

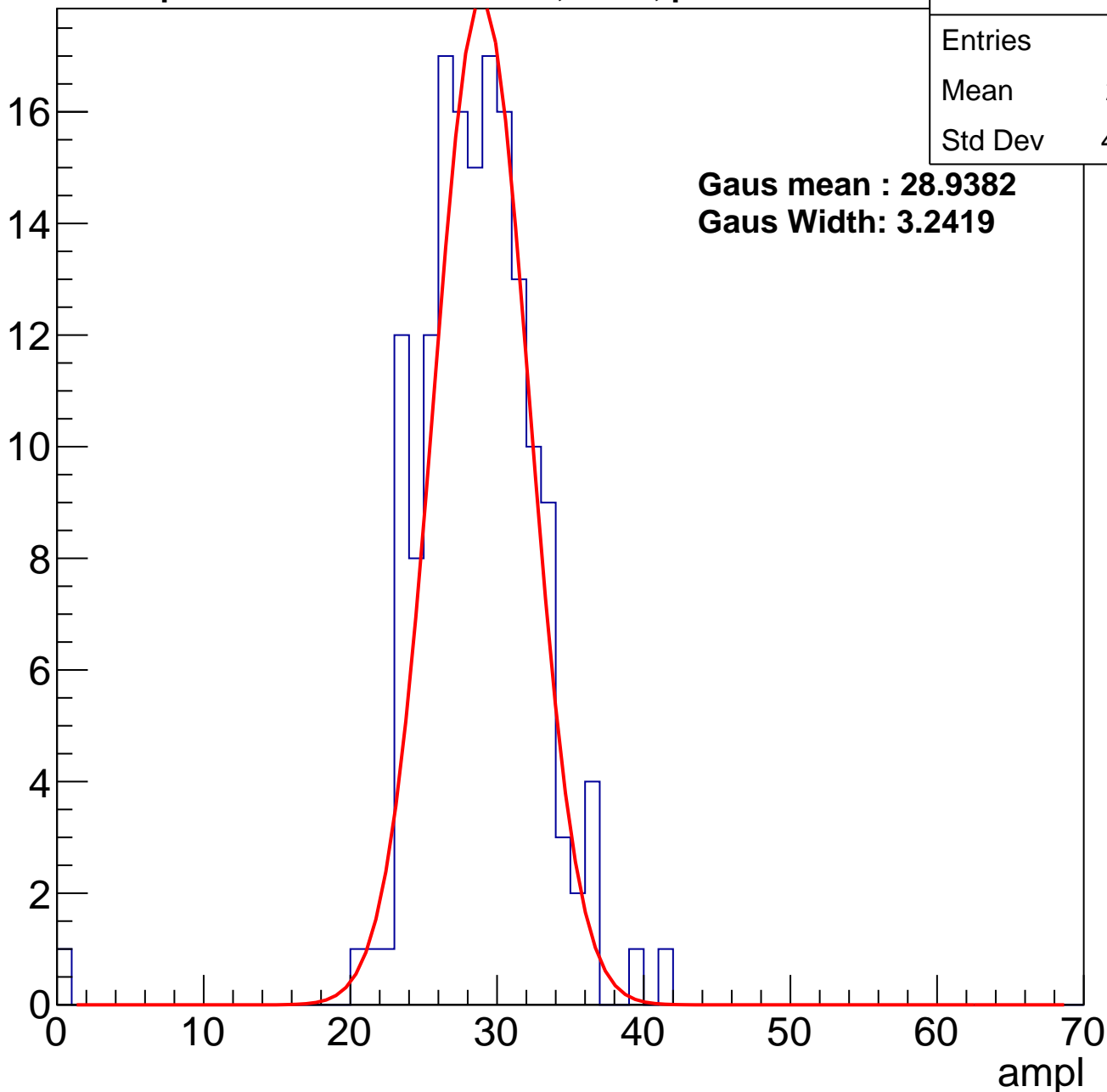
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	28.21
Std Dev	4.243

Gaus mean : 28.9382

Gaus Width: 3.2419

Entry



B1L001S, U16-ch13, adc1

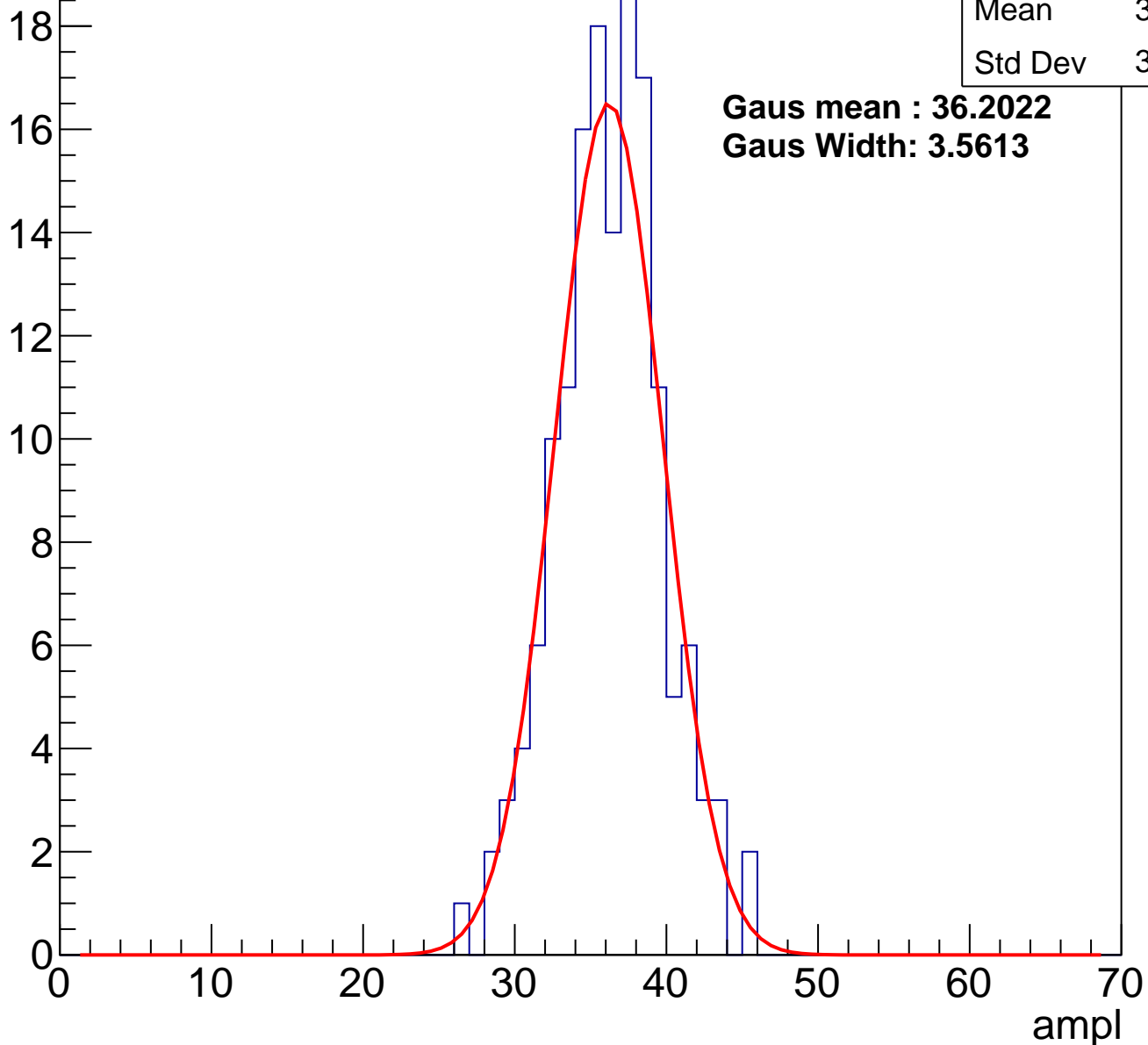
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.79
Std Dev	3.488

Gaus mean : 36.2022

Gaus Width: 3.5613

Entry



B1L001S, U16-ch13, adc2

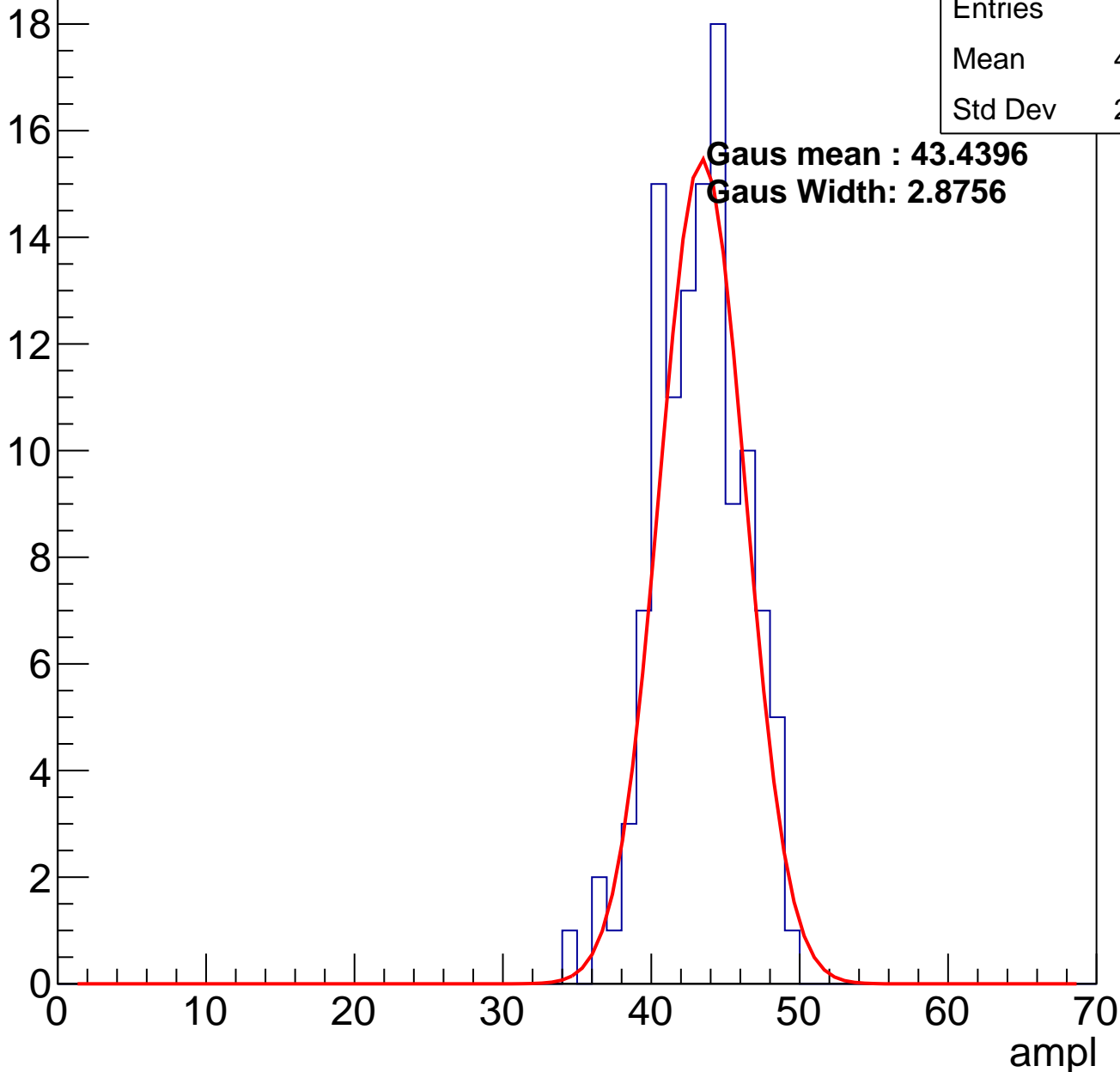
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	118
Mean	42.77
Std Dev	2.918

Gaus mean : 43.4396

Gaus Width: 2.8756

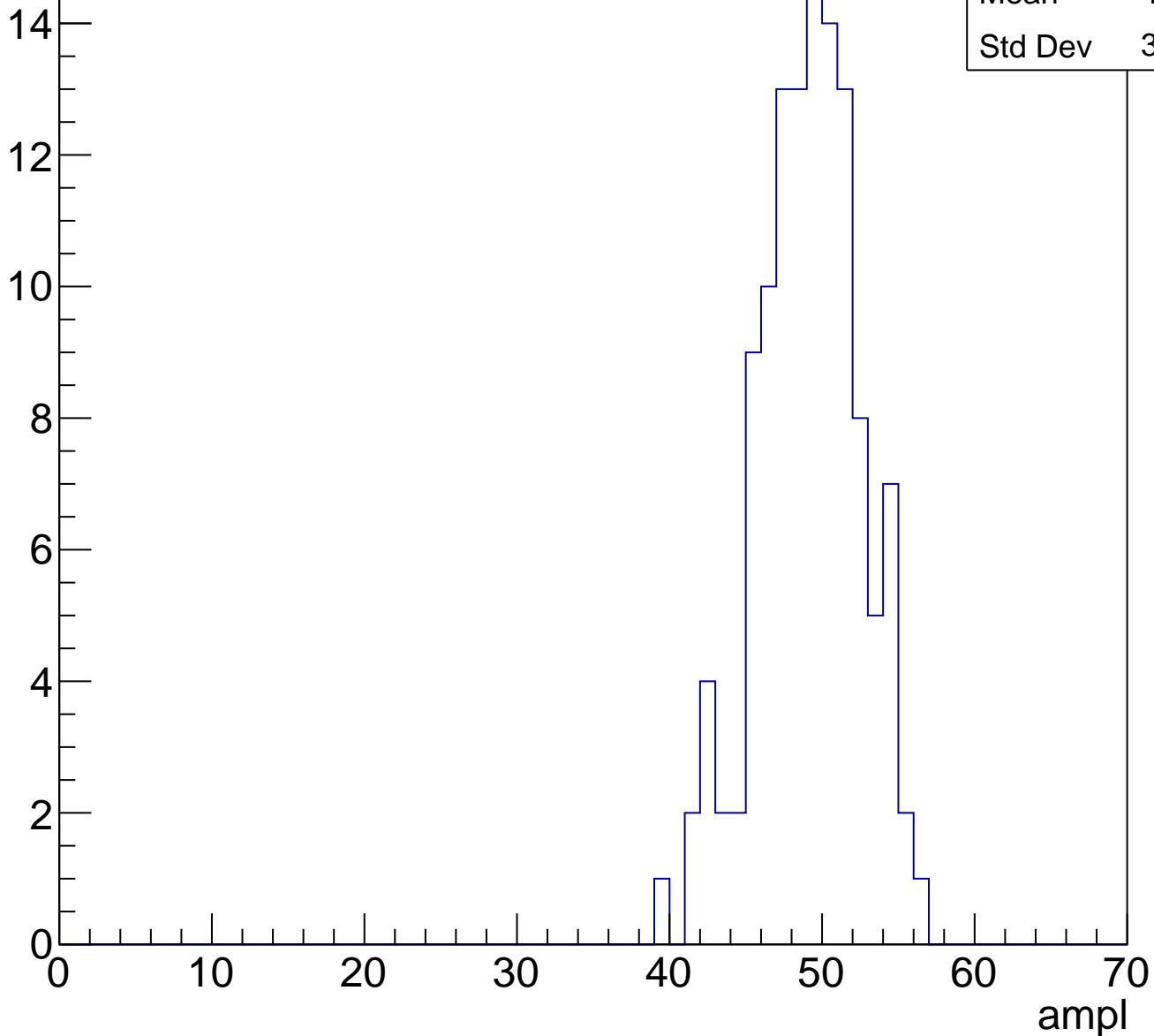


B1L001S, U16-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	48.64
Std Dev	3.332

Entry



B1L001S, U16-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	54.74
Std Dev	3.103

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

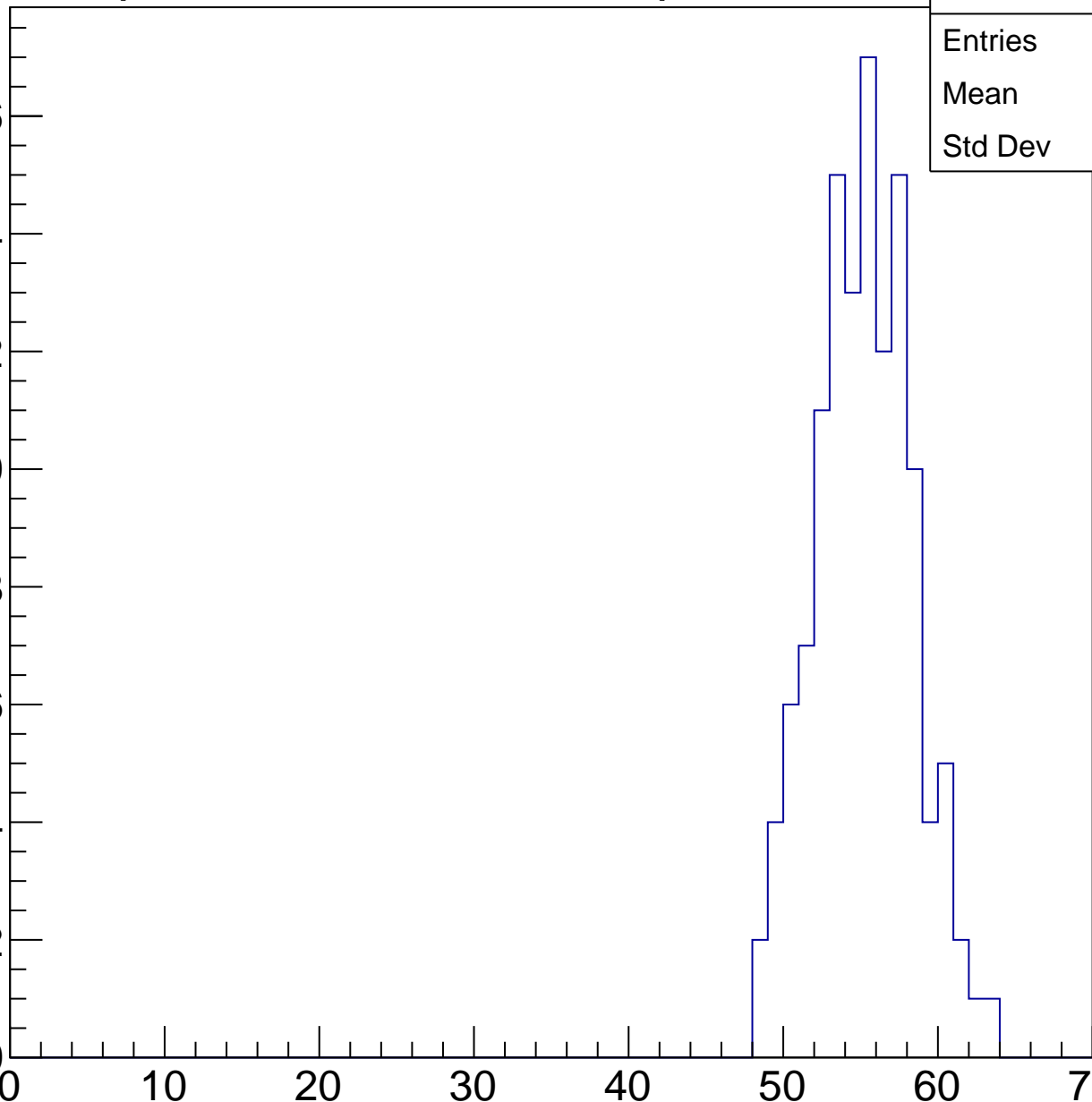
40

50

60

70

ampl



B1L001S, U16-ch13, adc5

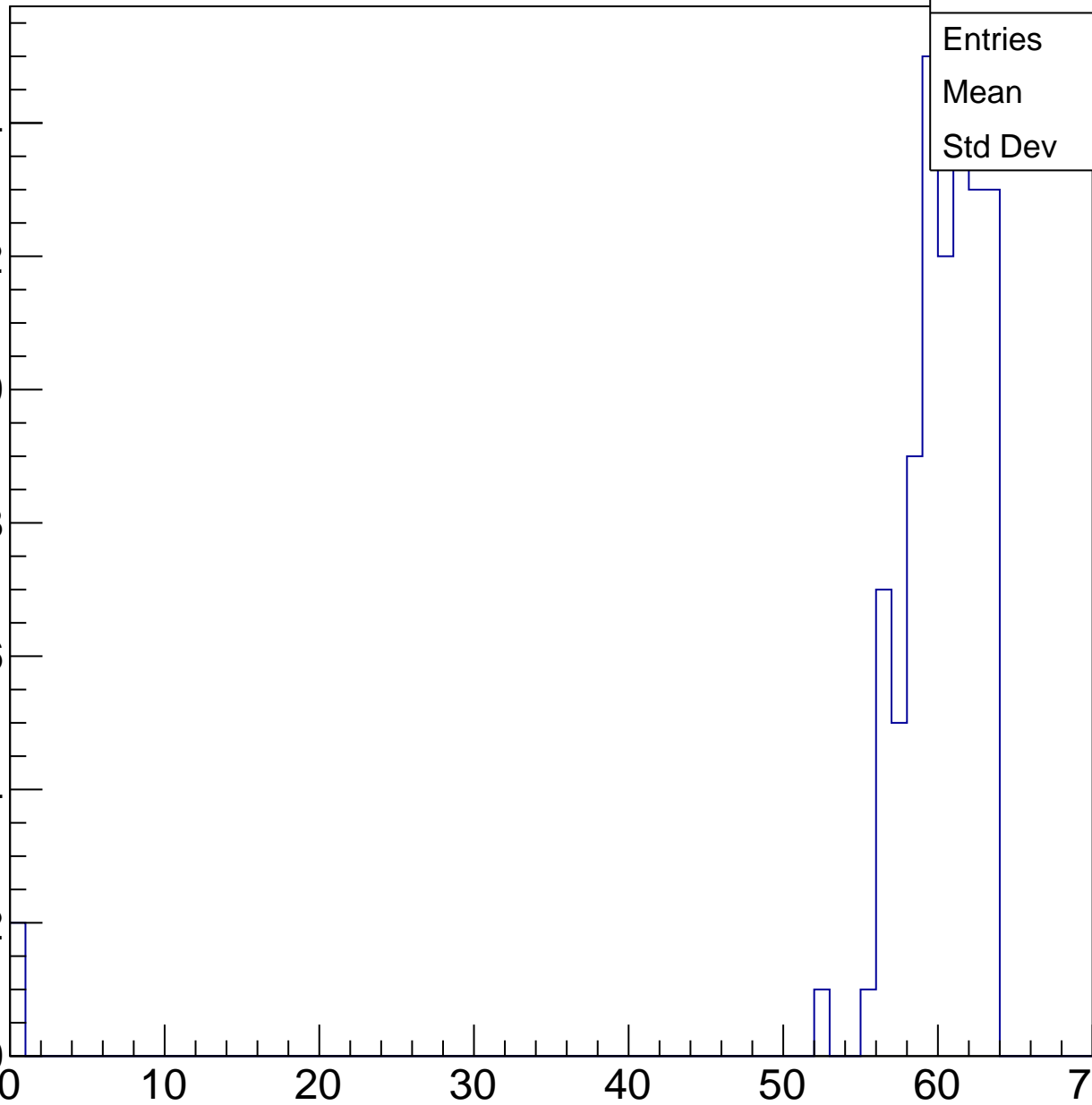
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	58.61
Std Dev	8.982

ampl

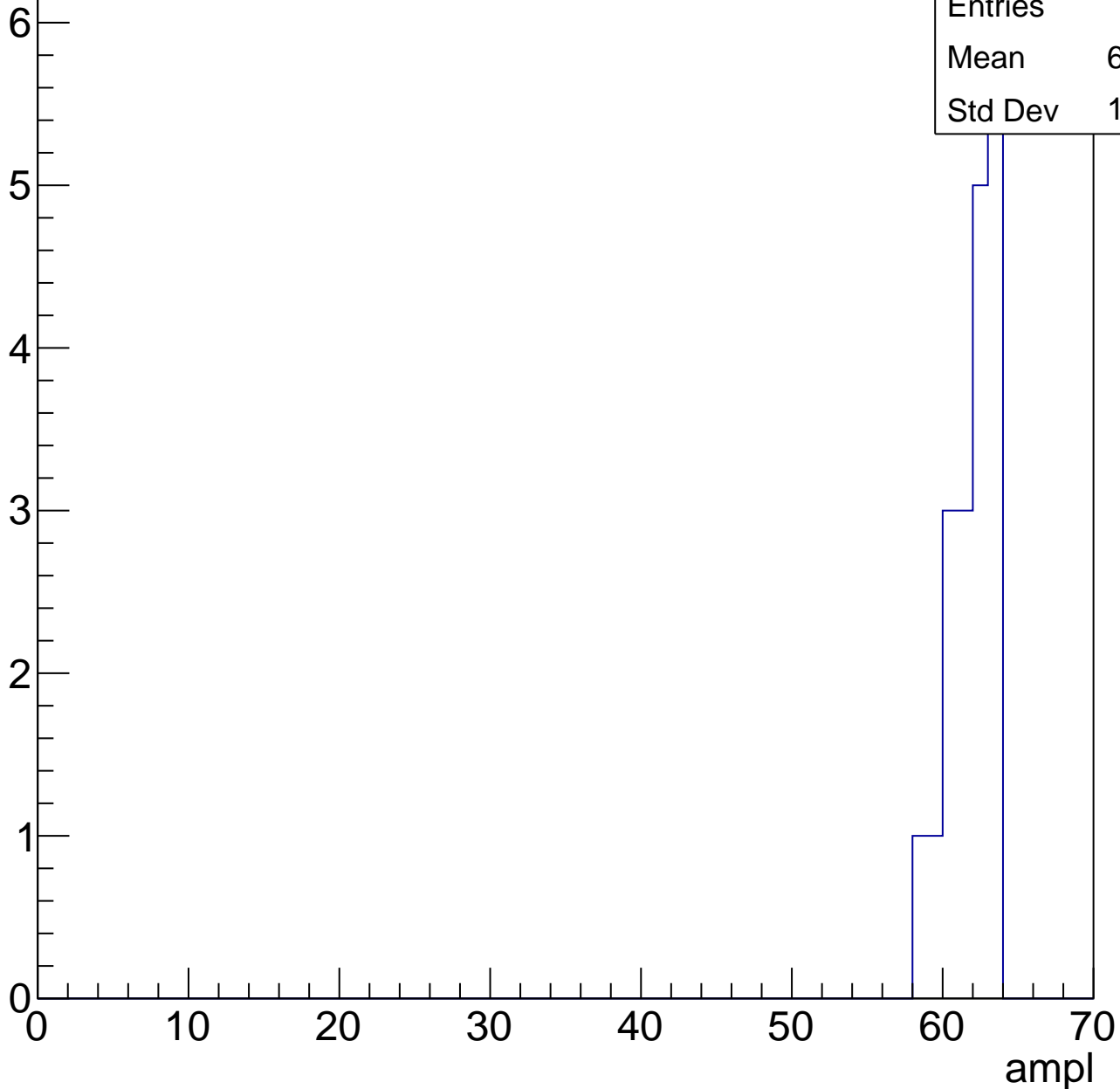


B1L001S, U16-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

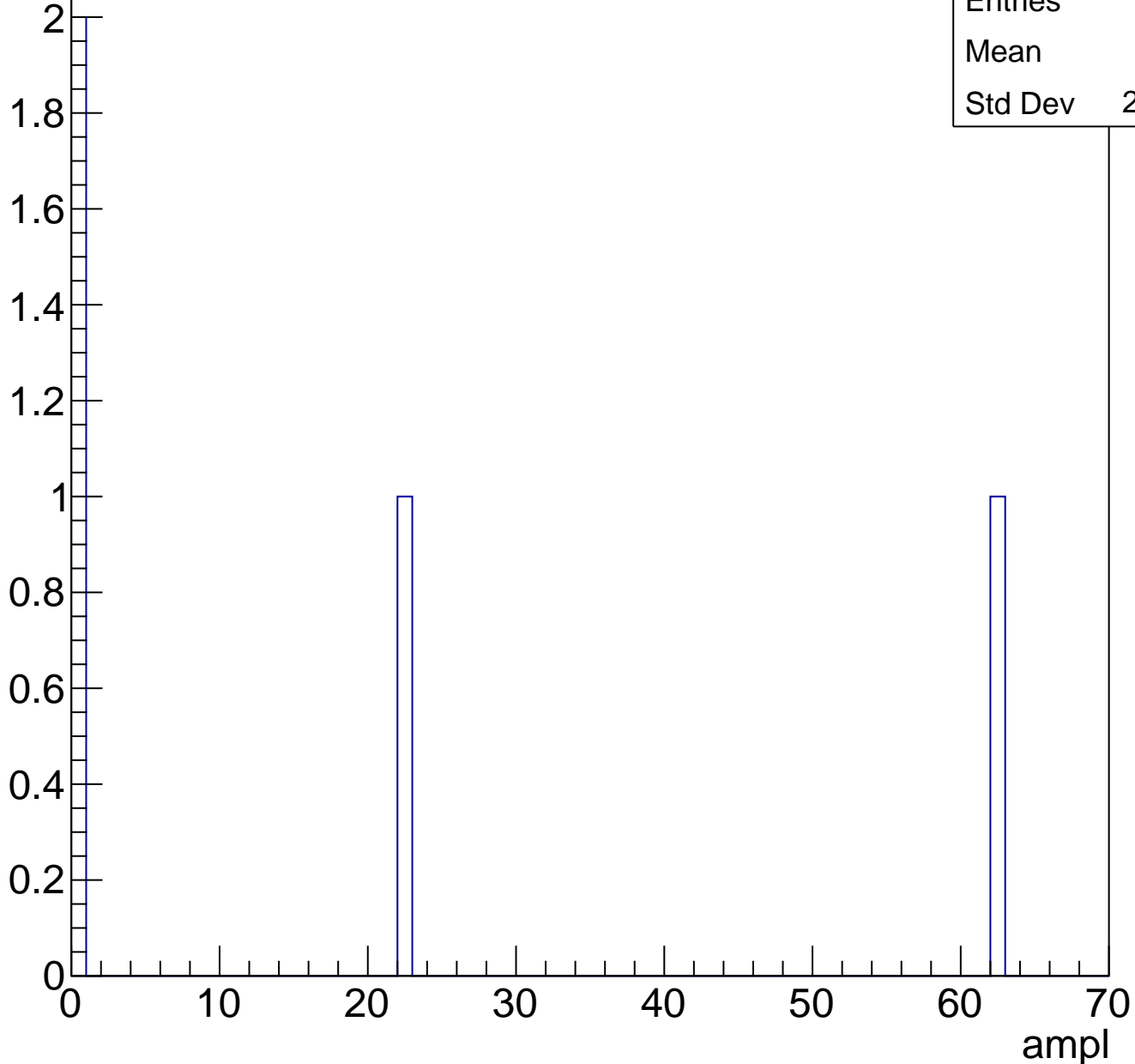
Entries	19
Mean	61.47
Std Dev	1.464



B1L001S, U16-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	21
Std Dev	25.32

B1L001S, U16-ch14, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	27.94
Std Dev	4.118

Gaus mean : 28.7998

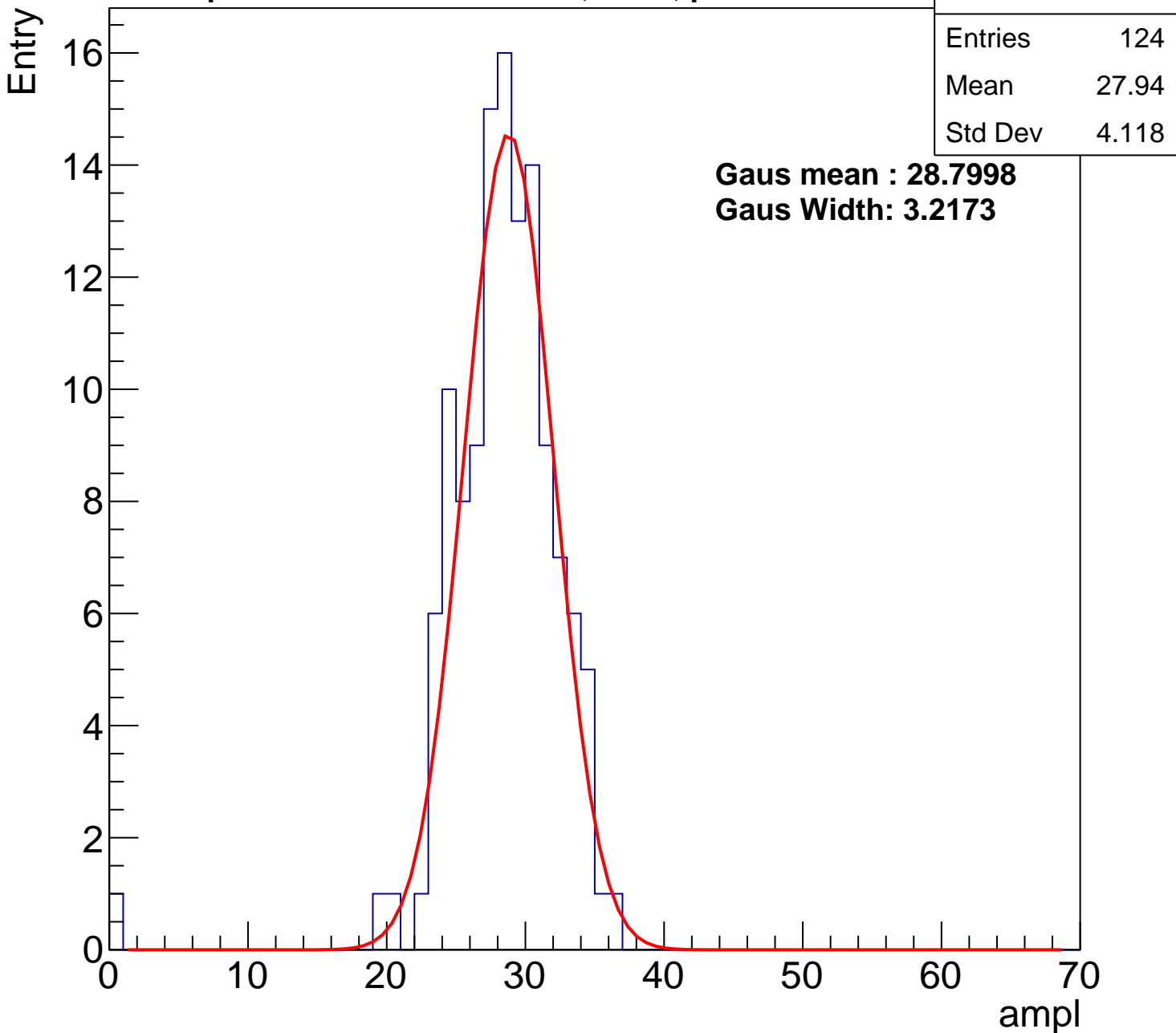
Gaus Width: 3.2173

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch14, adc1

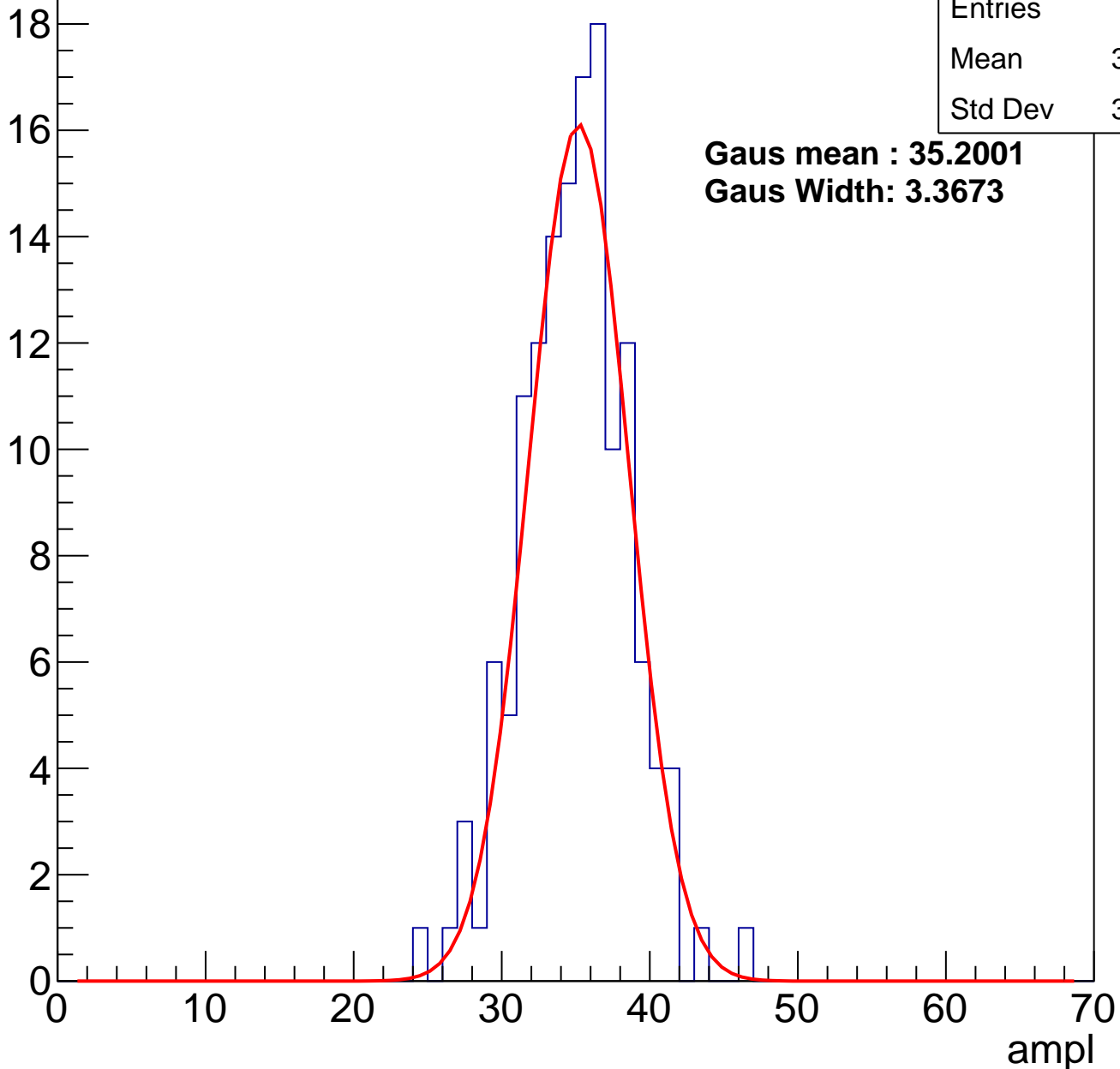
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	34.48
Std Dev	3.552

Gaus mean : 35.2001

Gaus Width: 3.3673

Entry



B1L001S, U16-ch14, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	41.33
Std Dev	3.112

Gaus mean : 42.0333

Gaus Width: 2.9541

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

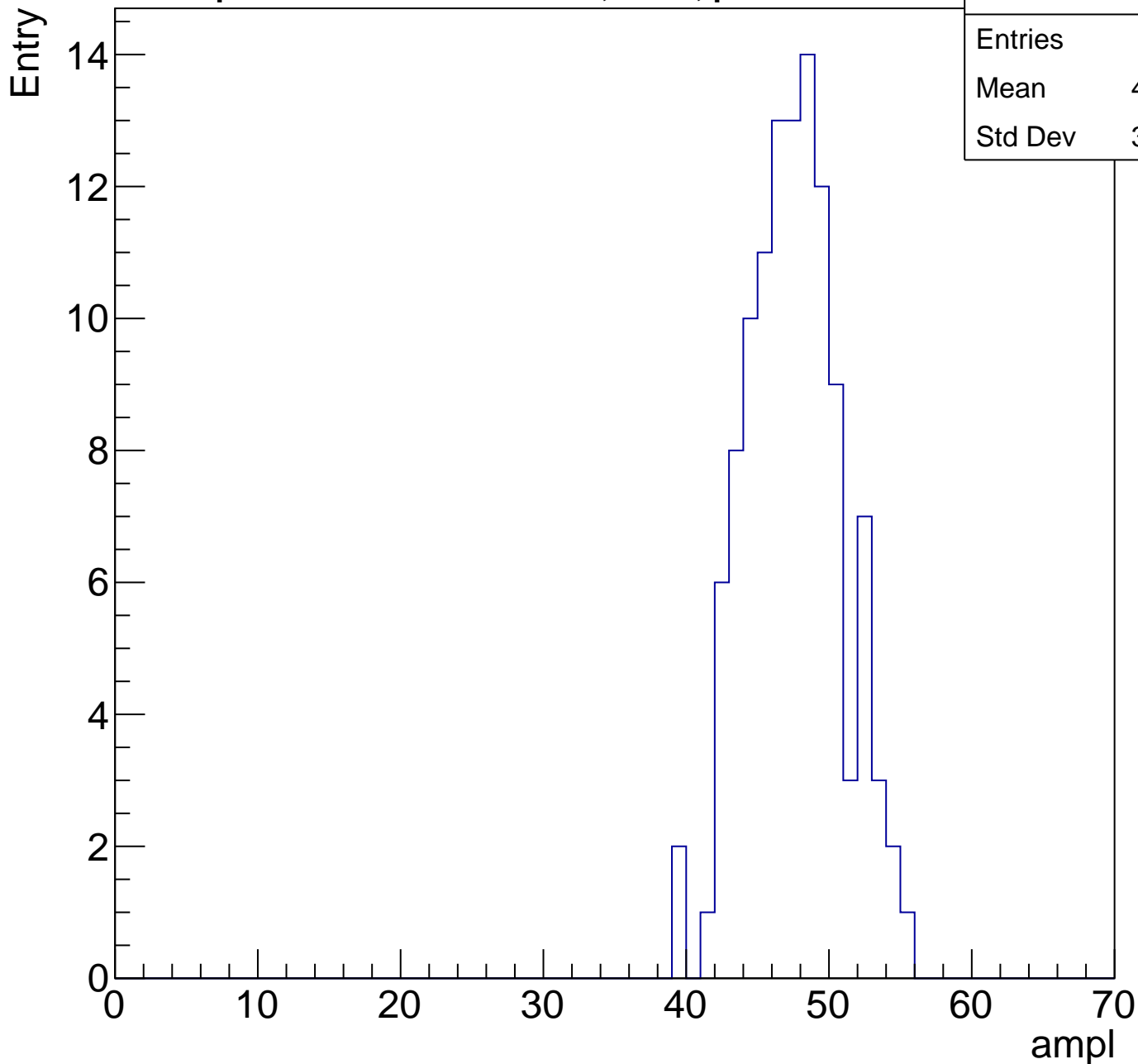
60

70

B1L001S, U16-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

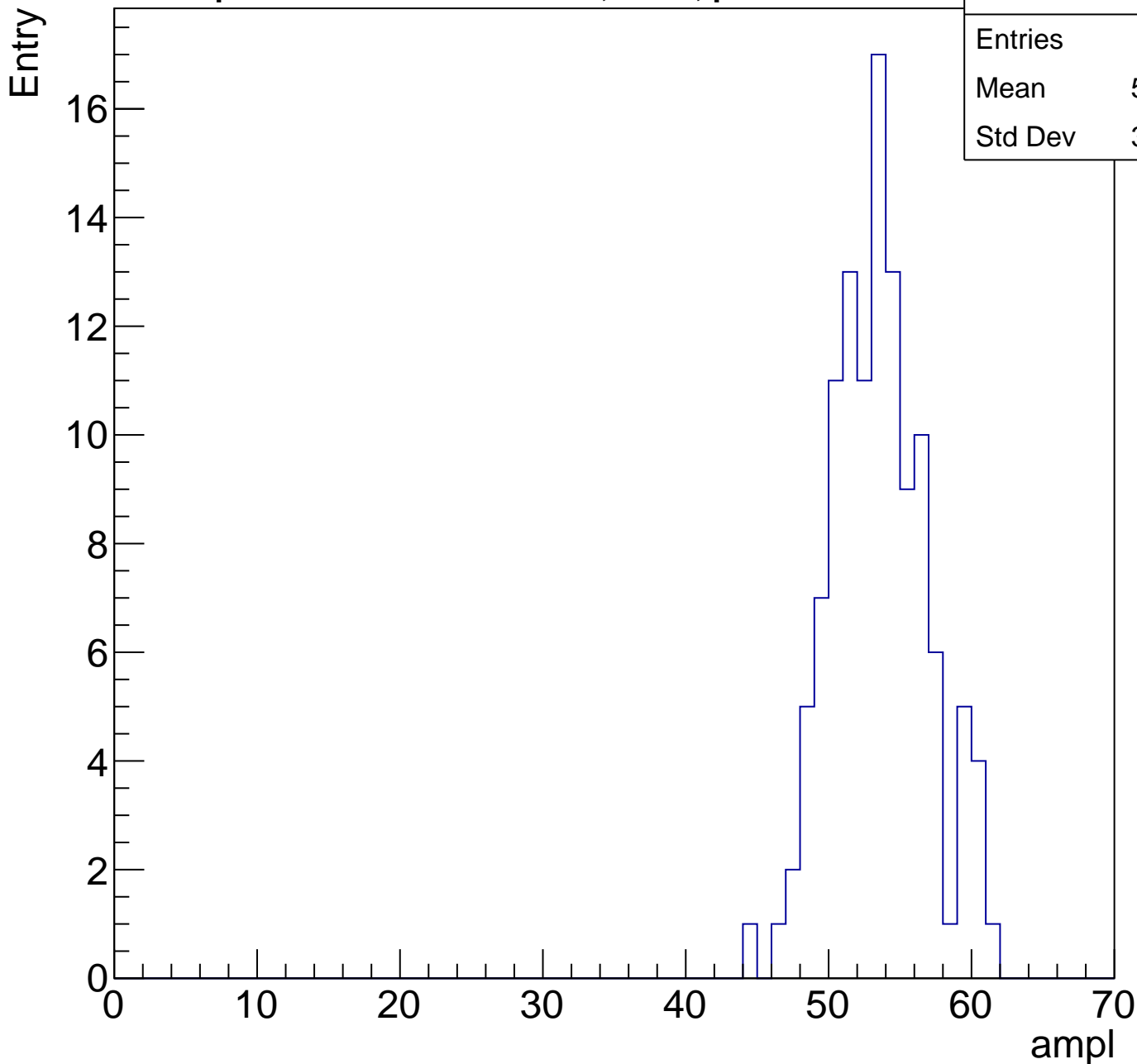
Entries	115
Mean	47.03
Std Dev	3.264



B1L001S, U16-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

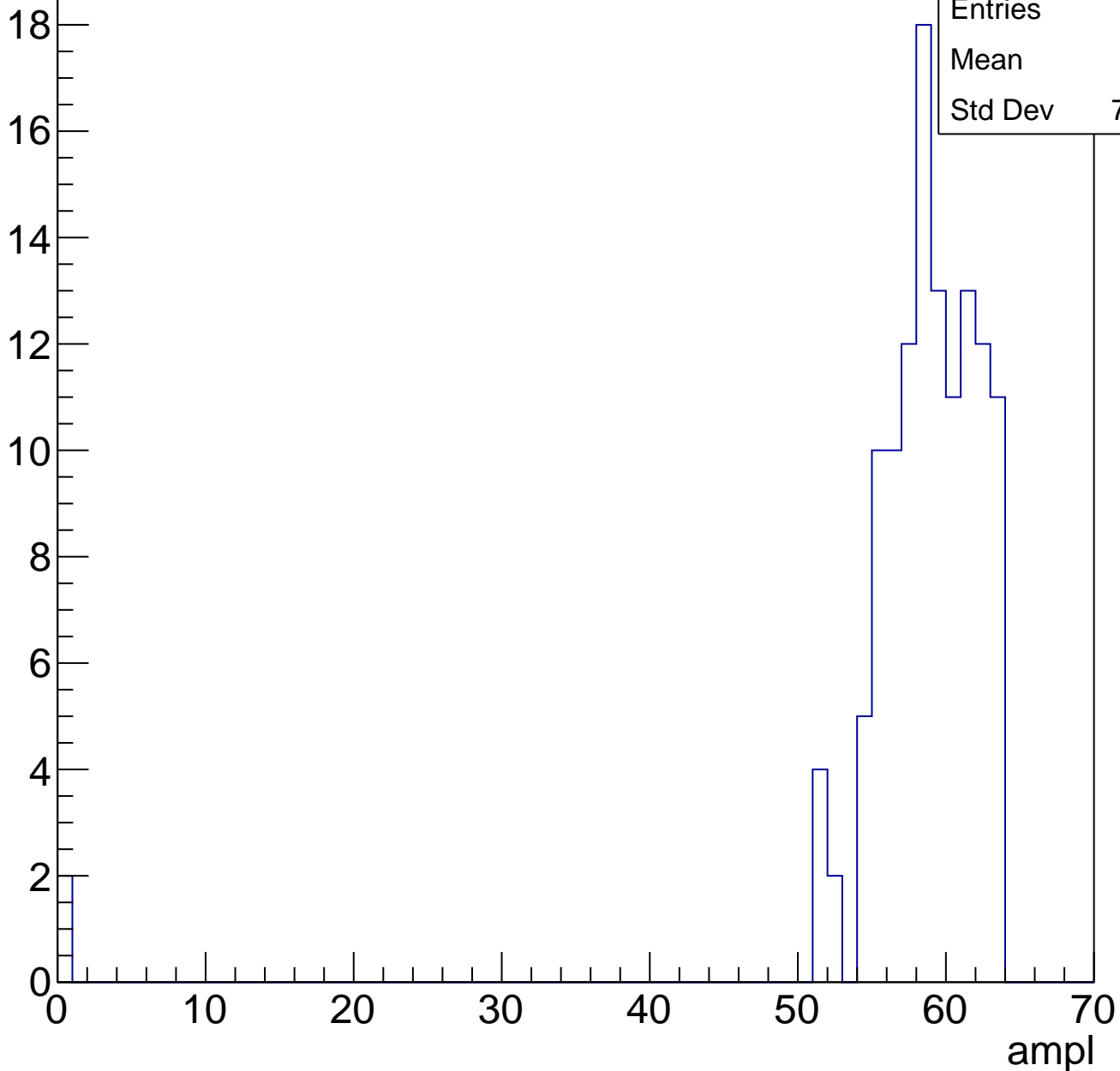
Entries	117
Mean	53.04
Std Dev	3.334



B1L001S, U16-ch14, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

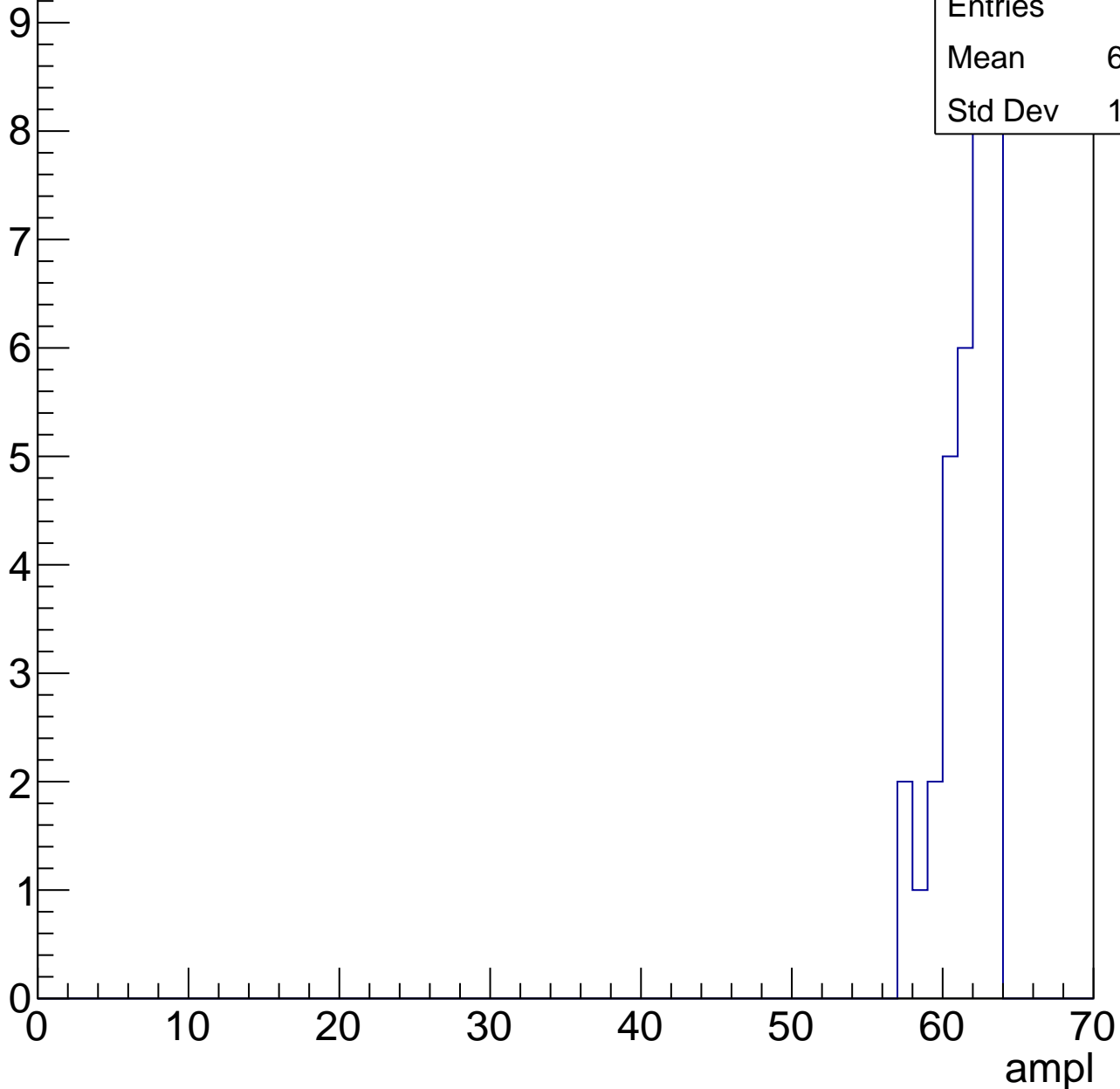


B1L001S, U16-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	33
Mean	61.18
Std Dev	1.714



B1L001S, U16-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

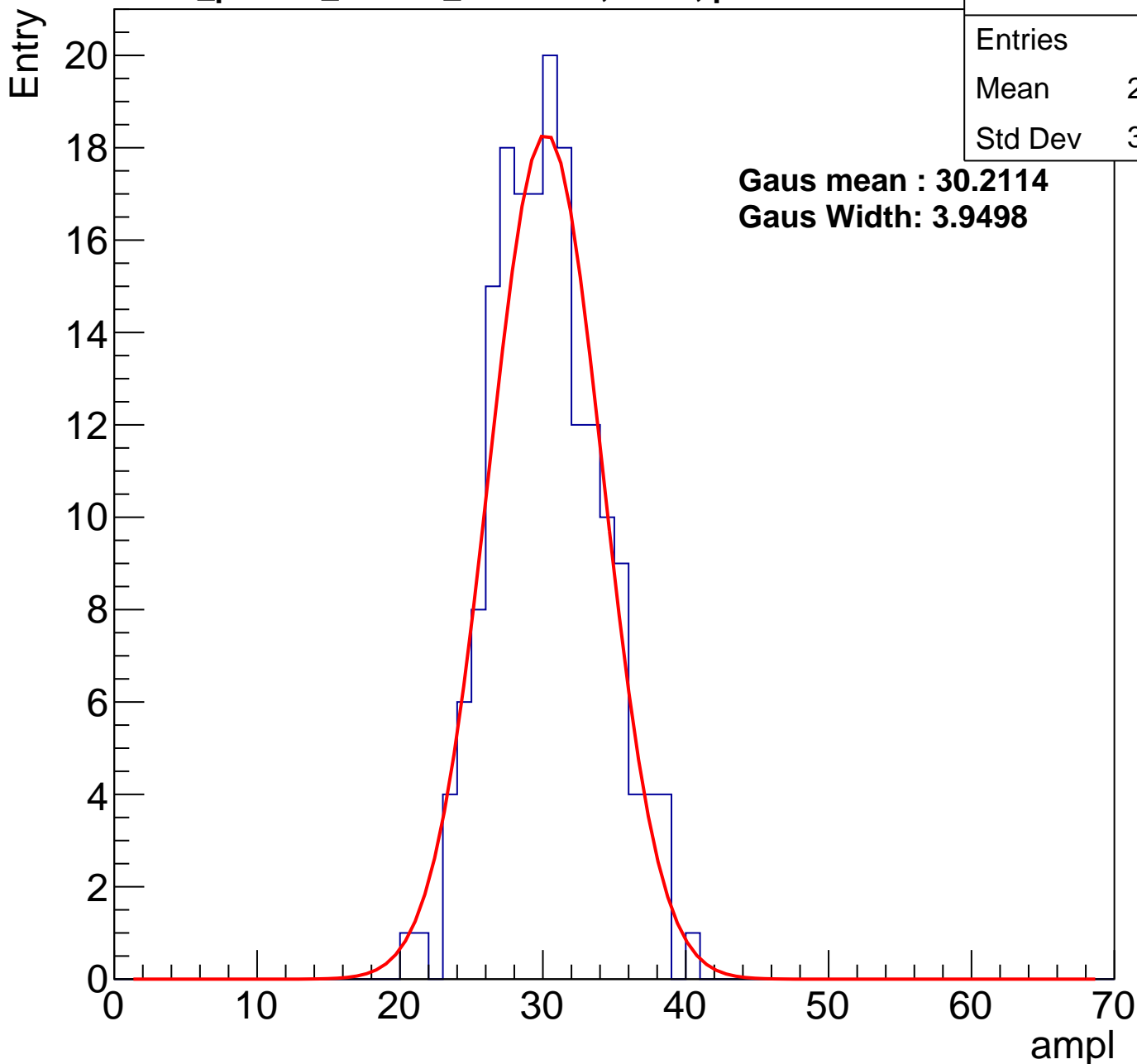
B1L001S, U16-ch15, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	181
Mean	29.83
Std Dev	3.738

Gaus mean : 30.2114

Gaus Width: 3.9498



B1L001S, U16-ch15, adc1

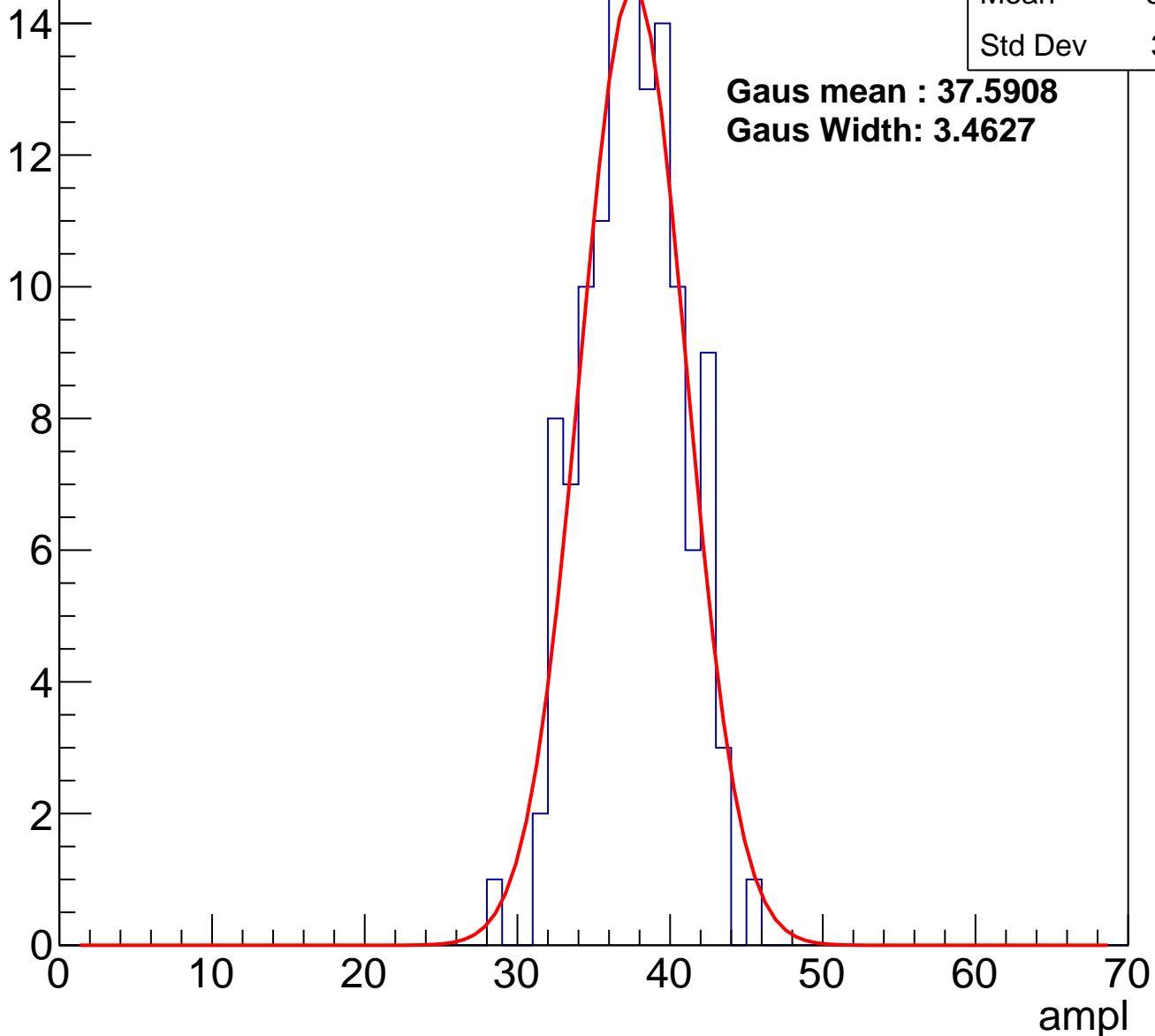
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	37.08
Std Dev	3.181

Gaus mean : 37.5908

Gaus Width: 3.4627

Entry



B1L001S, U16-ch15, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

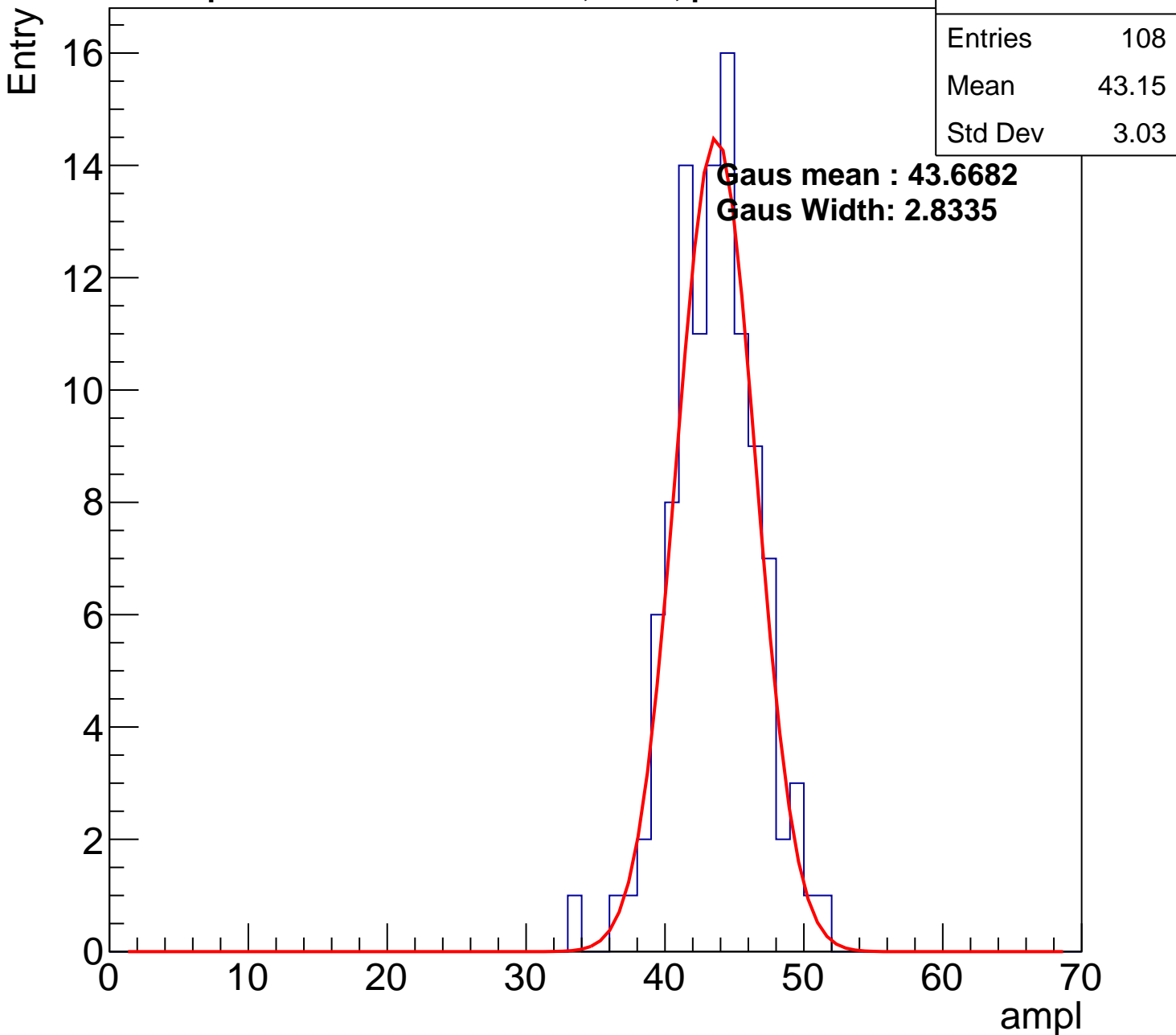
16
14
12
10
8
6
4
2
0

Entries	108
Mean	43.15
Std Dev	3.03

Gaus mean : 43.6682
Gaus Width: 2.8335

0 10 20 30 40 50 60 70

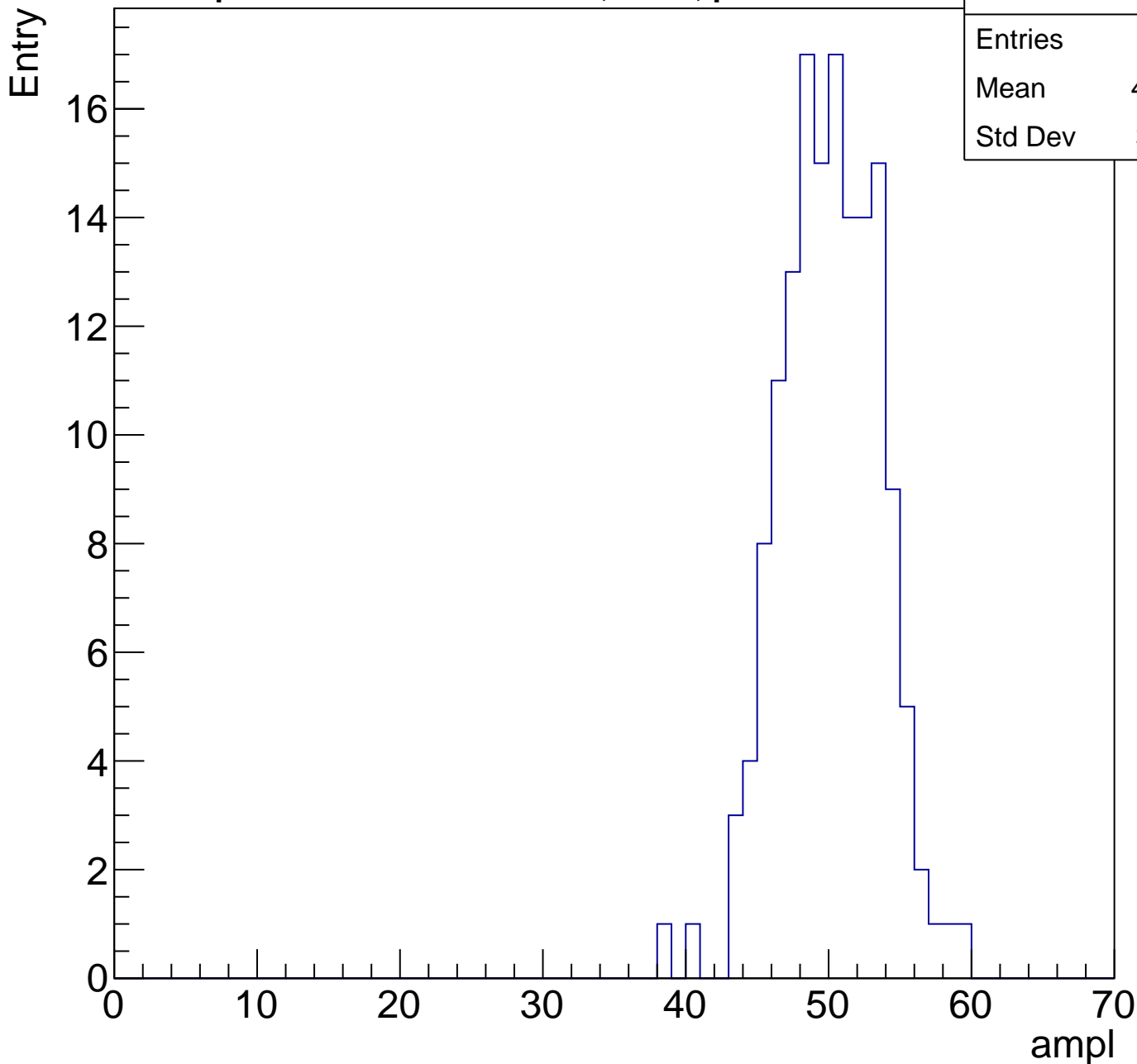
ampl



B1L001S, U16-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	49.64
Std Dev	3.471



B1L001S, U16-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

128

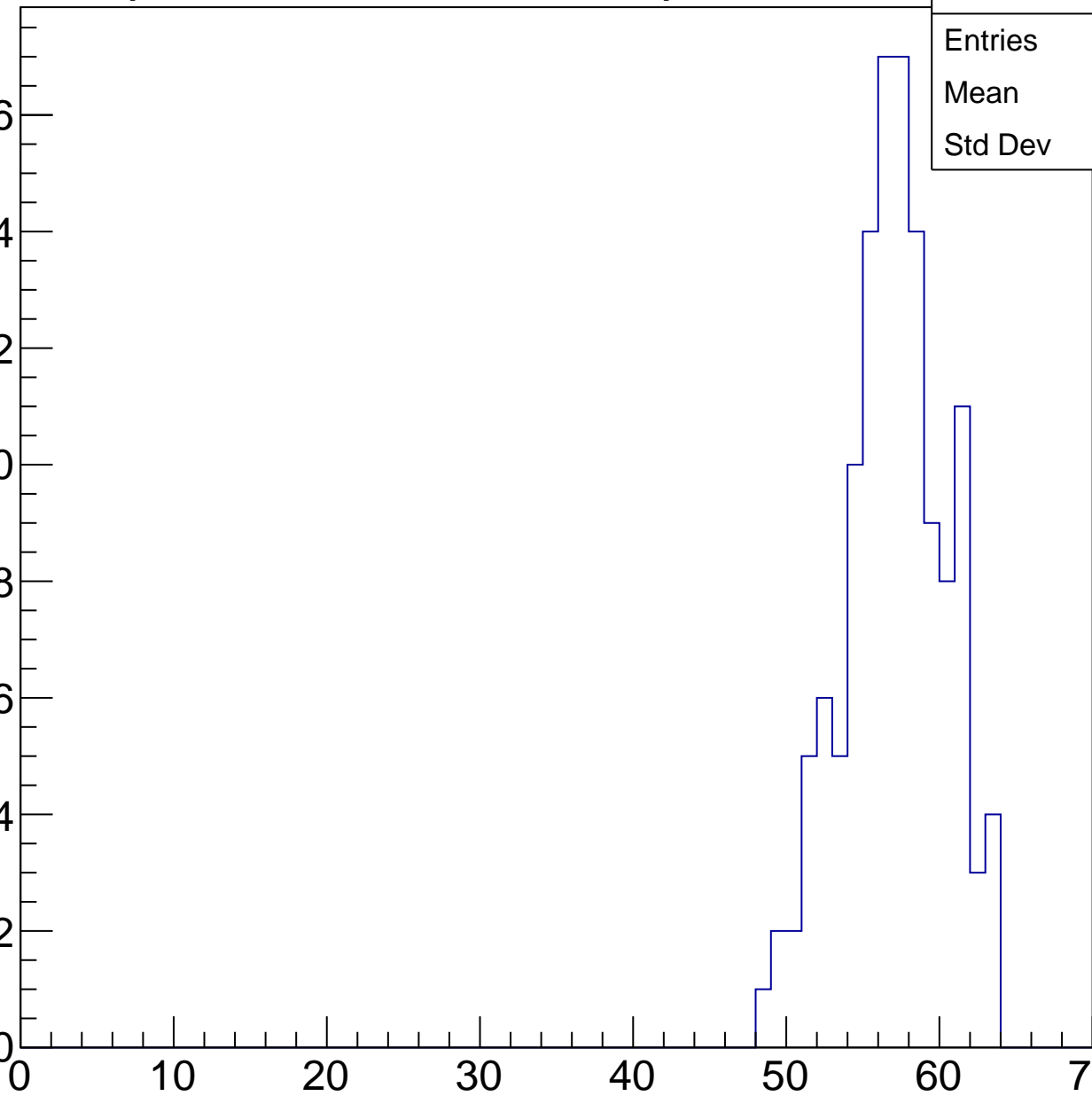
Mean

56.57

Std Dev

3.276

ampl



B1L001S, U16-ch15, adc5

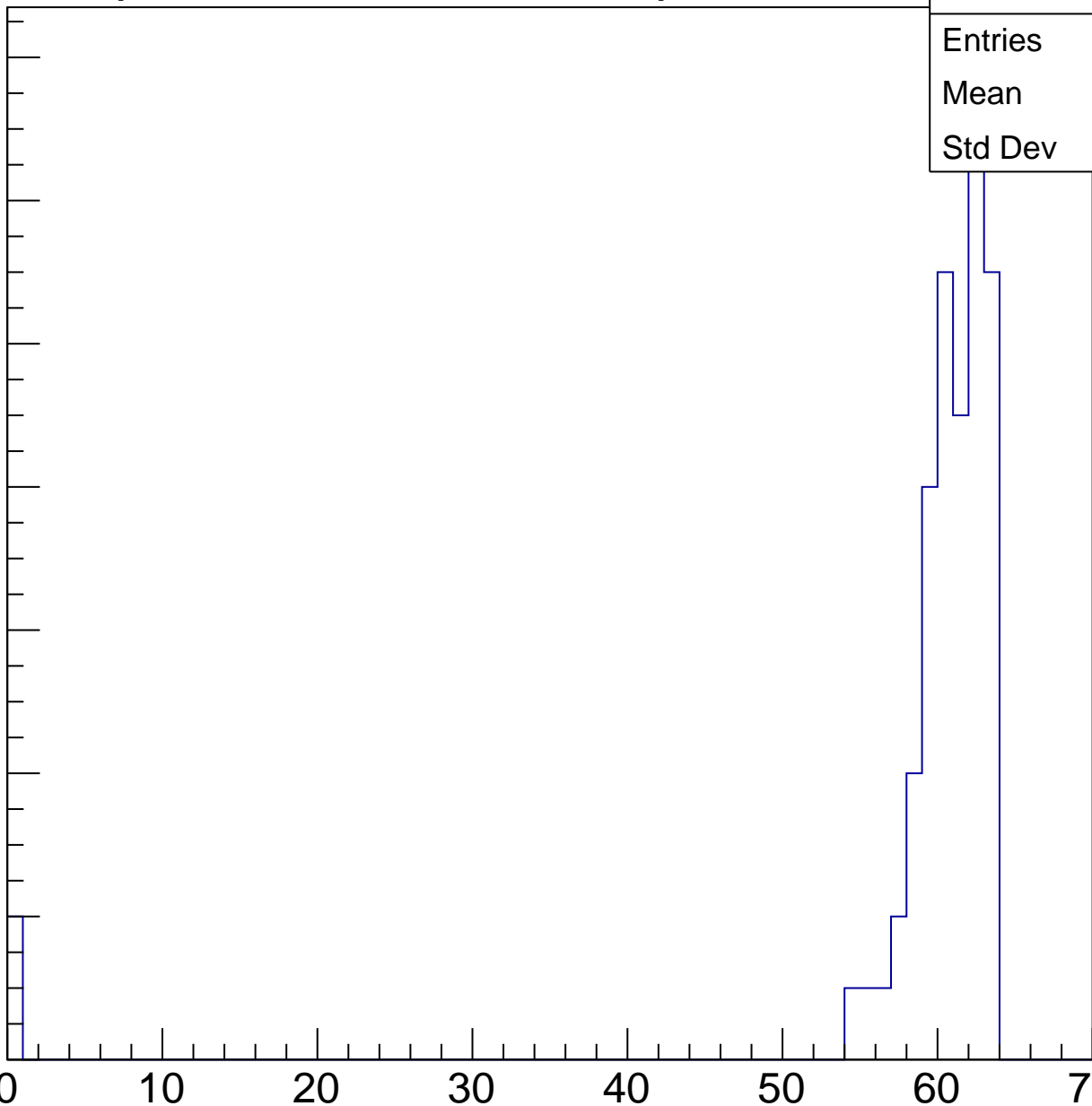
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	58.64
Std Dev	10.73

ampl

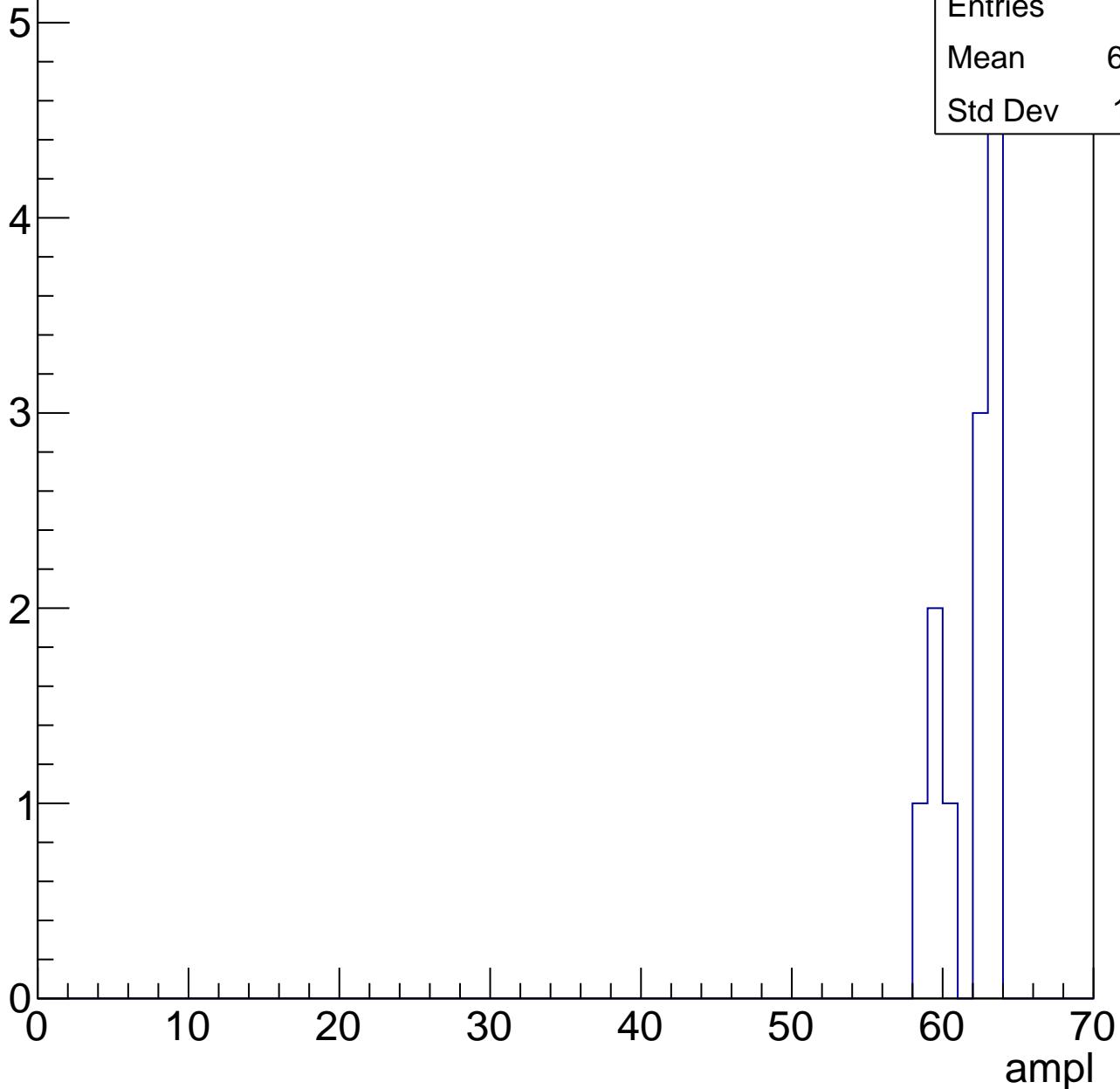


B1L001S, U16-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.42
Std Dev	1.801



B1L001S, U16-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

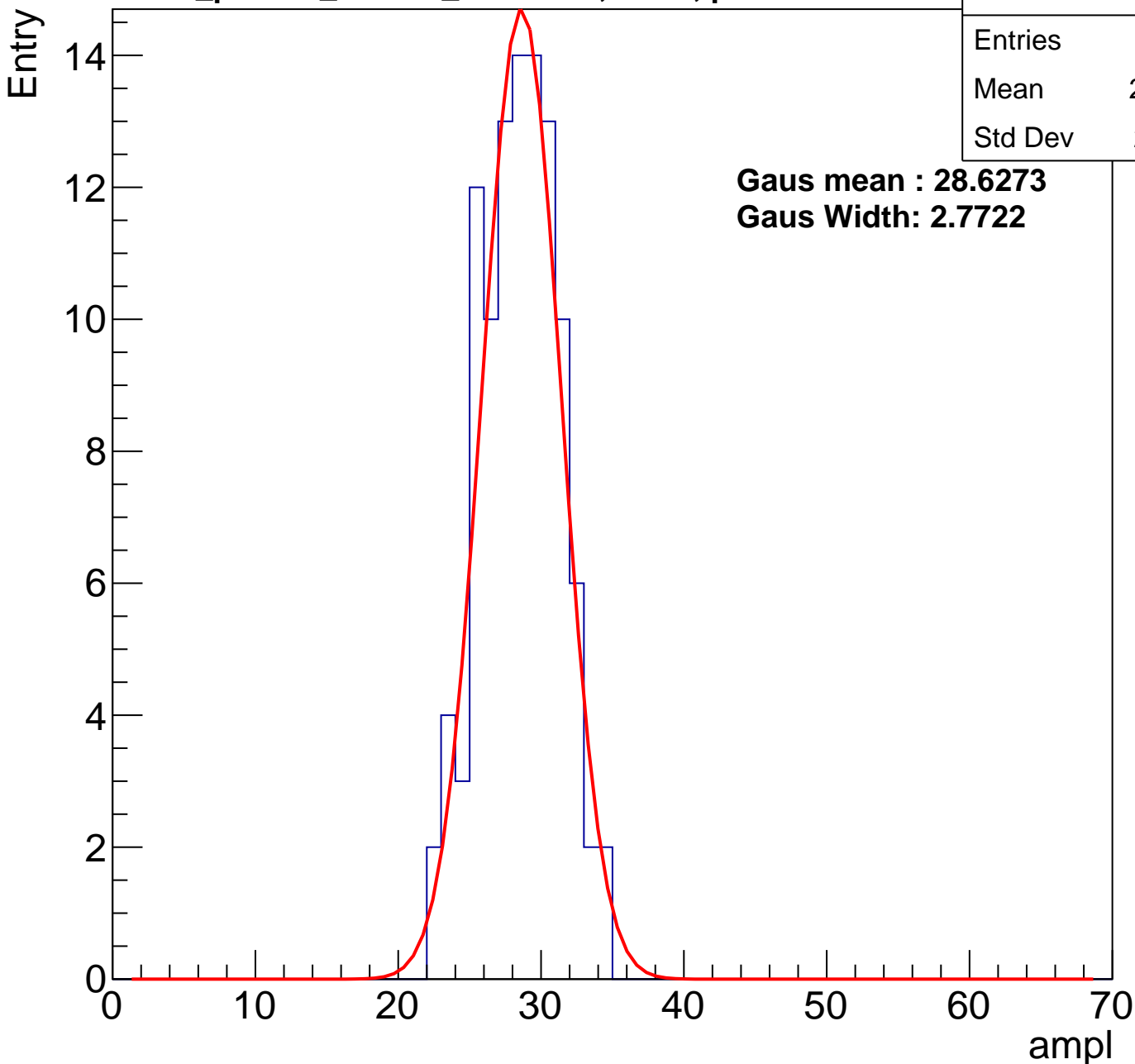
B1L001S, U16-ch16, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	28.03
Std Dev	2.681

Gaus mean : 28.6273

Gaus Width: 2.7722



B1L001S, U16-ch16, adc1

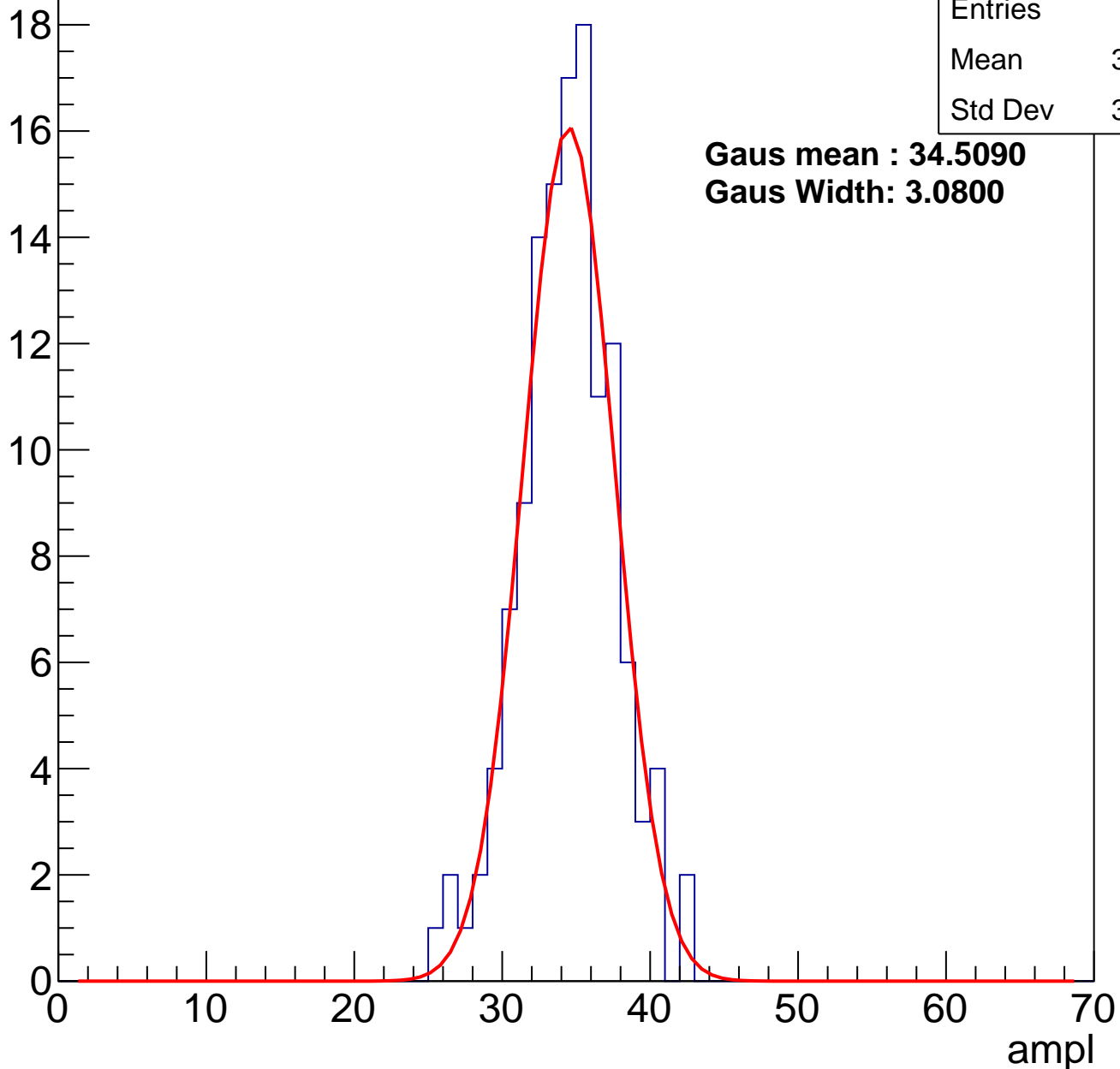
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	33.95
Std Dev	3.195

Gaus mean : 34.5090

Gaus Width: 3.0800

Entry



B1L001S, U16-ch16, adc2

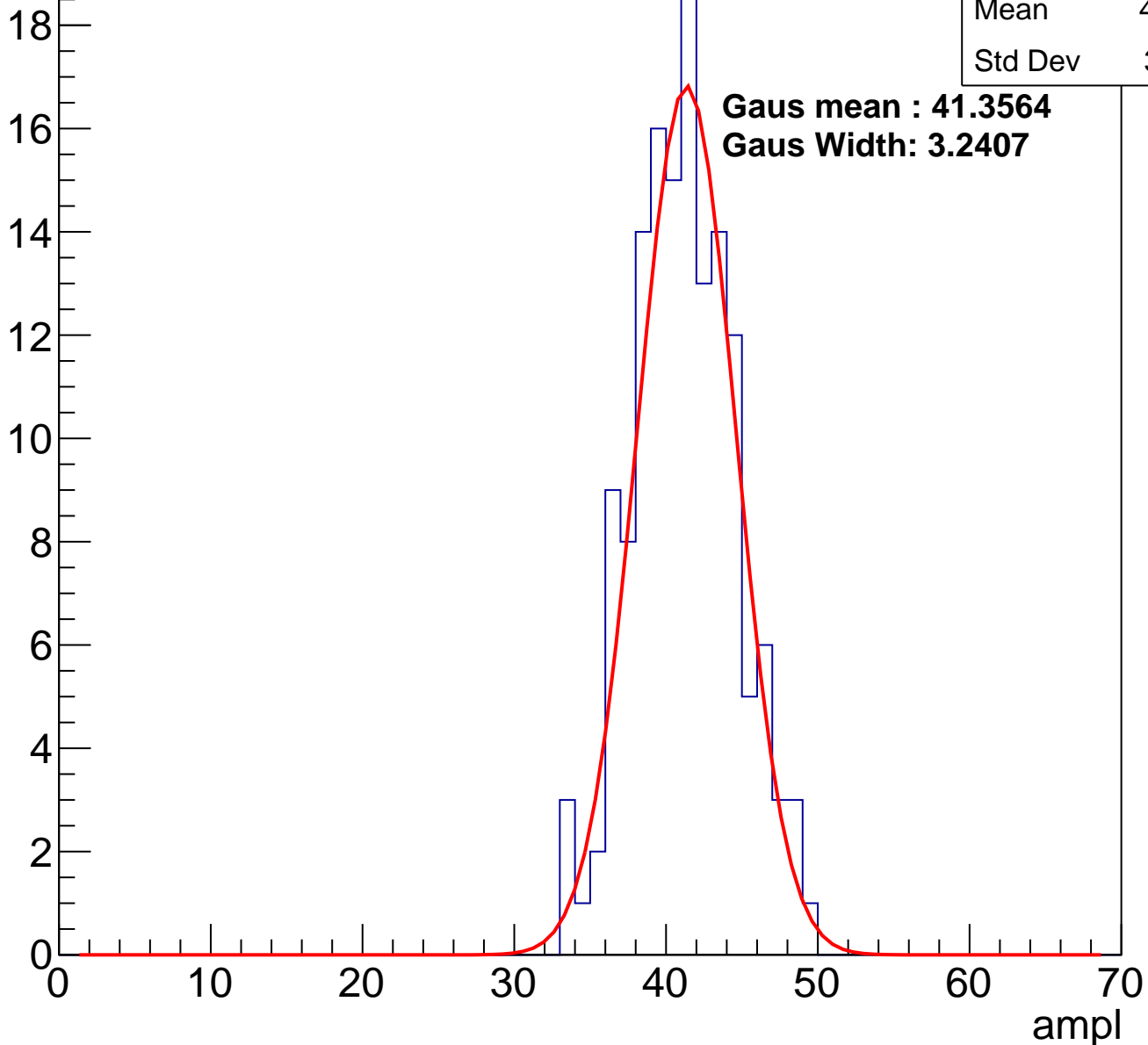
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	40.76
Std Dev	3.321

Gaus mean : 41.3564

Gaus Width: 3.2407

Entry



B1L001S, U16-ch16, adc3

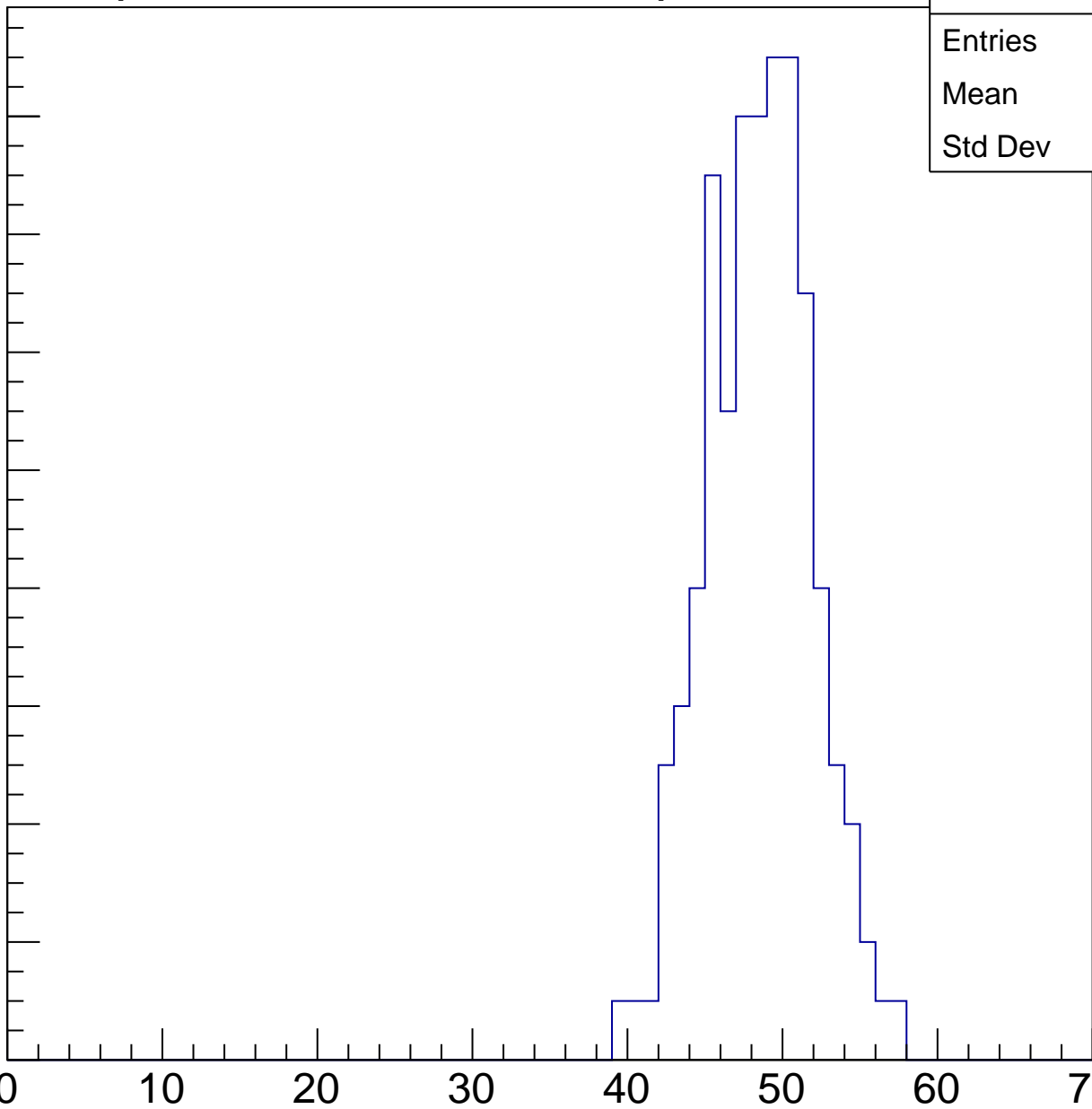
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	148
Mean	48.02
Std Dev	3.378

ampl



B1L001S, U16-ch16, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

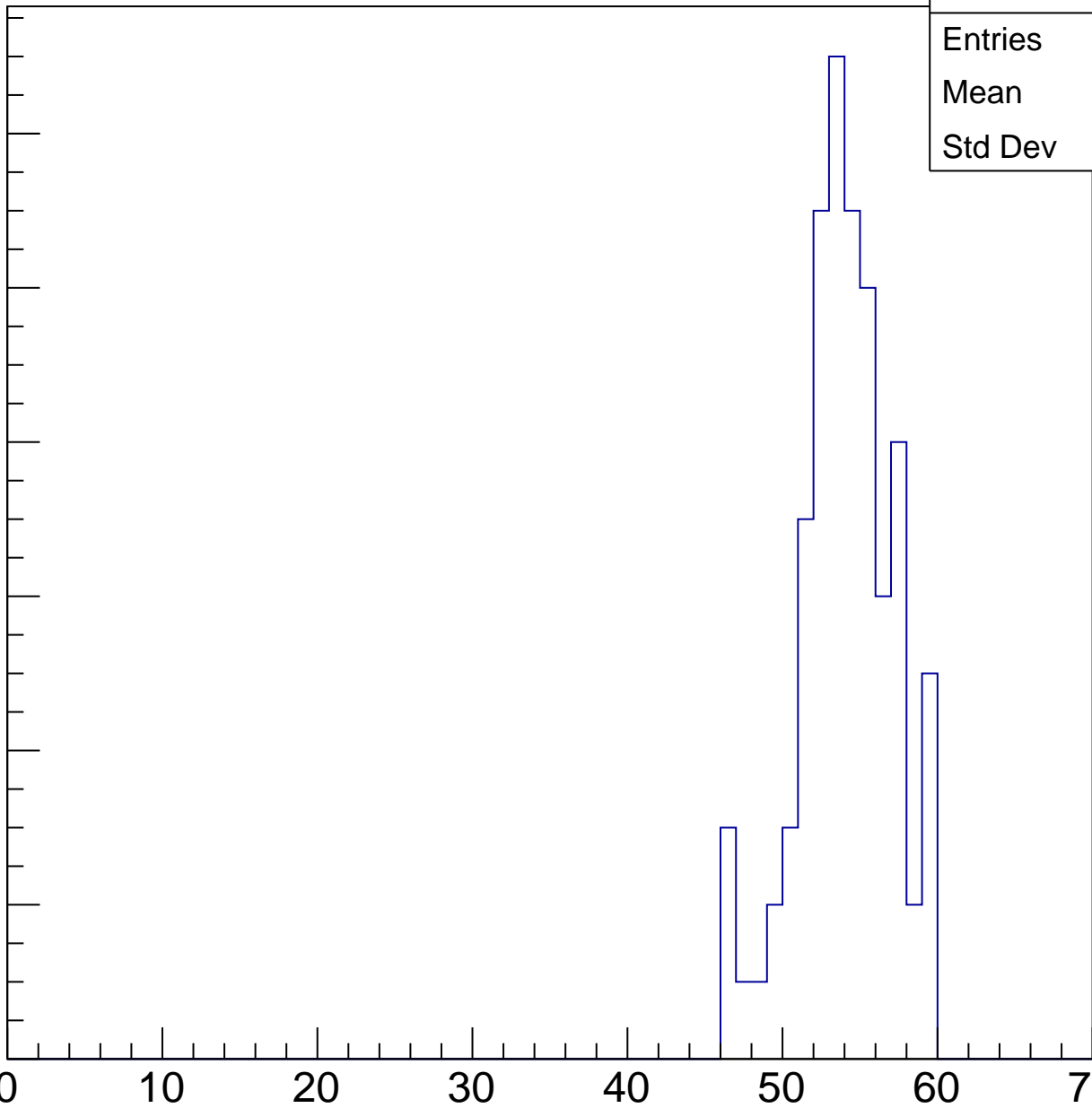
2

0

Entries	83
Mean	53.57
Std Dev	2.991

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch16, adc5

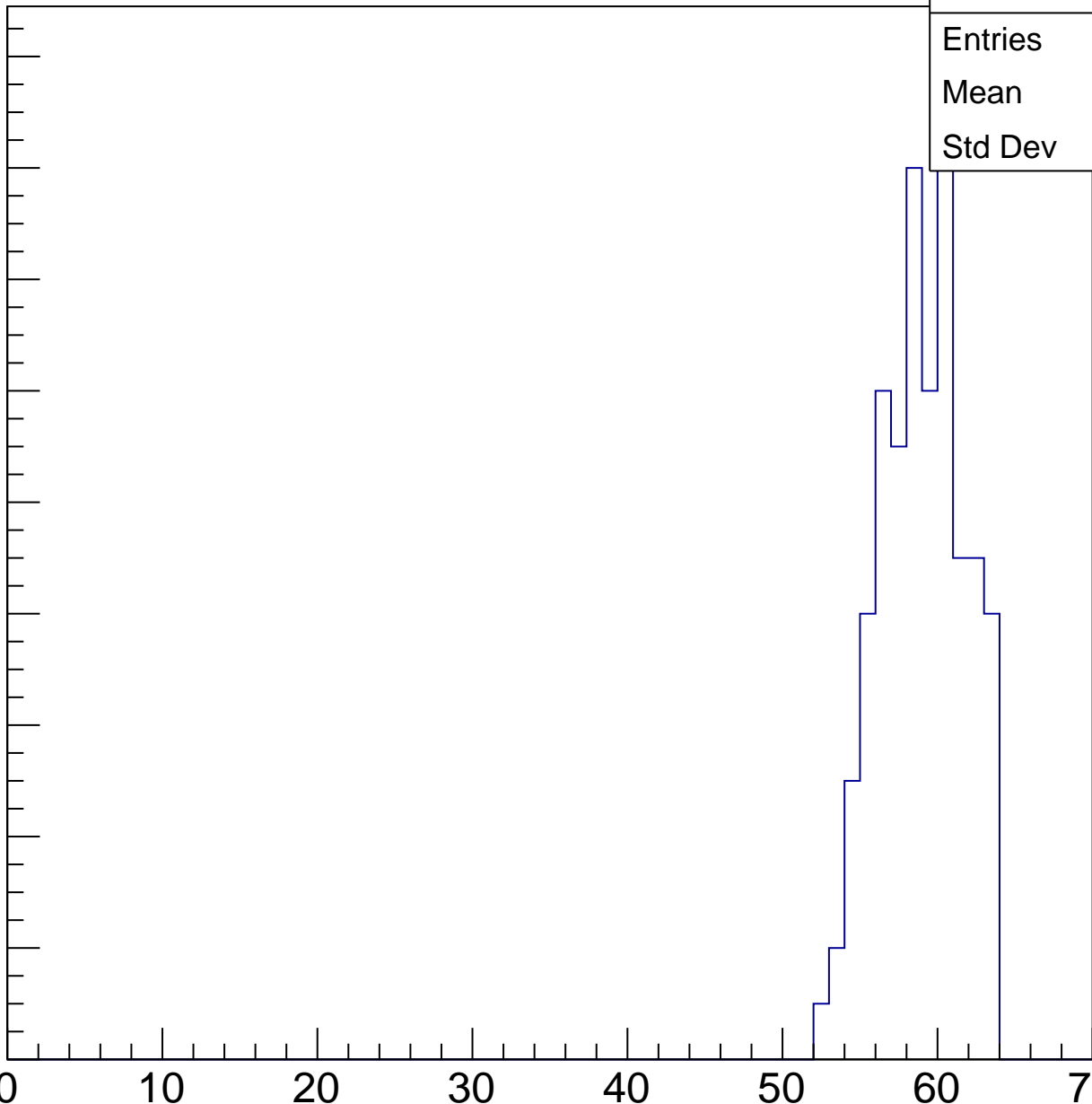
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.5
Std Dev	2.647

ampl



B1L001S, U16-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

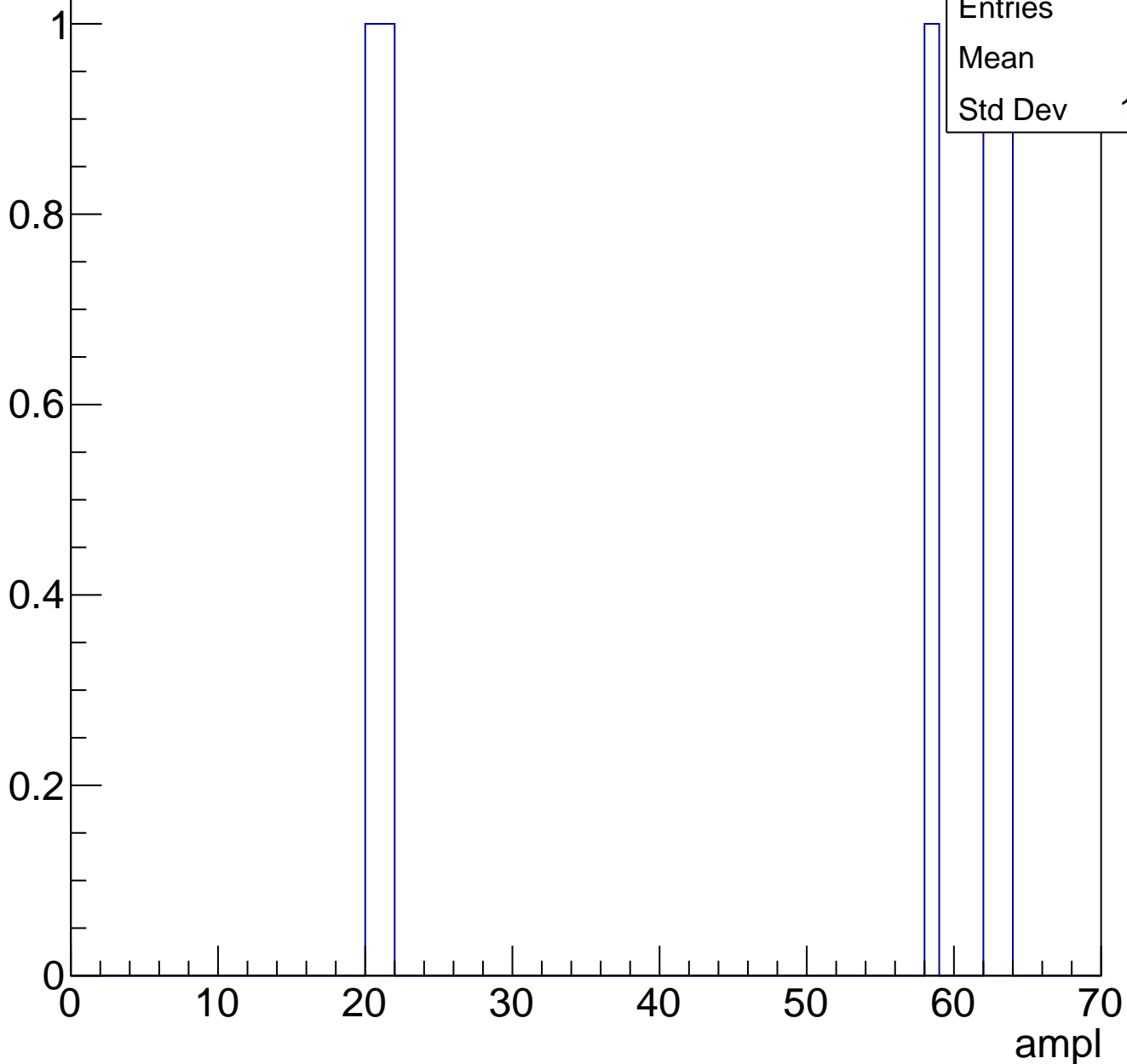
Entries	41
Mean	58.41
Std Dev	13.32

0

B1L001S, U16-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch17, adc0

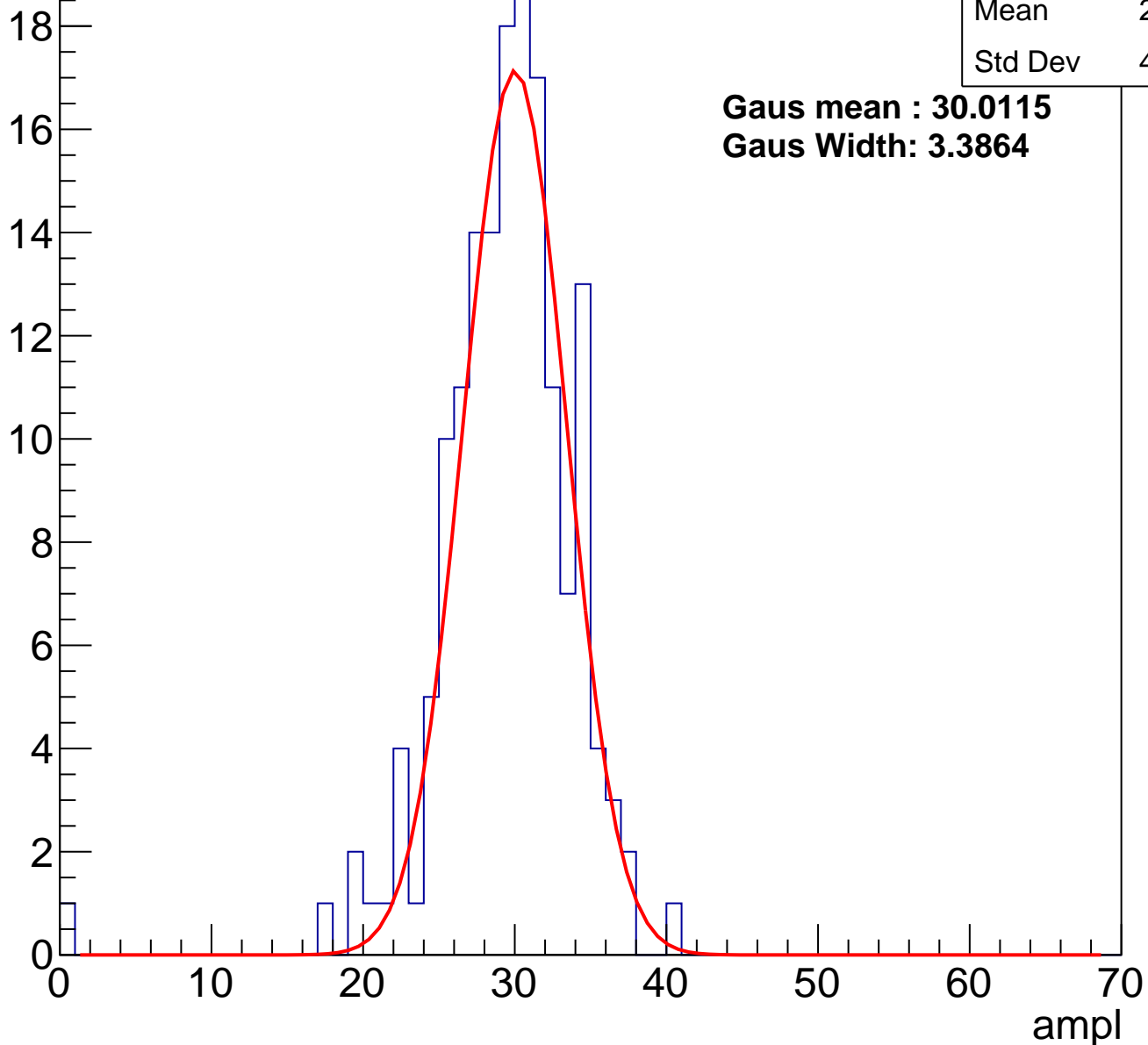
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	28.99
Std Dev	4.457

Gaus mean : 30.0115

Gaus Width: 3.3864

Entry



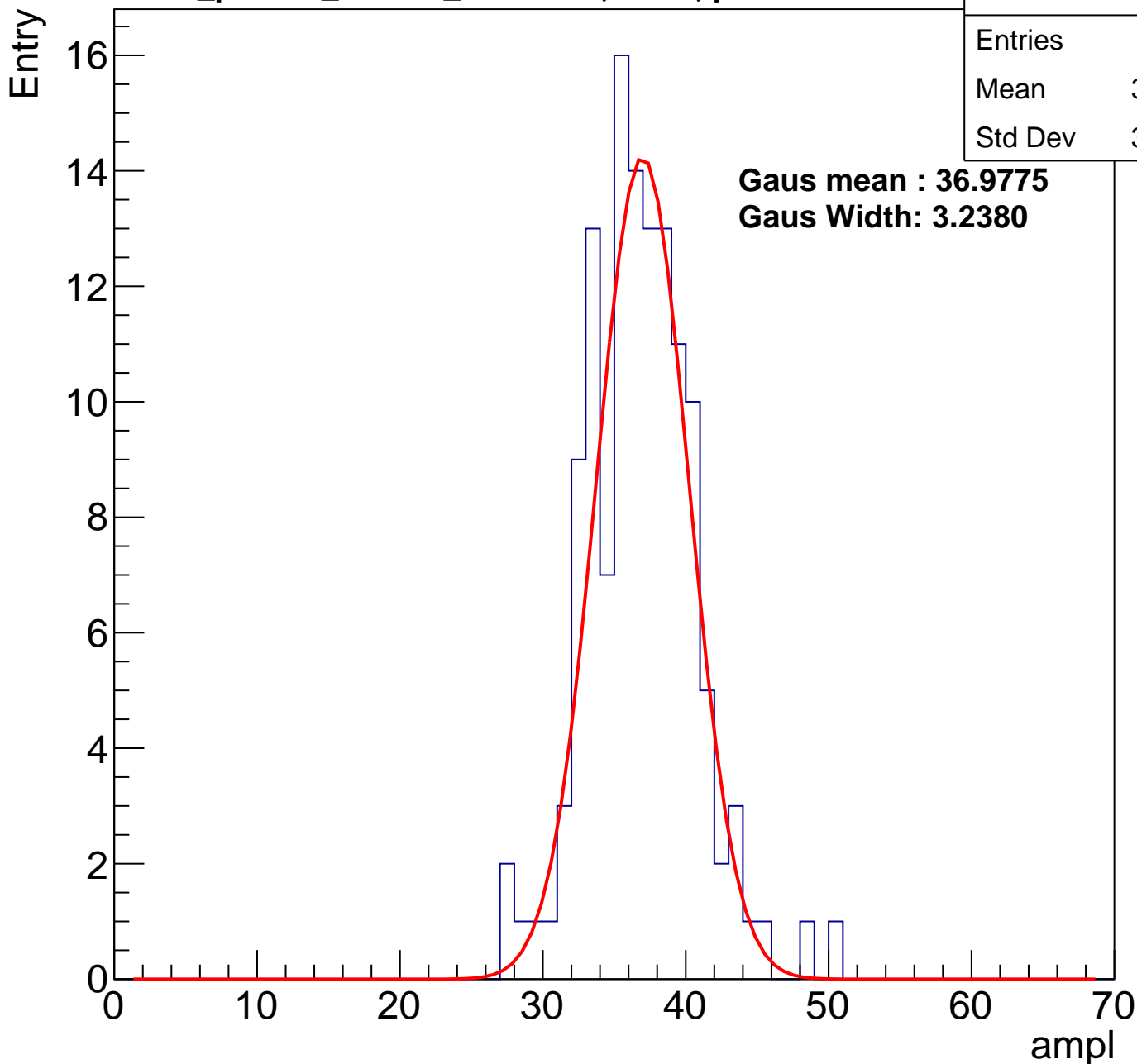
B1L001S, U16-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.42
Std Dev	3.772

Gaus mean : 36.9775

Gaus Width: 3.2380



B1L001S, U16-ch17, adc2

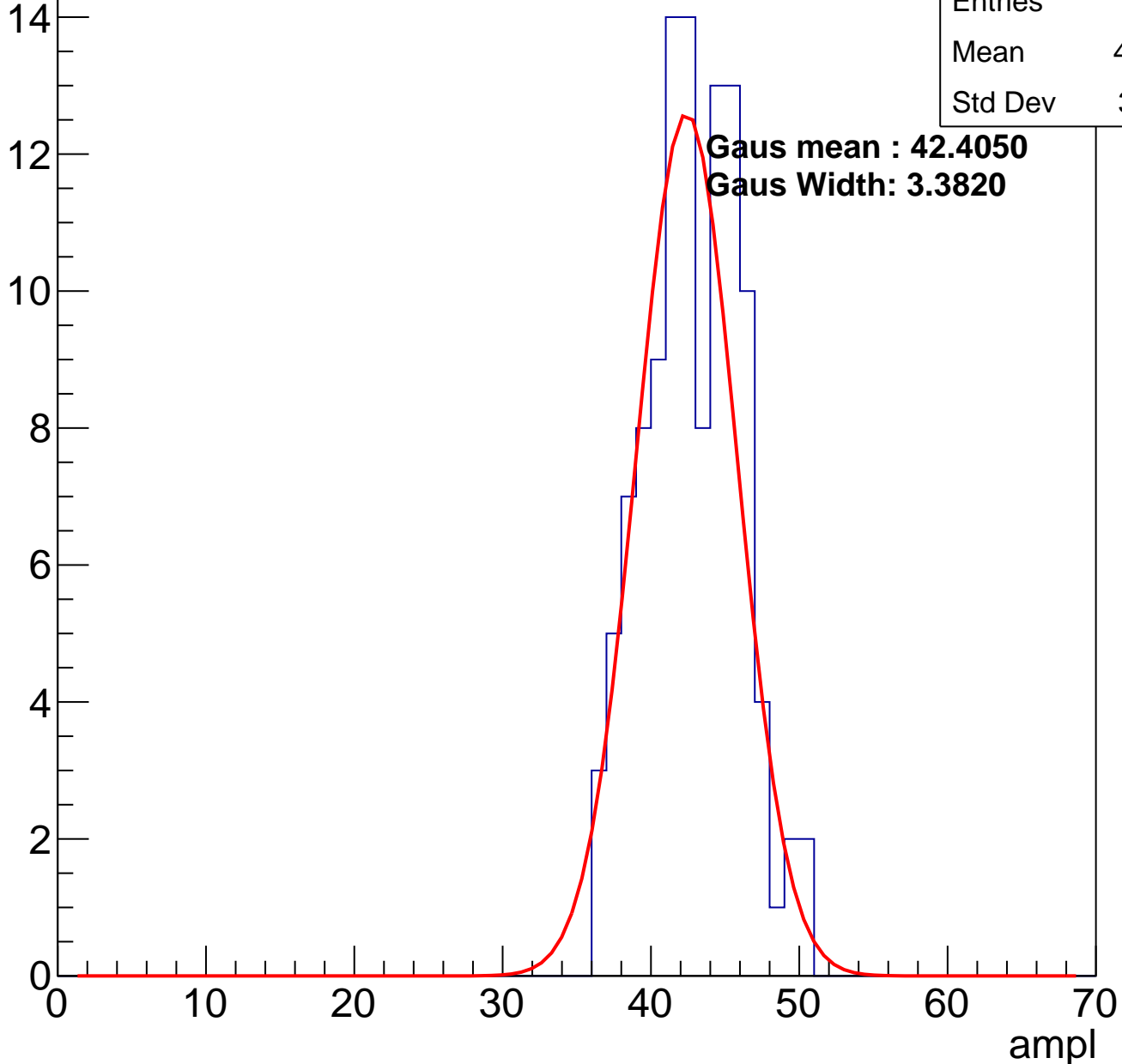
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	42.37
Std Dev	3.191

Gaus mean : 42.4050

Gaus Width: 3.3820

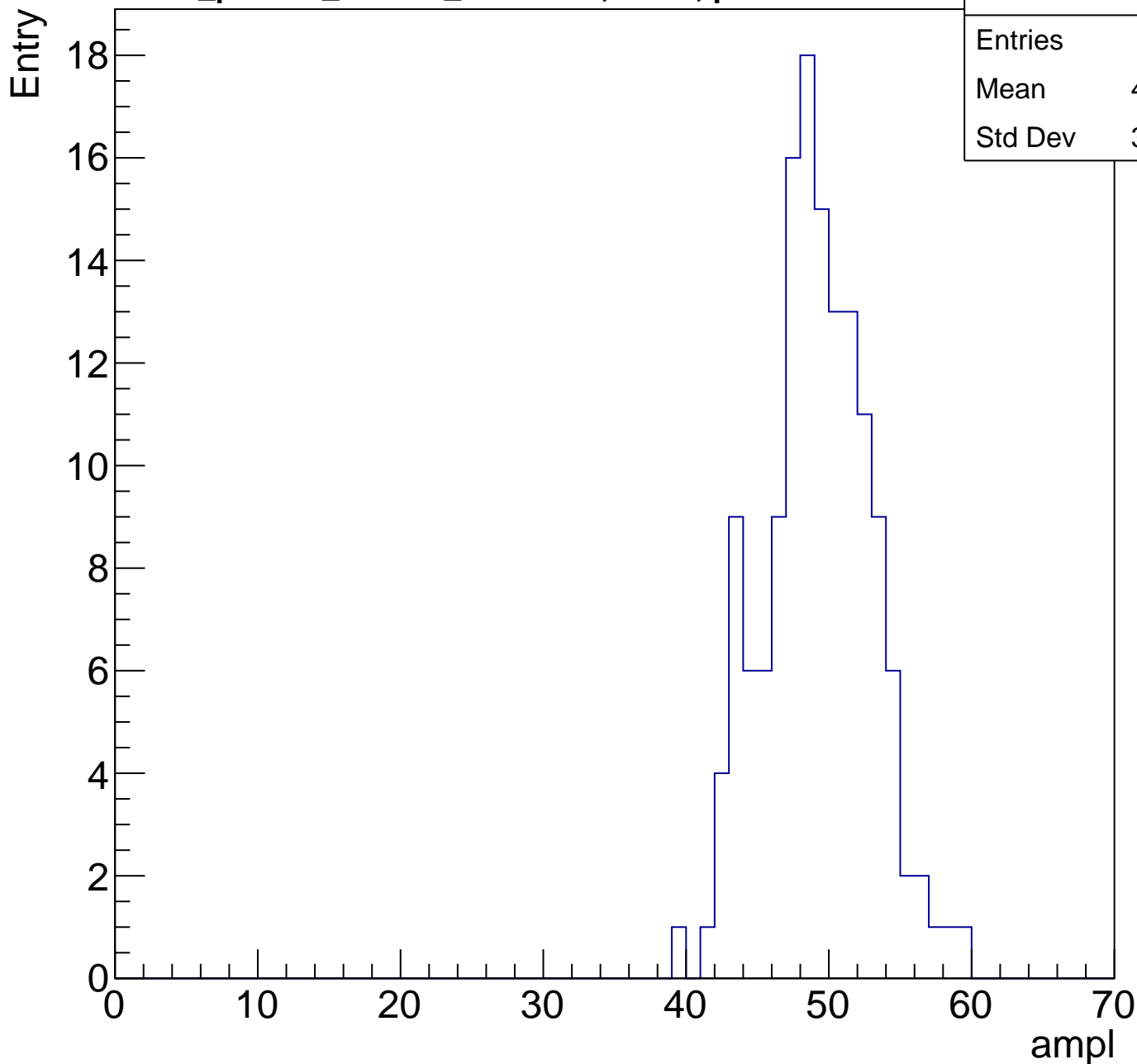
Entry



B1L001S, U16-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

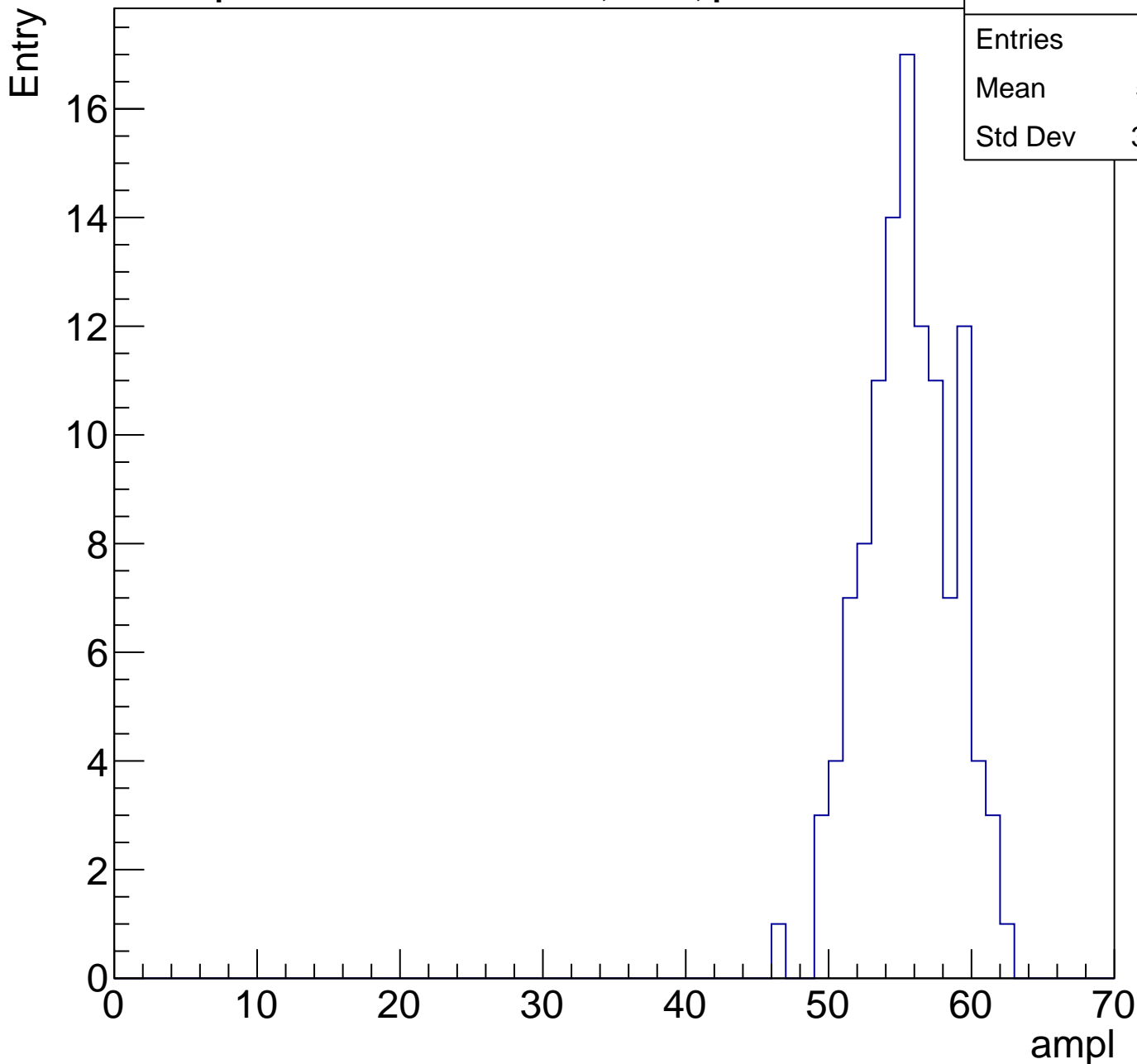
Entries	144
Mean	48.72
Std Dev	3.685



B1L001S, U16-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	55.11
Std Dev	3.089



B1L001S, U16-ch17, adc5

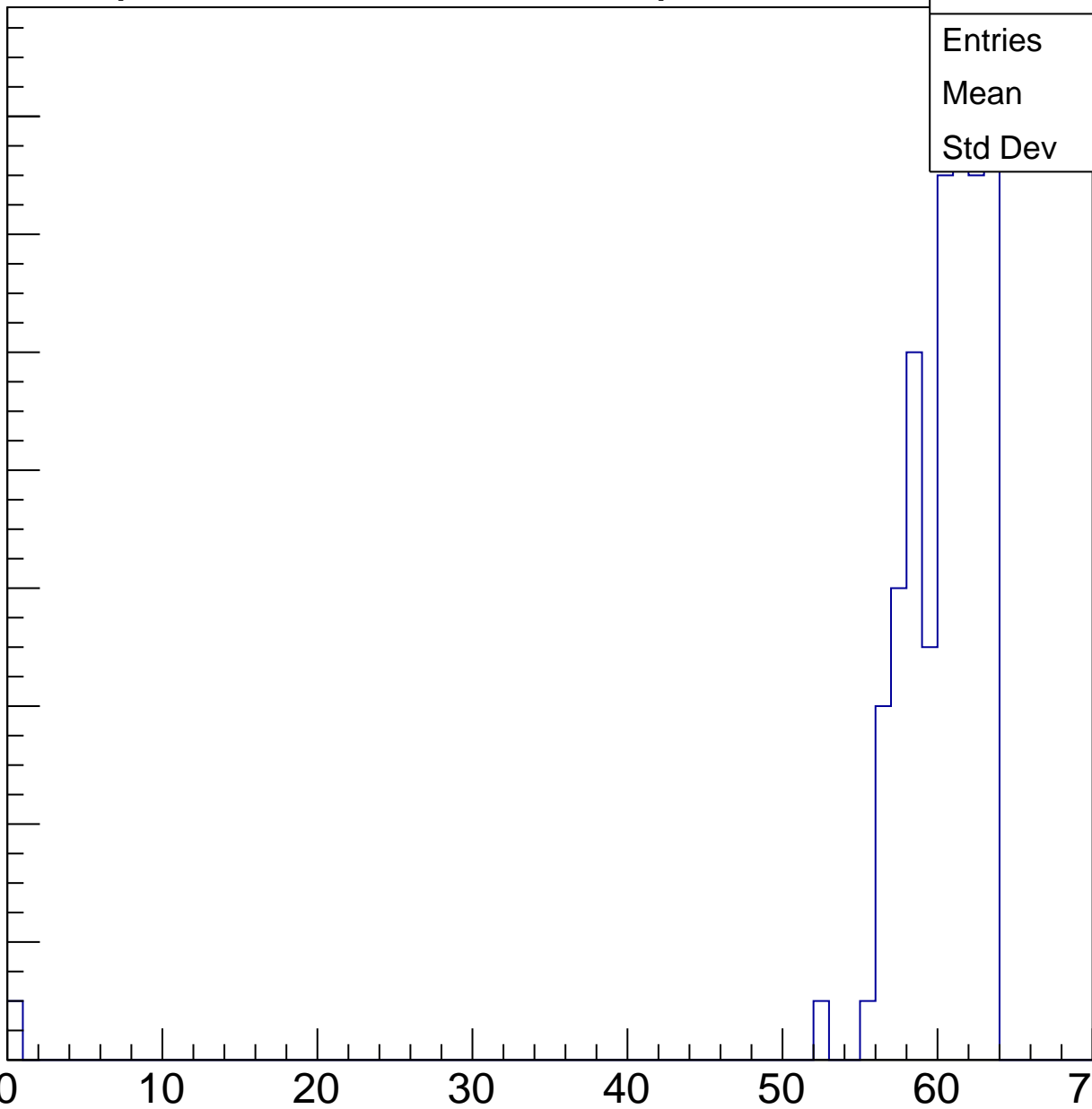
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.44
Std Dev	6.444

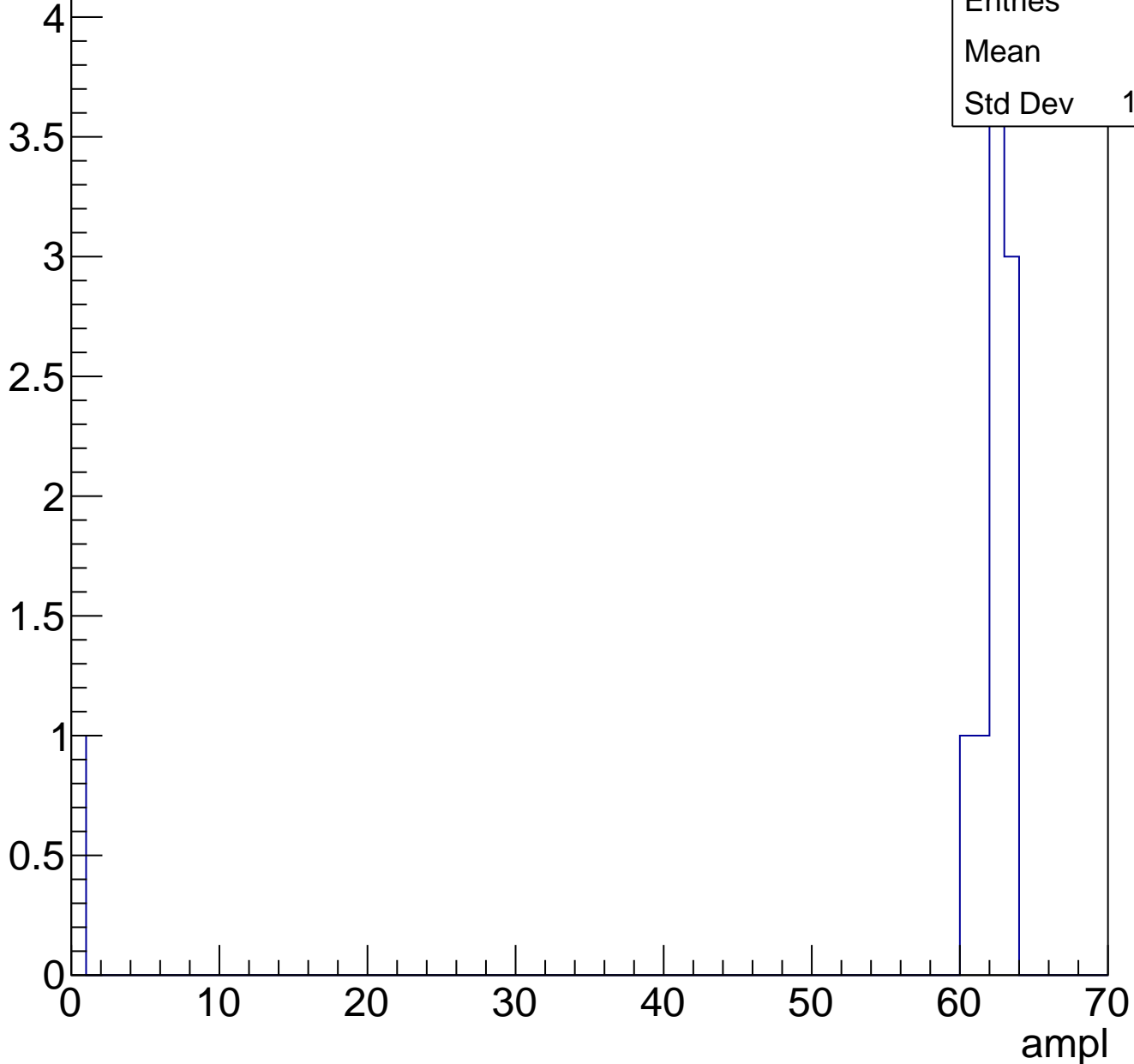
ampl



B1L001S, U16-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch18, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	28.84
Std Dev	3.442

Gaus mean : 29.1627

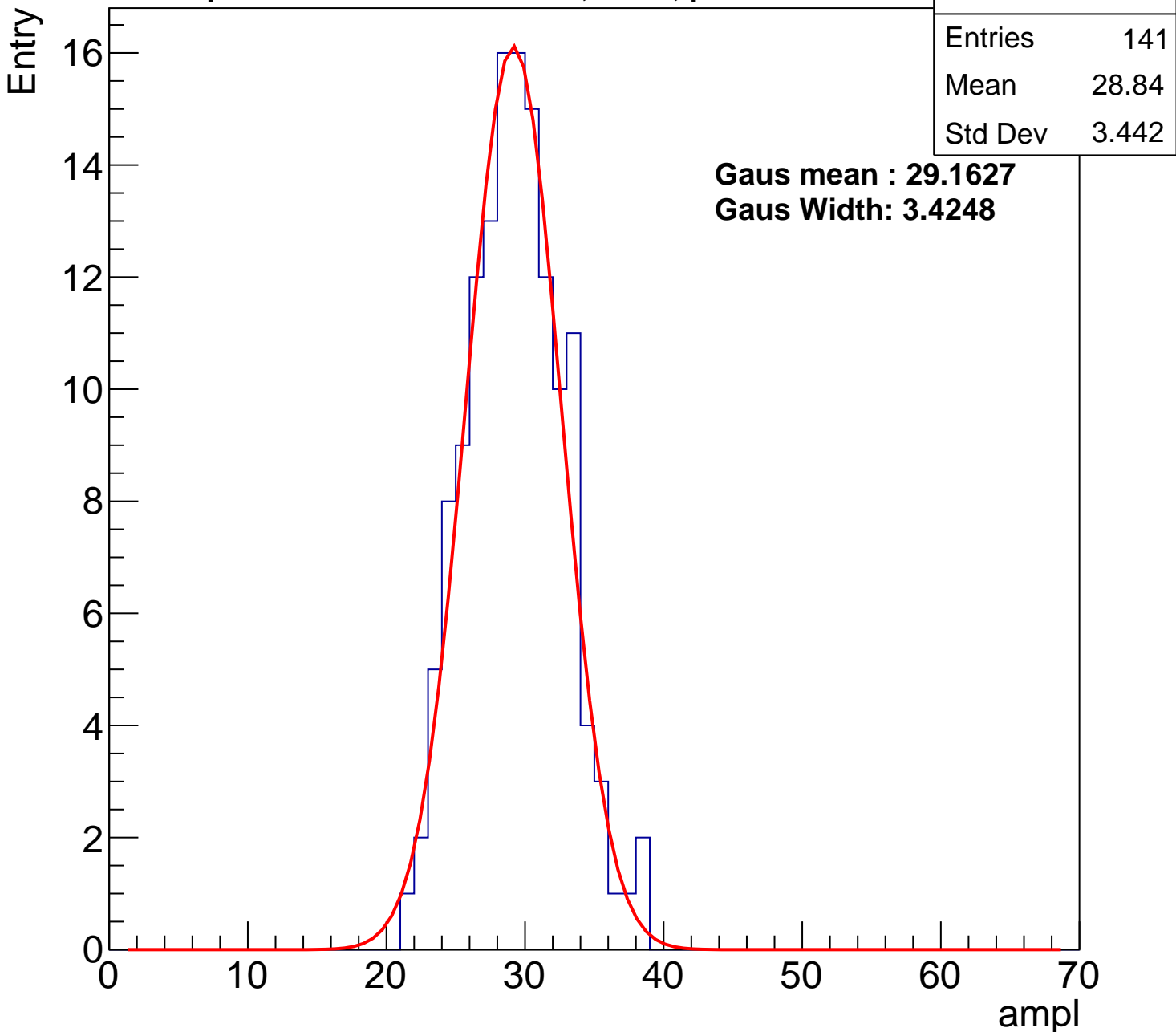
Gaus Width: 3.4248

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



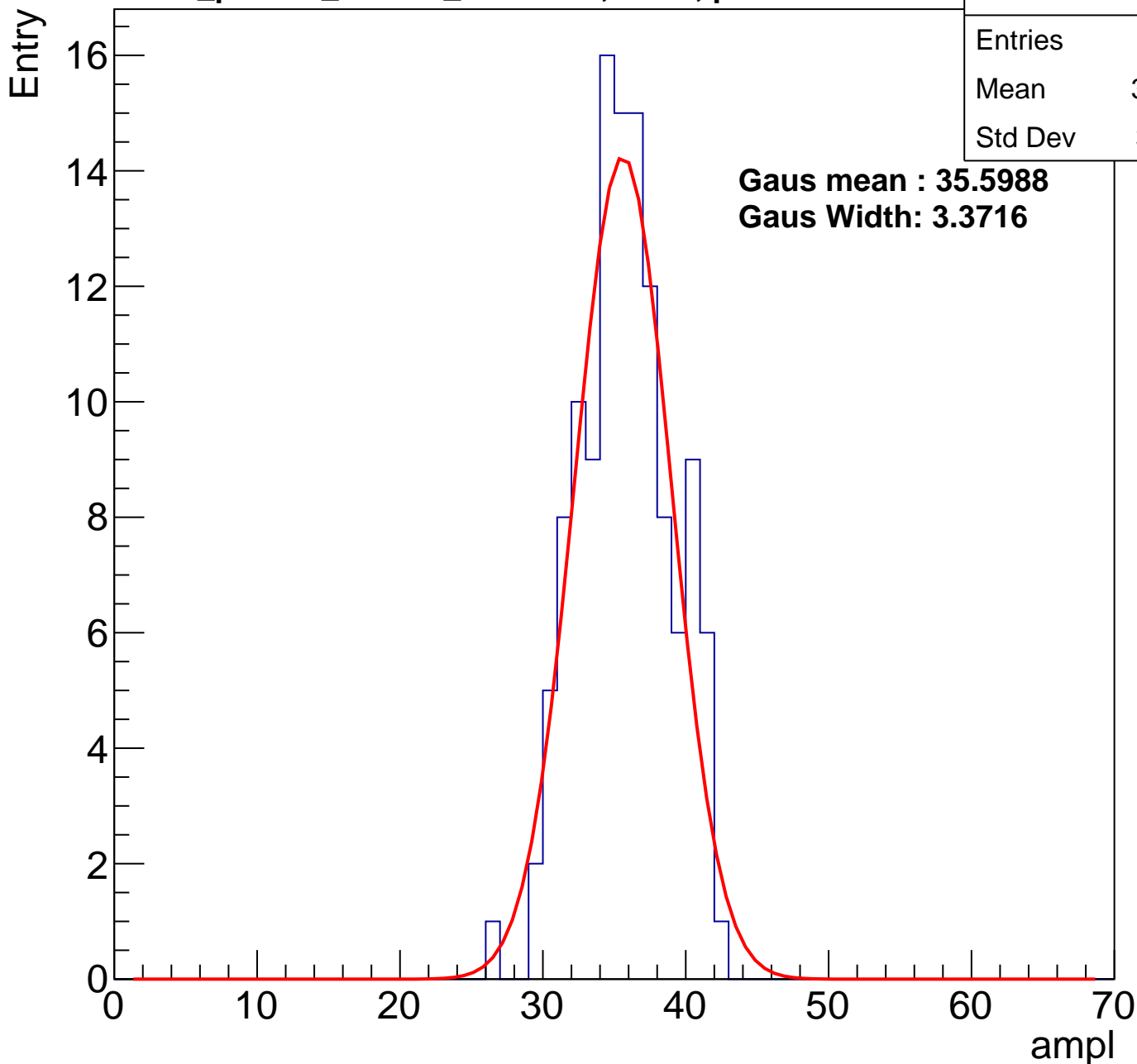
B1L001S, U16-ch18, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.27
Std Dev	3.201

Gaus mean : 35.5988

Gaus Width: 3.3716



B1L001S, U16-ch18, adc2

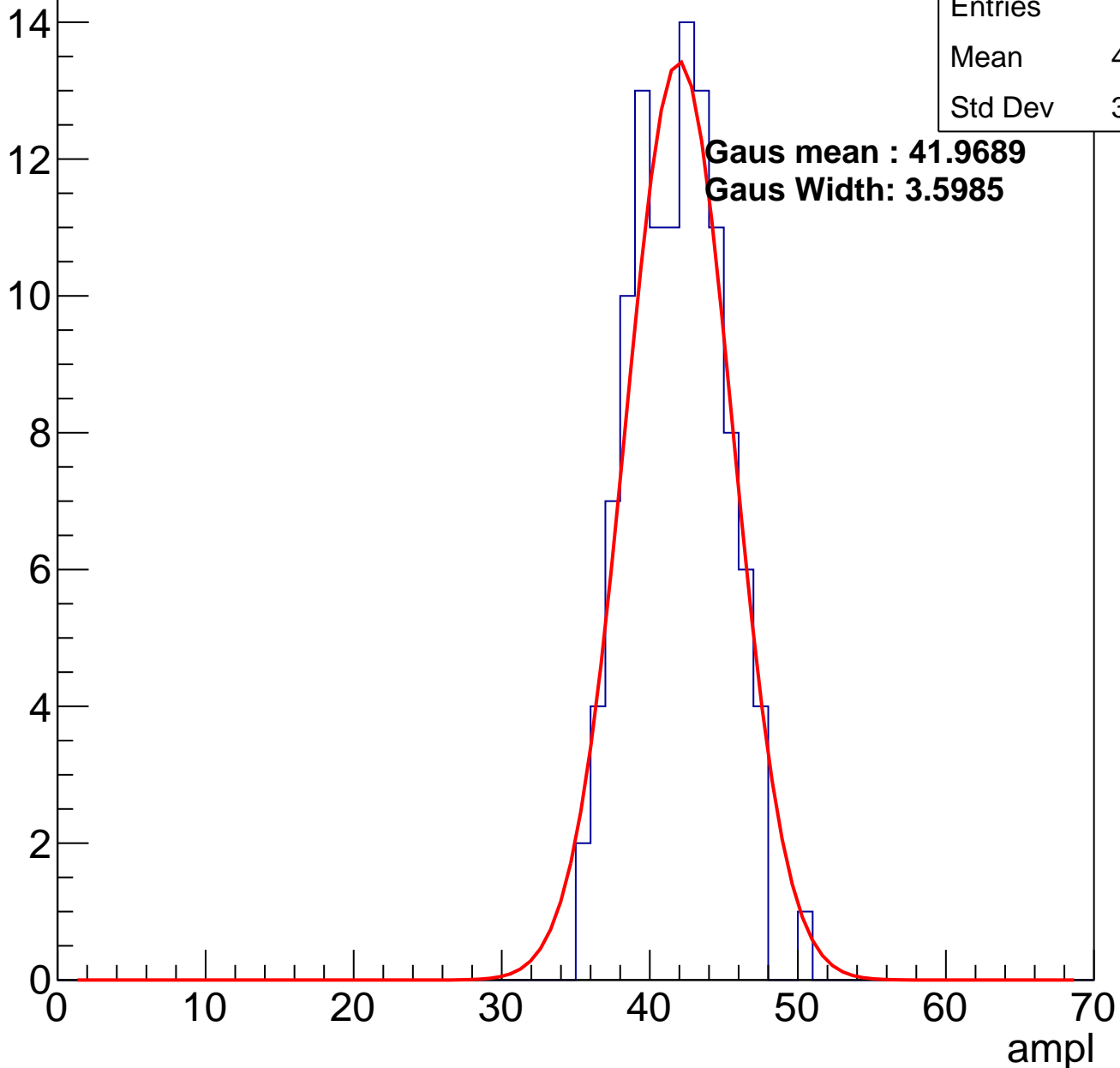
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	41.36
Std Dev	3.079

Gaus mean : 41.9689

Gaus Width: 3.5985

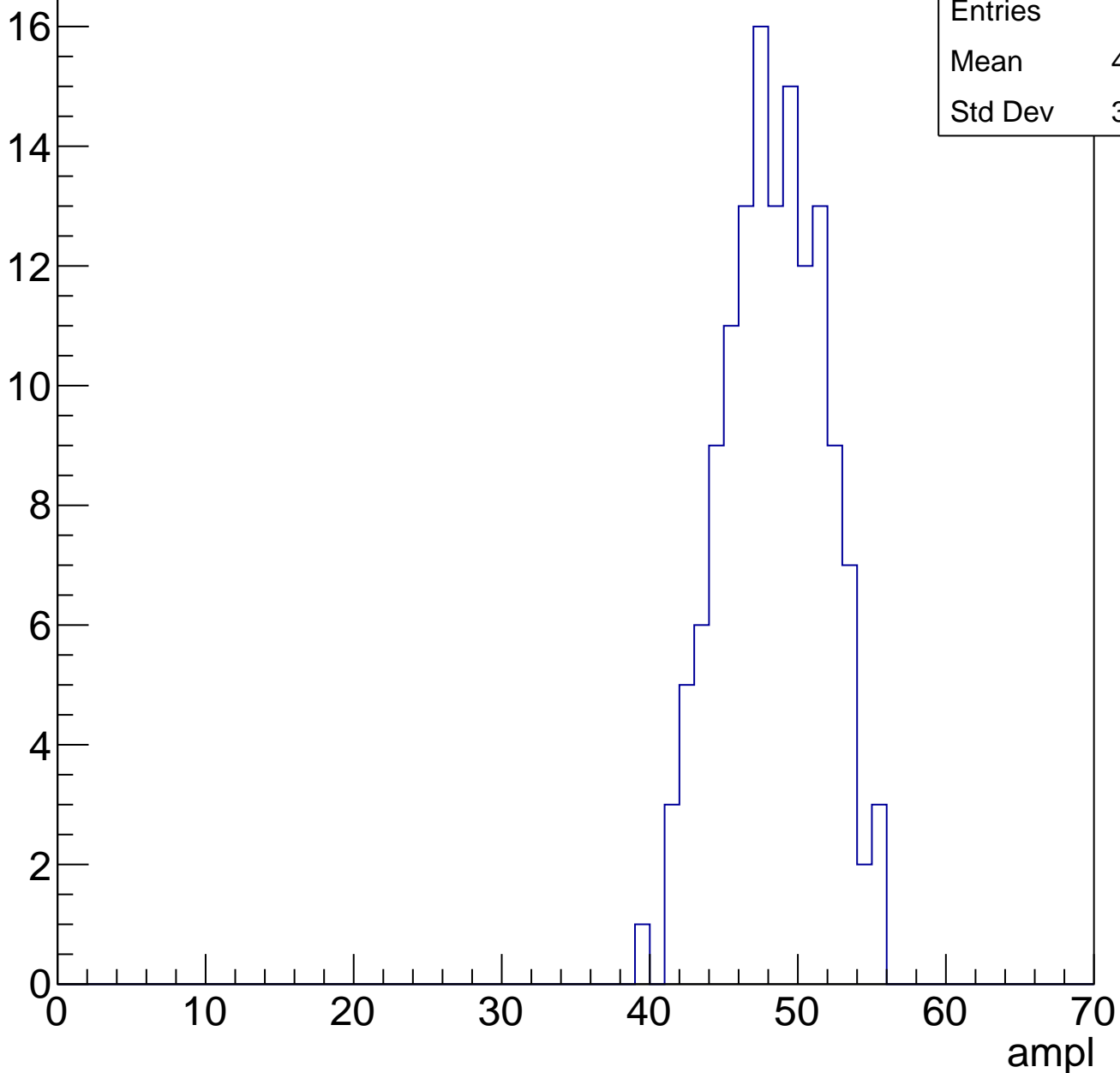
Entry



B1L001S, U16-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

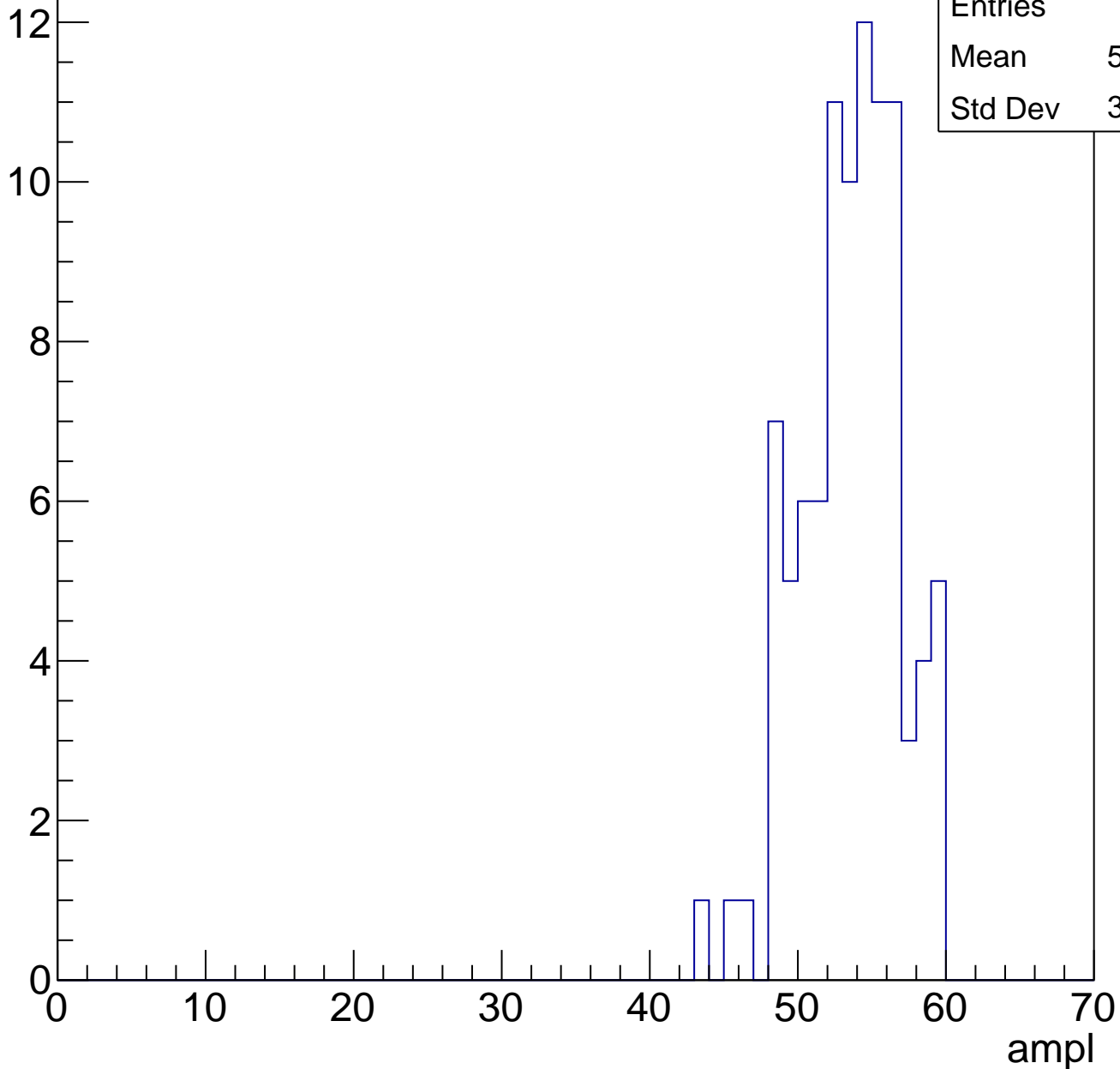


B1L001S, U16-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	53.09
Std Dev	3.325

Entry



B1L001S, U16-ch18, adc5

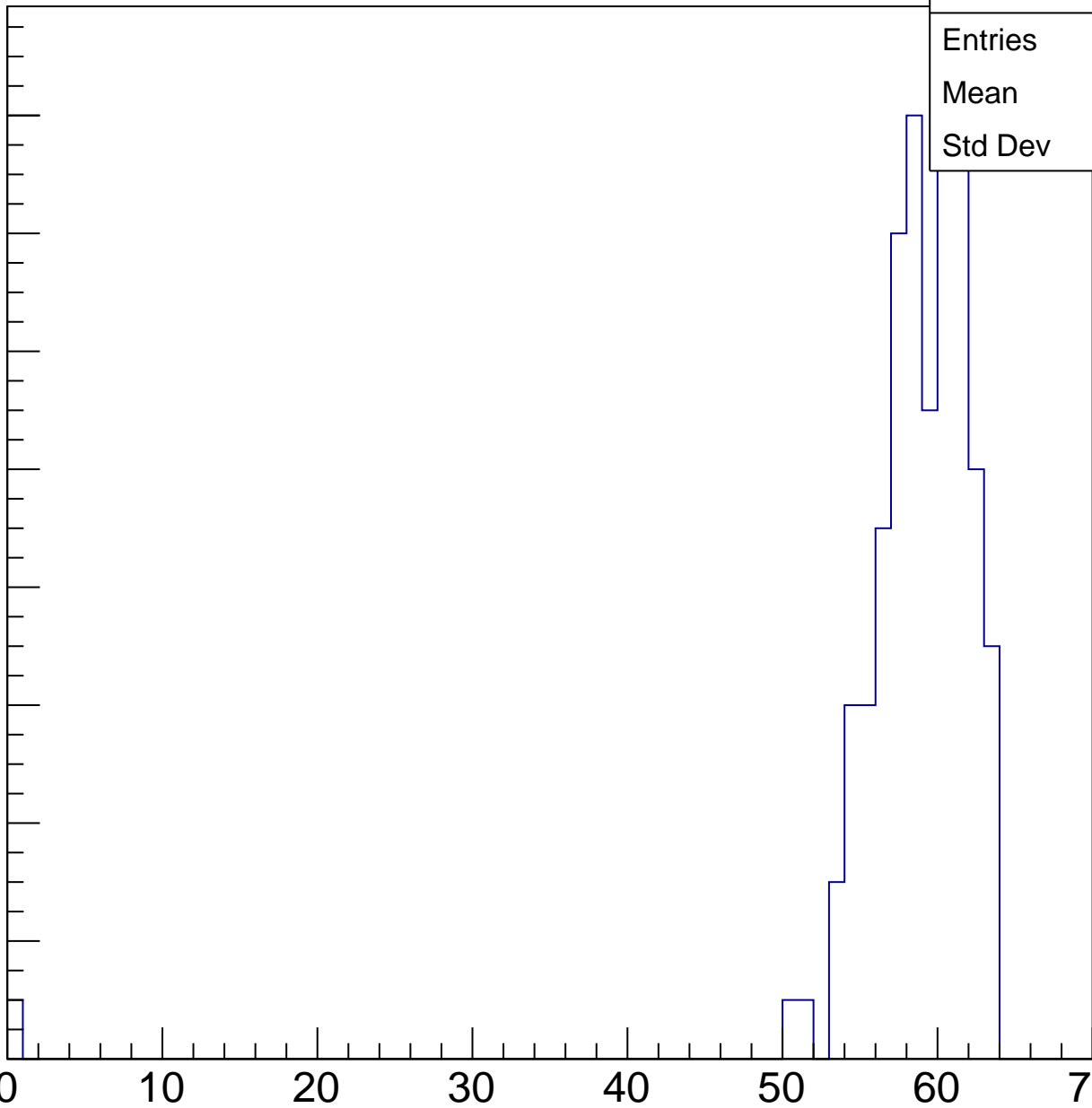
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	58.05
Std Dev	6.043

ampl



B1L001S, U16-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

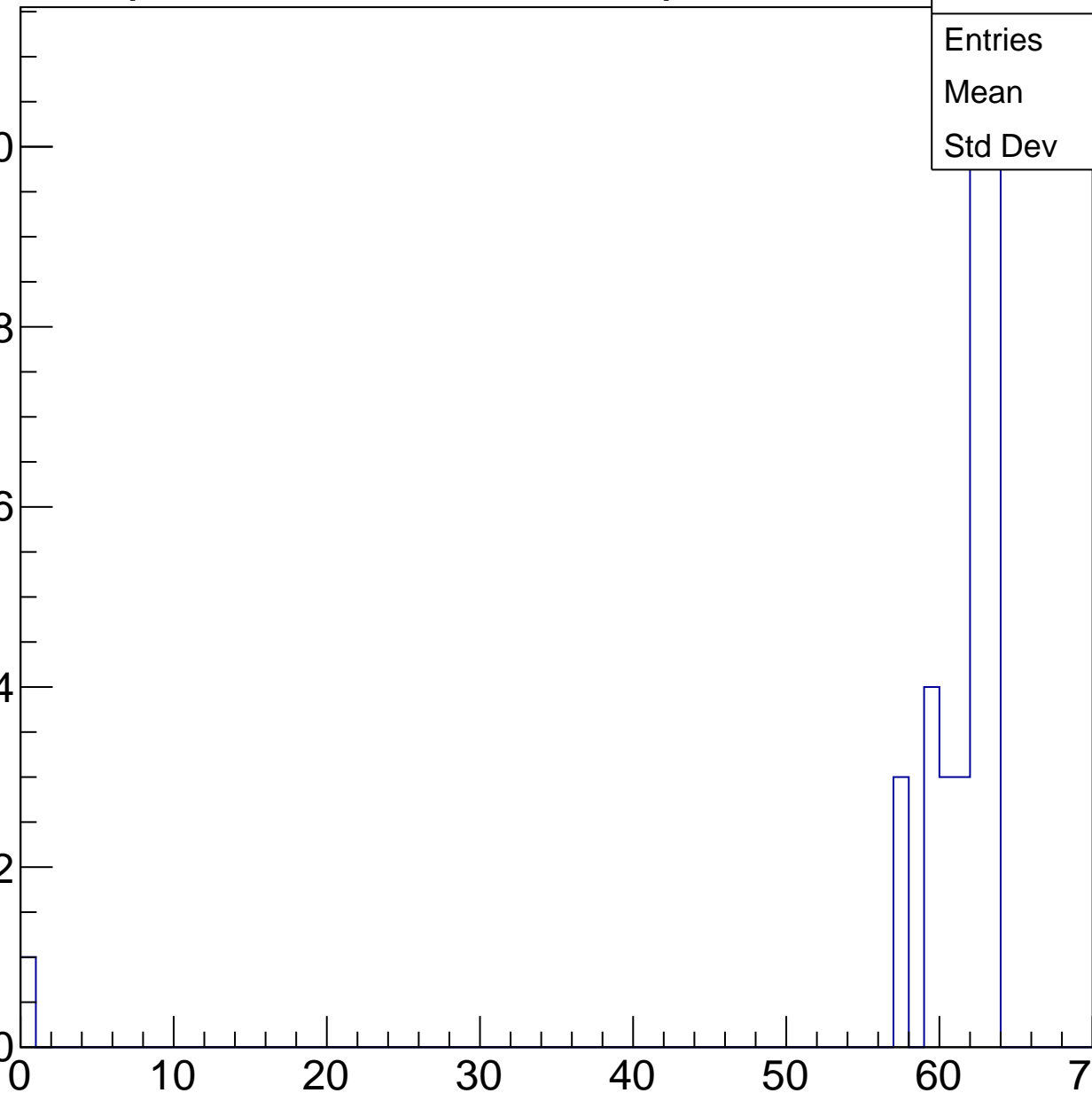
4

2

0

Entries	35
Mean	59.51
Std Dev	10.37

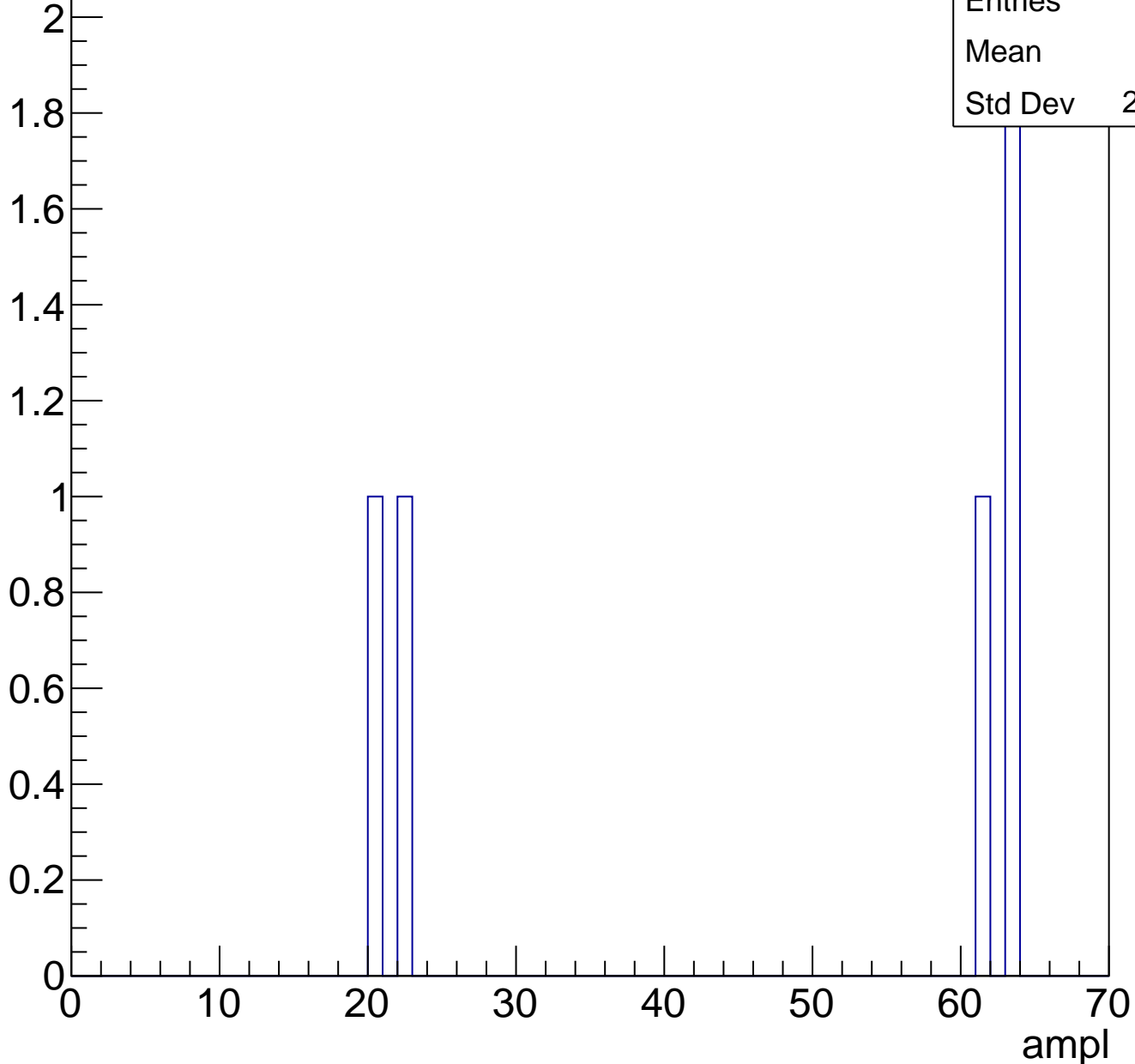
ampl



B1L001S, U16-ch18, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	30.96
Std Dev	4.24

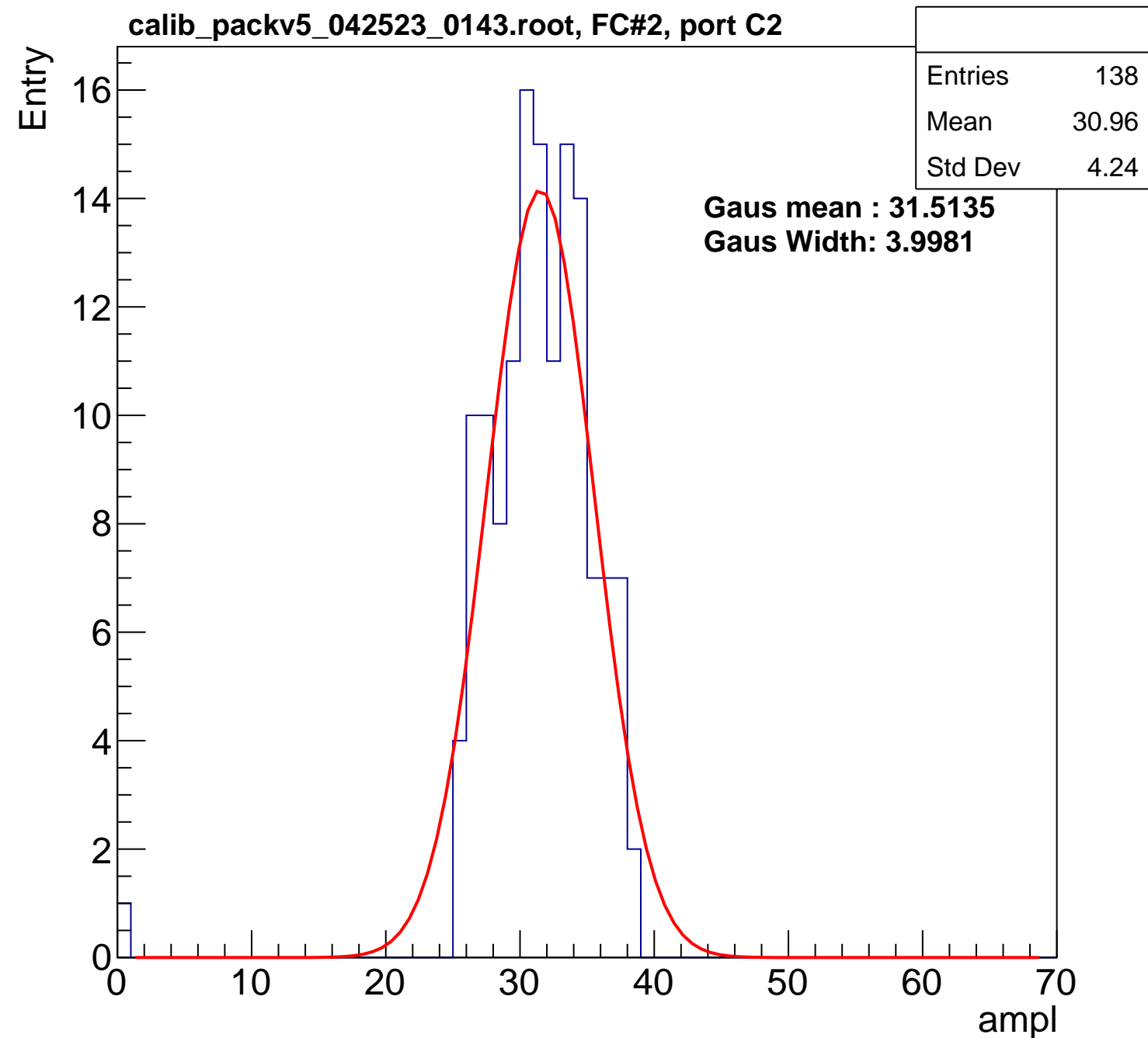
Gaus mean : 31.5135

Gaus Width: 3.9981

Entry

16
14
12
10
8
6
4
2
0

ampl

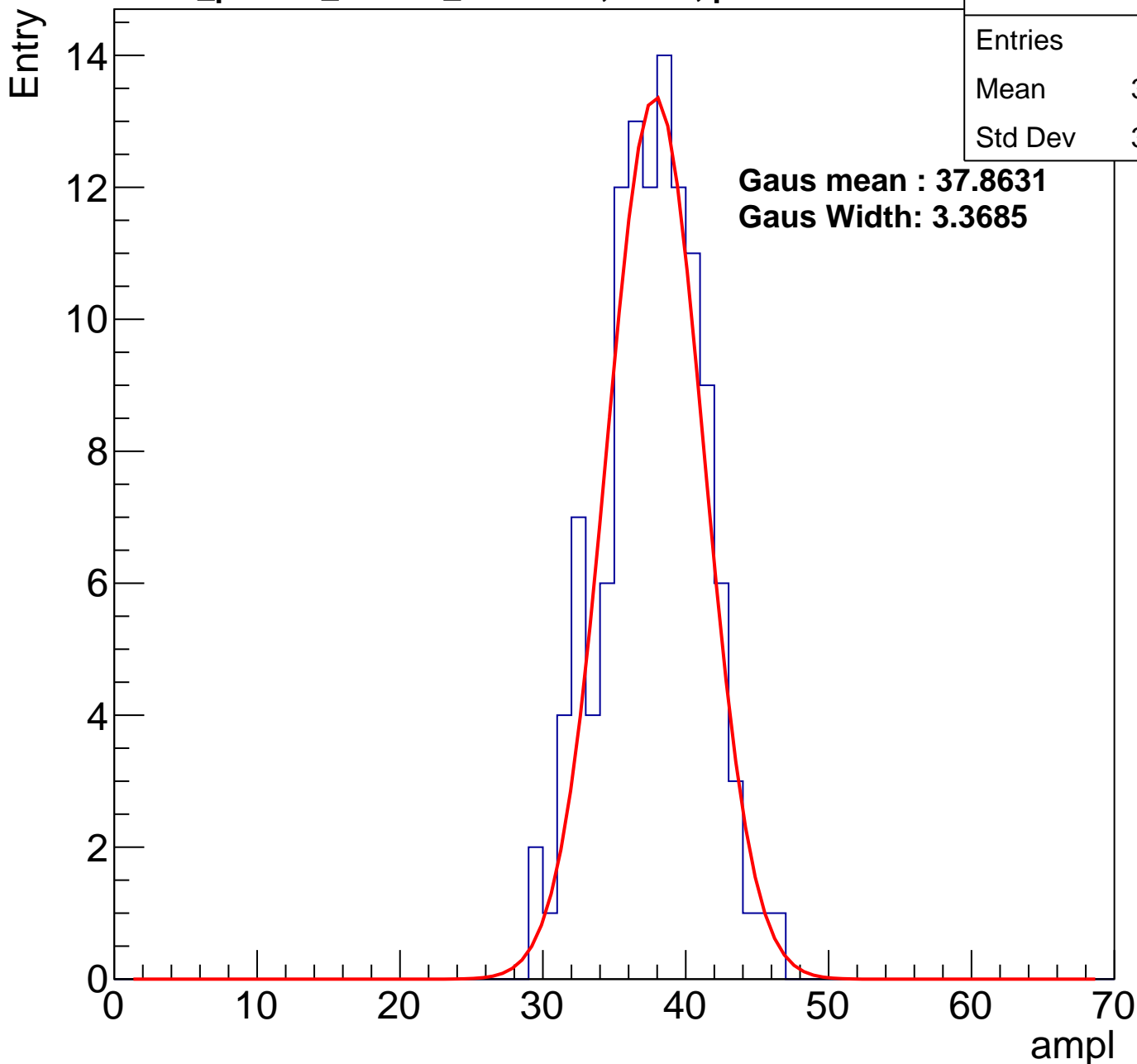


B1L001S, U16-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.22
Std Dev	3.462

Gaus mean : 37.8631
Gaus Width: 3.3685



B1L001S, U16-ch19, adc2

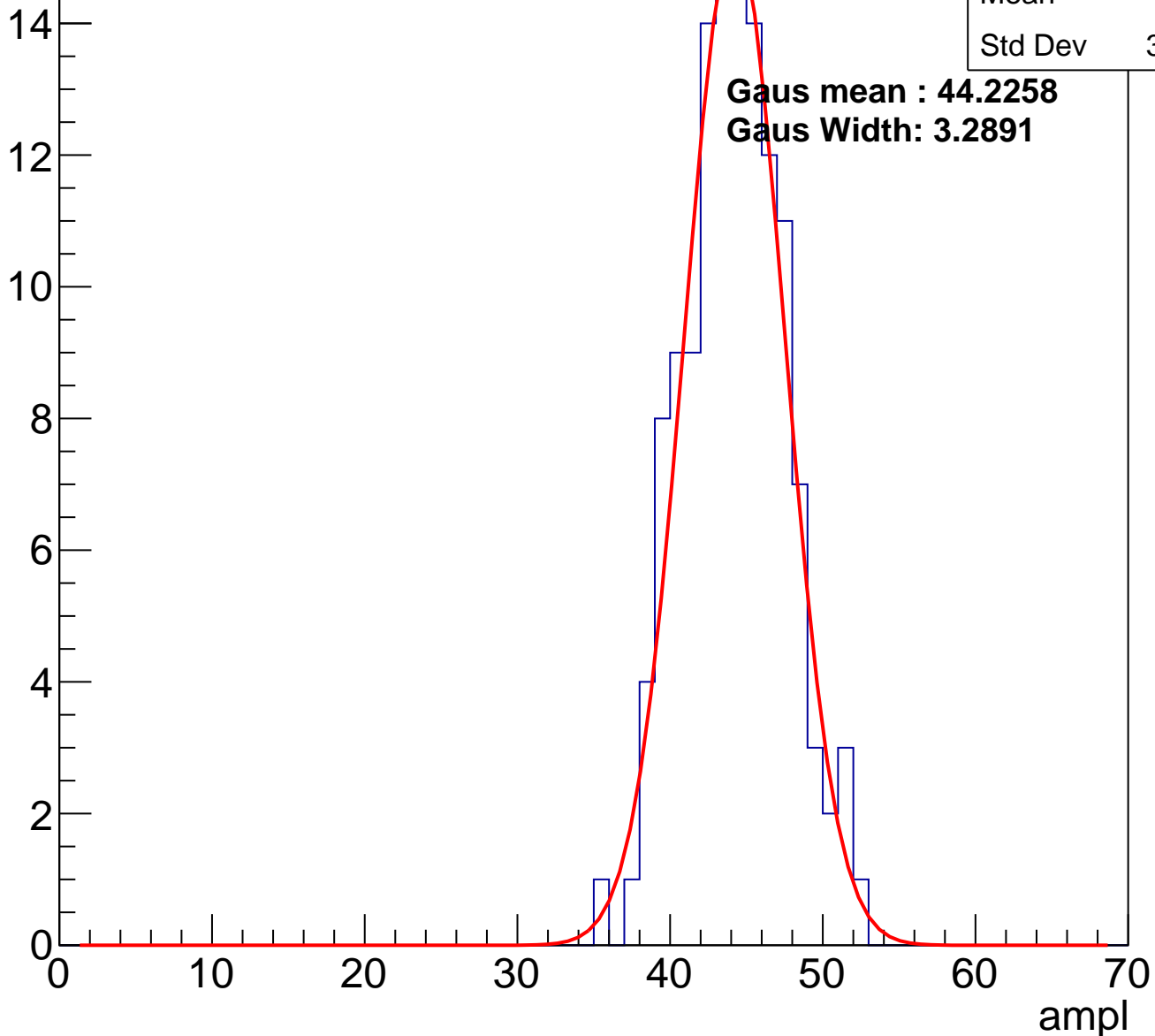
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	43.76
Std Dev	3.287

Gaus mean : 44.2258

Gaus Width: 3.2891

Entry

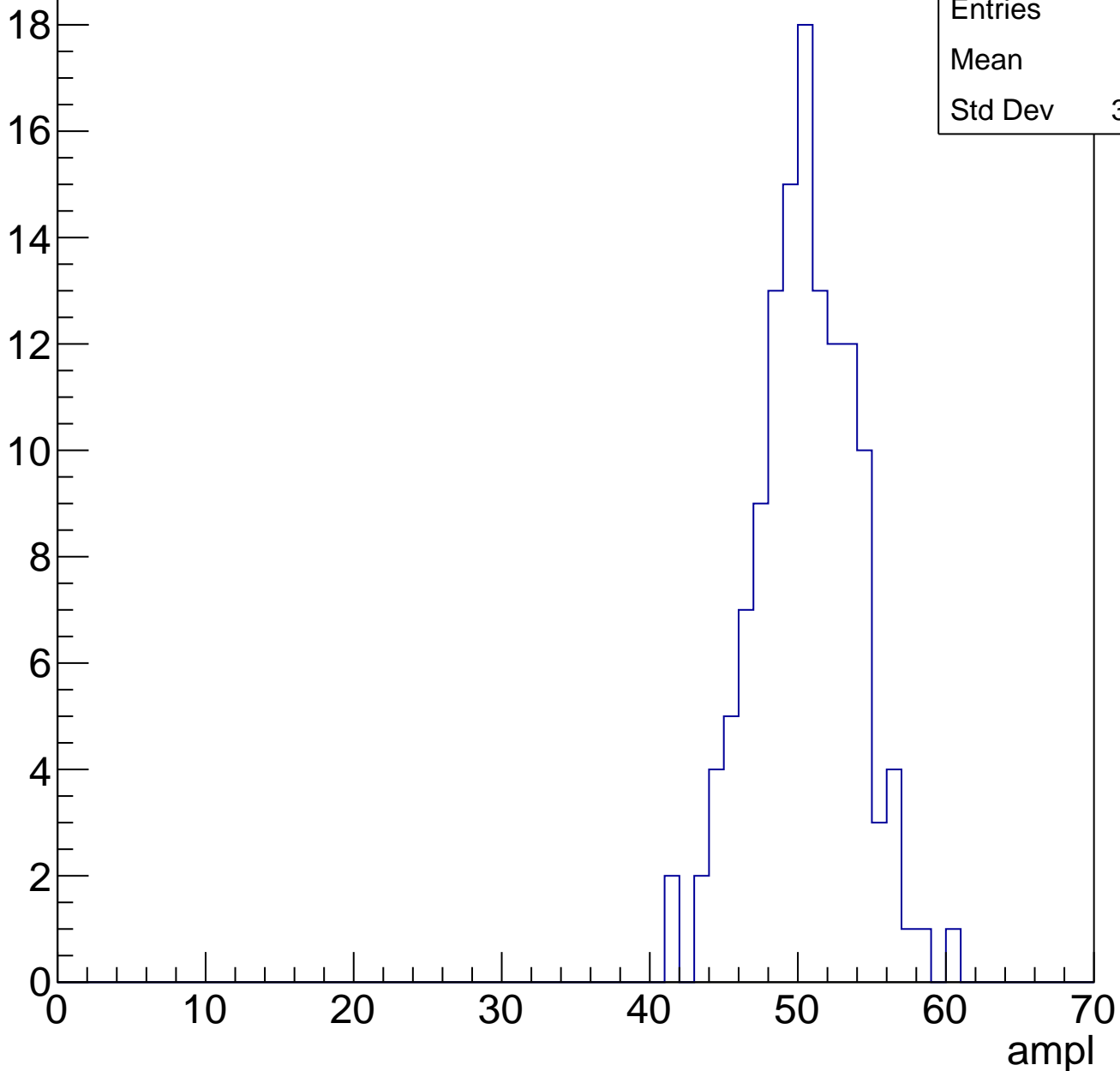


B1L001S, U16-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	50
Std Dev	3.427

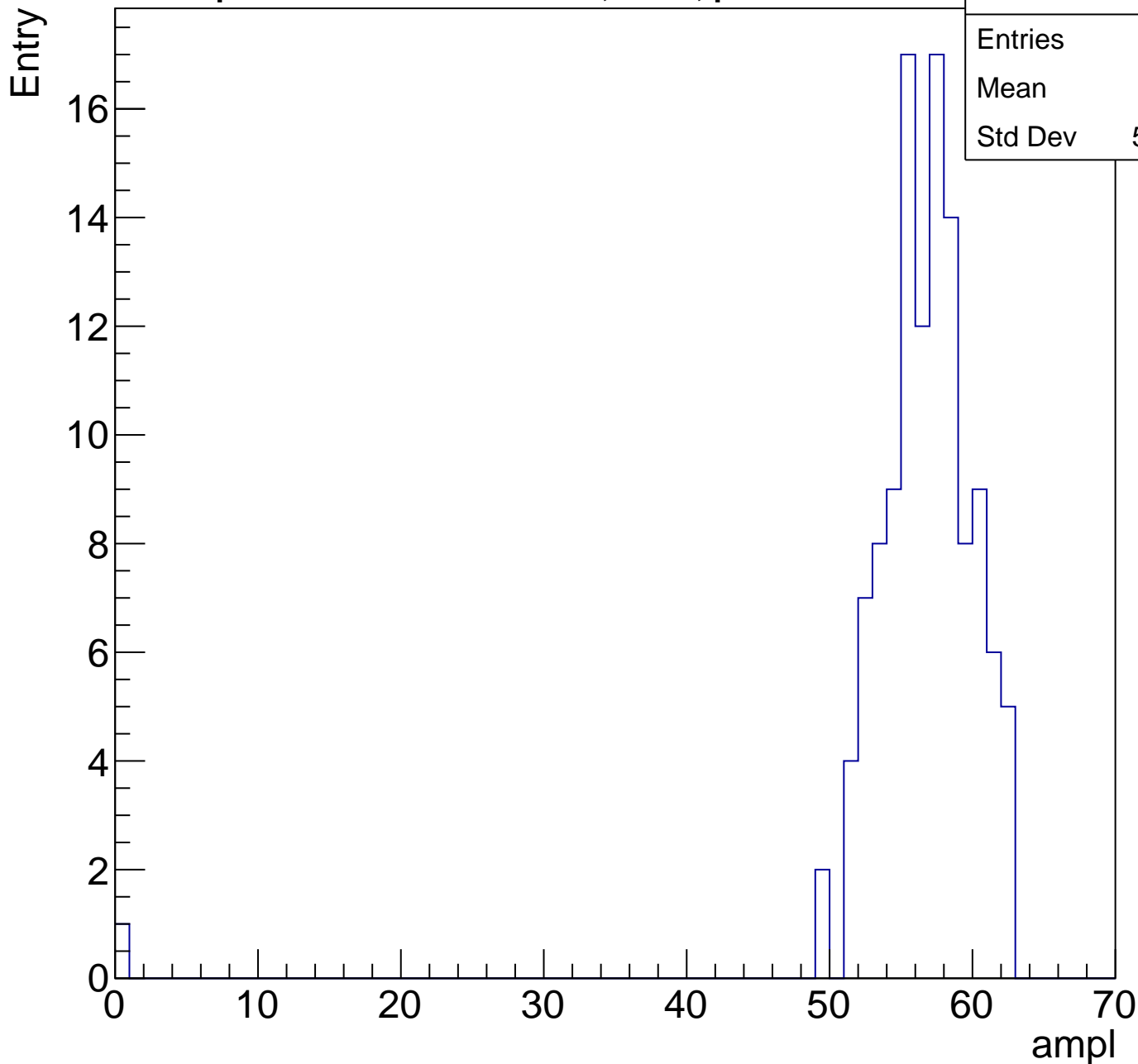
Entry



B1L001S, U16-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.9
Std Dev	5.933



B1L001S, U16-ch19, adc5

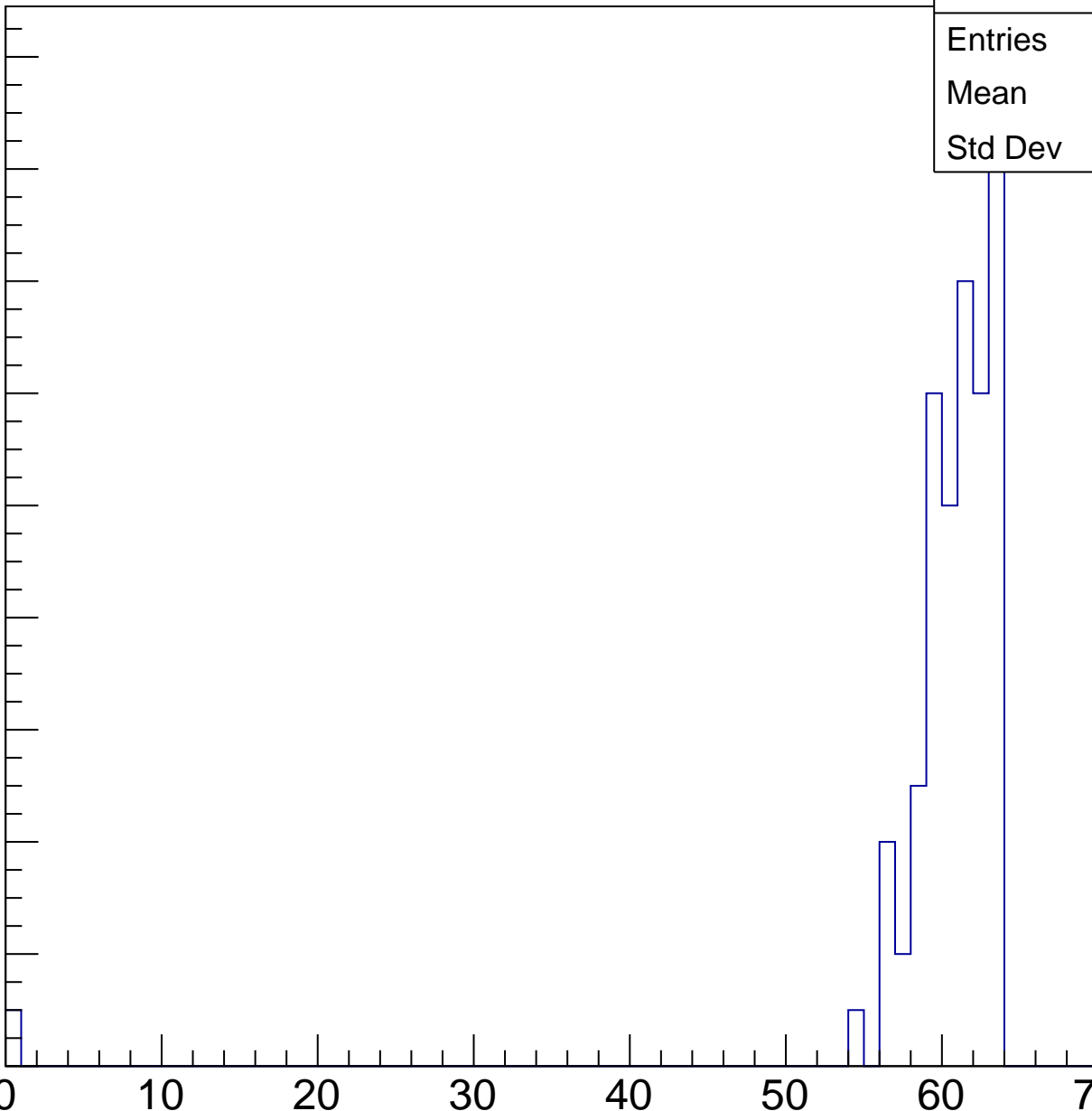
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.77
Std Dev	7.089

ampl



B1L001S, U16-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

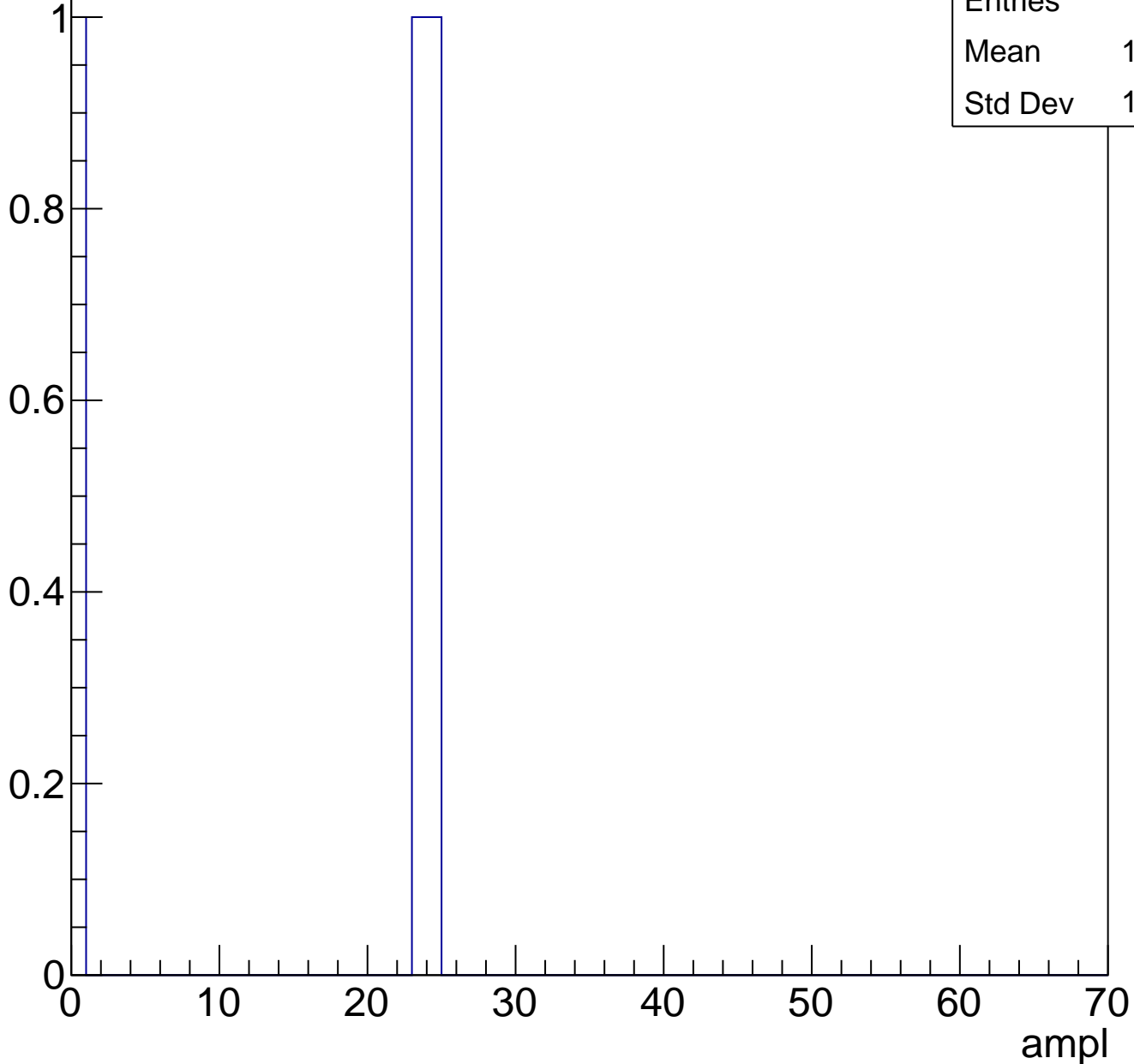
Entry



B1L001S, U16-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch20, adc0

calib_packv5_042523_0143.root, FC#2, port C2

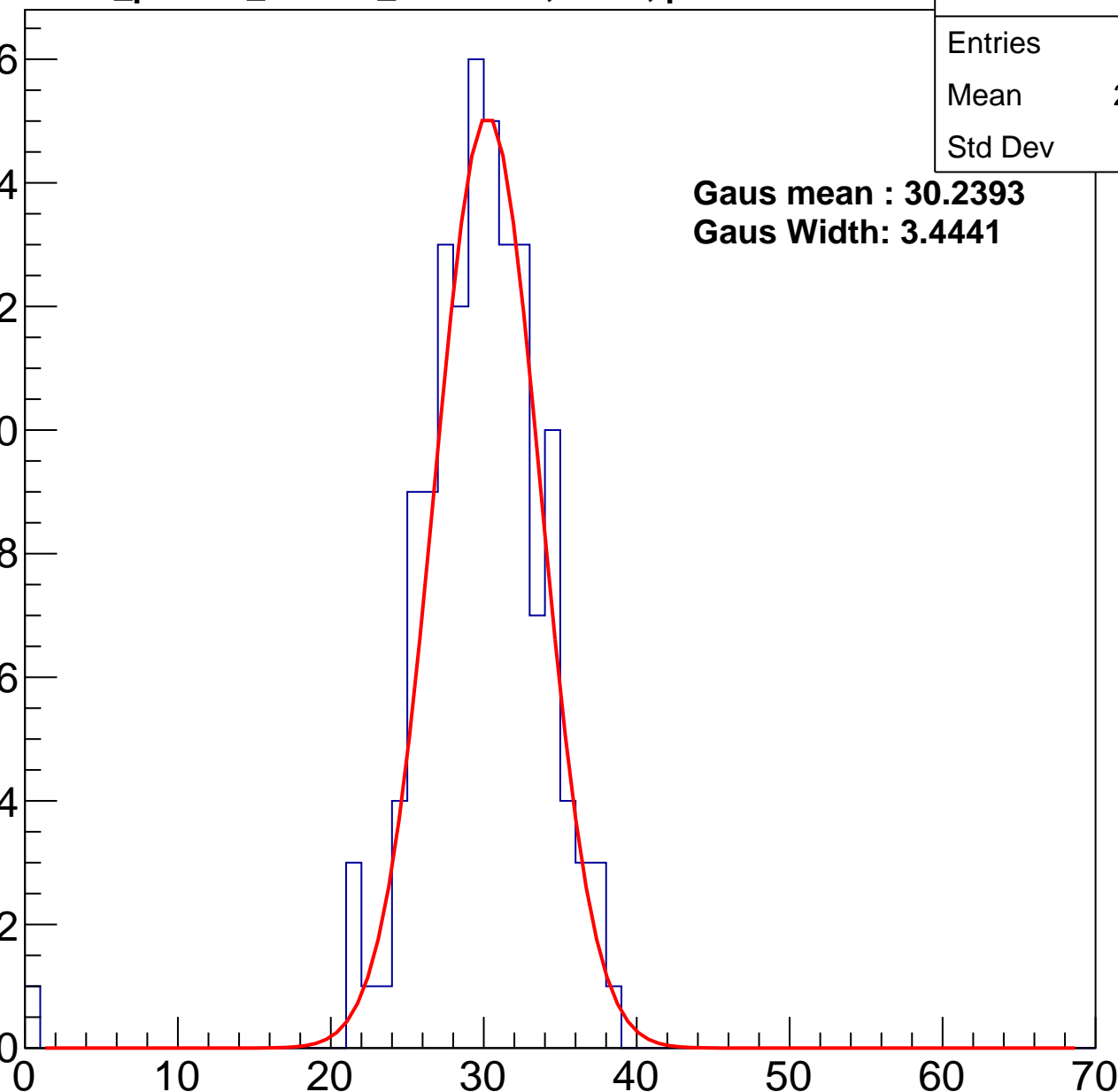
Entry

16
14
12
10
8
6
4
2
0

Entries	138
Mean	29.36
Std Dev	4.34

Gaus mean : 30.2393

Gaus Width: 3.4441



ampl

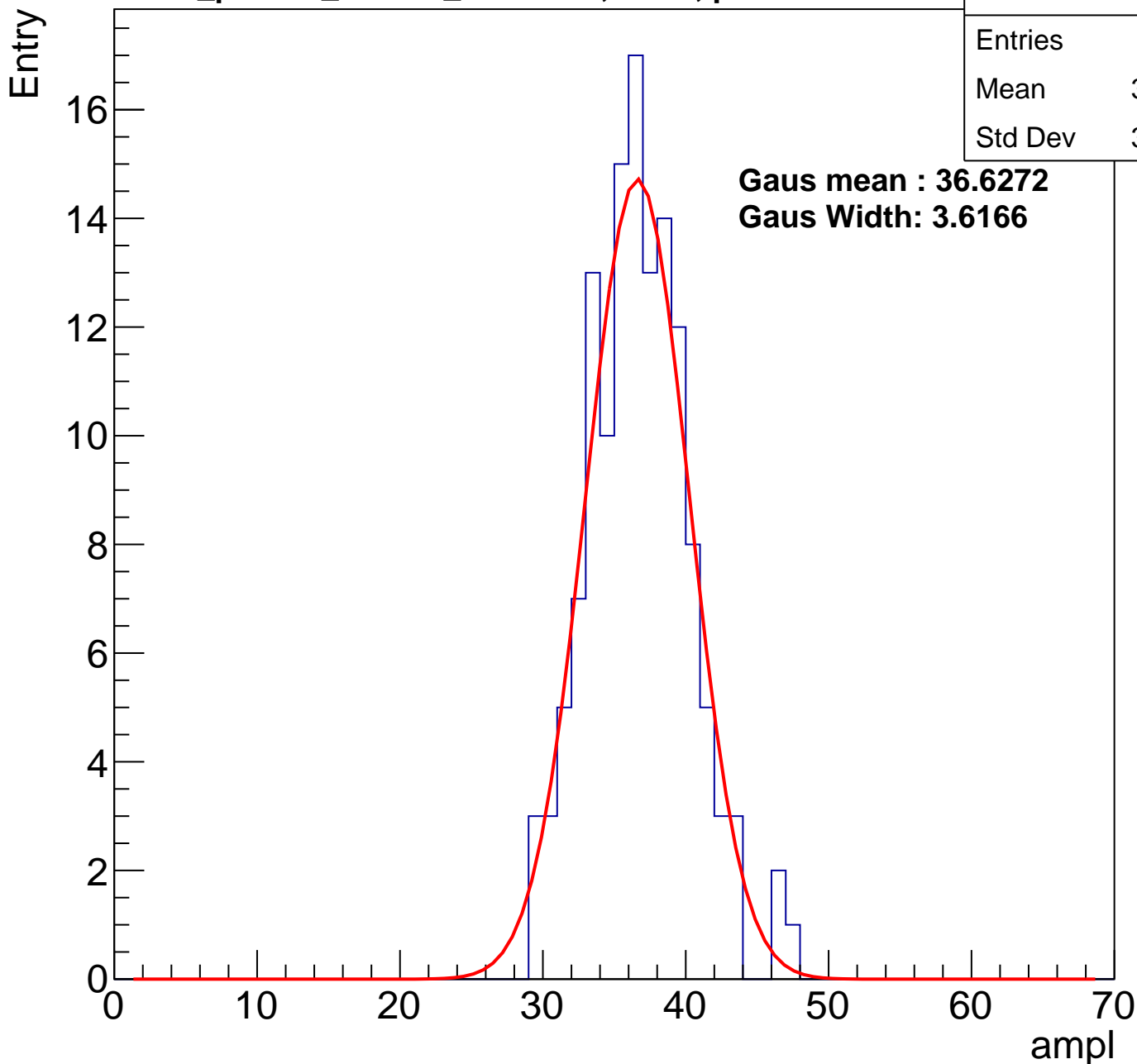
B1L001S, U16-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	36.28
Std Dev	3.519

Gaus mean : 36.6272

Gaus Width: 3.6166



B1L001S, U16-ch20, adc2

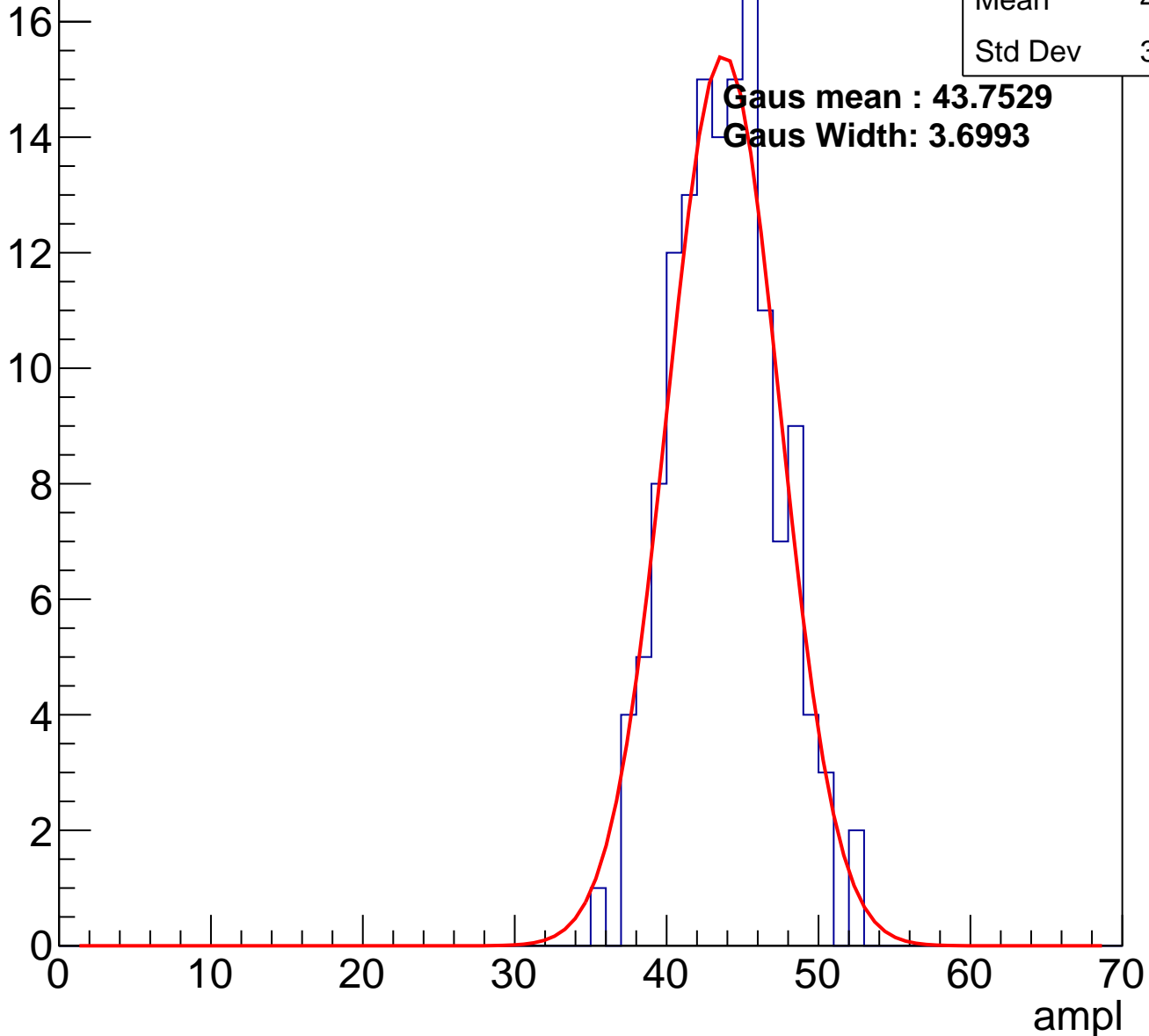
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	140
Mean	43.37
Std Dev	3.369

Gaus mean : 43.7529

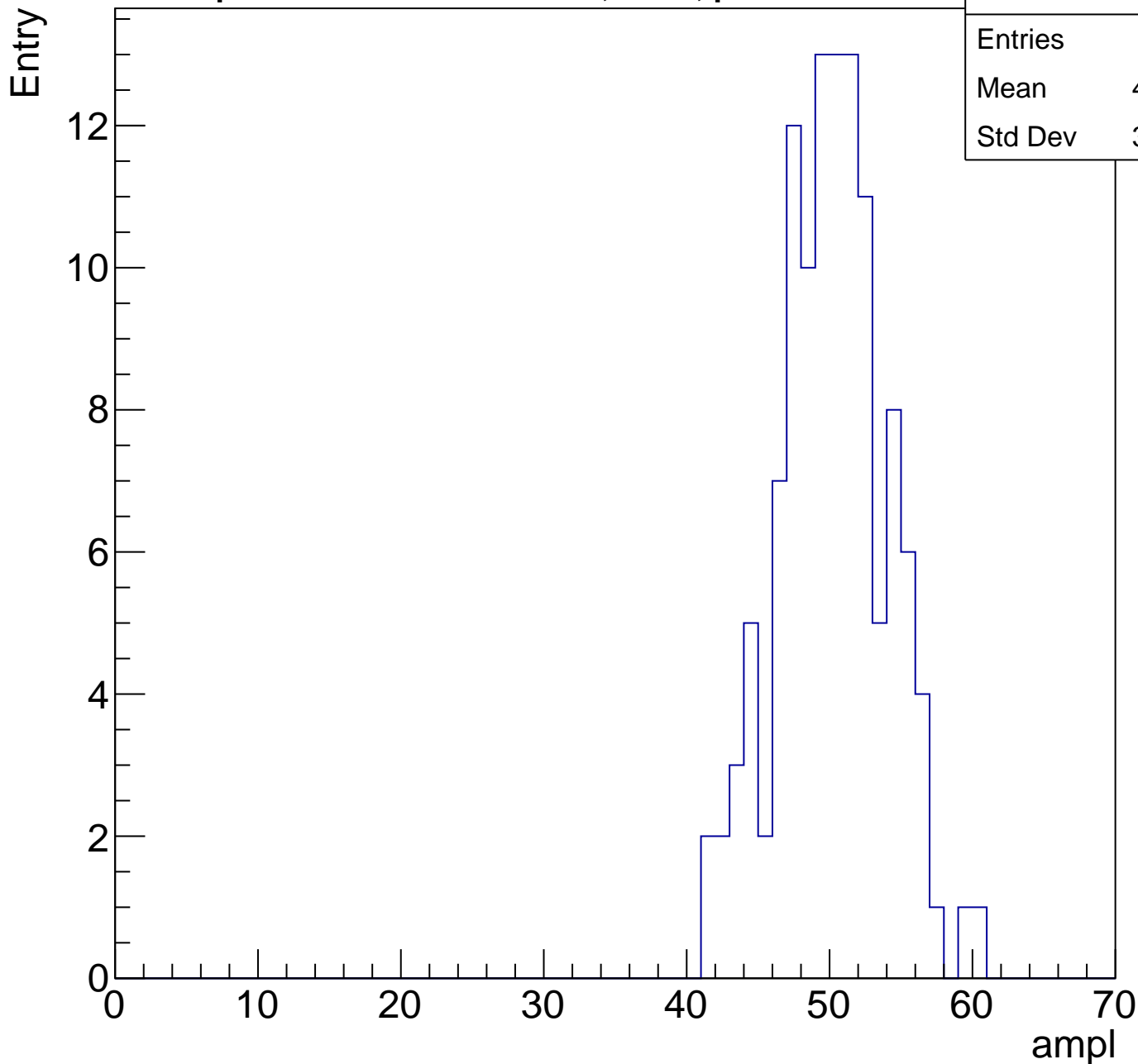
Gaus Width: 3.6993



B1L001S, U16-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

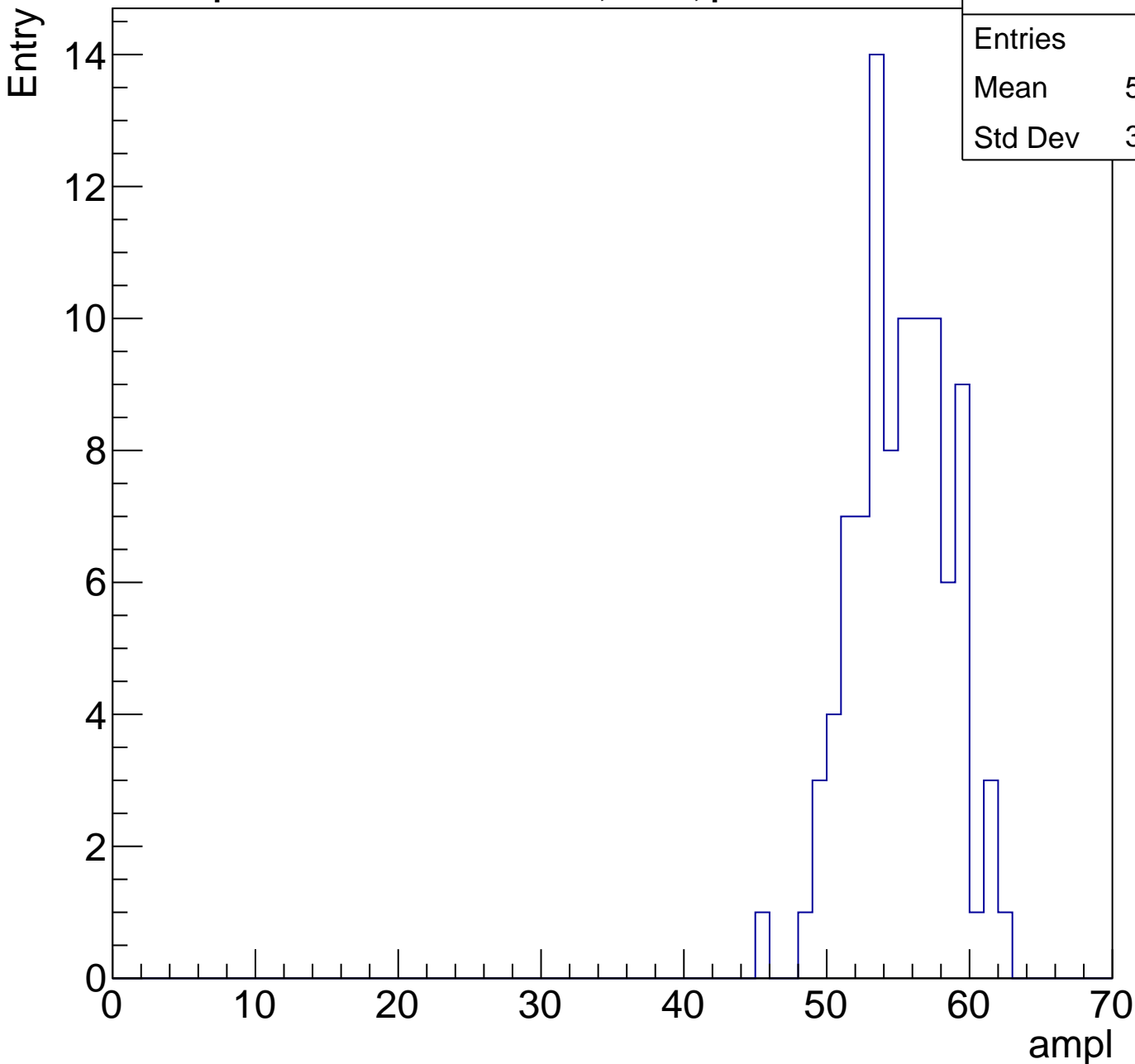
Entries	119
Mean	49.75
Std Dev	3.765



B1L001S, U16-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

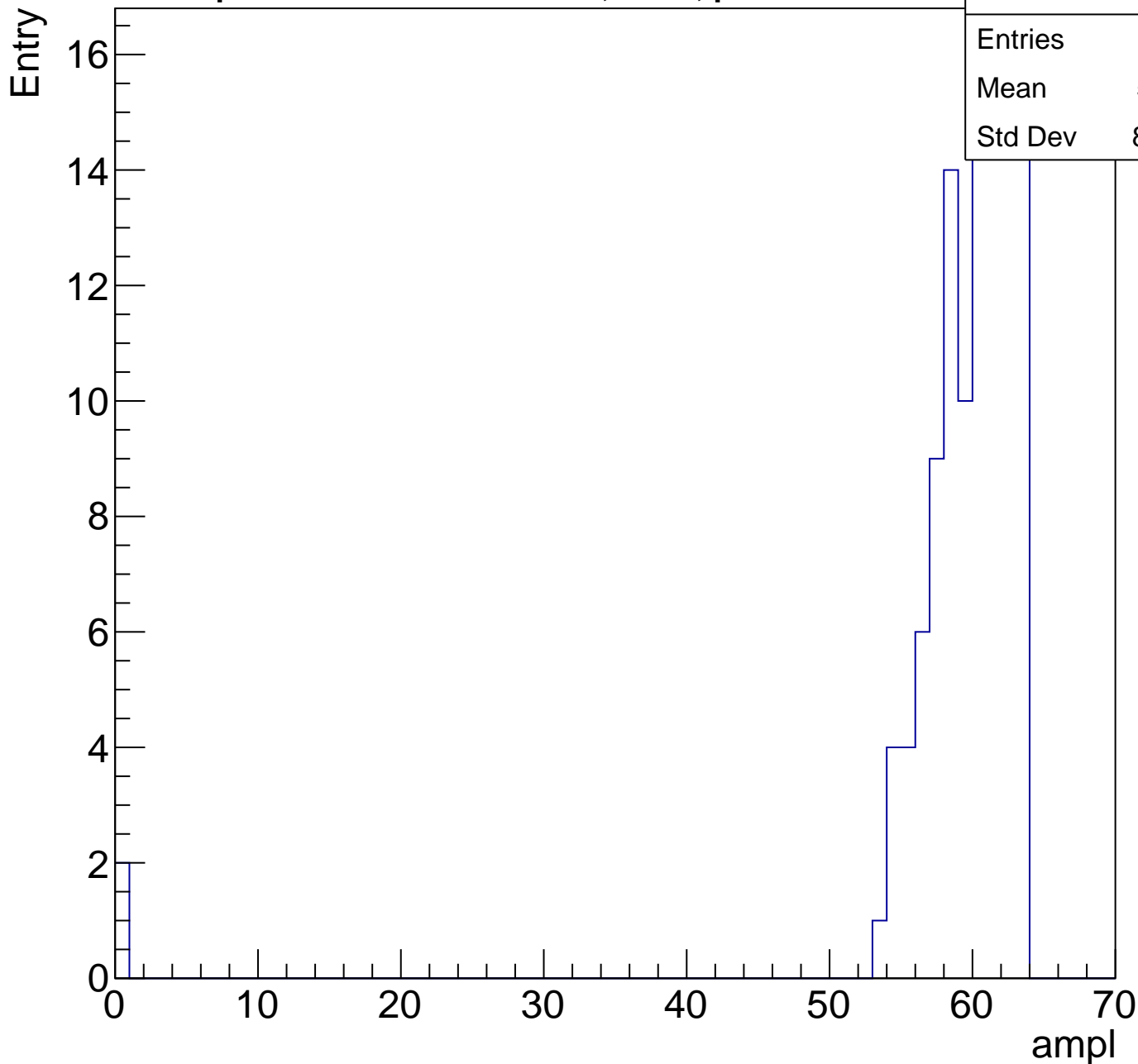
Entries	95
Mean	54.73
Std Dev	3.275



B1L001S, U16-ch20, adc5

calib_packv5_042523_0143.root, FC#2, port C2

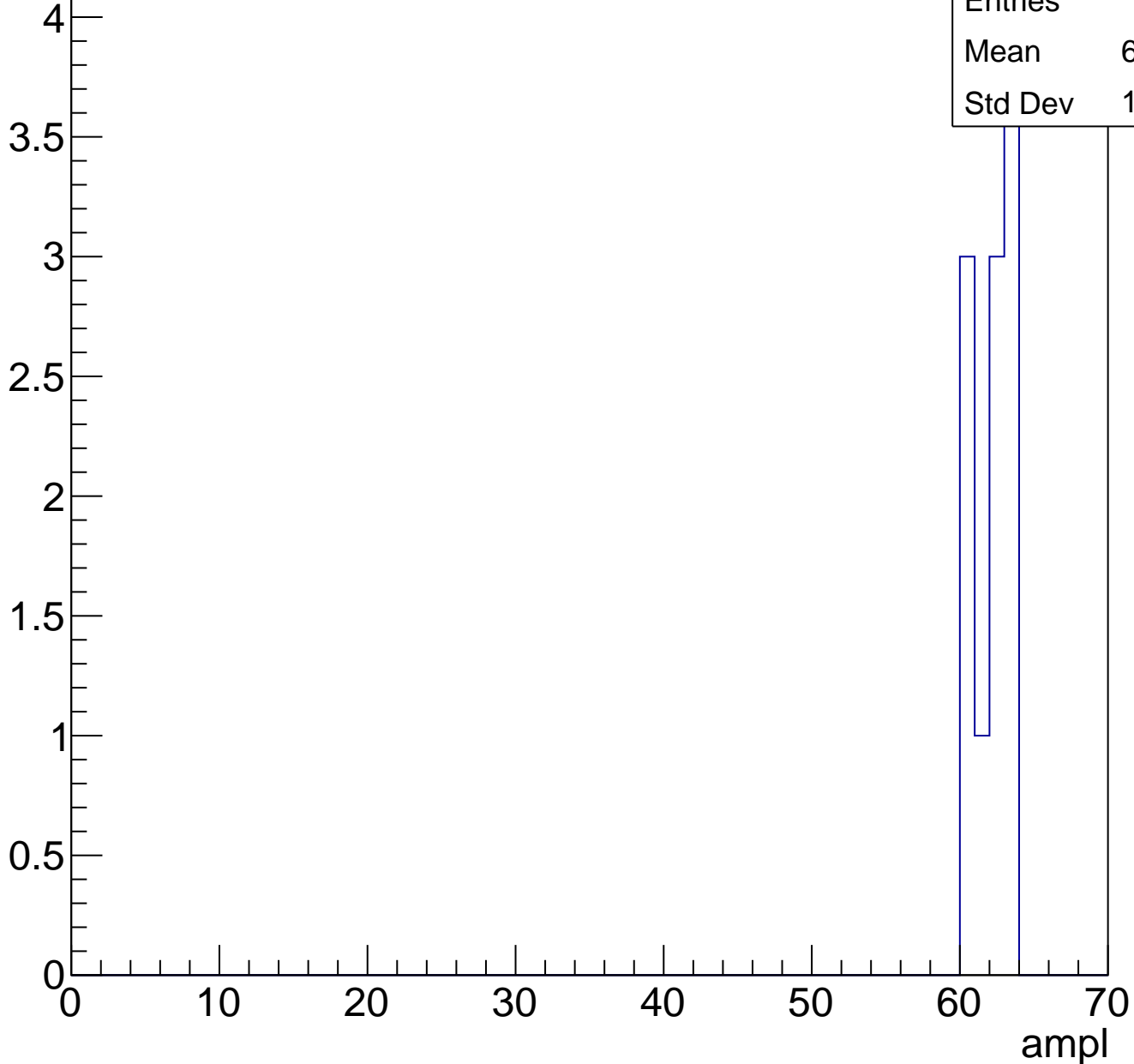
Entries	112
Mean	58.51
Std Dev	8.293



B1L001S, U16-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch21, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.22
Std Dev	3.167

Gaus mean : 29.9127

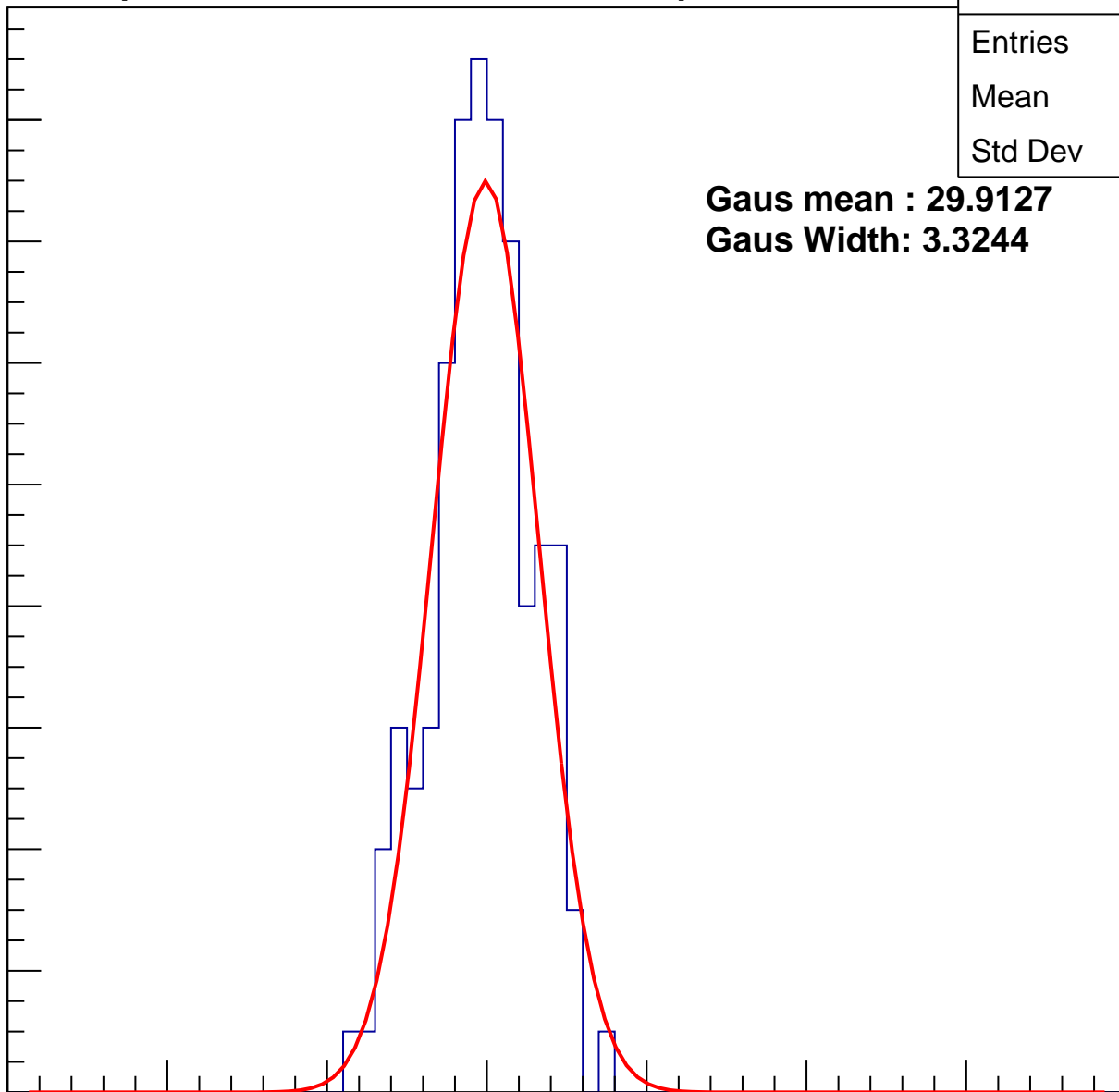
Gaus Width: 3.3244

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch21, adc1

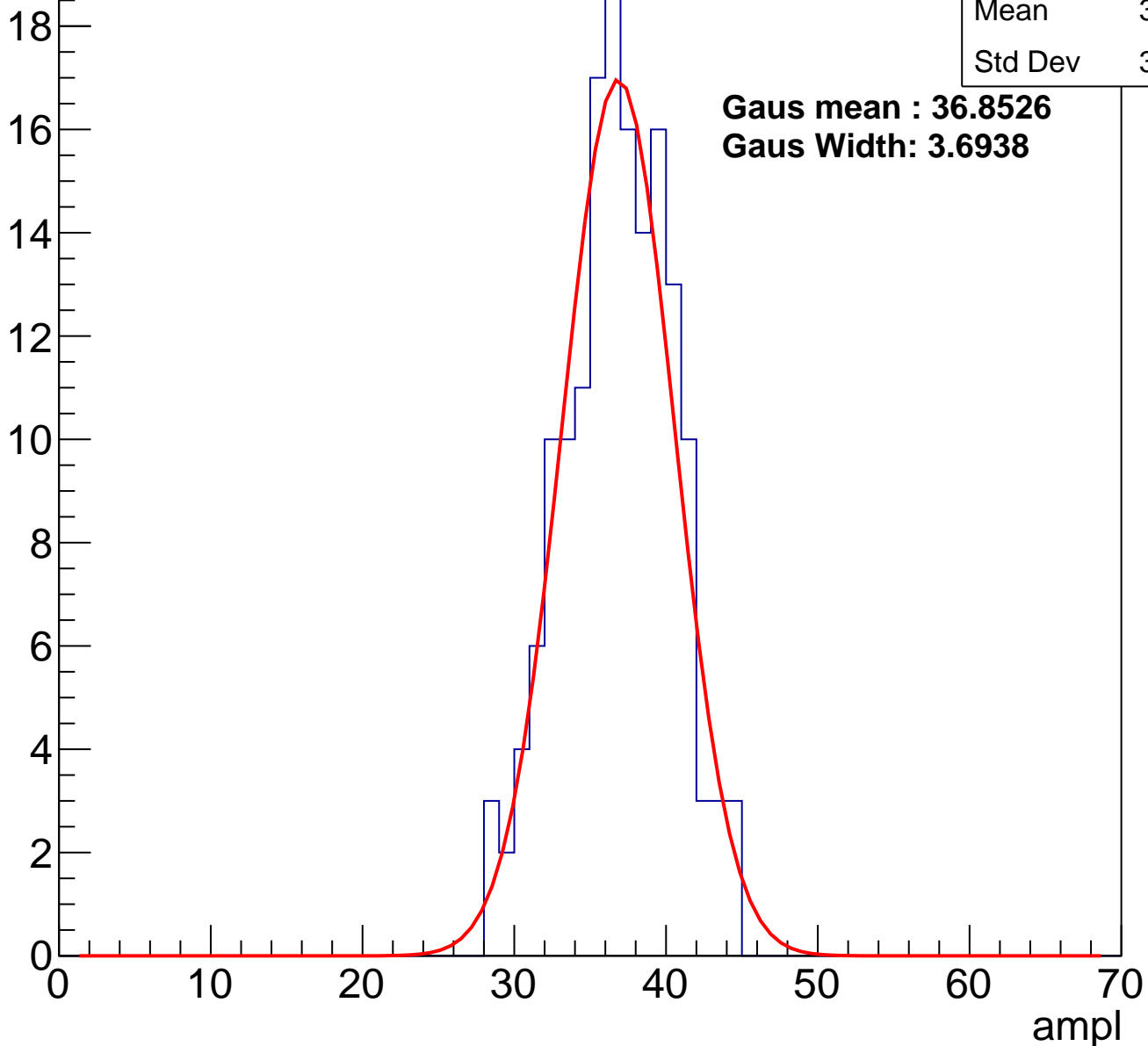
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	36.35
Std Dev	3.532

Gaus mean : 36.8526

Gaus Width: 3.6938

Entry



B1L001S, U16-ch21, adc2

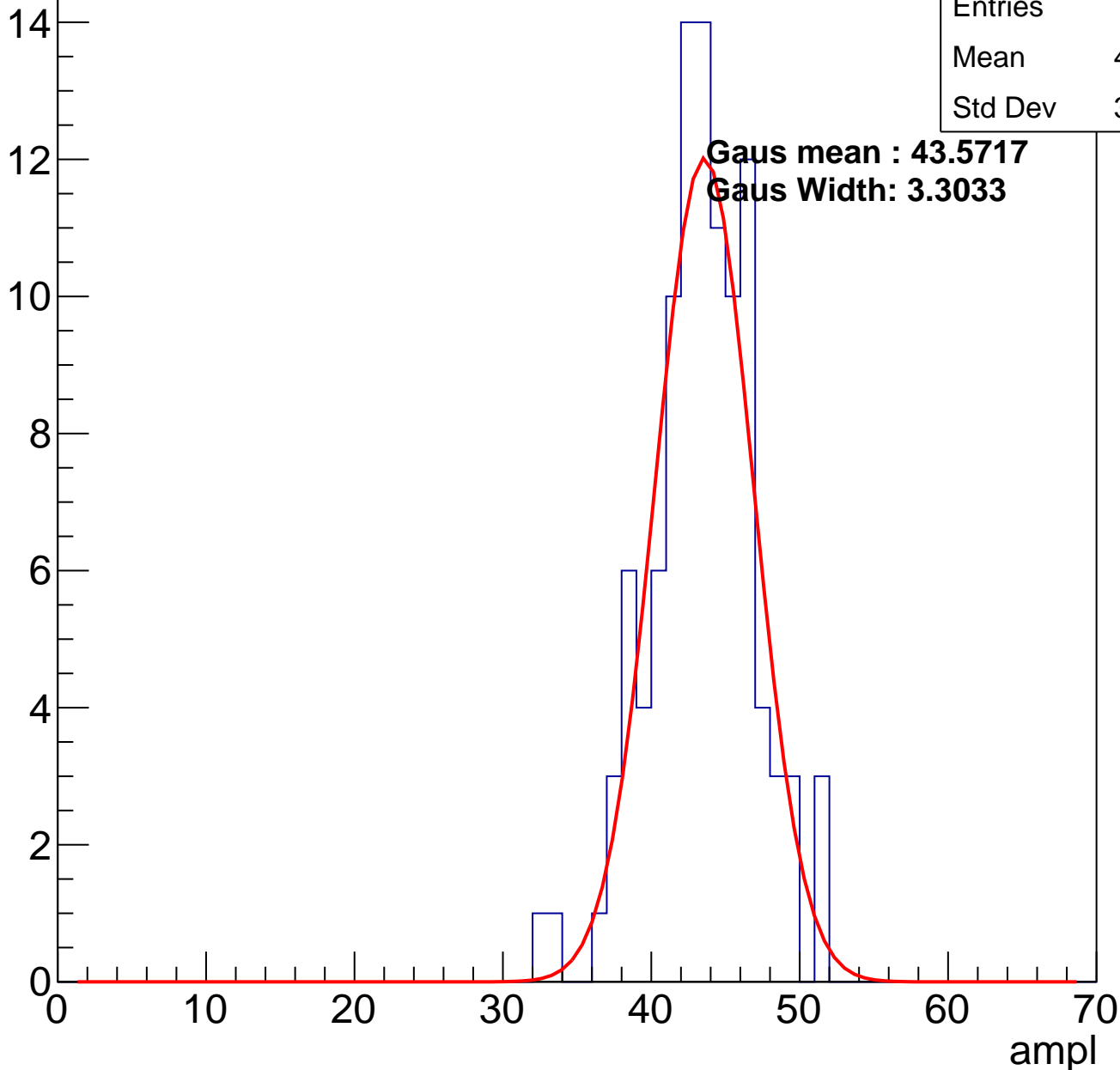
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	106
Mean	42.96
Std Dev	3.499

Gaus mean : 43.5717

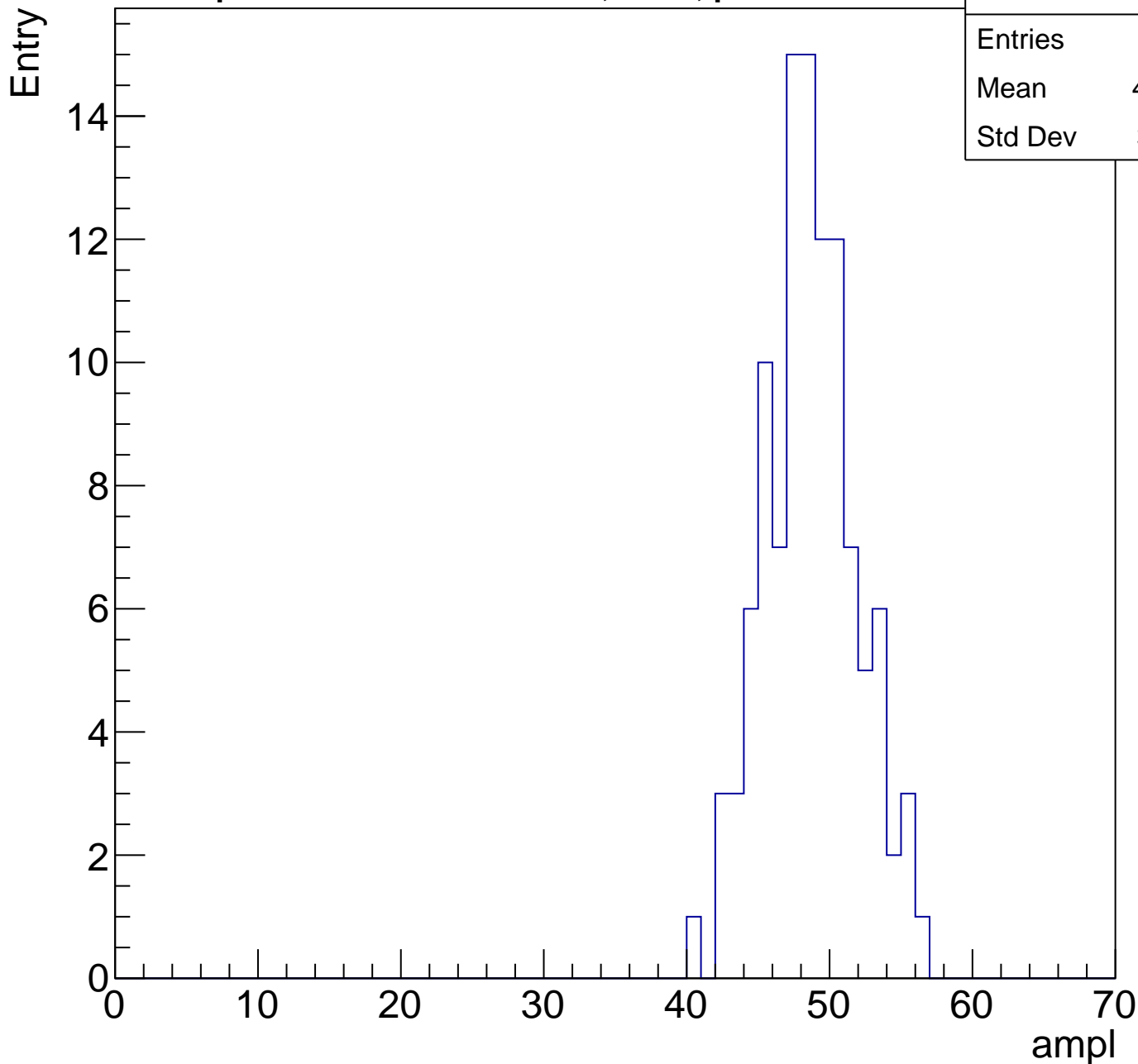
Gaus Width: 3.3033



B1L001S, U16-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

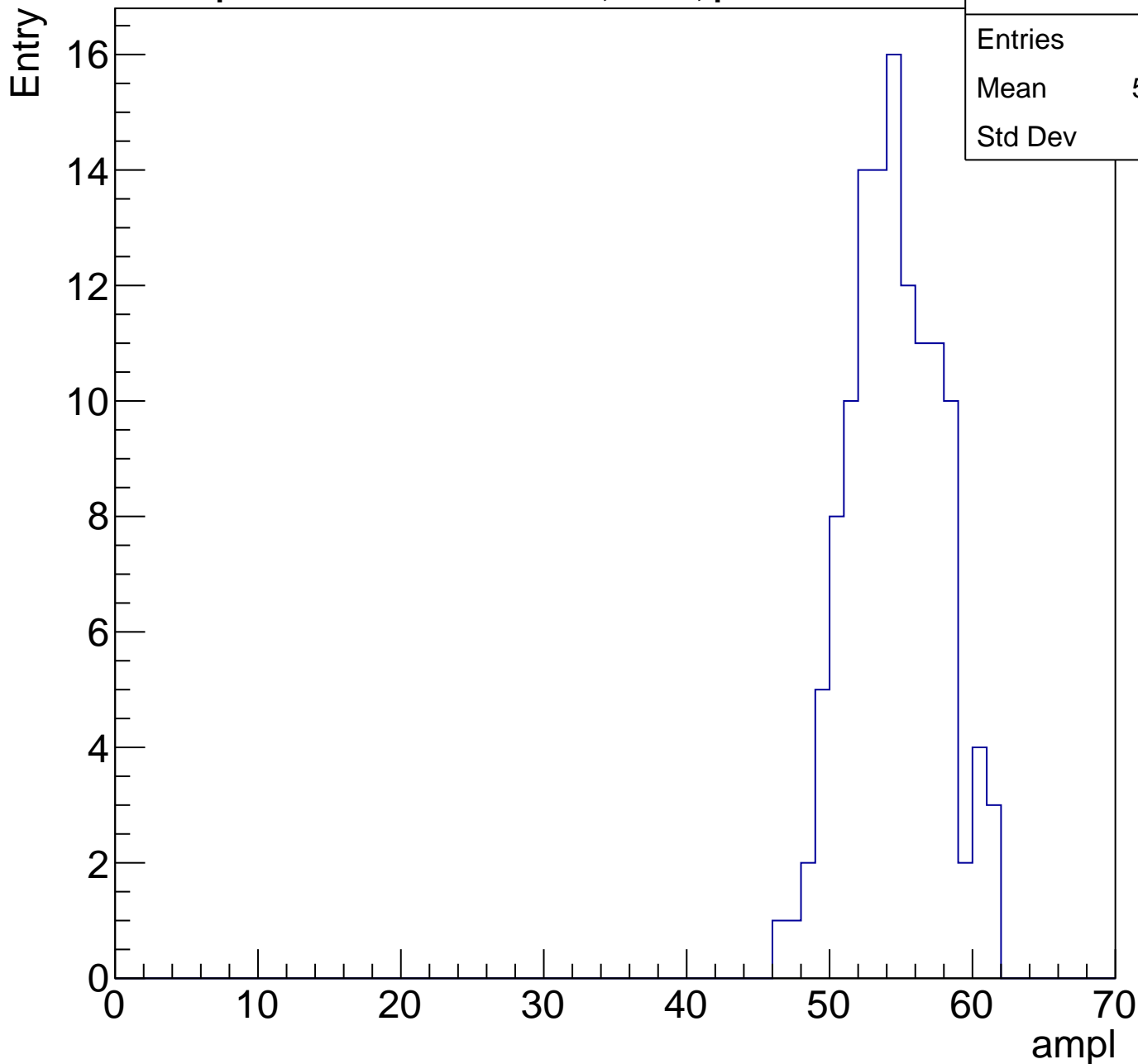
Entries	108
Mean	48.22
Std Dev	3.201



B1L001S, U16-ch21, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	54.05
Std Dev	3.18



B1L001S, U16-ch21, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	59.08
Std Dev	6.11

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

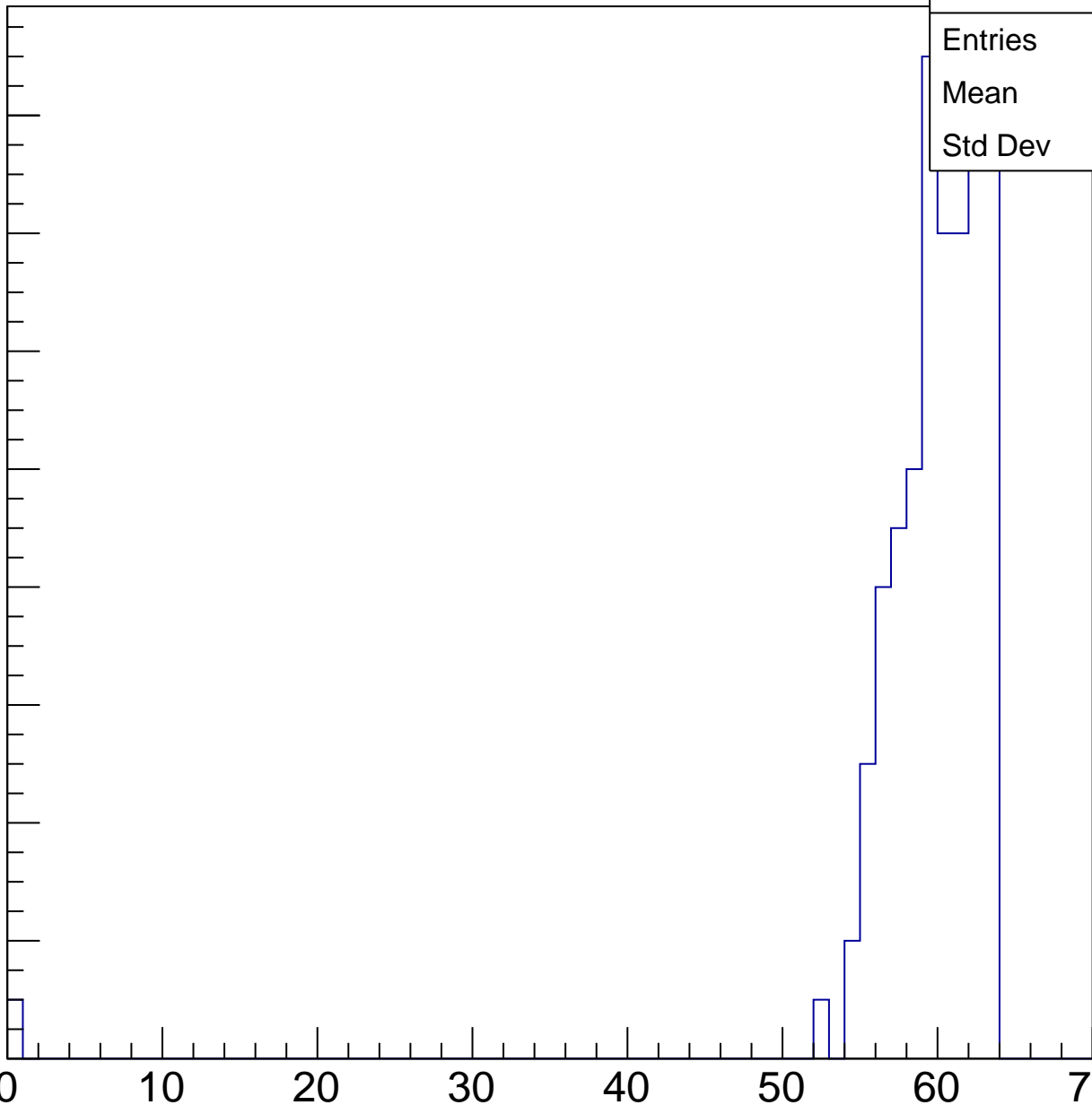
40

50

60

70

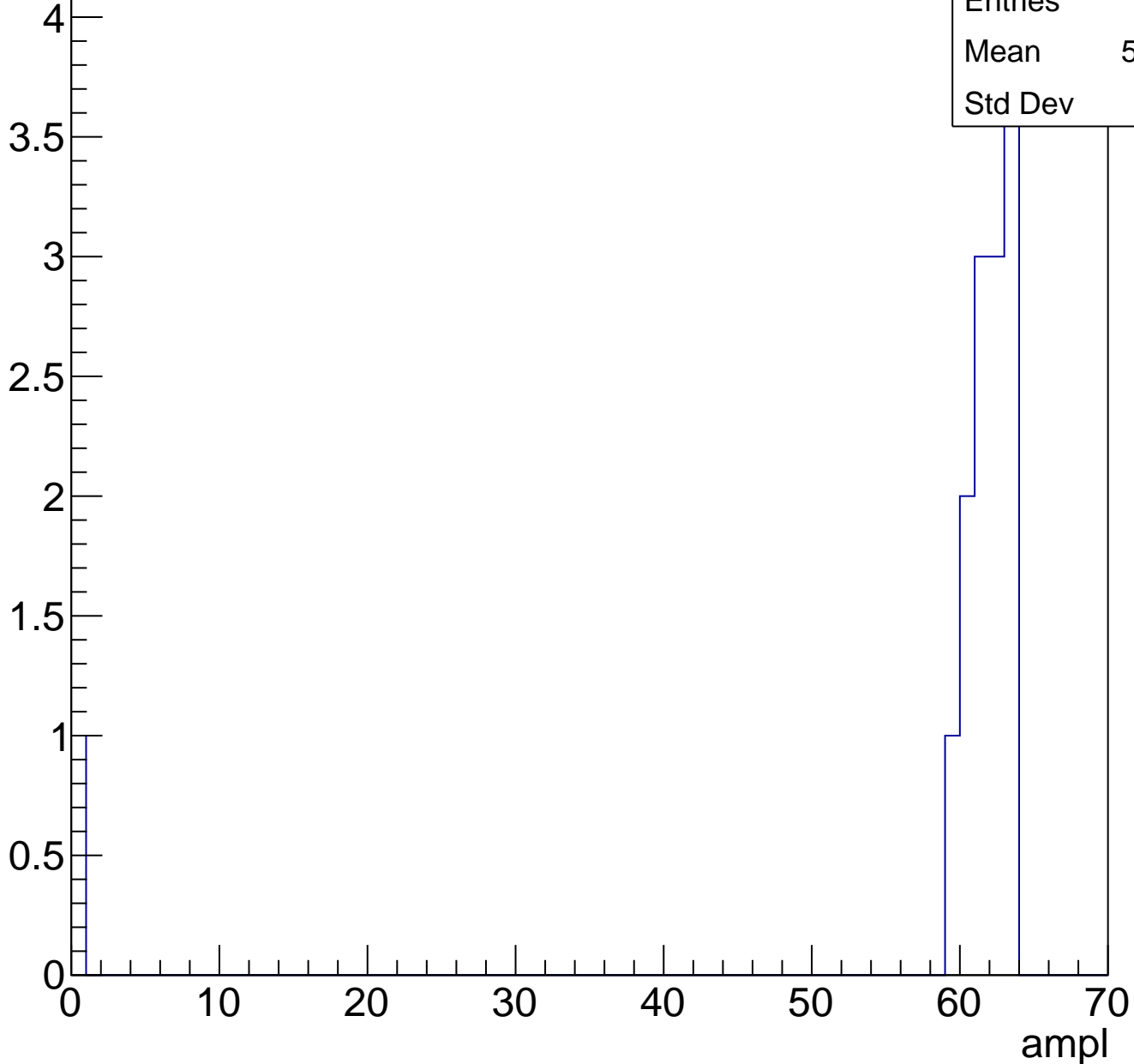
ampl



B1L001S, U16-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	29.81
Std Dev	5.995

Gaus mean : 31.2612

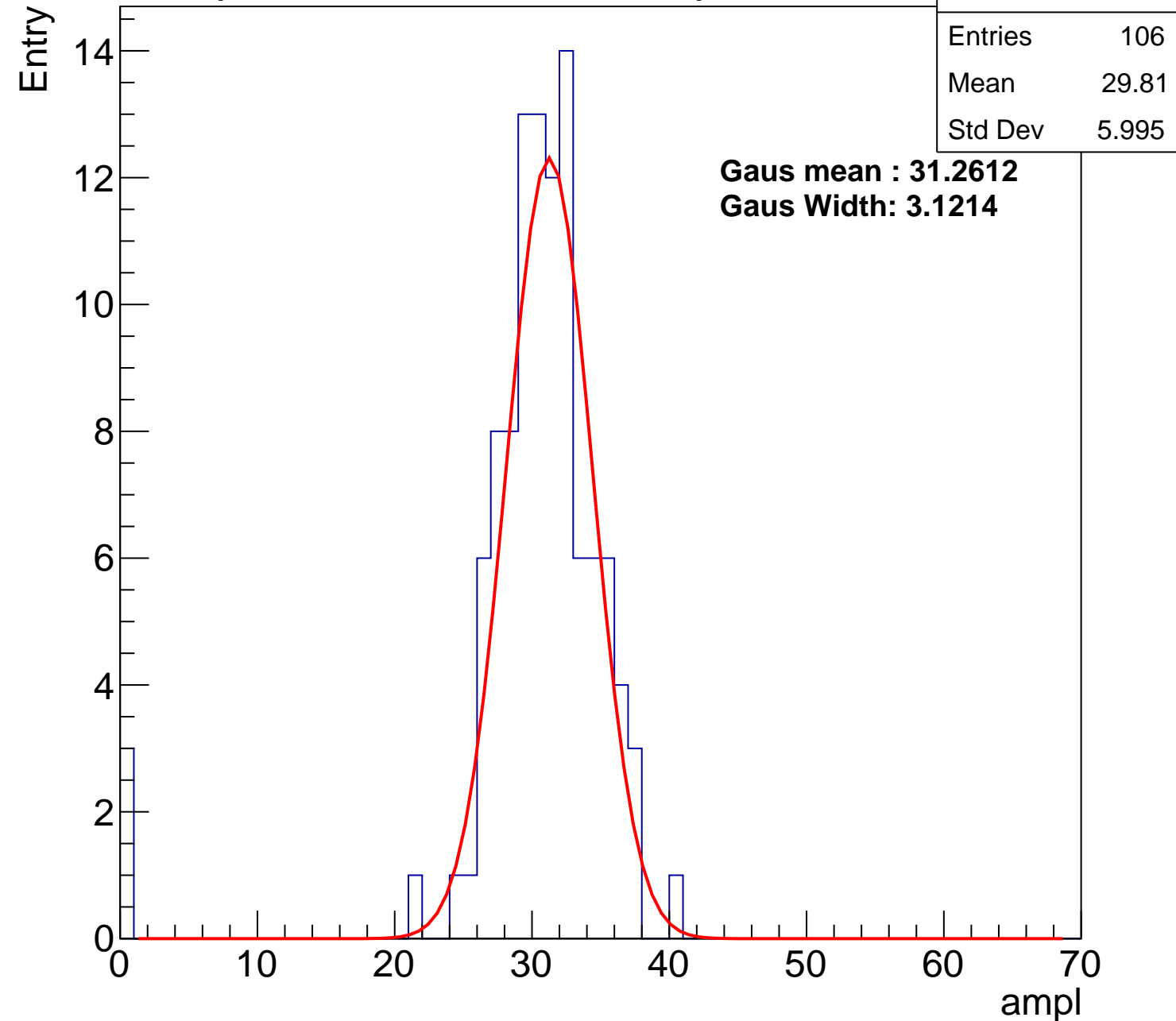
Gaus Width: 3.1214

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch22, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

130

Mean

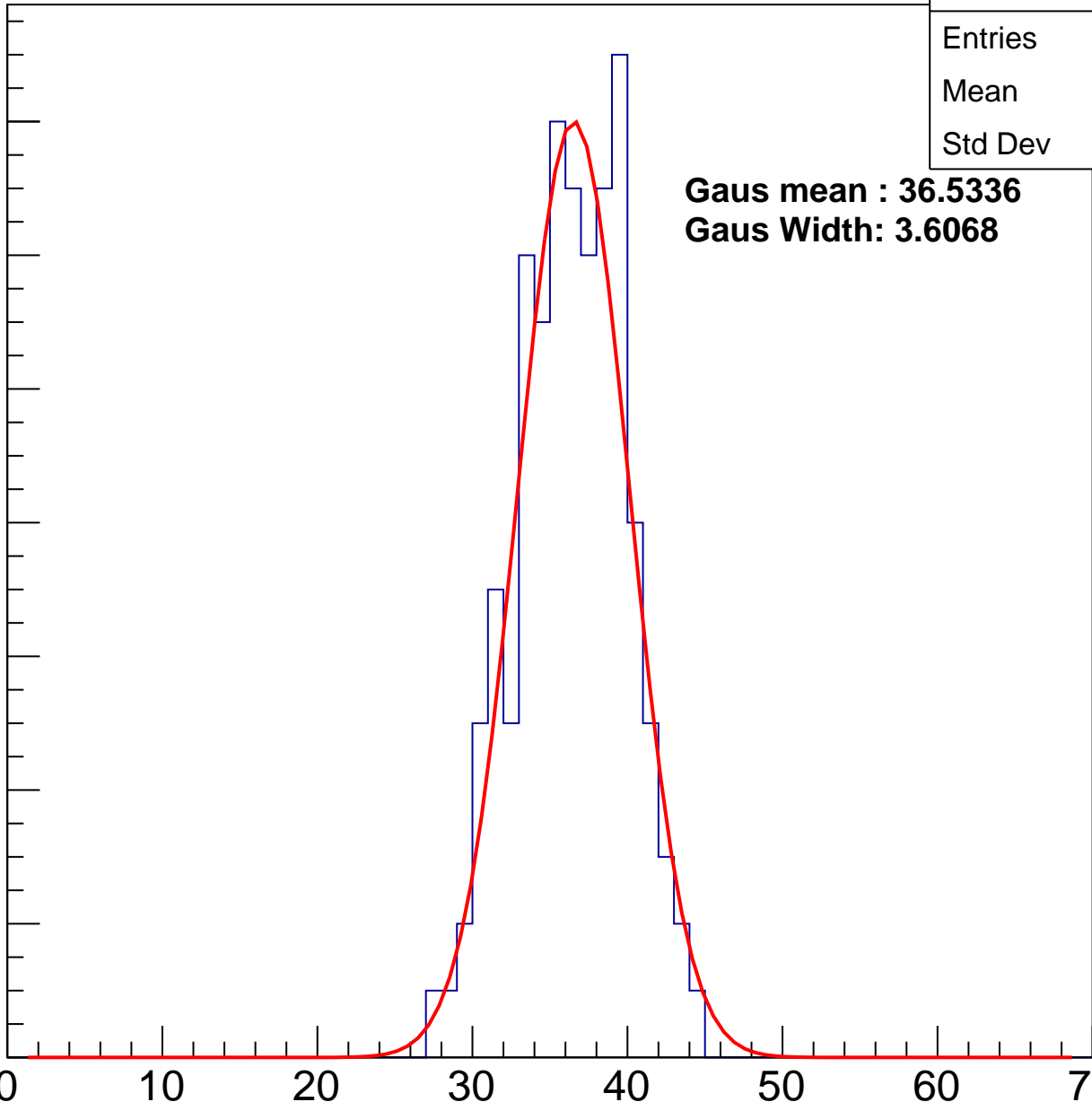
35.94

Std Dev

3.47

Gaus mean : 36.5336

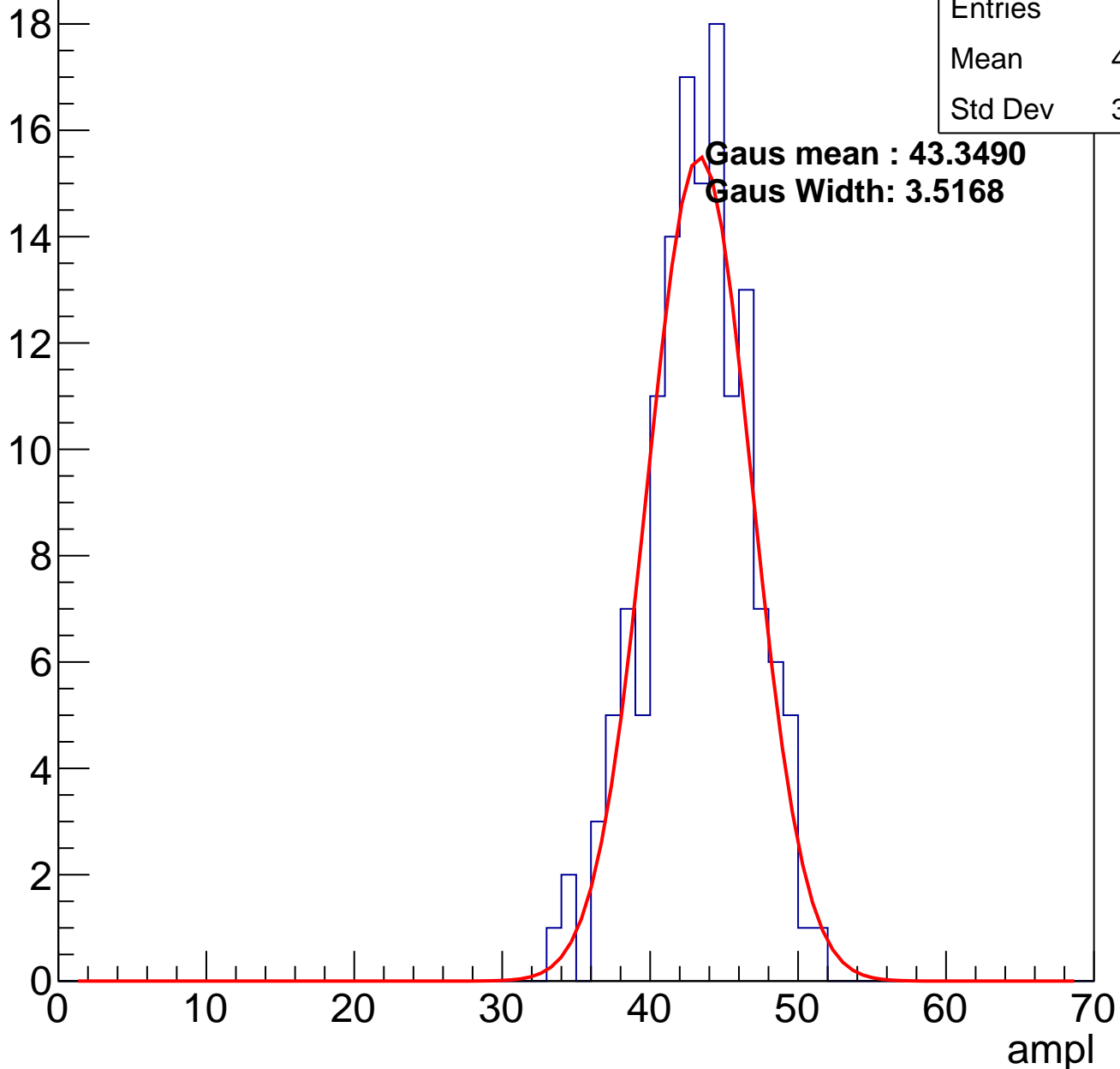
Gaus Width: 3.6068



B1L001S, U16-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

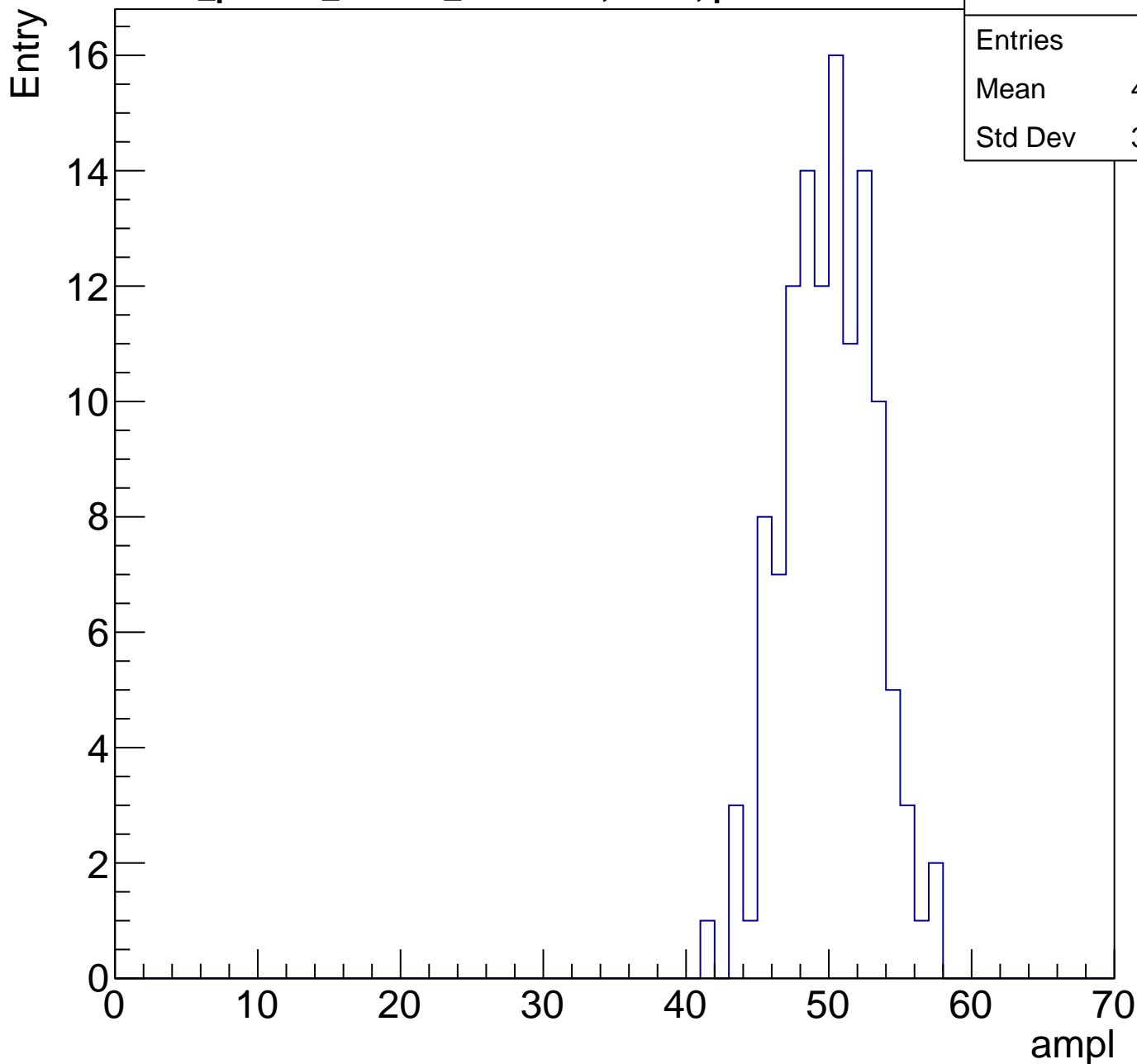
Entry



B1L001S, U16-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

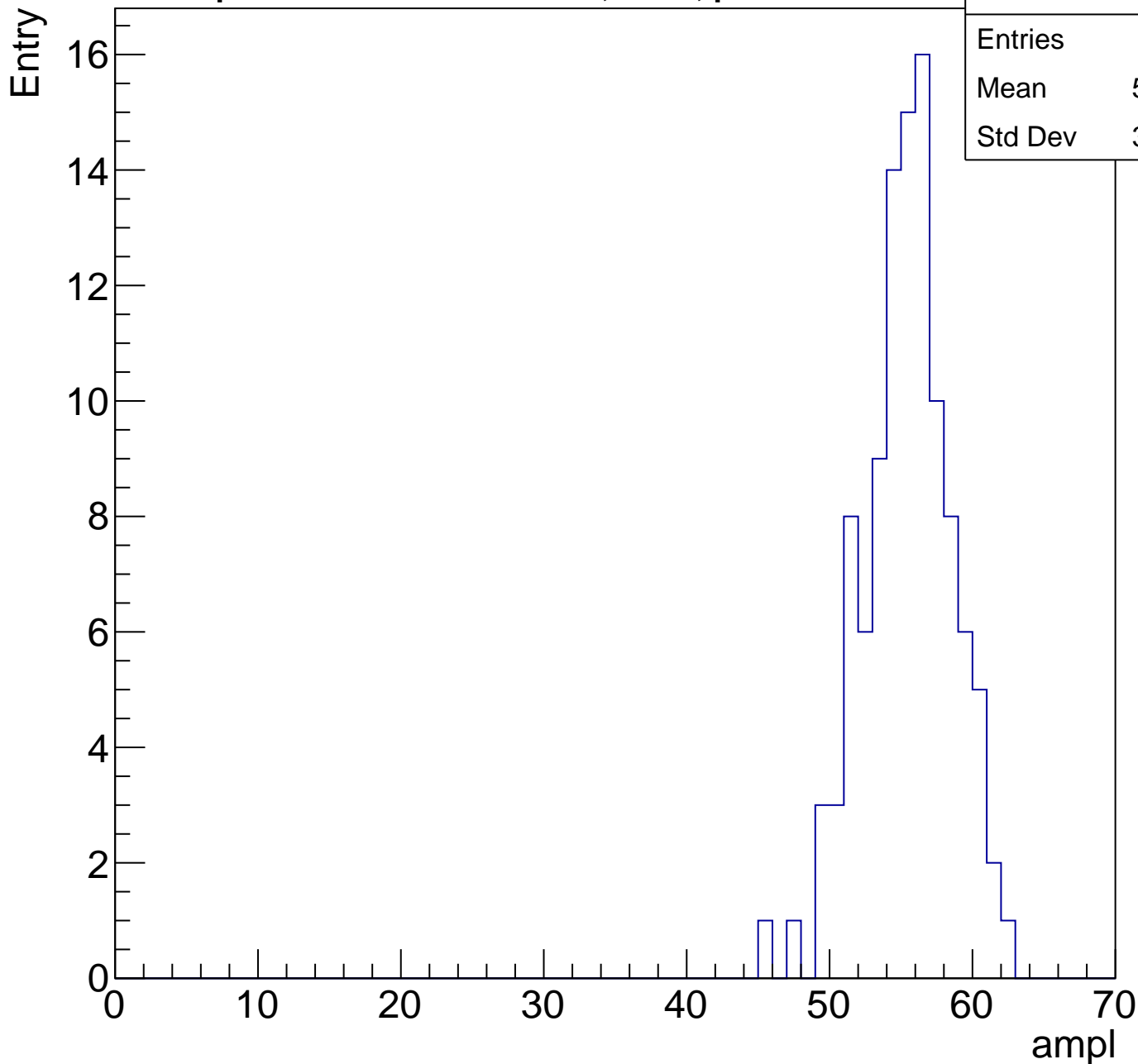
Entries	120
Mean	49.53
Std Dev	3.125



B1L001S, U16-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	54.95
Std Dev	3.116



B1L001S, U16-ch22, adc5

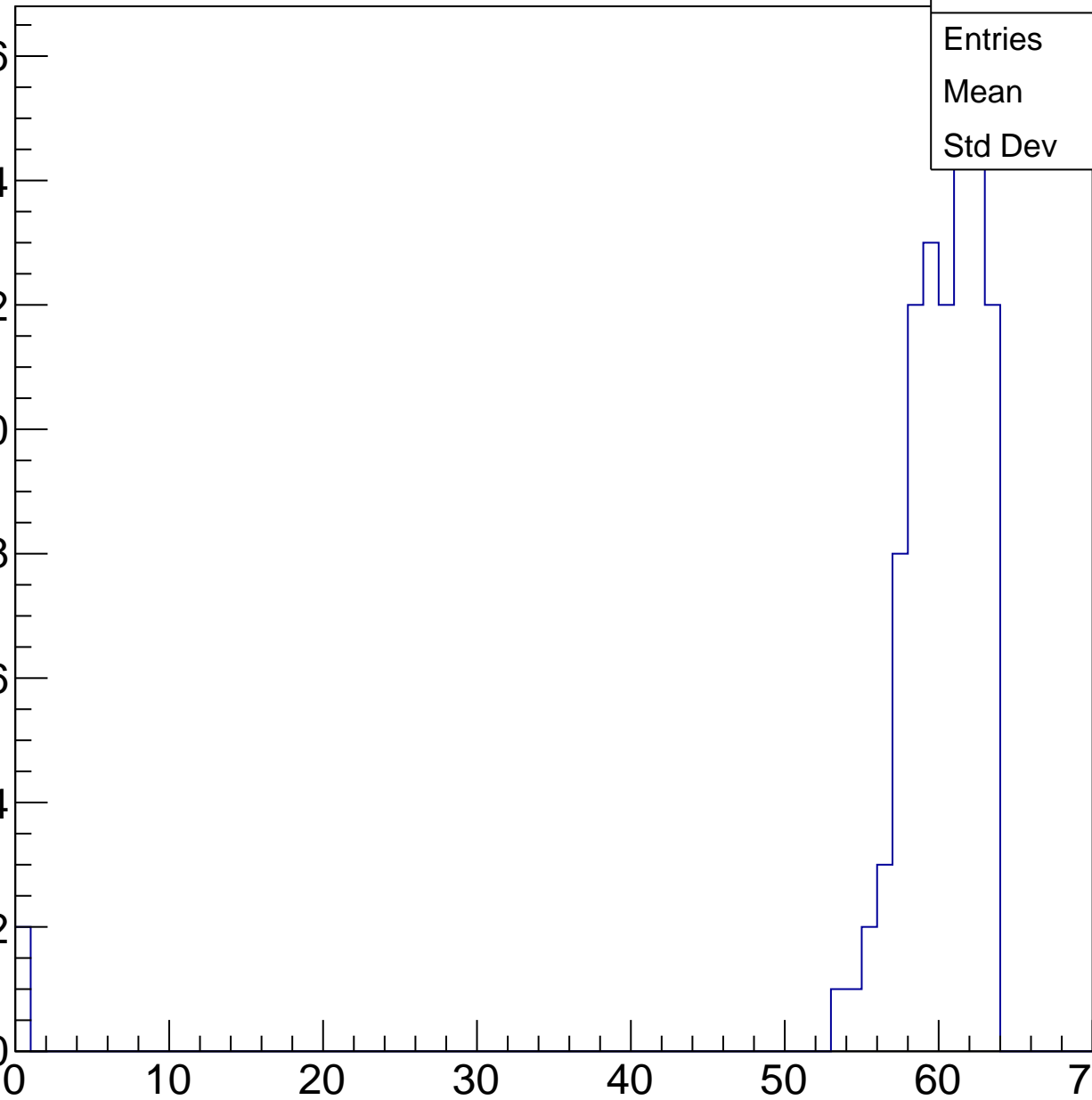
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.65
Std Dev	8.763

ampl



B1L001S, U16-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

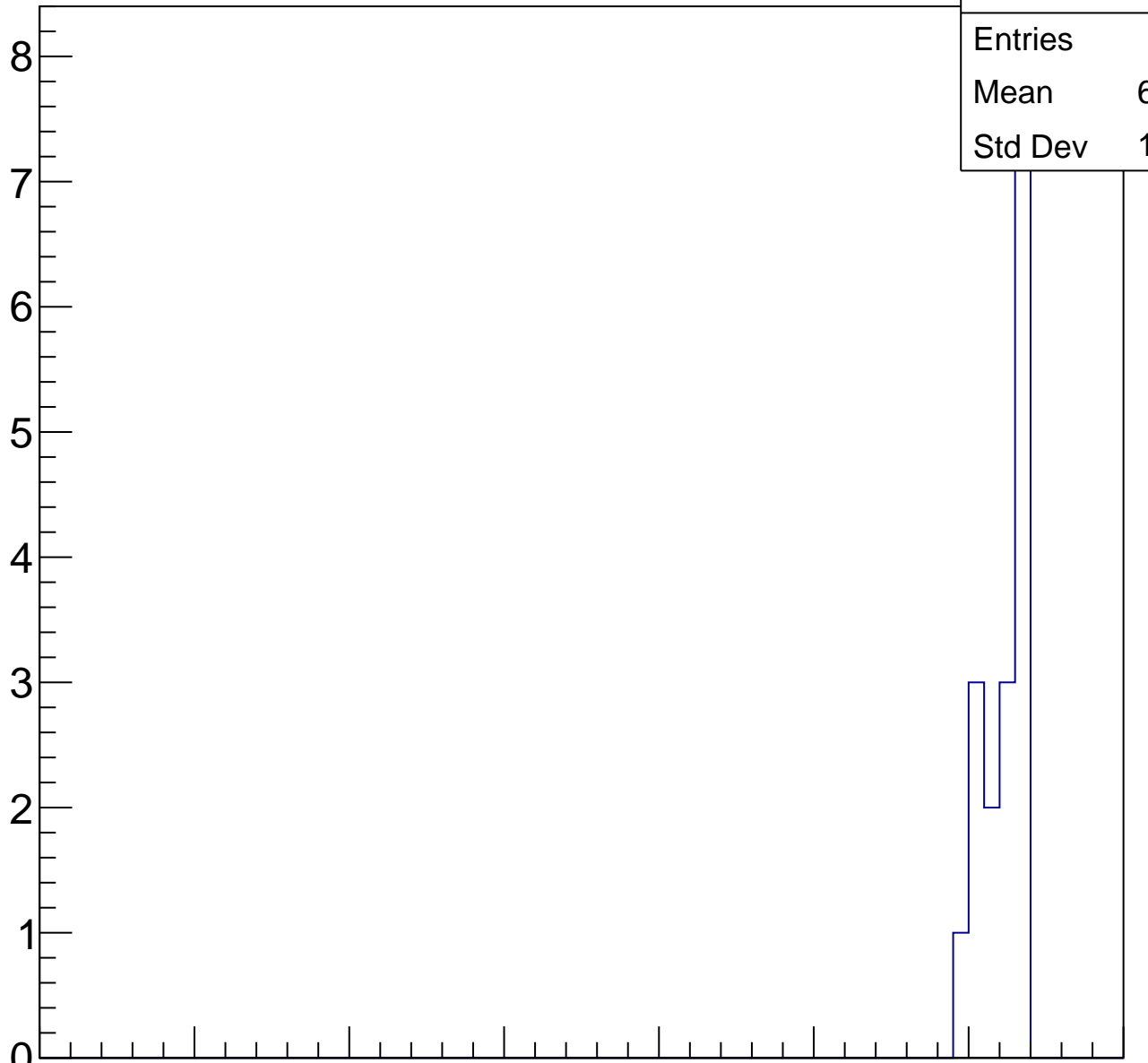
Entry

8
7
6
5
4
3
2
1
0

Entries	17
Mean	61.82
Std Dev	1.339

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

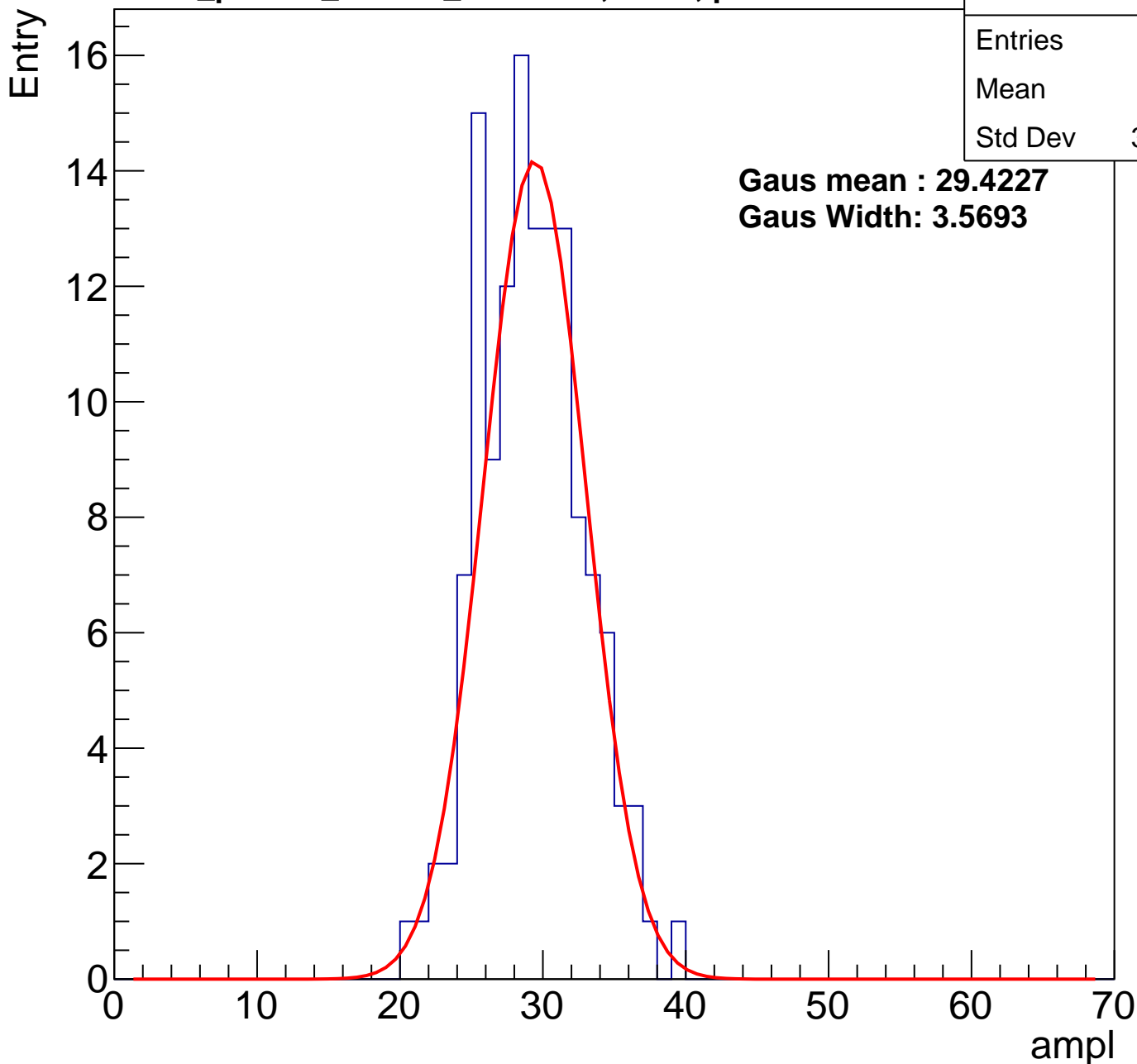
B1L001S, U16-ch23, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	28.8
Std Dev	3.562

Gaus mean : 29.4227

Gaus Width: 3.5693



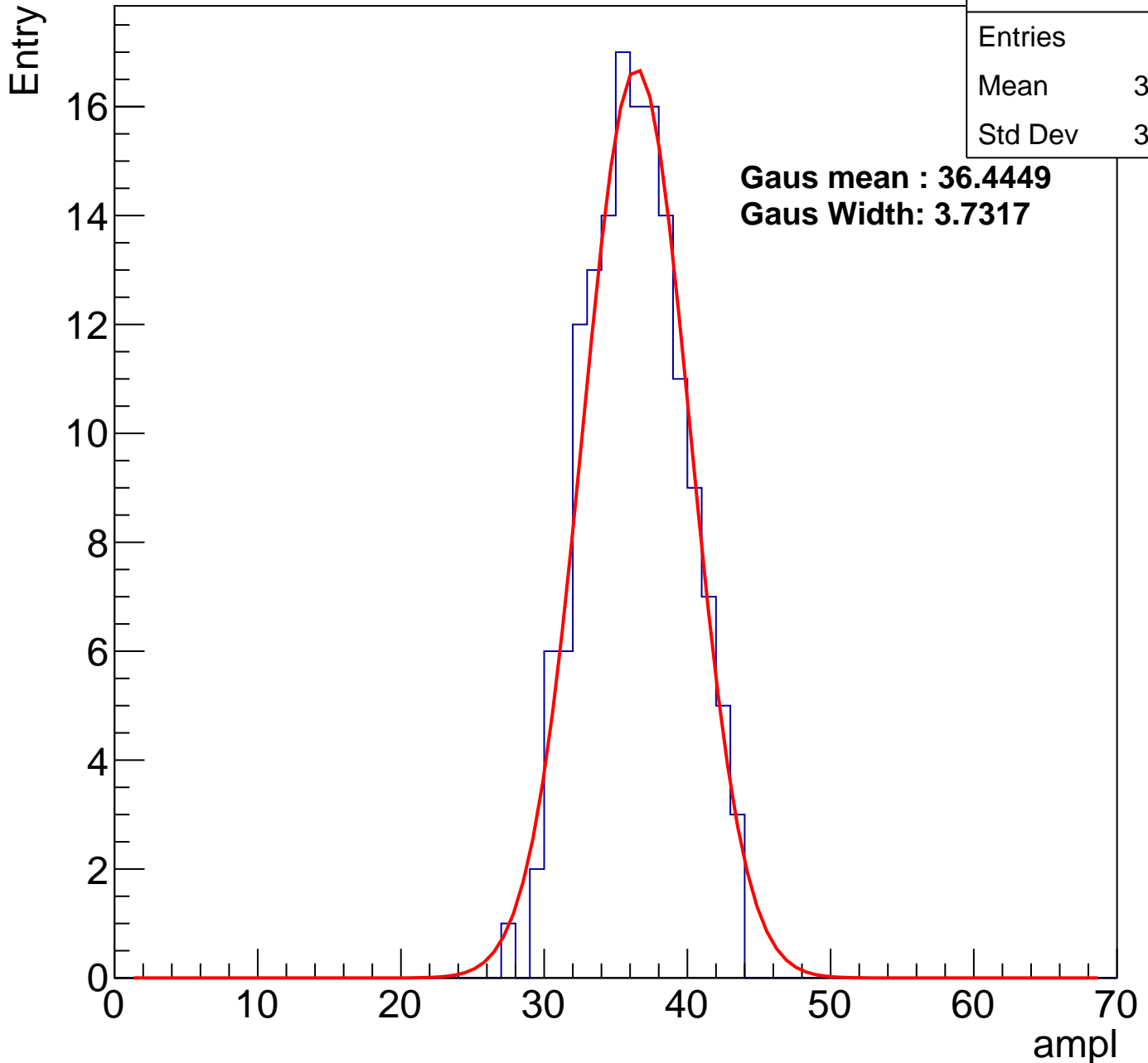
B1L001S, U16-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	35.86
Std Dev	3.384

Gaus mean : 36.4449

Gaus Width: 3.7317



B1L001S, U16-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	111
Mean	42.56
Std Dev	2.869

Gaus mean : 43.0184

Gaus Width: 3.0193

0

2

4

6

8

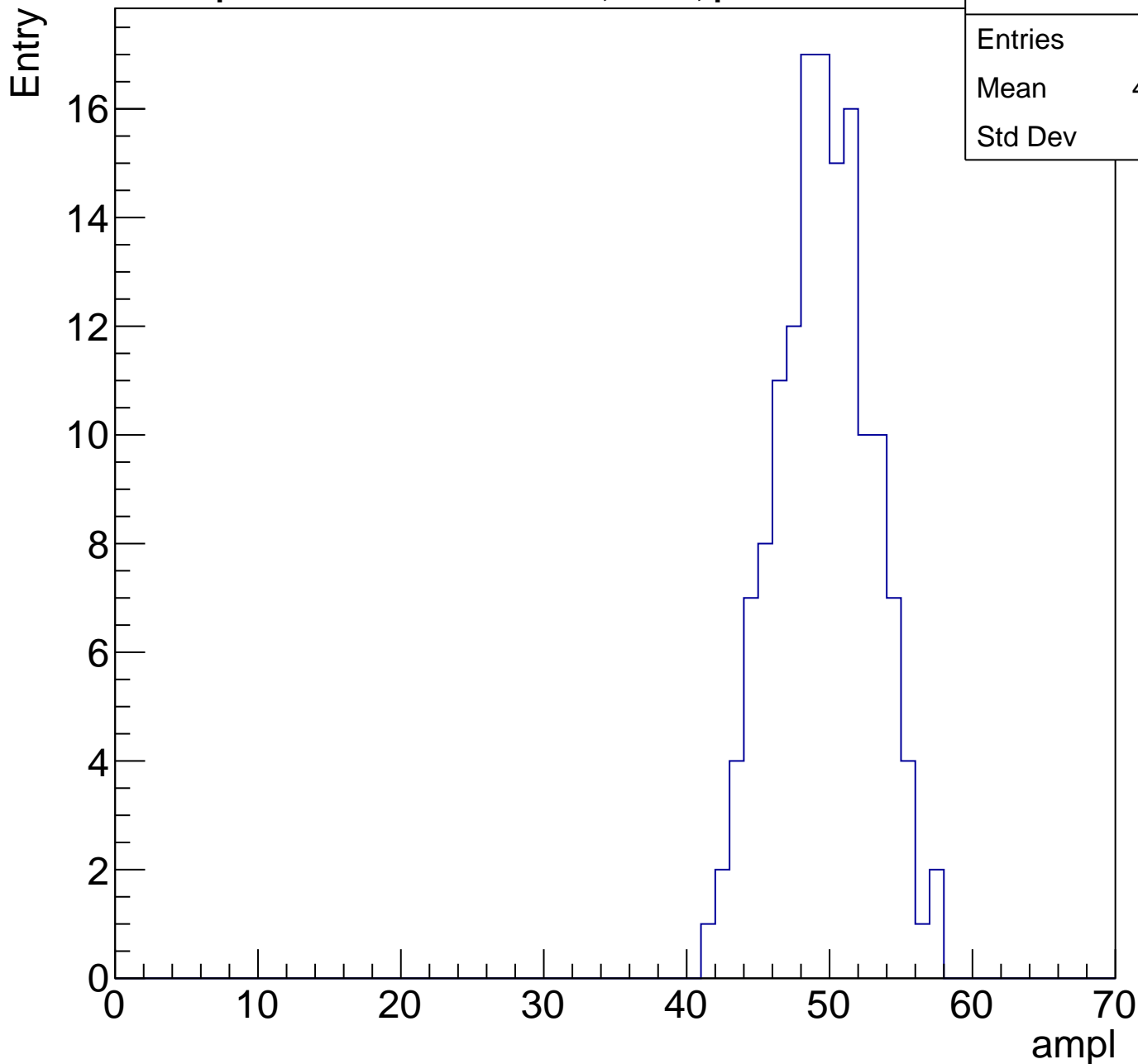
10

12

B1L001S, U16-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

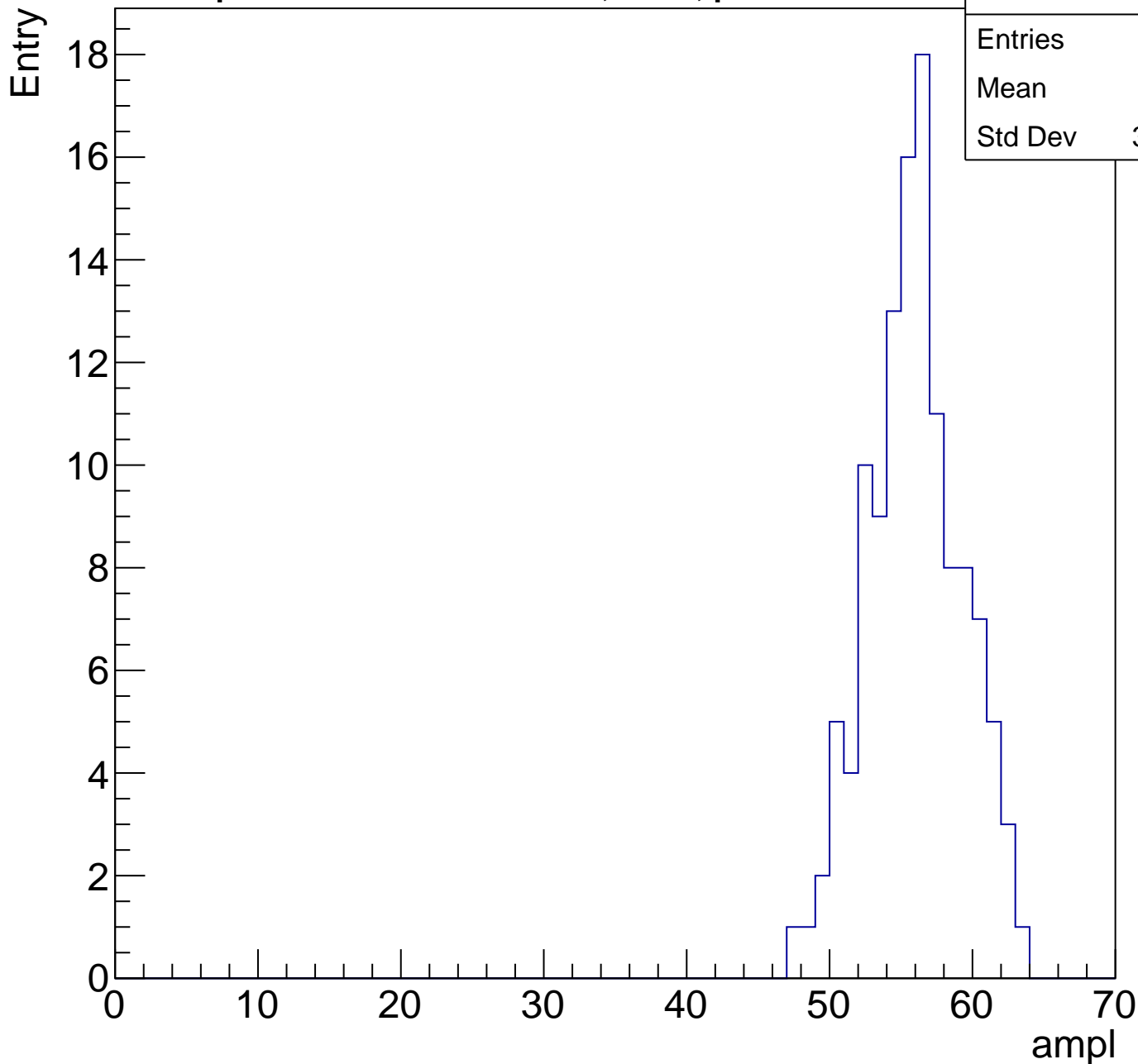
Entries	144
Mean	49.08
Std Dev	3.33



B1L001S, U16-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	55.5
Std Dev	3.277



B1L001S, U16-ch23, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	80
Mean	58.58
Std Dev	9.606

ampl

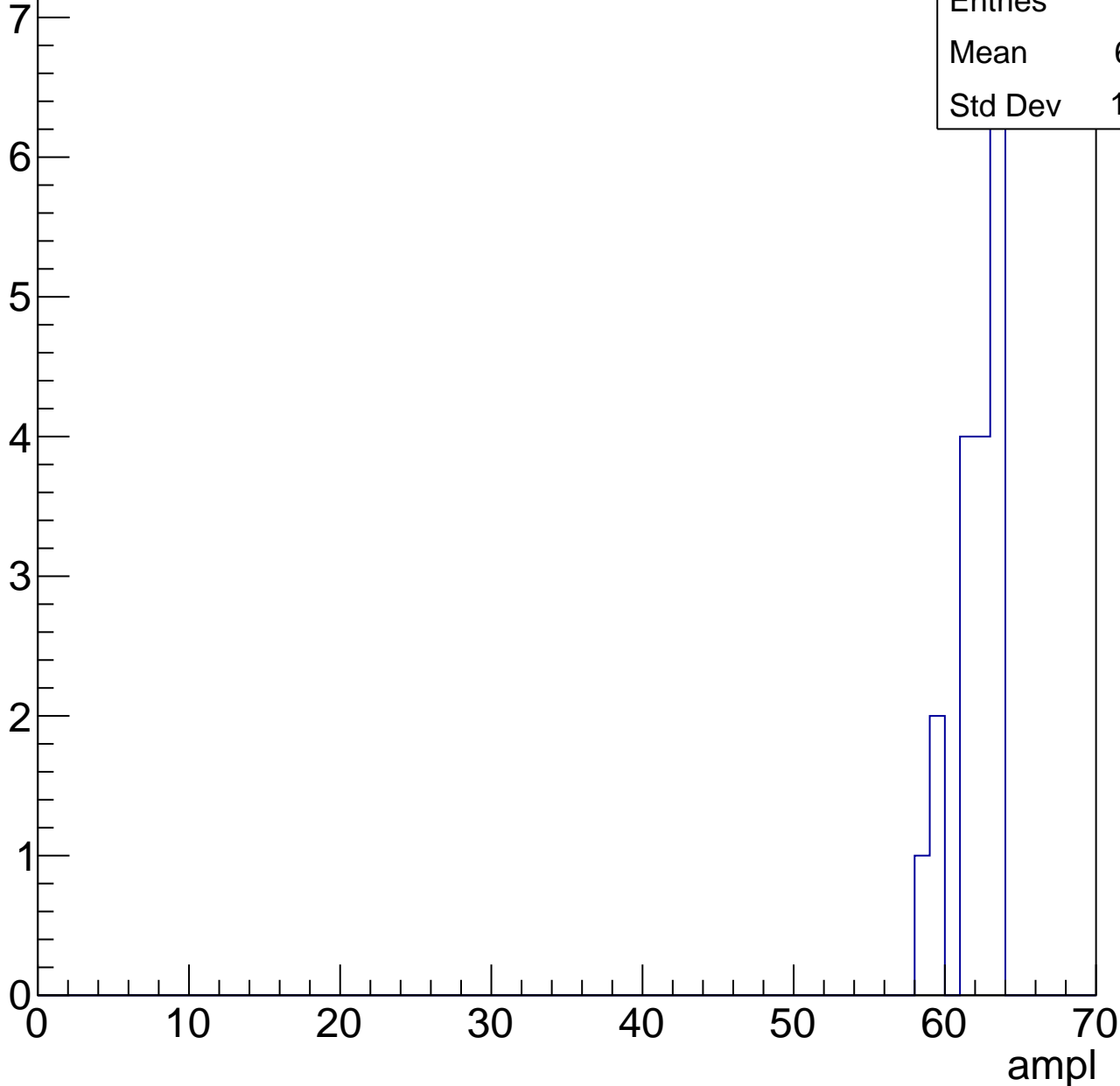
0 10 20 30 40 50 60 70

B1L001S, U16-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

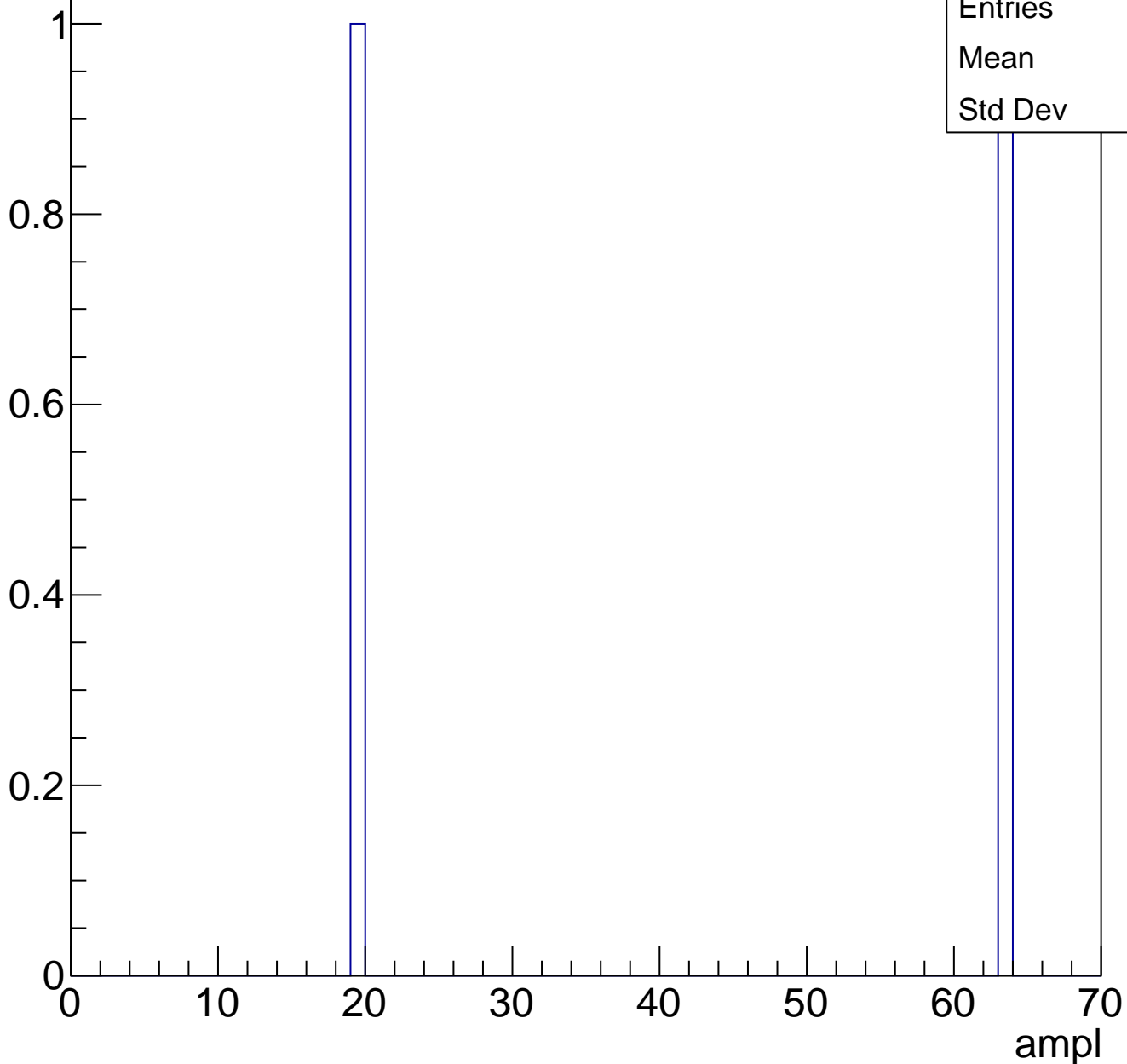
Entries	18
Mean	61.61
Std Dev	1.533



B1L001S, U16-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch24, adc0

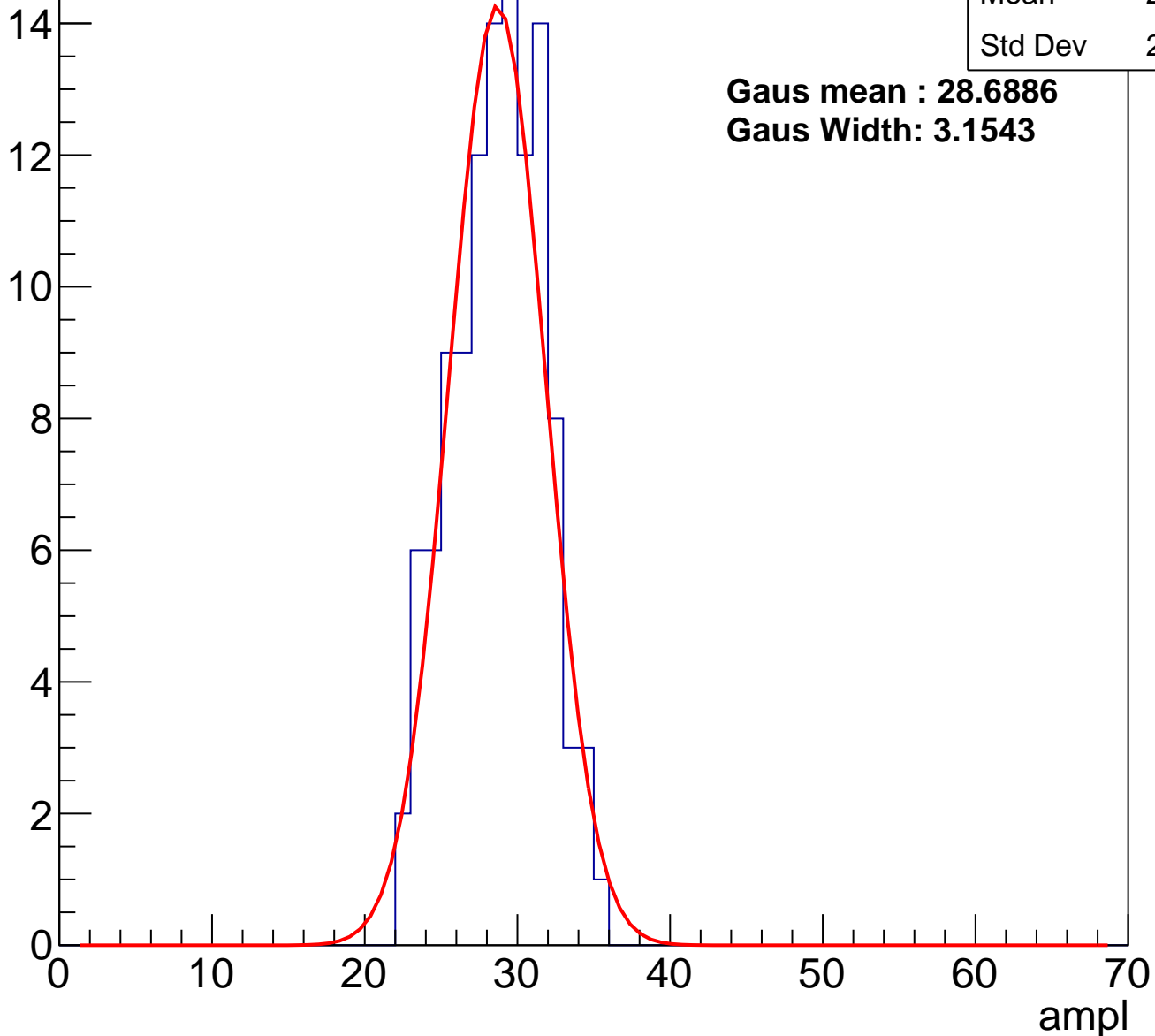
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	28.26
Std Dev	2.944

Gaus mean : 28.6886

Gaus Width: 3.1543

Entry



B1L001S, U16-ch24, adc1

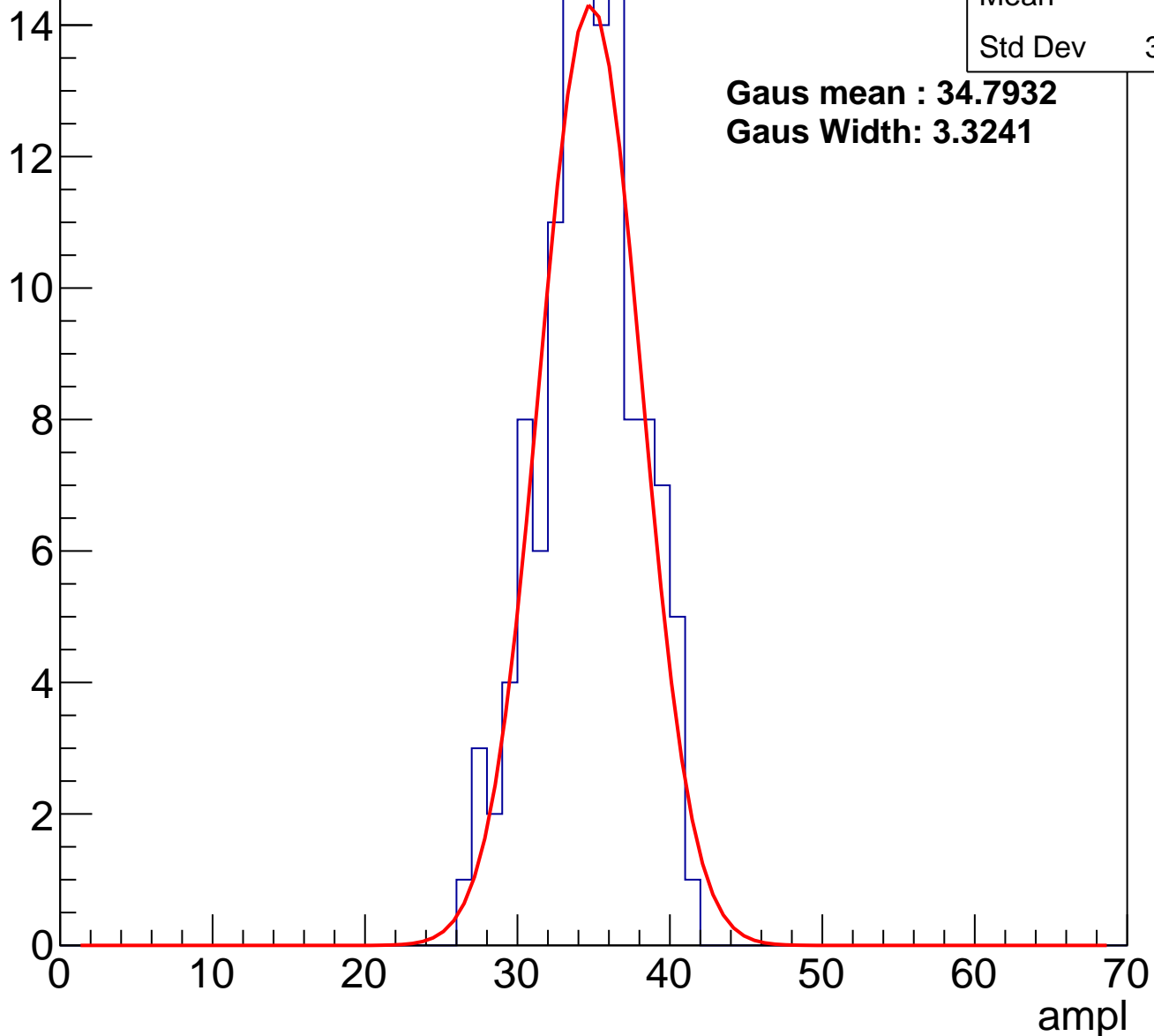
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	34.2
Std Dev	3.248

Gaus mean : 34.7932

Gaus Width: 3.3241

Entry



B1L001S, U16-ch24, adc2

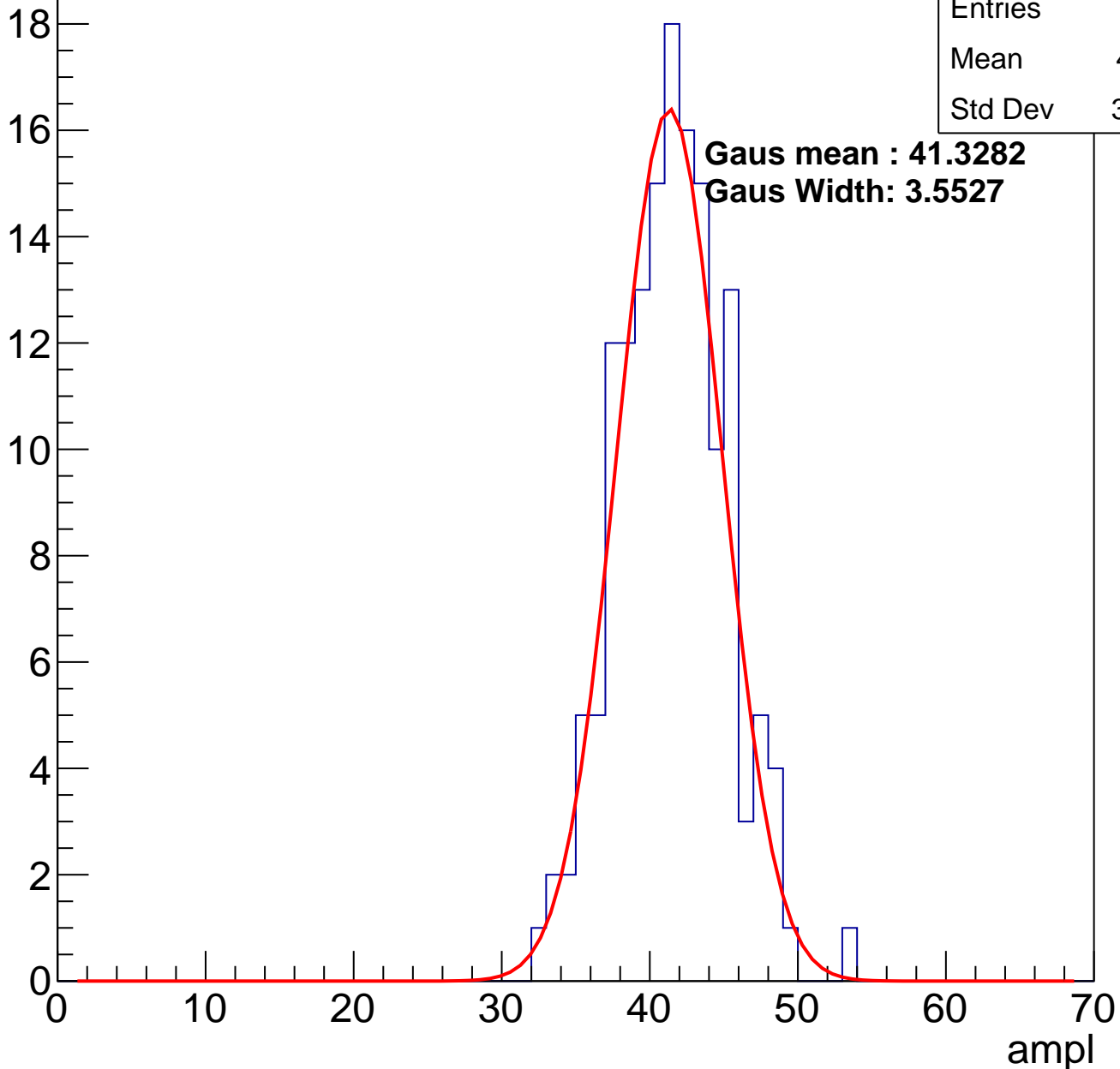
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	41.01
Std Dev	3.626

Gaus mean : 41.3282

Gaus Width: 3.5527

Entry



B1L001S, U16-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	47.81
Std Dev	3.14

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

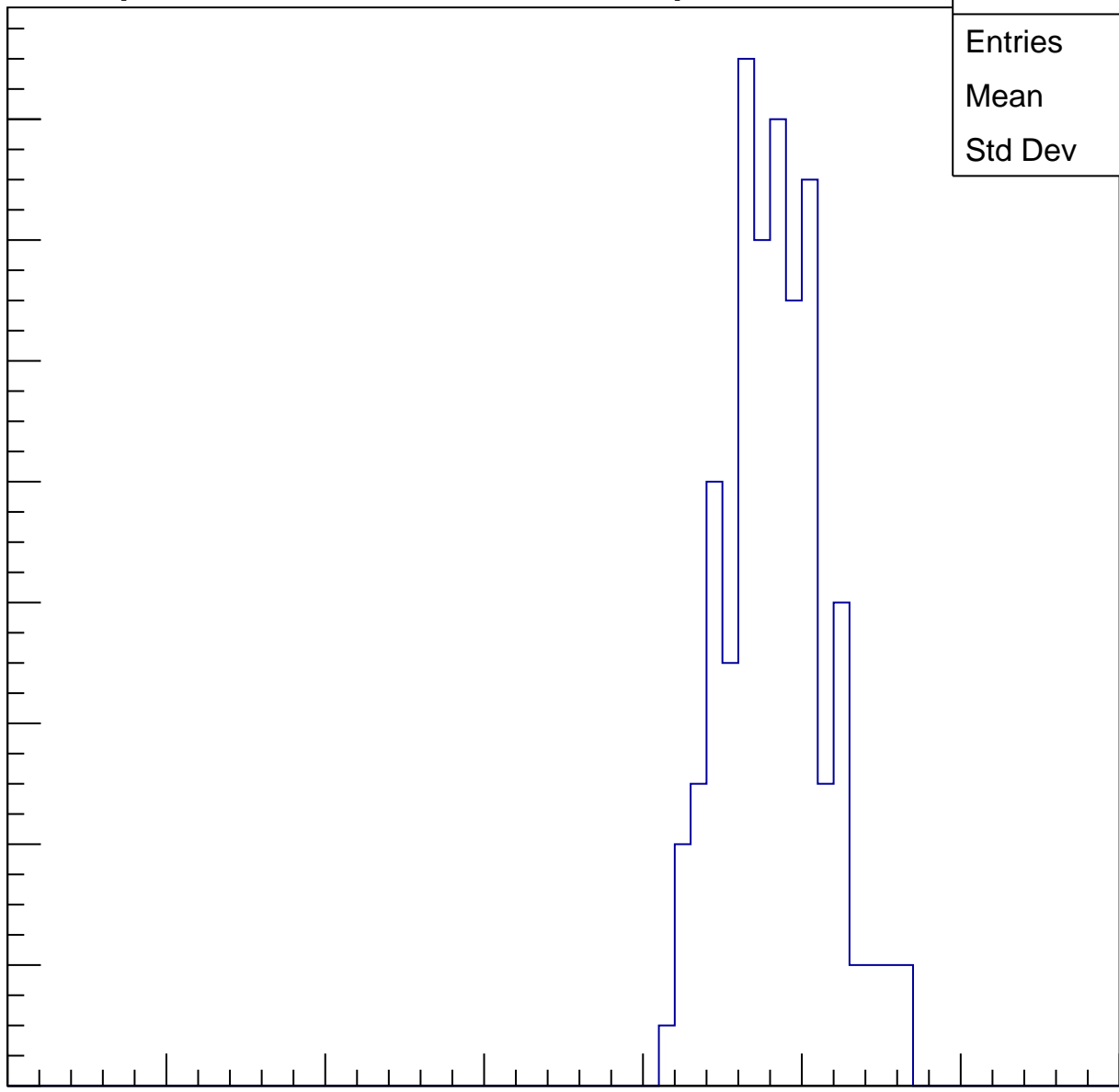
40

50

60

70

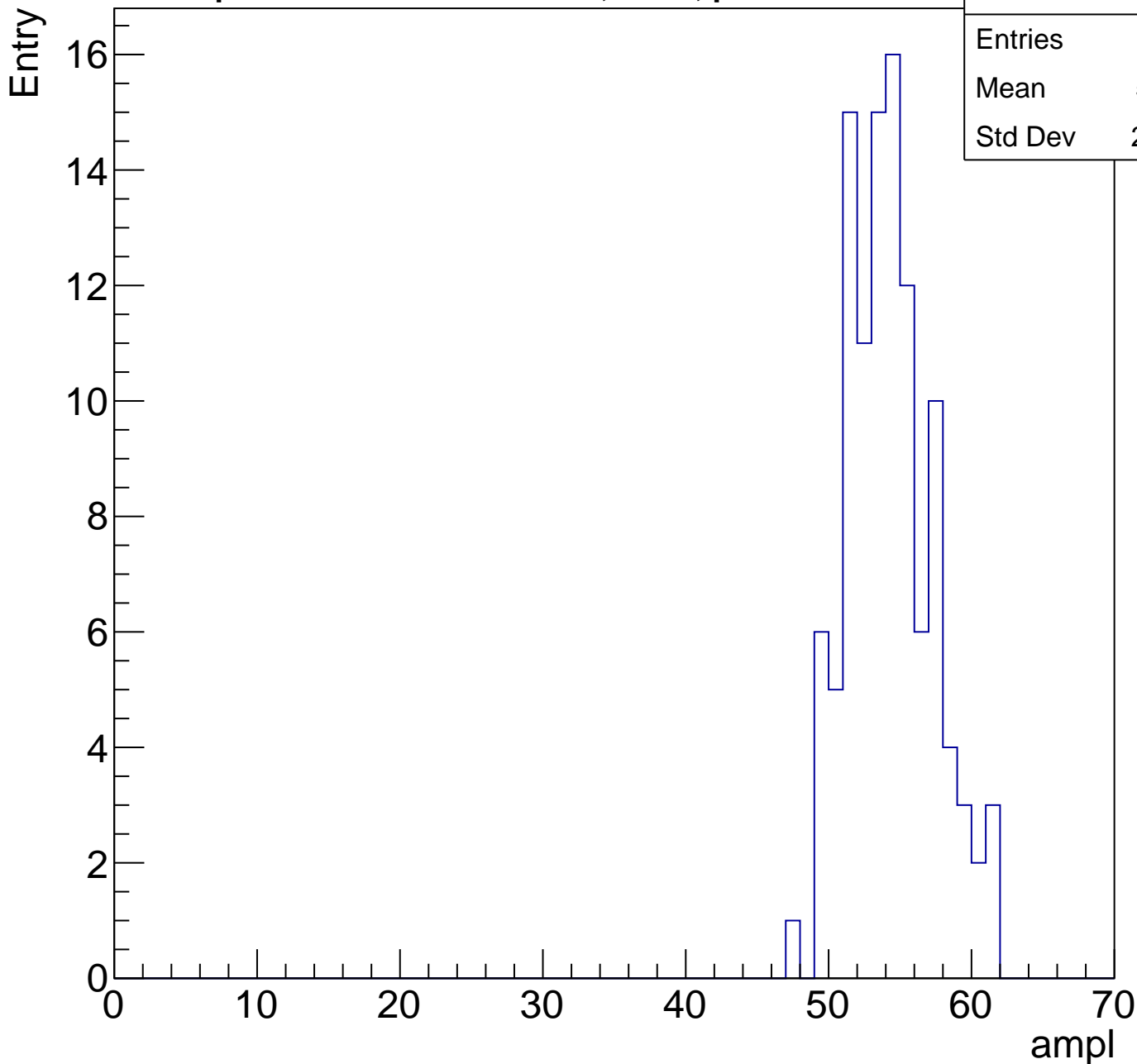
ampl



B1L001S, U16-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	53.81
Std Dev	2.957



B1L001S, U16-ch24, adc5

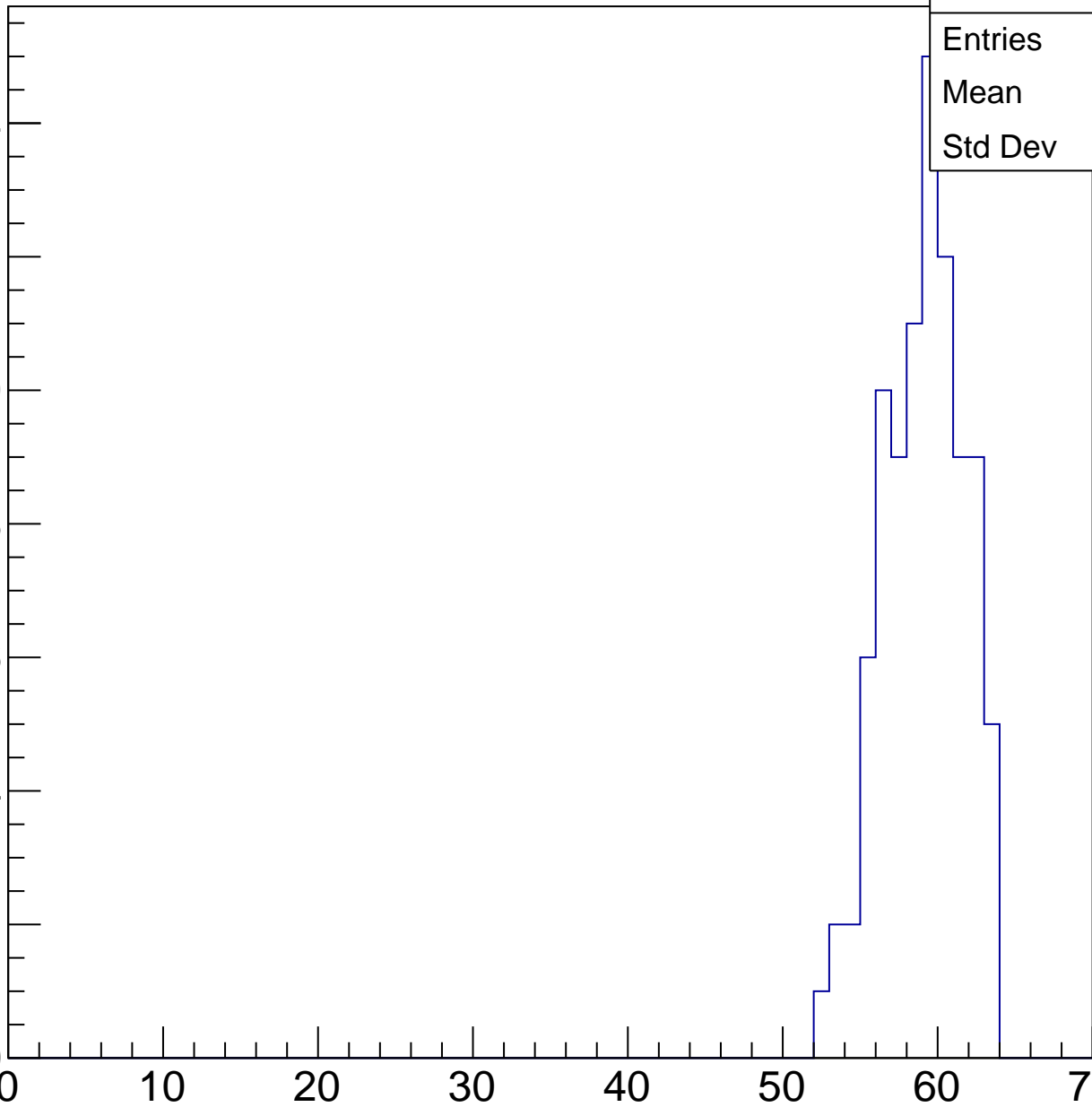
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.62
Std Dev	2.567

ampl



B1L001S, U16-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

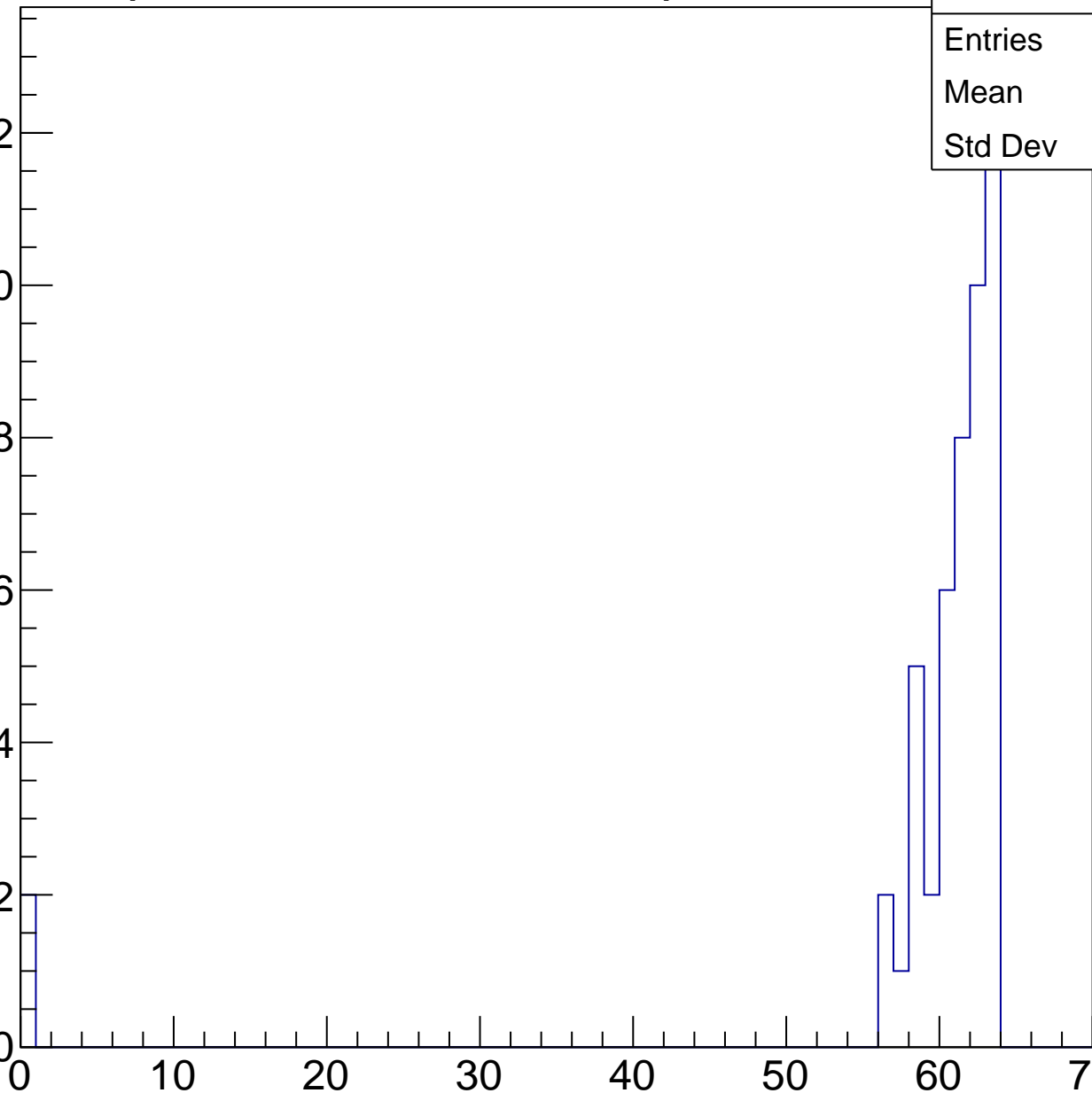
4

2

0

Entries	49
Mean	58.45
Std Dev	12.21

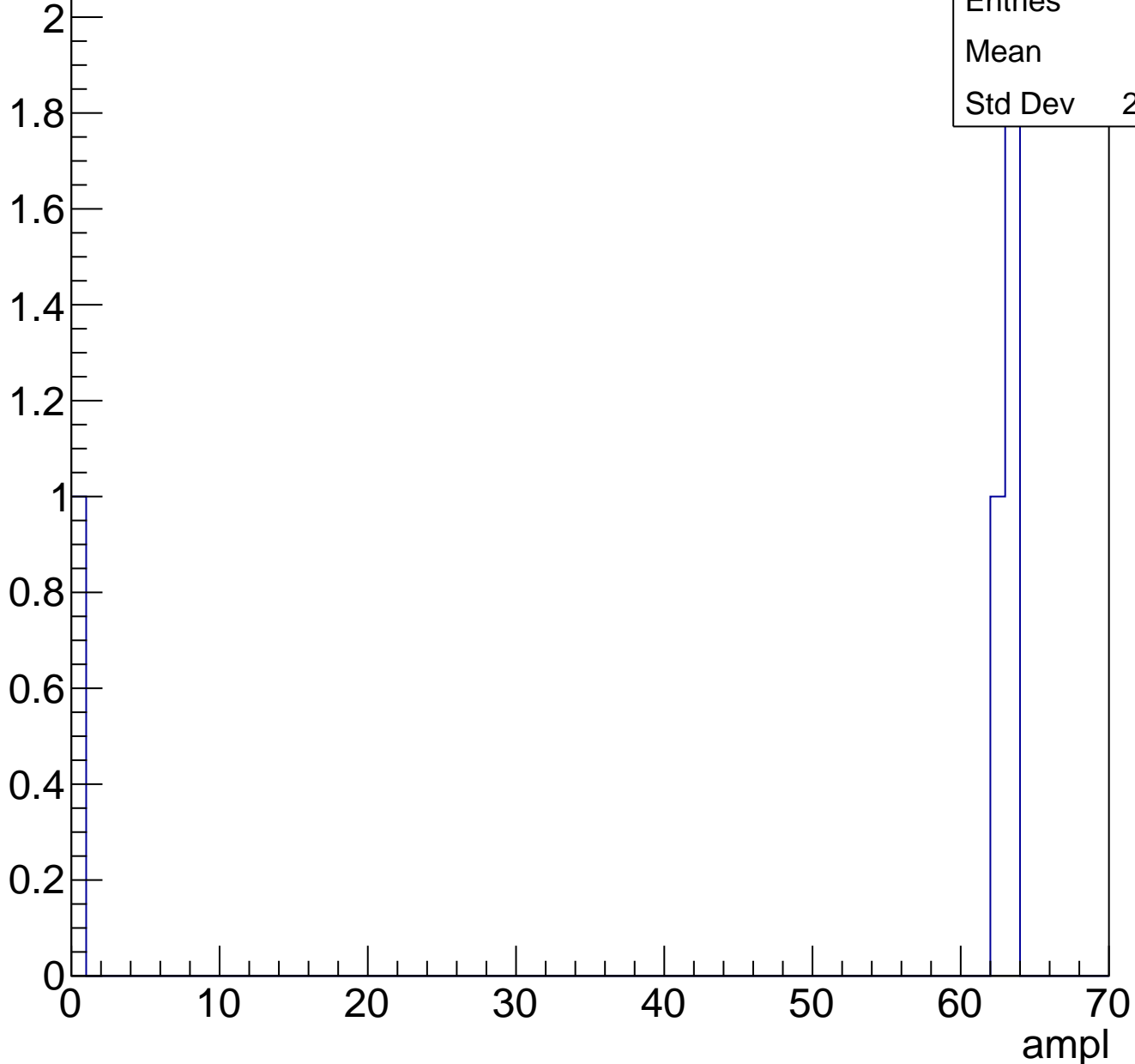
ampl



B1L001S, U16-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

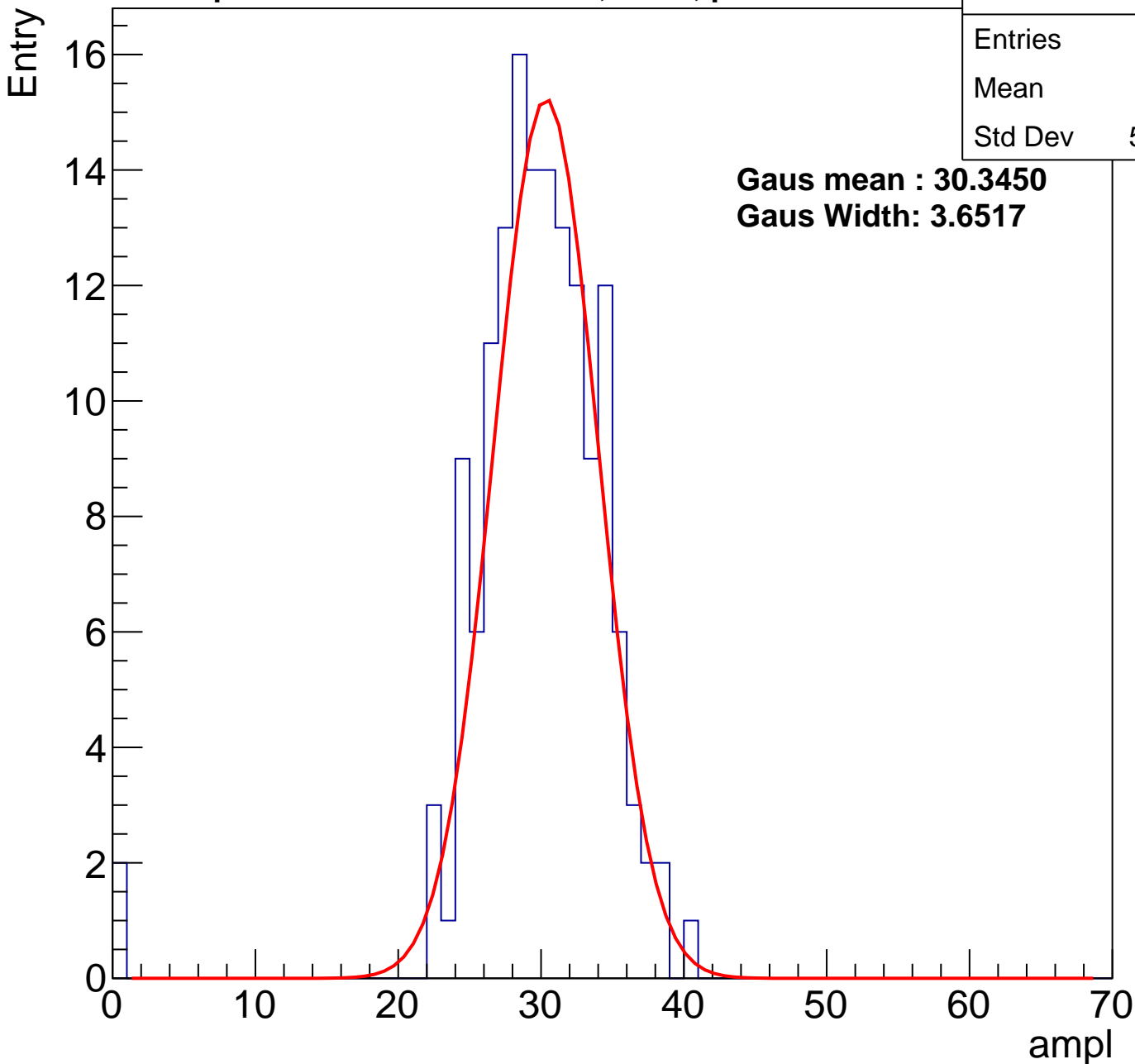


B1L001S, U16-ch25, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	29.3
Std Dev	5.004

Gaus mean : 30.3450
Gaus Width: 3.6517



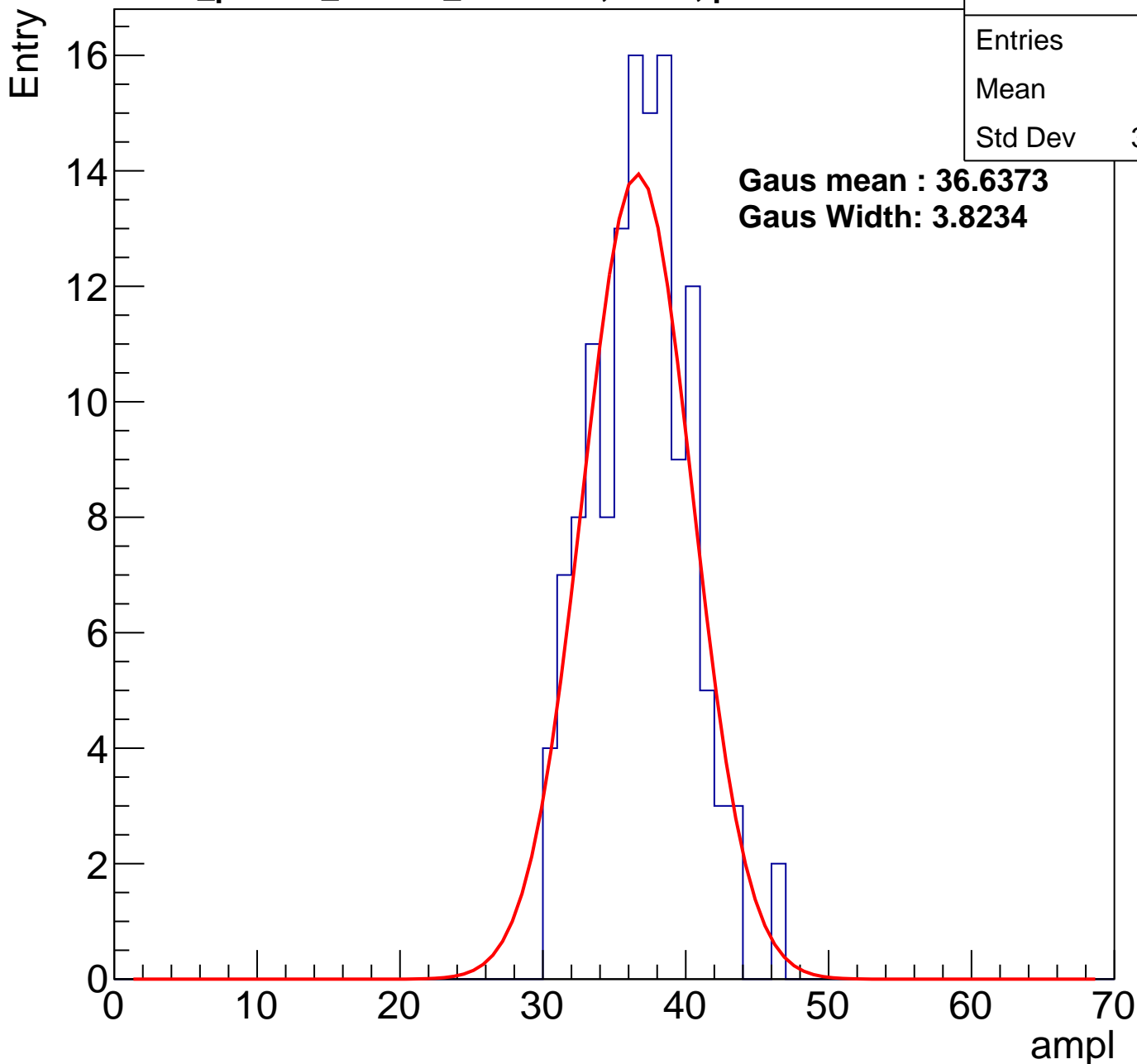
B1L001S, U16-ch25, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.4
Std Dev	3.373

Gaus mean : 36.6373

Gaus Width: 3.8234



B1L001S, U16-ch25, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

109

Mean

42.83

Std Dev

3.115

Gaus mean : 43.5489

Gaus Width: 3.1703

0

10

20

30

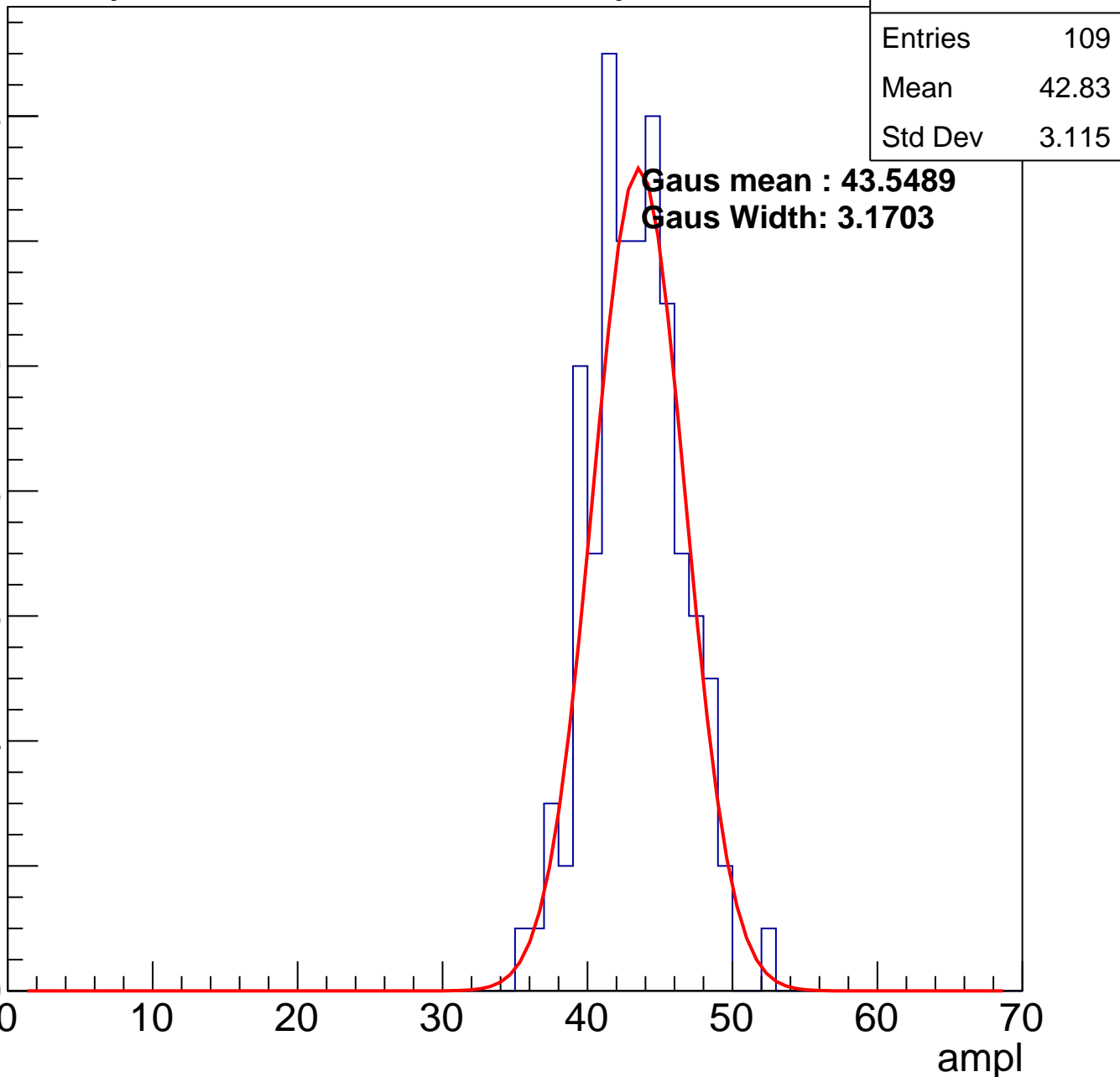
40

50

60

70

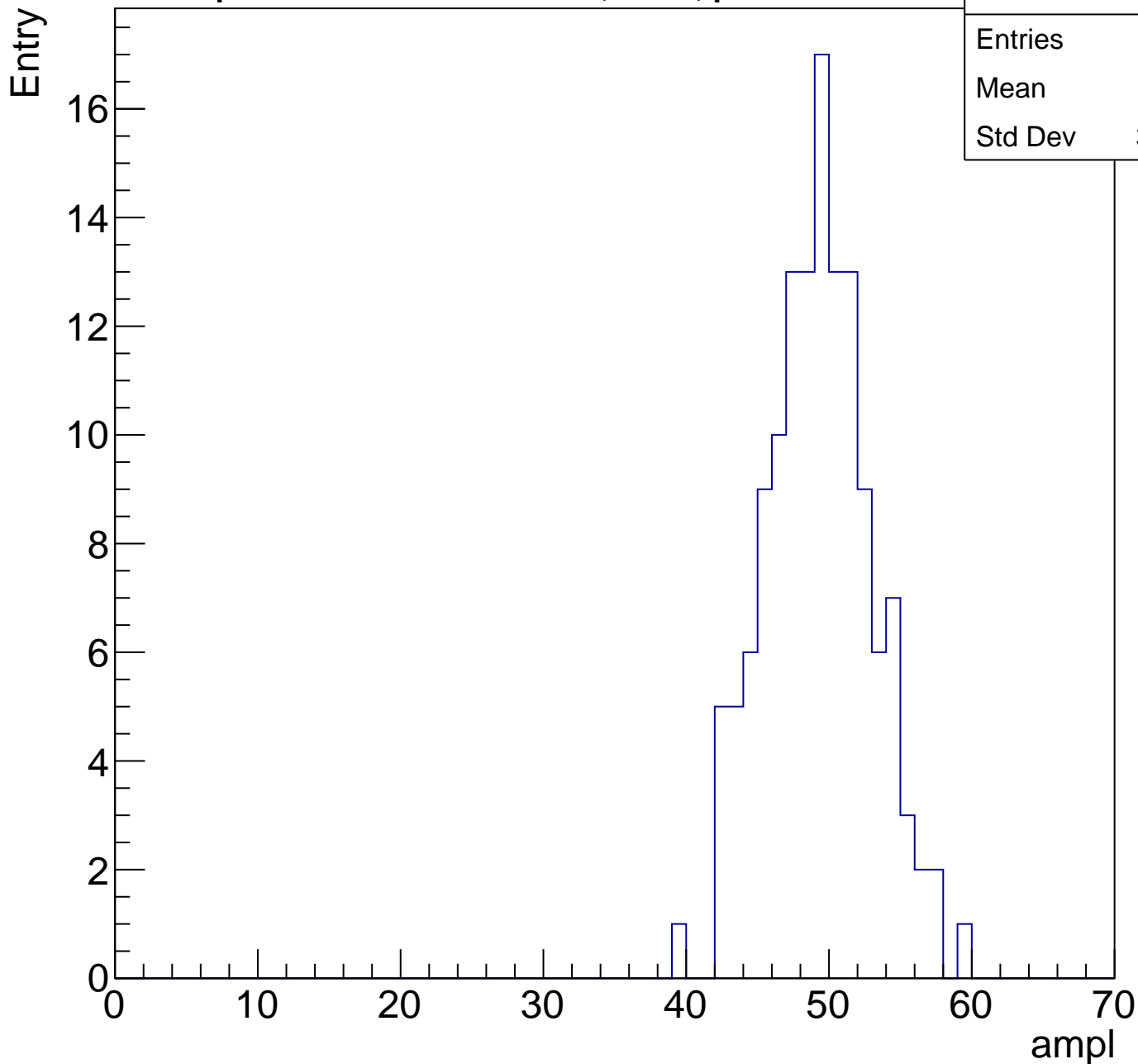
ampl



B1L001S, U16-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

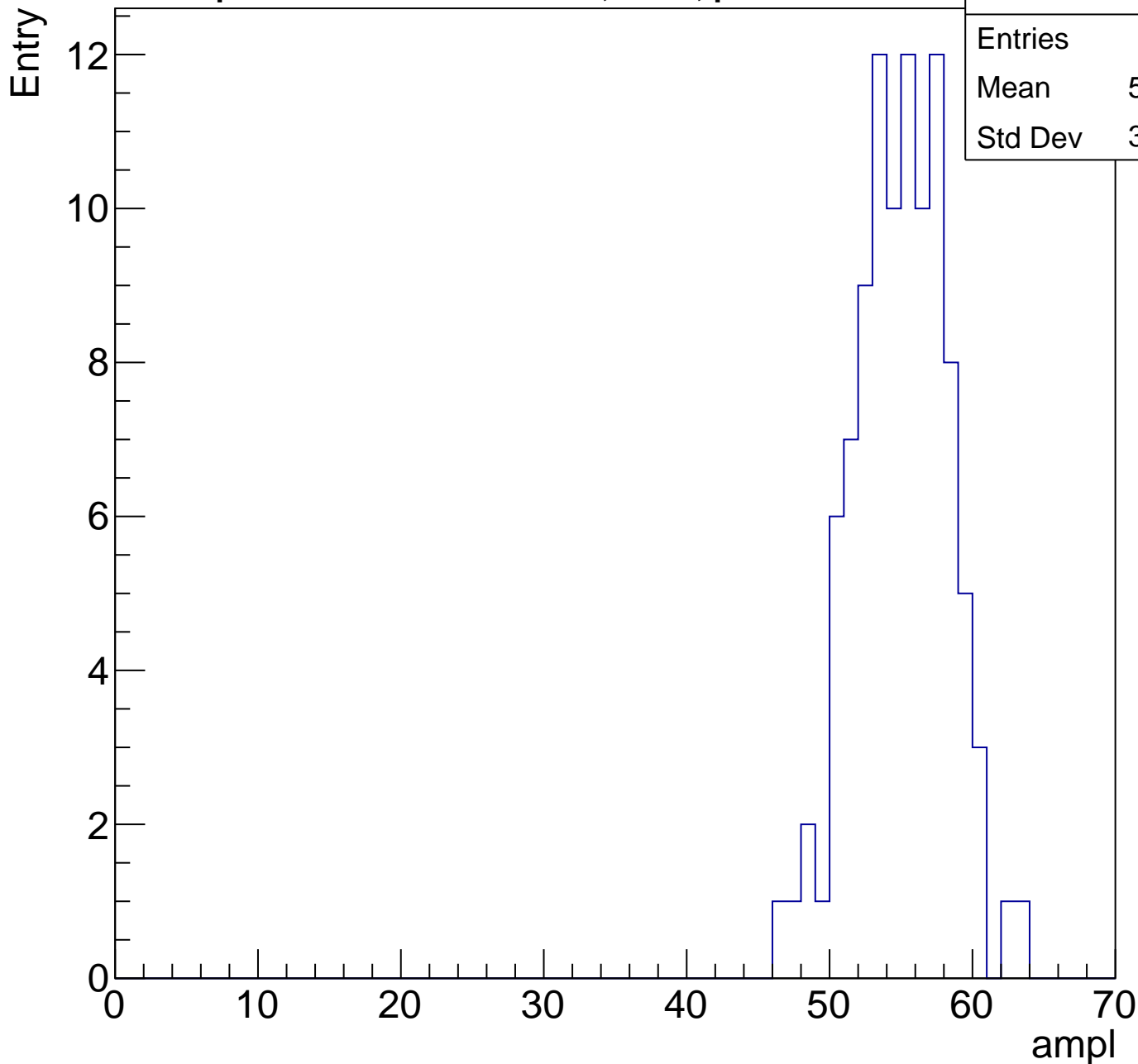
Entries	135
Mean	48.8
Std Dev	3.671



B1L001S, U16-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	54.52
Std Dev	3.232



B1L001S, U16-ch25, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	106
Mean	58.61
Std Dev	6.373

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

7

6

5

4

3

2

1

0

Entries	23
Mean	59.09
Std Dev	12.65

0

10

20

30

40

50

60

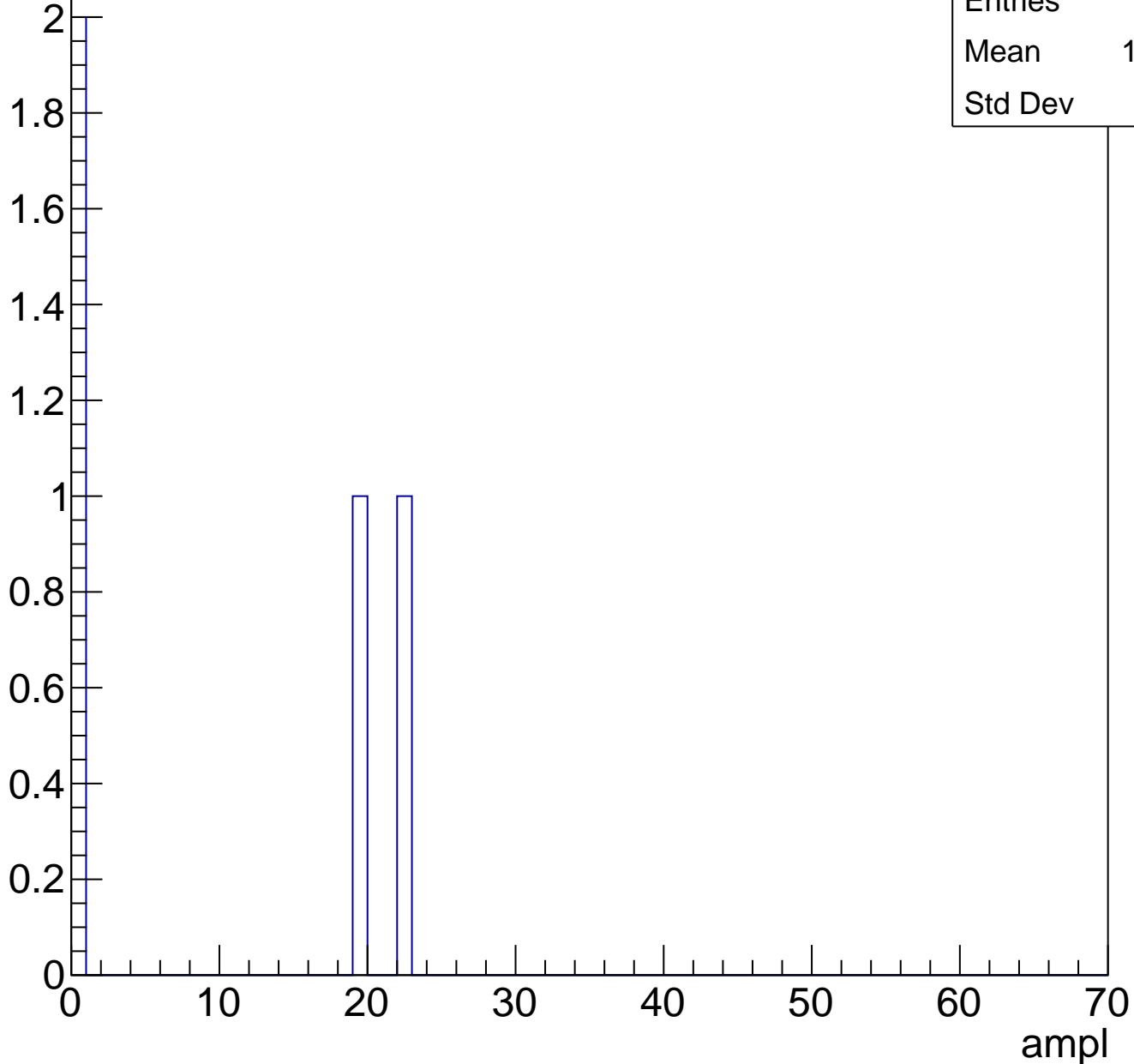
70

ampl

B1L001S, U16-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	10.25
Std Dev	10.3

B1L001S, U16-ch26, adc0

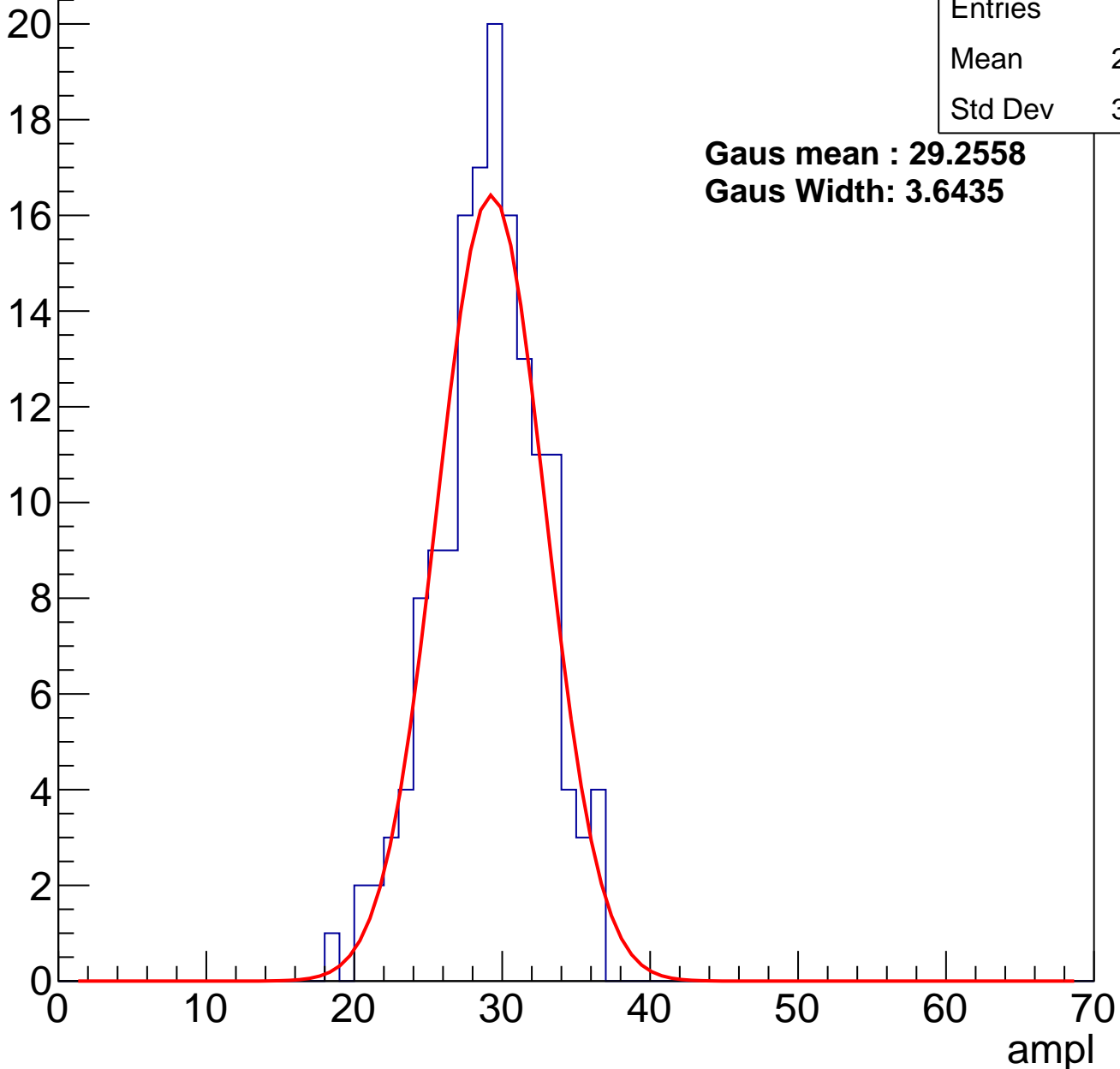
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	28.63
Std Dev	3.555

Gaus mean : 29.2558

Gaus Width: 3.6435

Entry

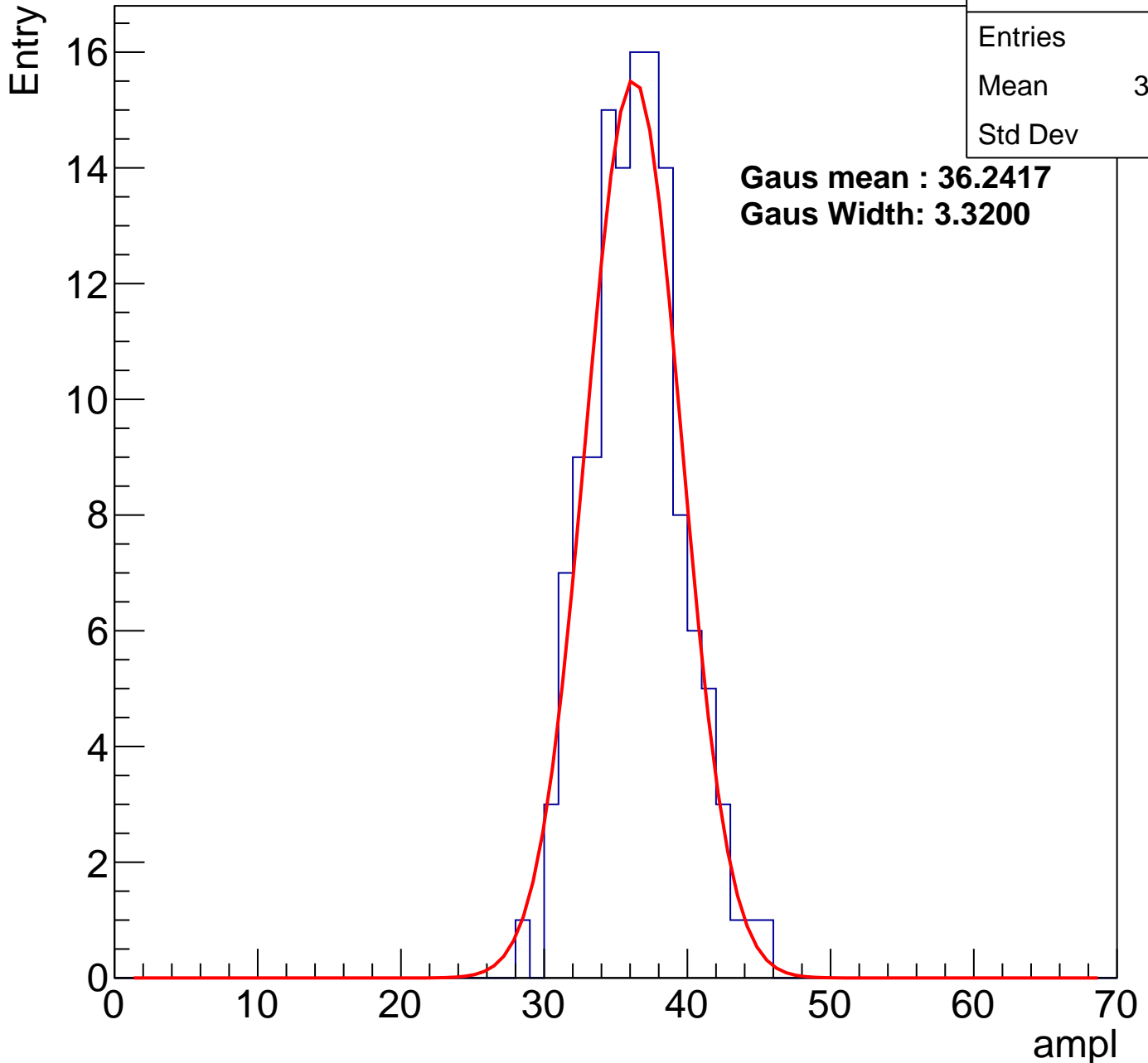


B1L001S, U16-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.93
Std Dev	3.19

Gaus mean : 36.2417
Gaus Width: 3.3200



B1L001S, U16-ch26, adc2

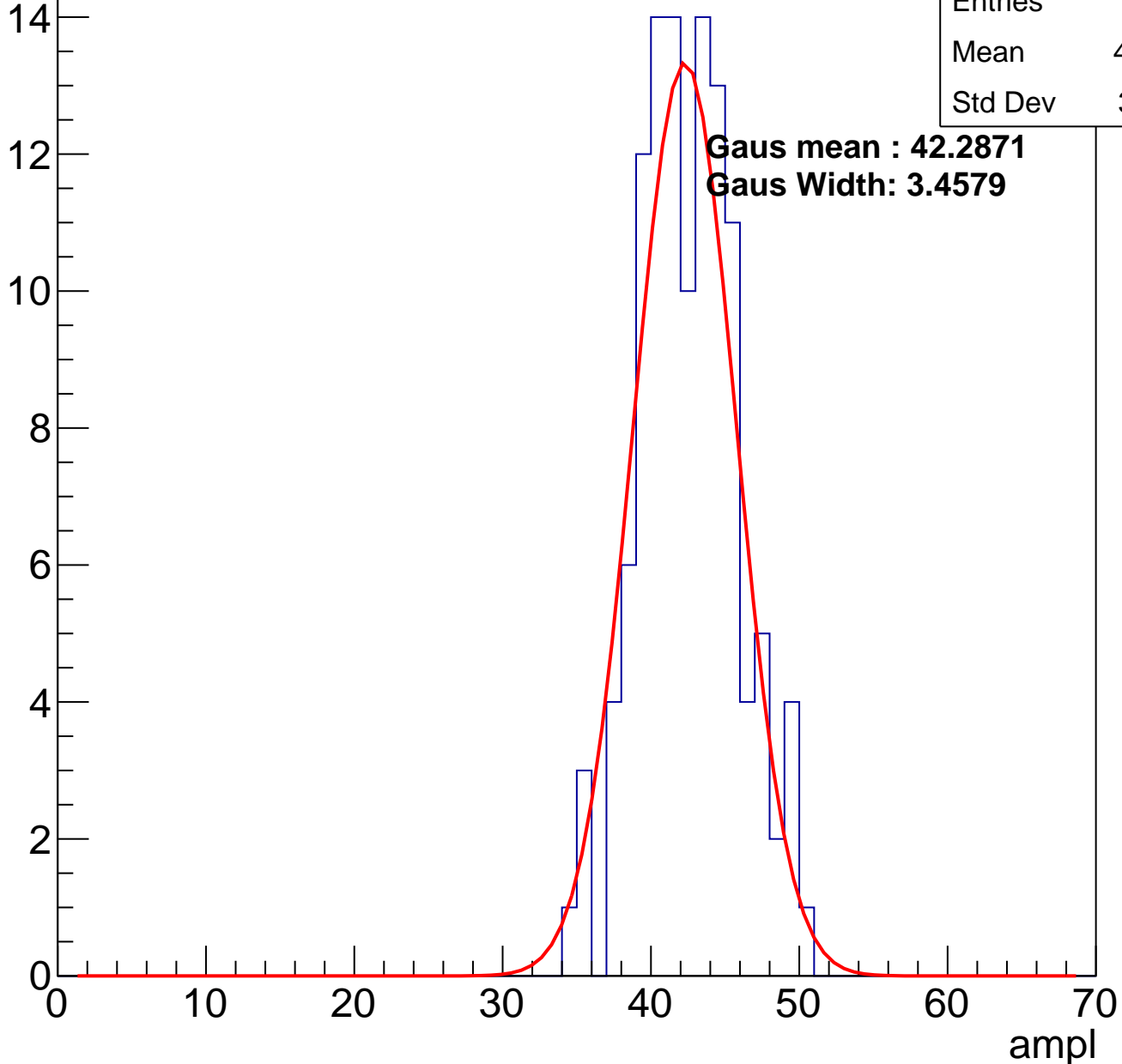
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.09
Std Dev	3.281

Entry

Gaus mean : 42.2871

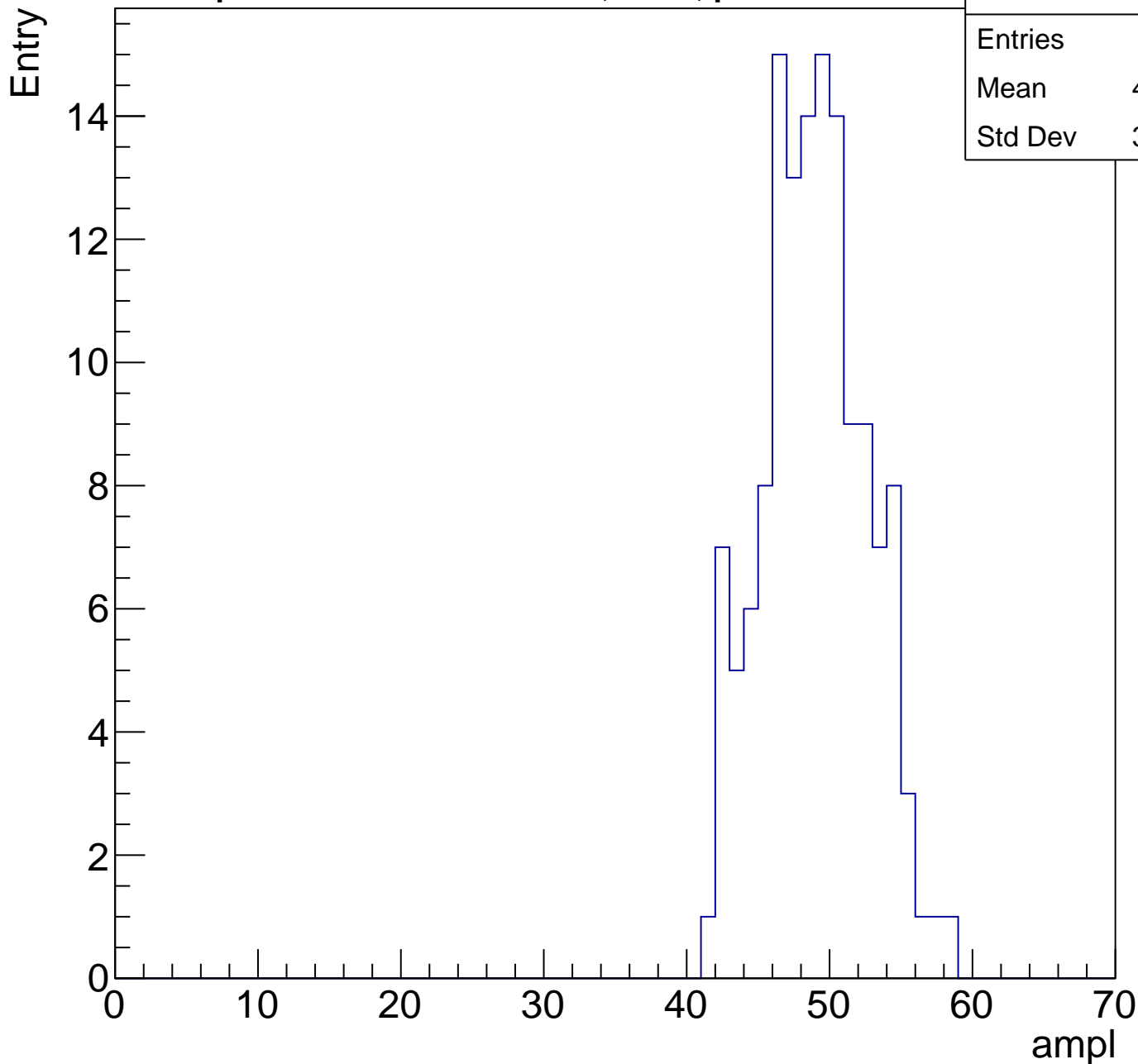
Gaus Width: 3.4579



B1L001S, U16-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

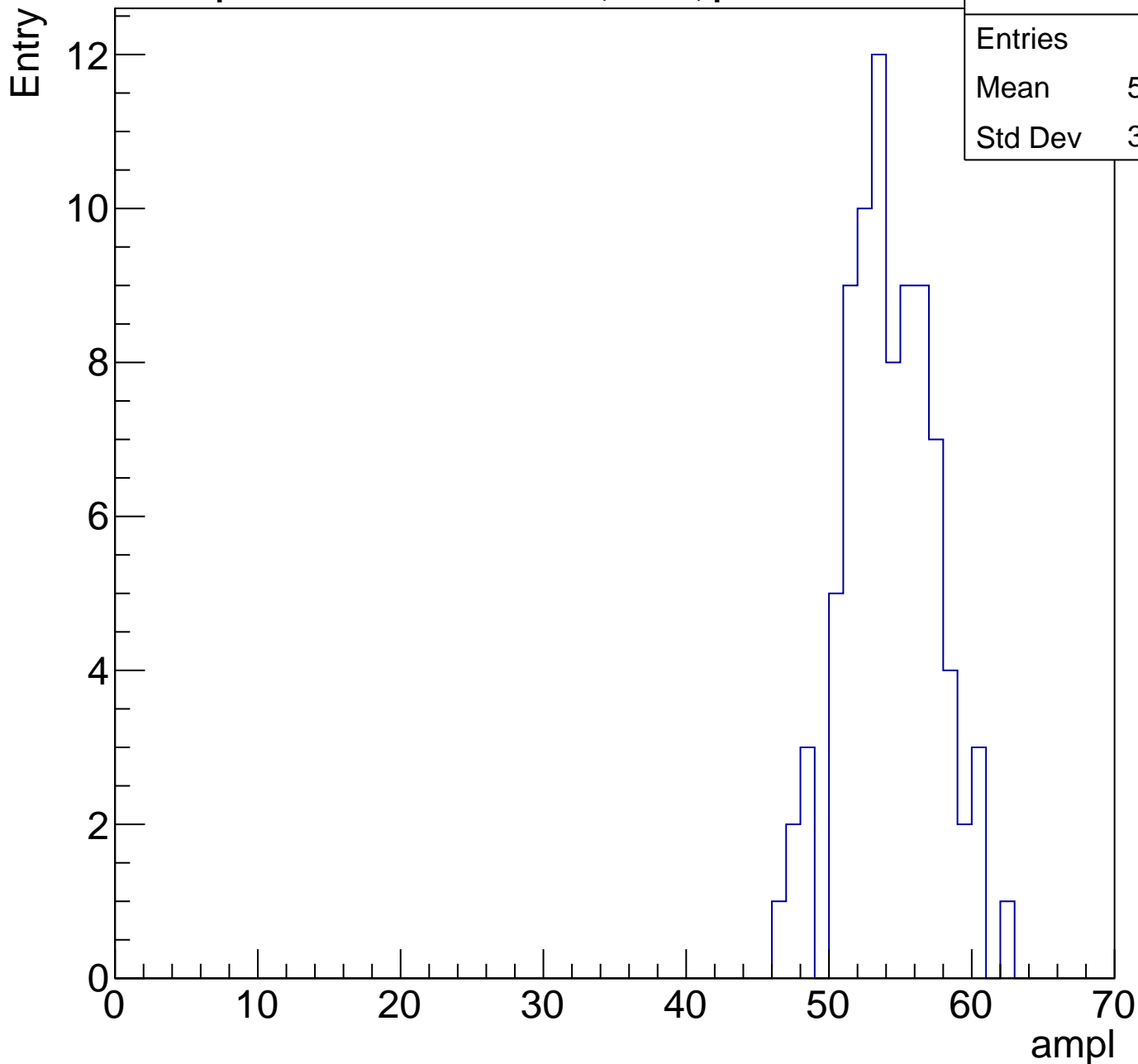
Entries	137
Mean	48.53
Std Dev	3.605



B1L001S, U16-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	85
Mean	53.78
Std Dev	3.204

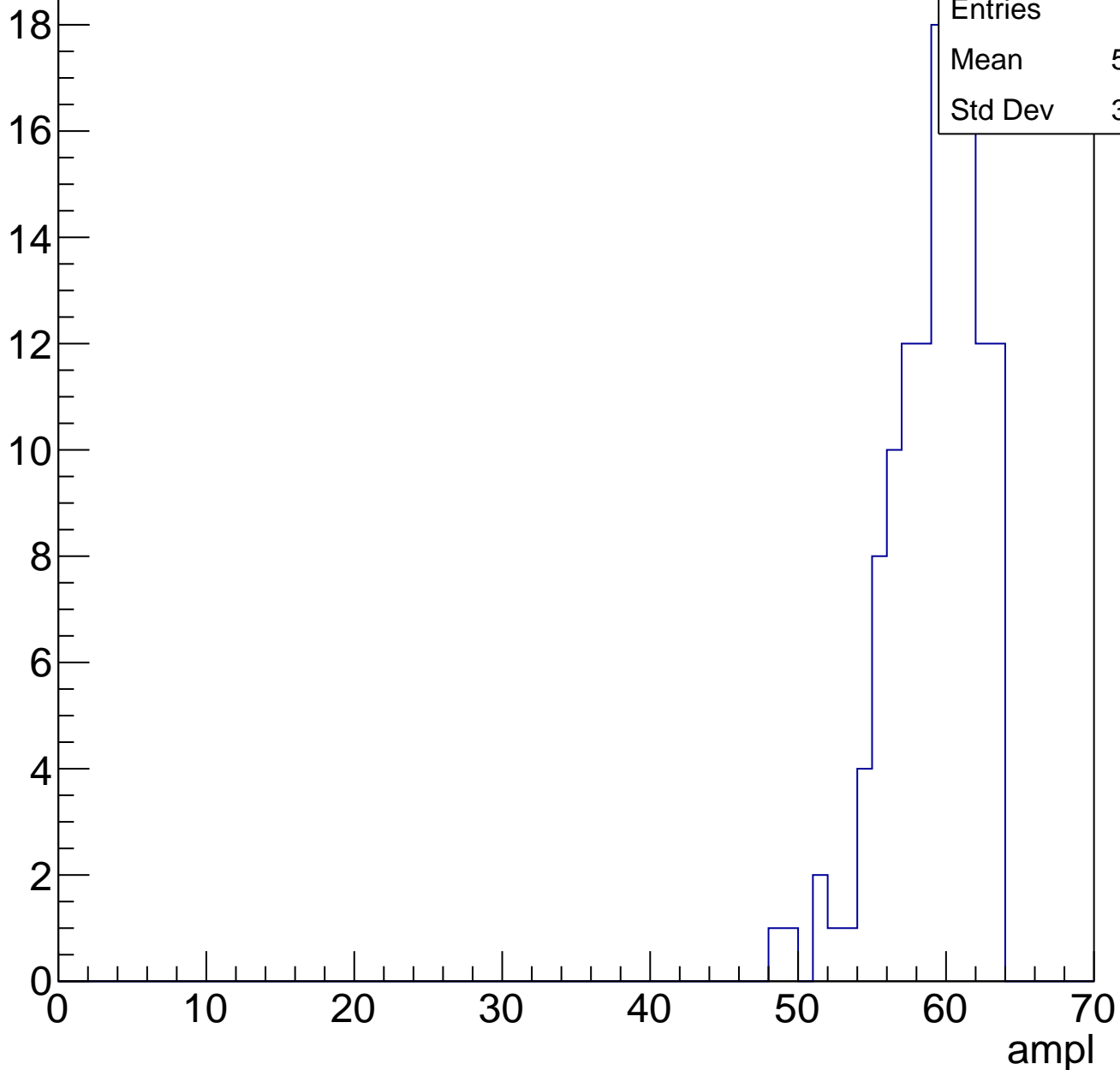


B1L001S, U16-ch26, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	58.73
Std Dev	3.057

Entry

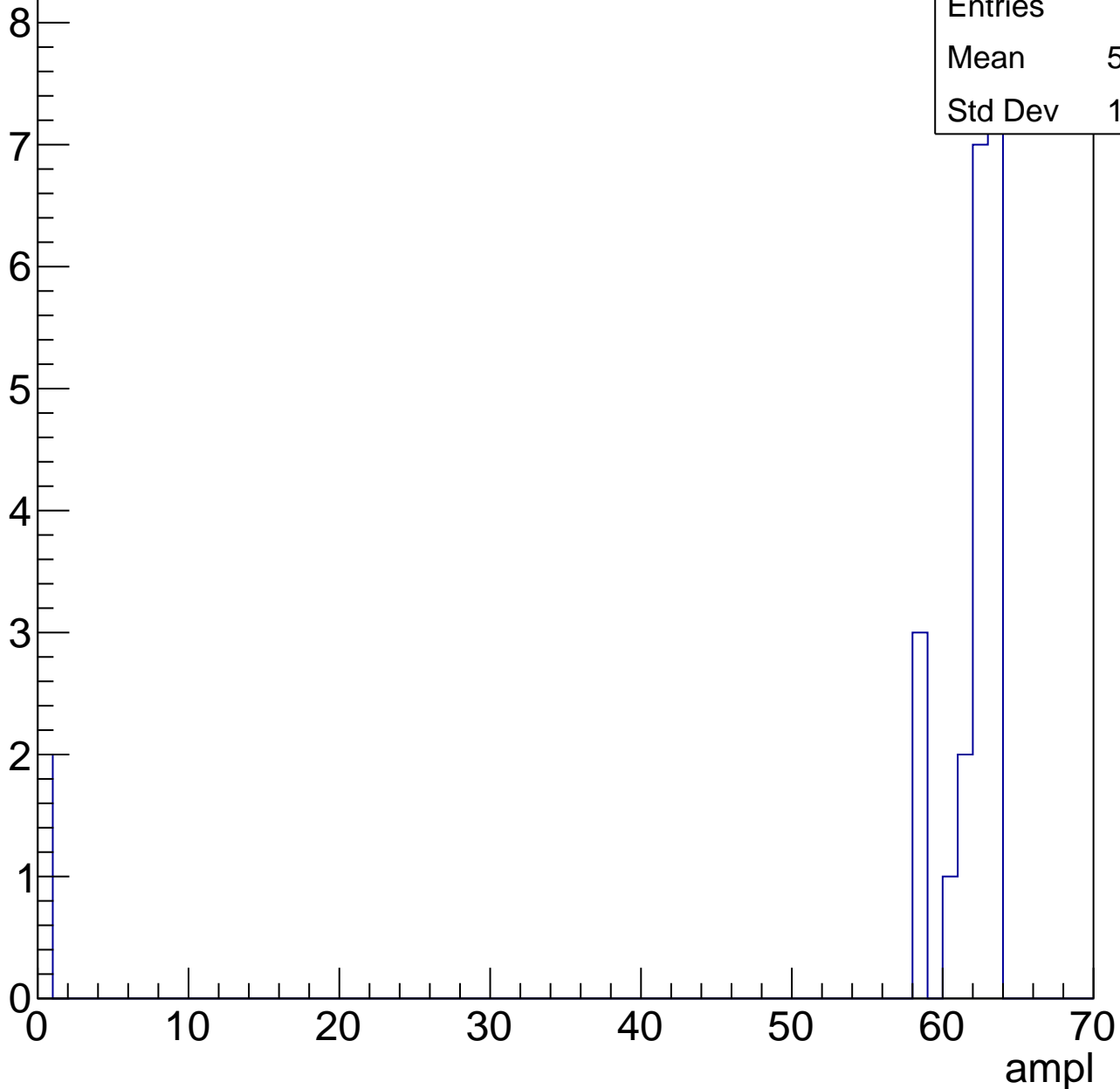


B1L001S, U16-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

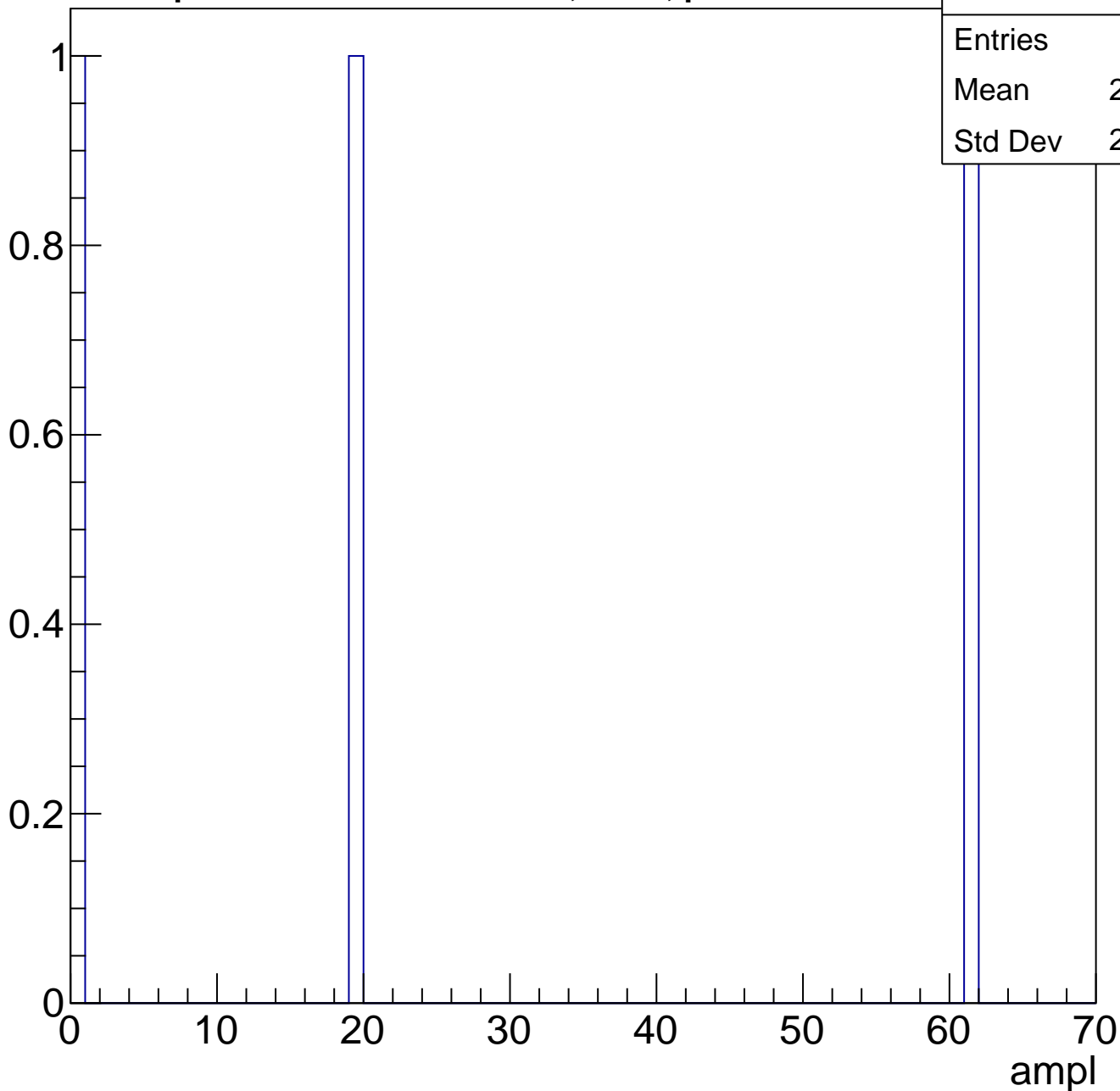
Entries	23
Mean	56.26
Std Dev	17.44



B1L001S, U16-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	26.67
Std Dev	25.49

B1L001S, U16-ch27, adc0

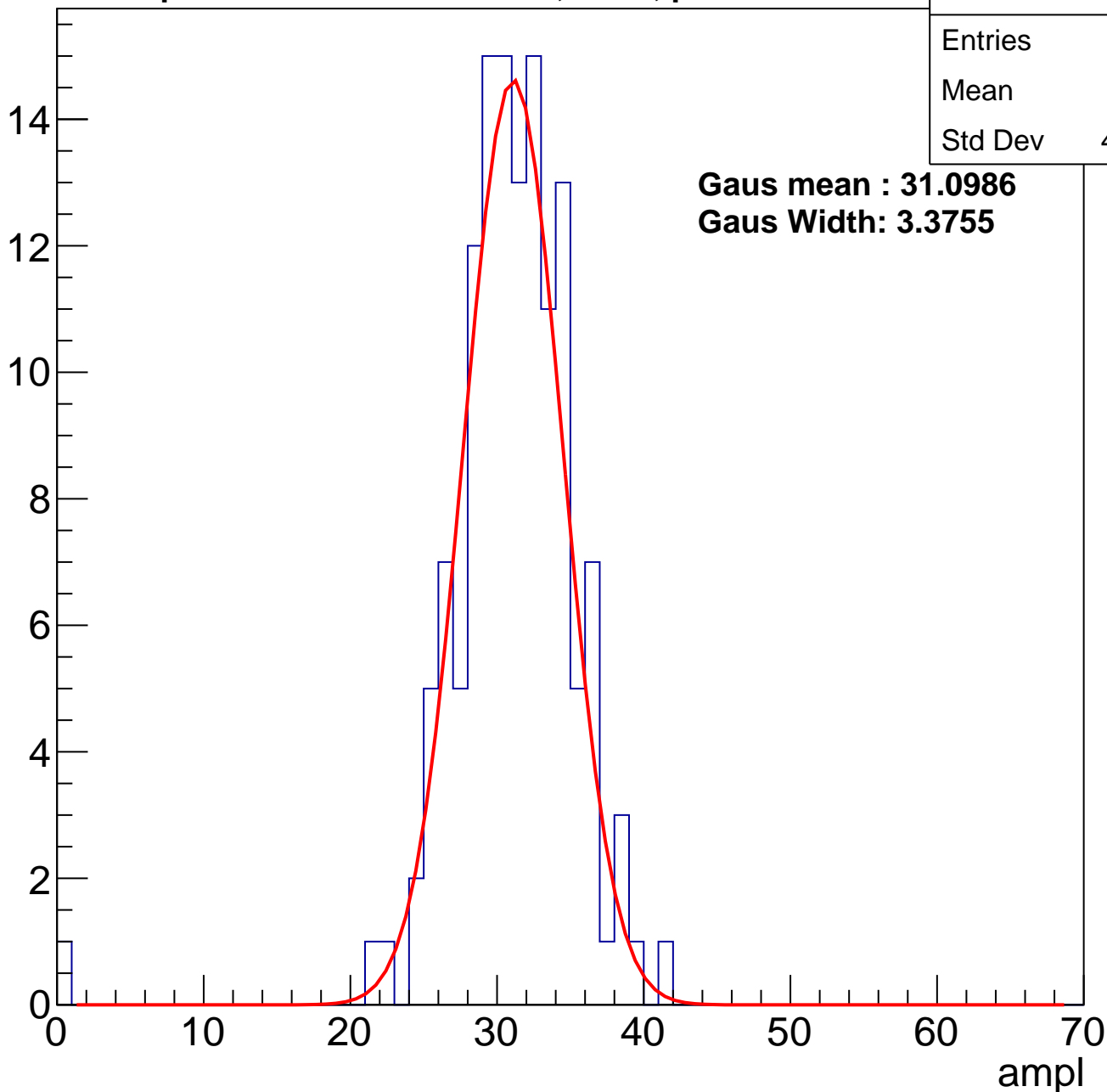
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.6
Std Dev	4.417

Gaus mean : 31.0986

Gaus Width: 3.3755

Entry



B1L001S, U16-ch27, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.9
Std Dev	3.431

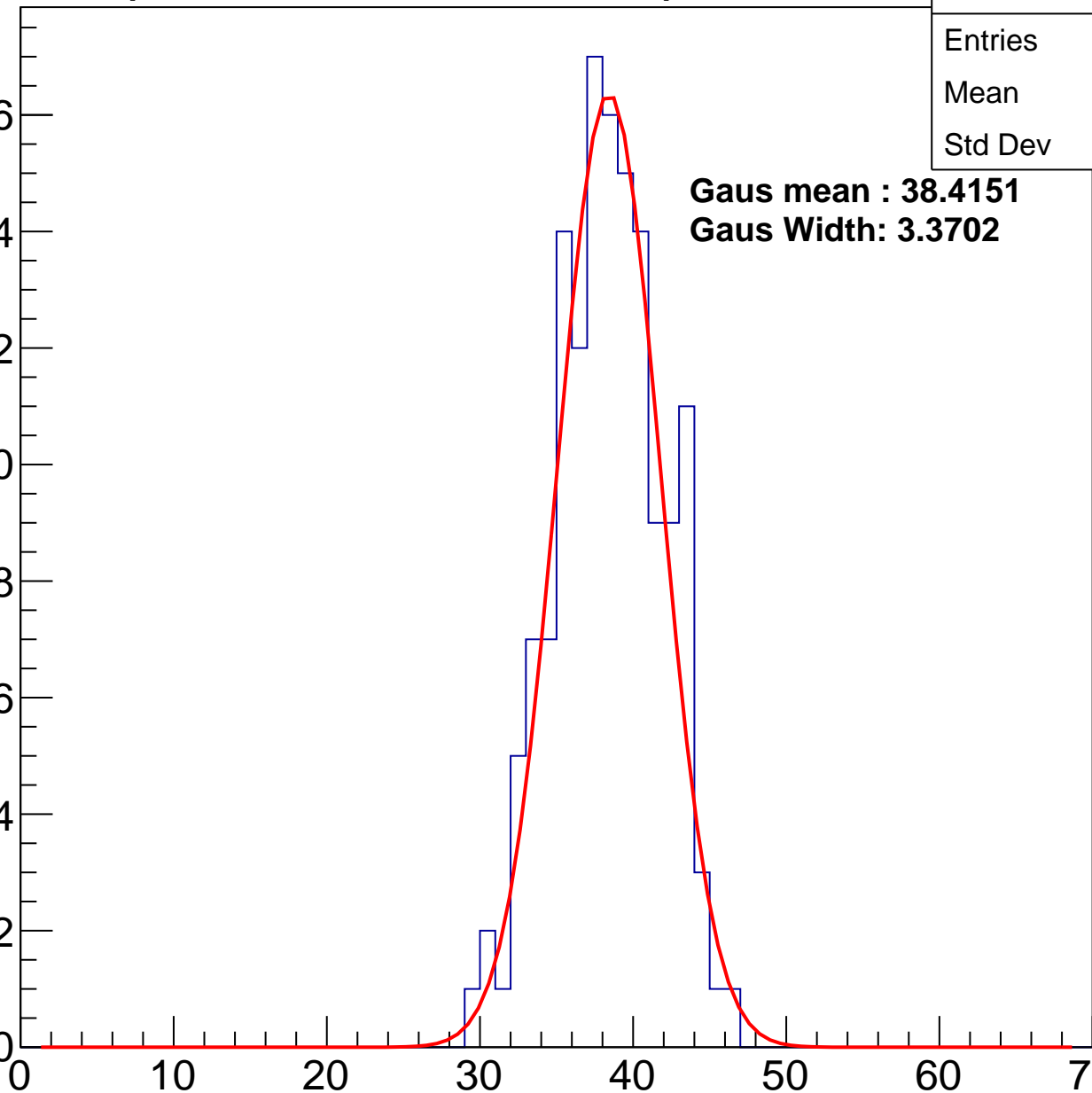
Gaus mean : 38.4151

Gaus Width: 3.3702

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

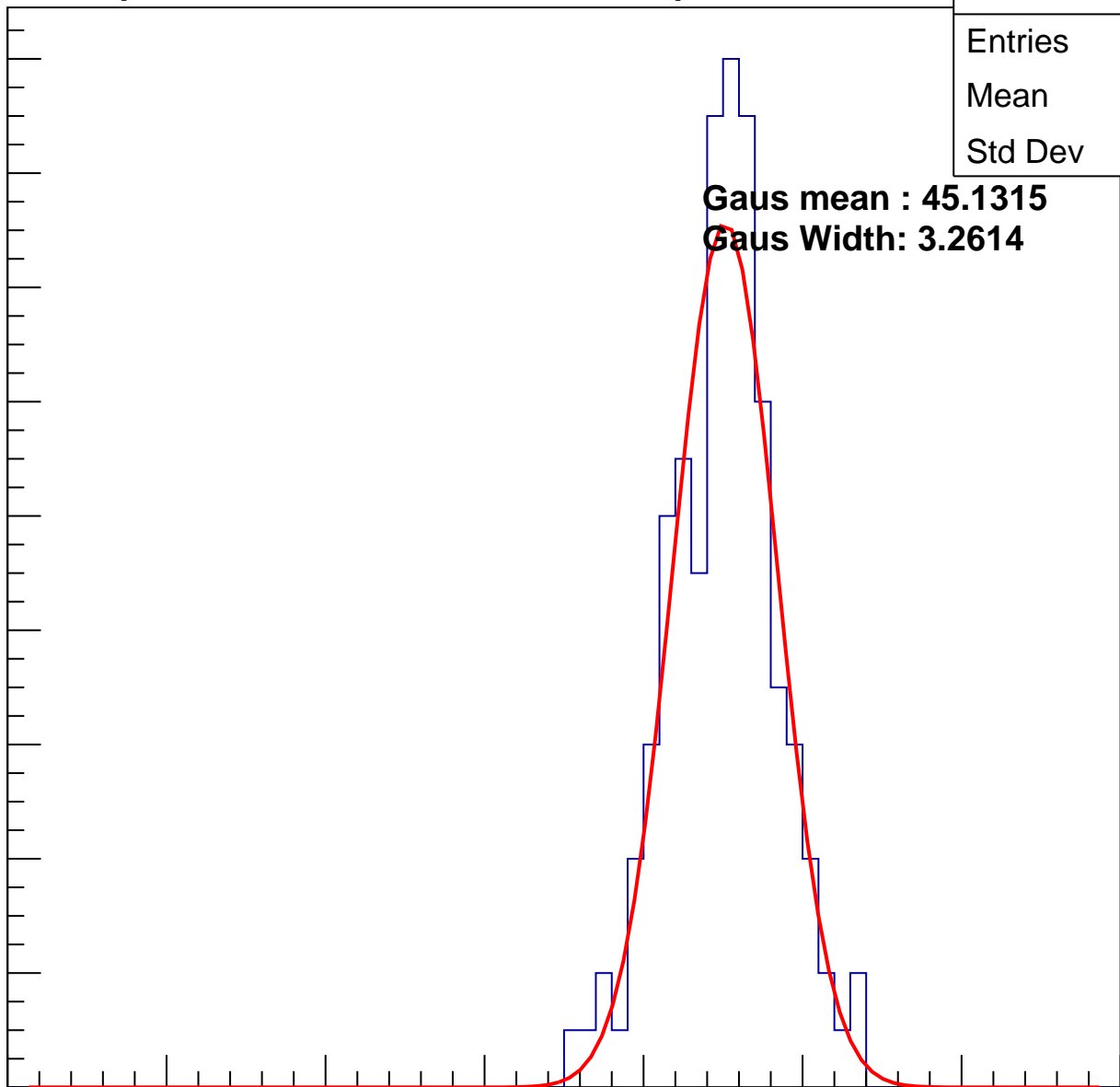
Entries	131
Mean	44.52
Std Dev	3.391

Gaus mean : 45.1315

Gaus Width: 3.2614

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch27, adc3

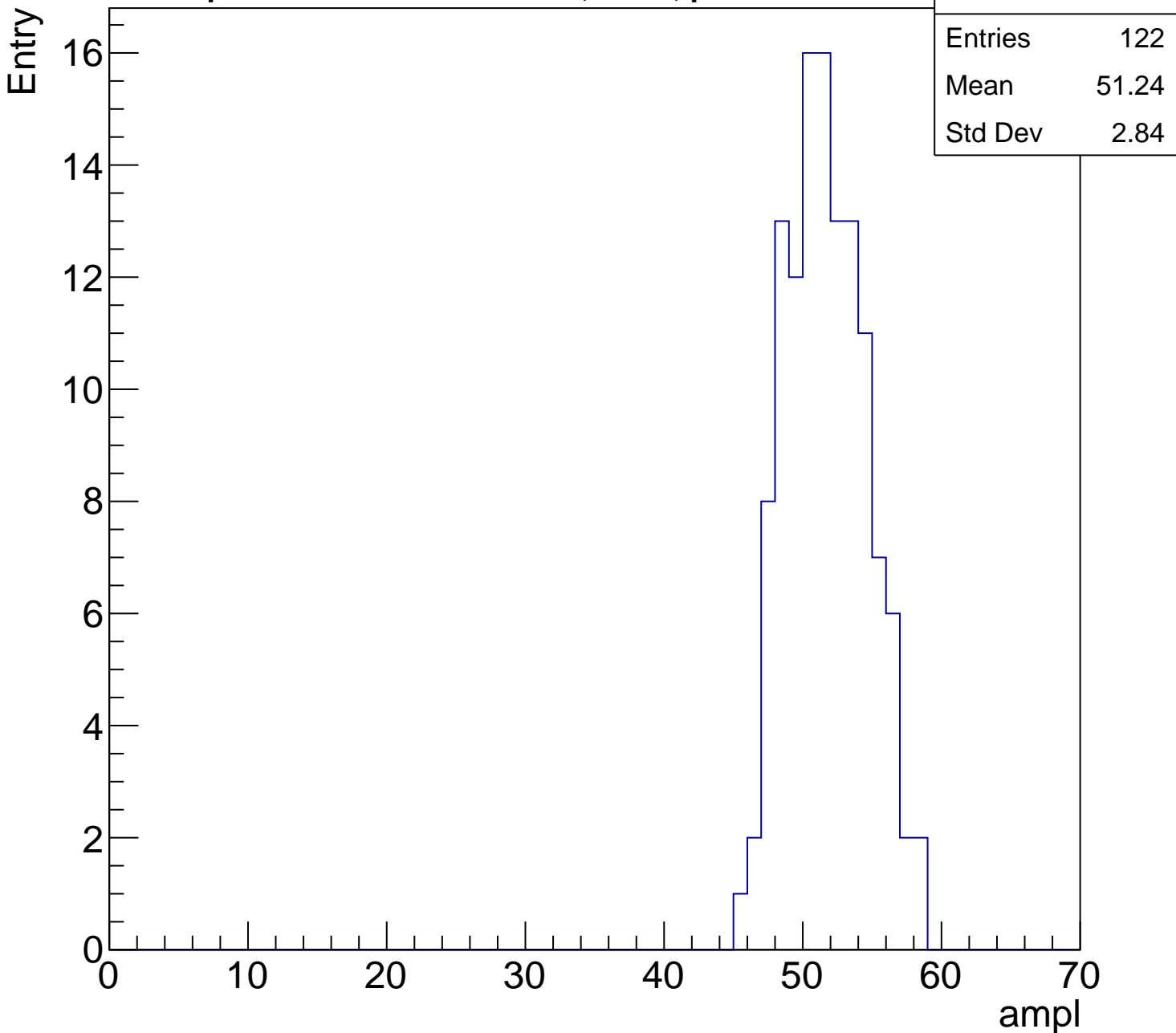
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	51.24
Std Dev	2.84

ampl



B1L001S, U16-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

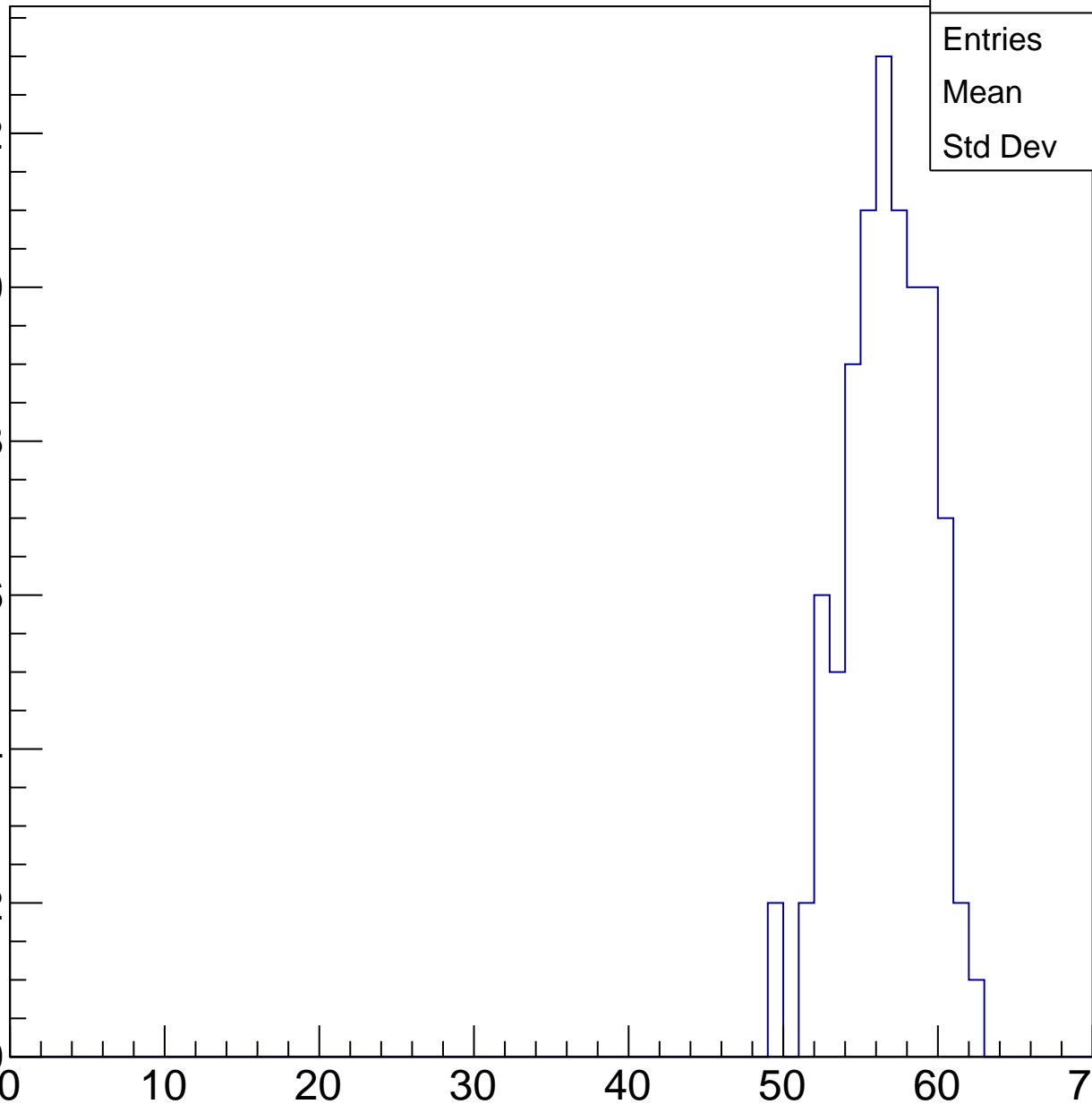
40

50

60

ampl

Entries	89
Mean	56.15
Std Dev	2.75



B1L001S, U16-ch27, adc5

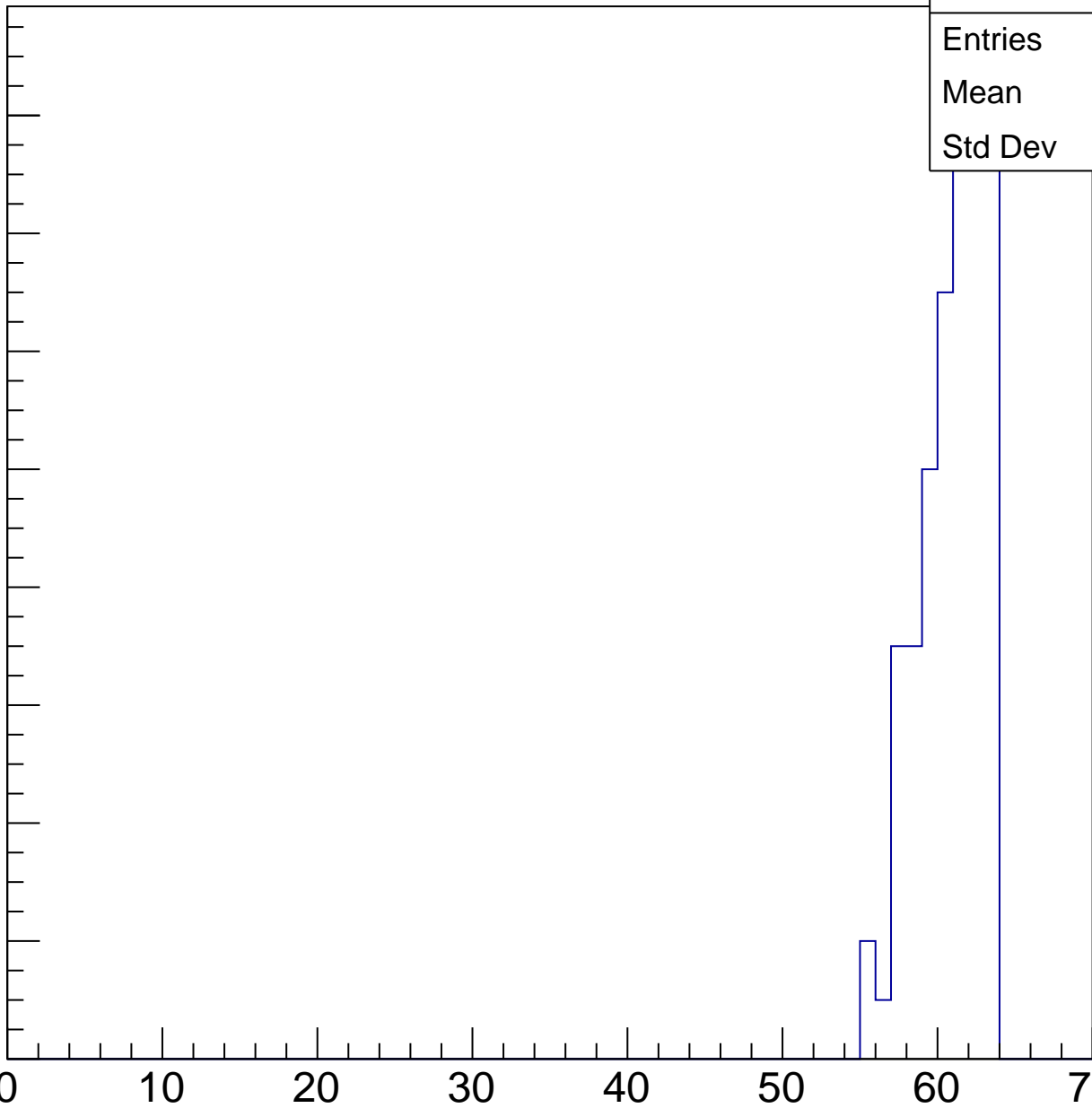
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	60.46
Std Dev	2.061

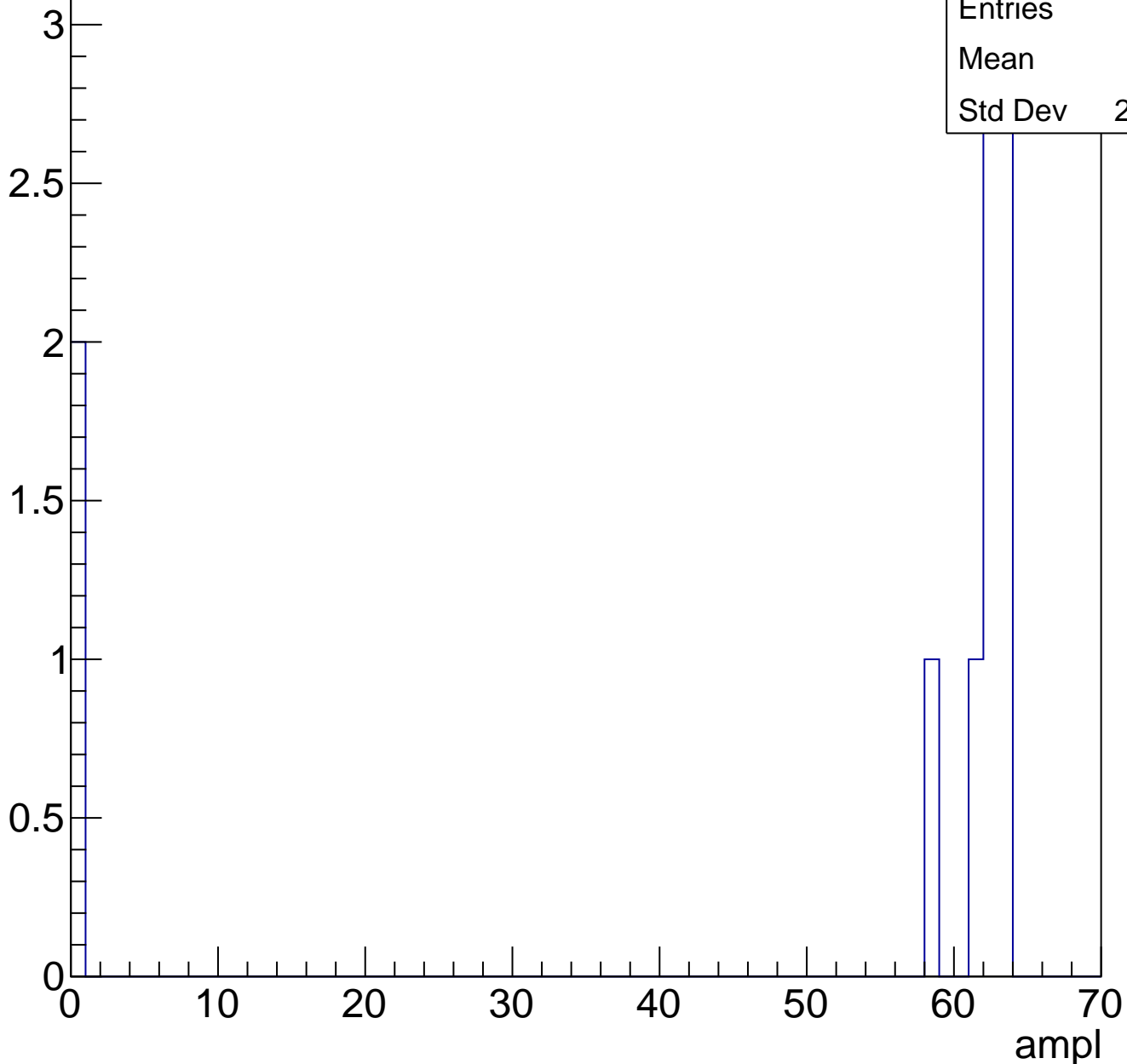
ampl



B1L001S, U16-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



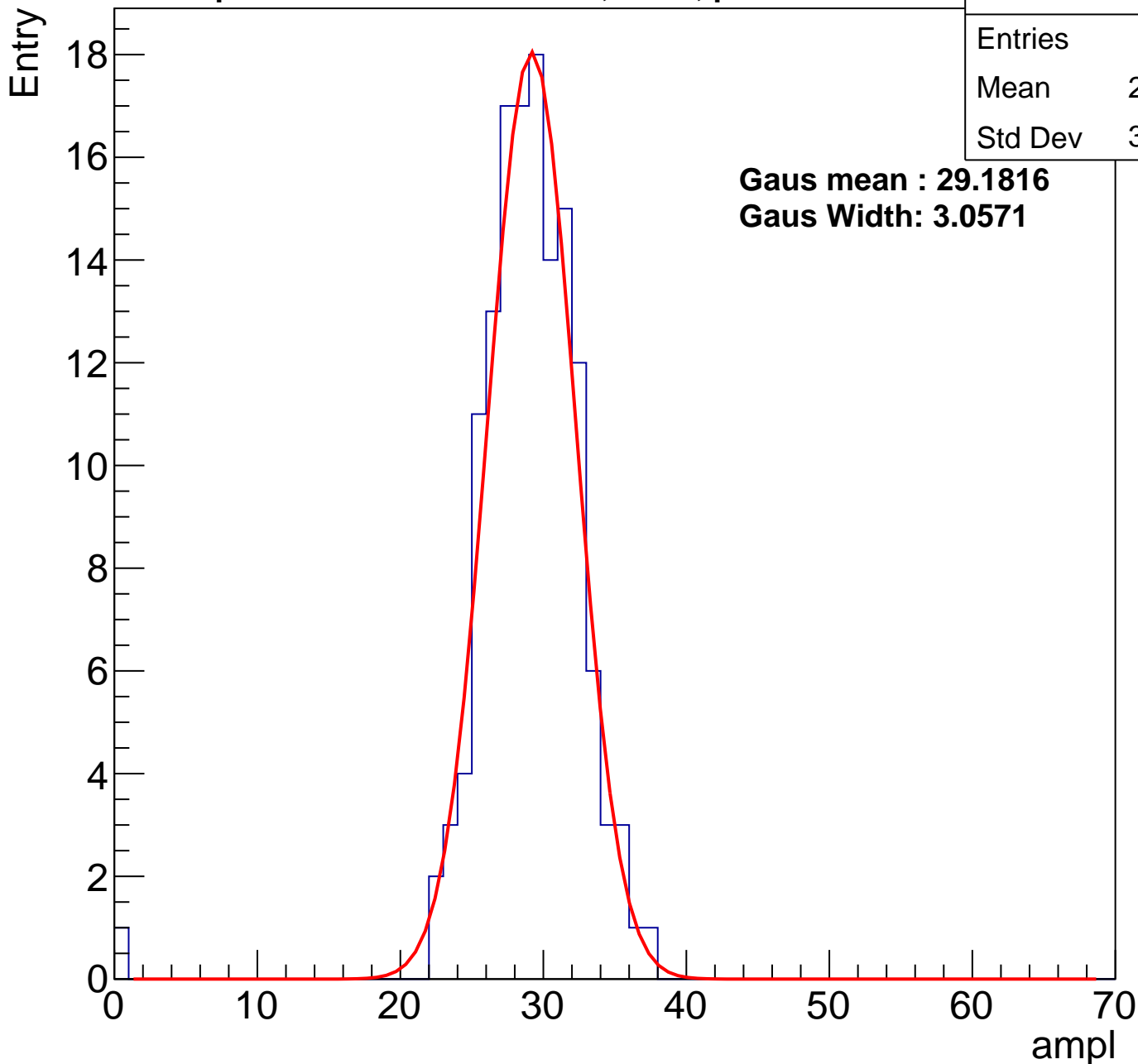
B1L001S, U16-ch28, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	28.55
Std Dev	3.832

Gaus mean : 29.1816

Gaus Width: 3.0571



B1L001S, U16-ch28, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	35.82
Std Dev	3.188

Gaus mean : 36.4024

Gaus Width: 3.2414

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

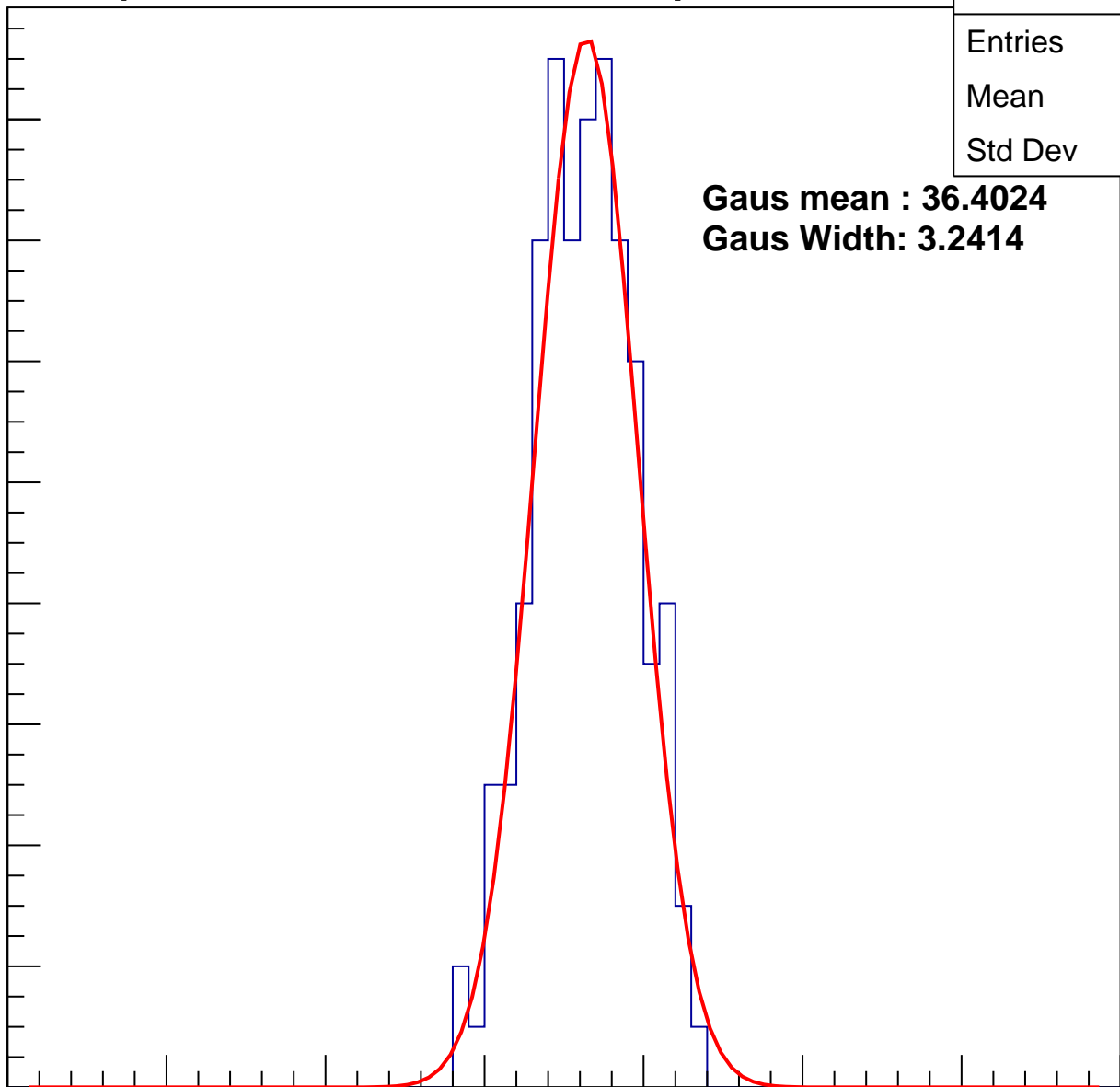
40

50

60

70

ampl



B1L001S, U16-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

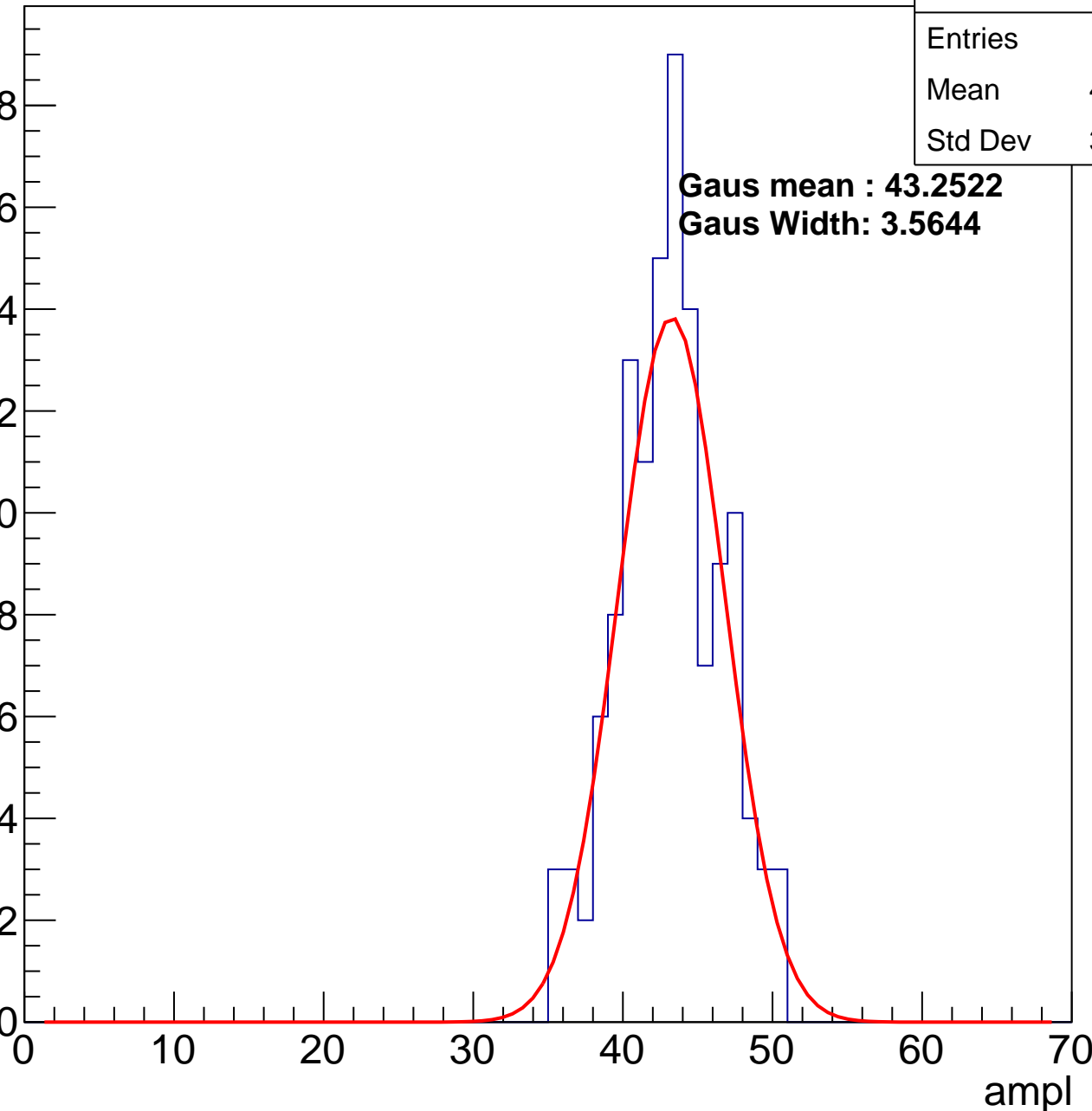
Entry

18
16
14
12
10
8
6
4
2
0

Entries	130
Mean	42.68
Std Dev	3.386

Gaus mean : 43.2522

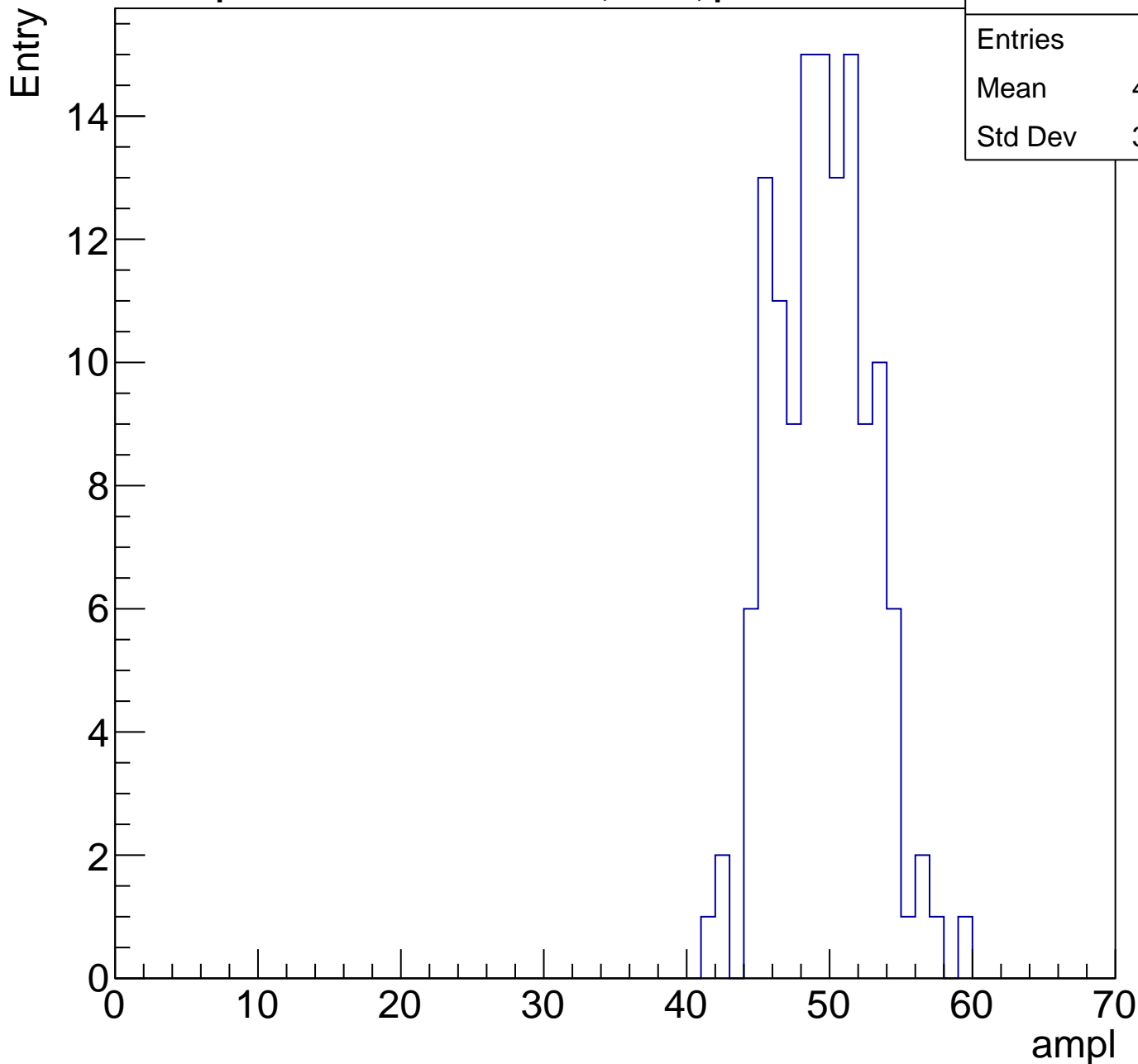
Gaus Width: 3.5644



B1L001S, U16-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

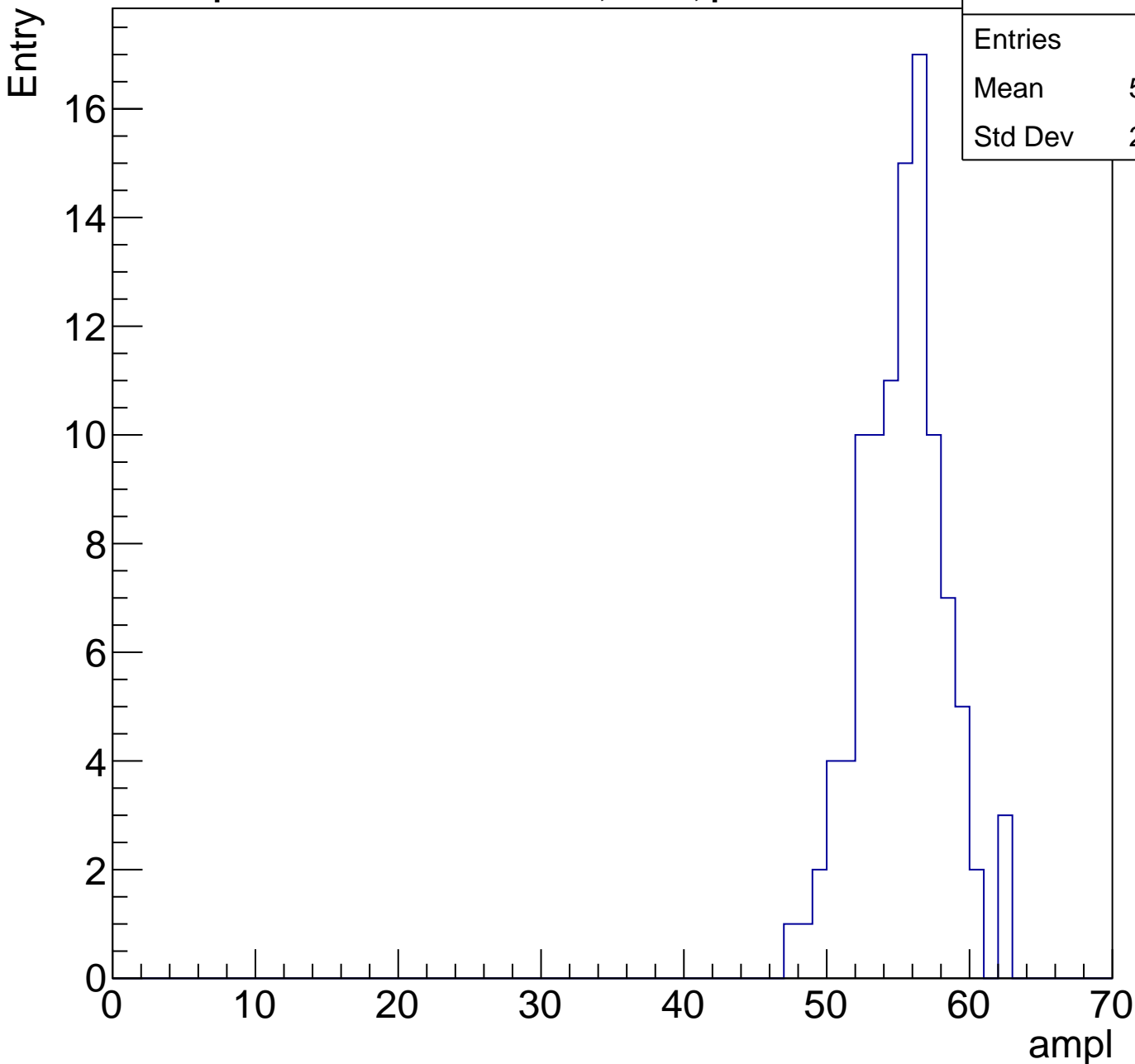
Entries	130
Mean	49.06
Std Dev	3.318



B1L001S, U16-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

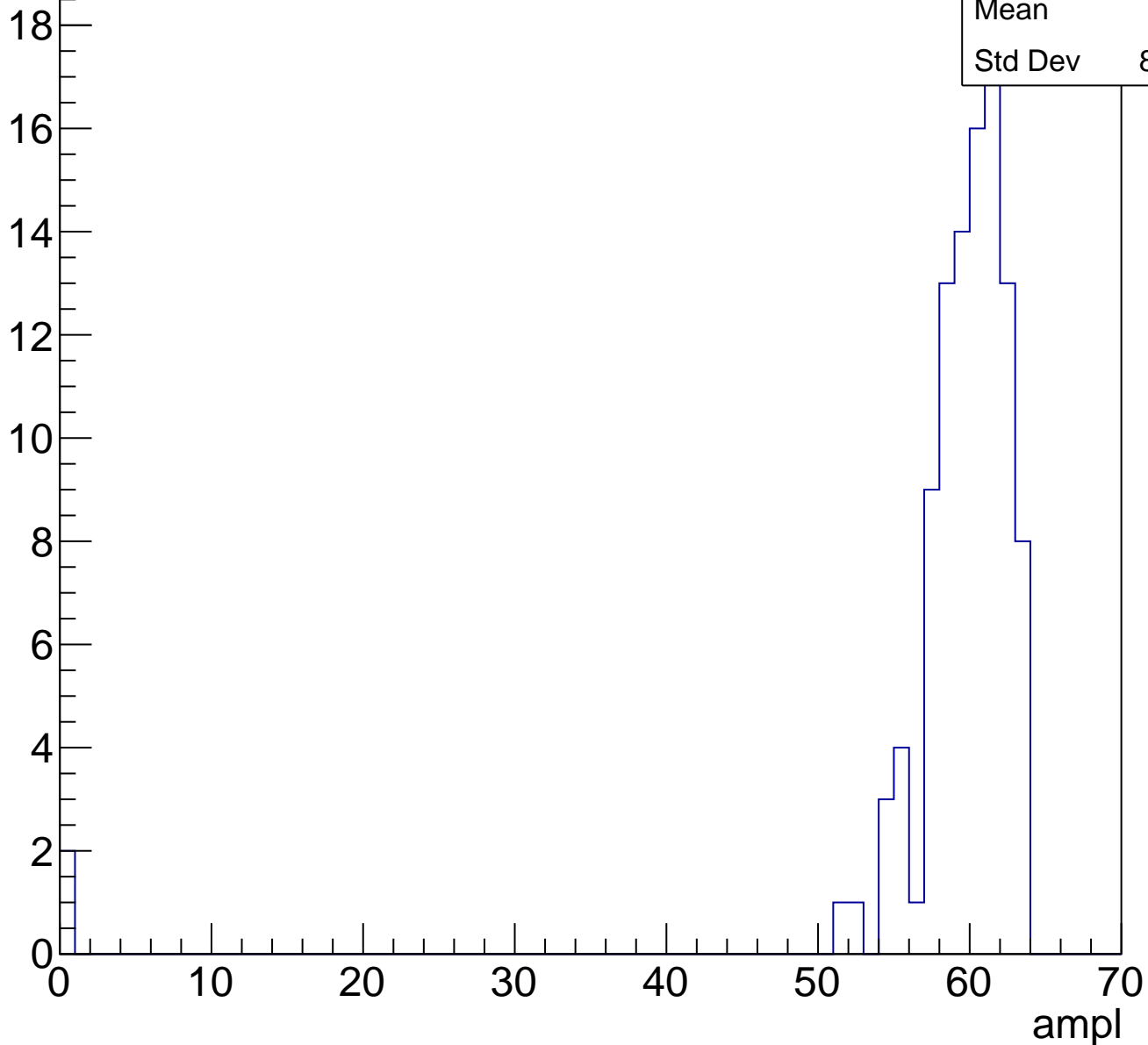
Entries	102
Mean	54.85
Std Dev	2.945



B1L001S, U16-ch28, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

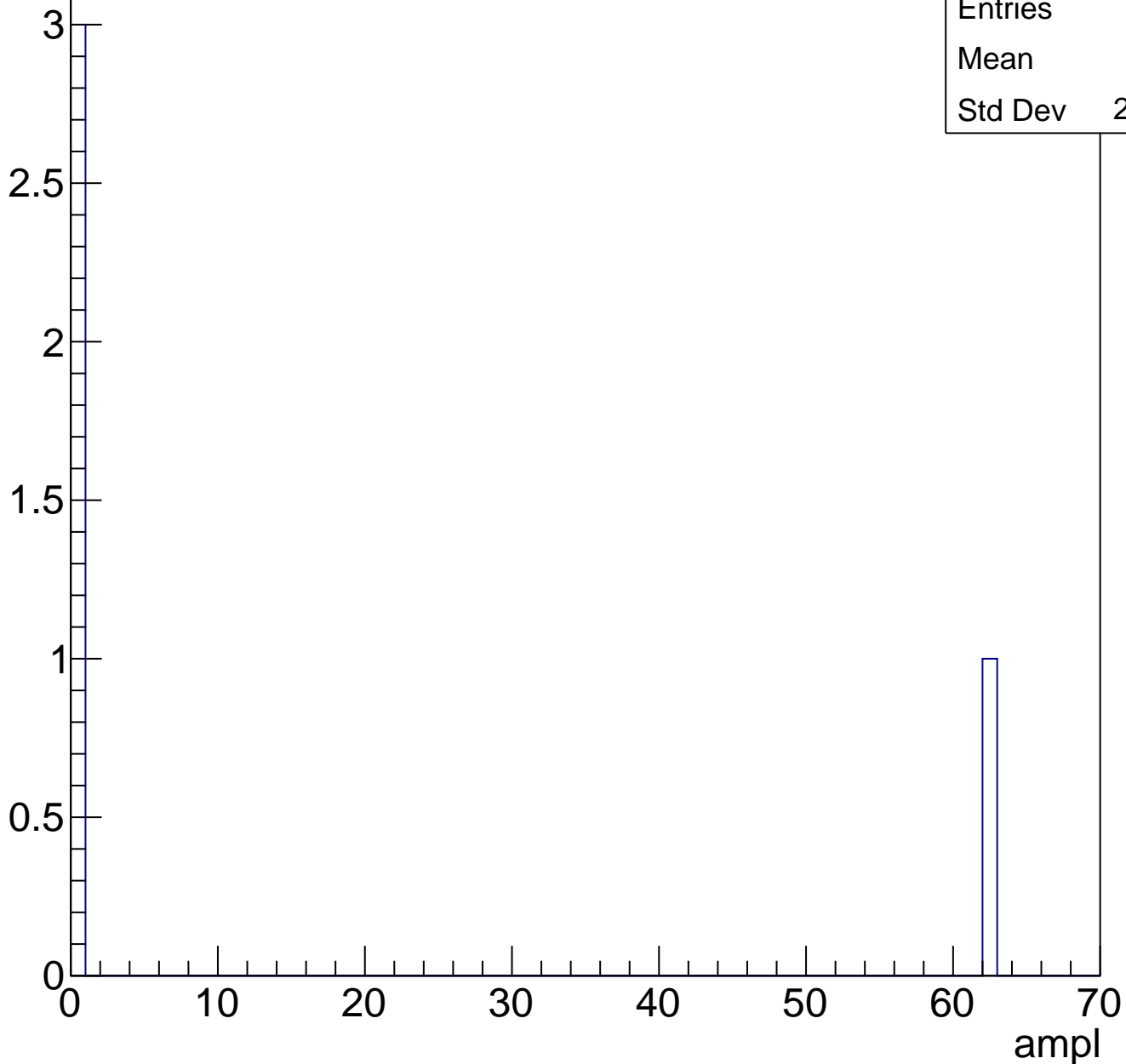
ampl

Entries	18
Mean	62.39
Std Dev	1.008

B1L001S, U16-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch29, adc0

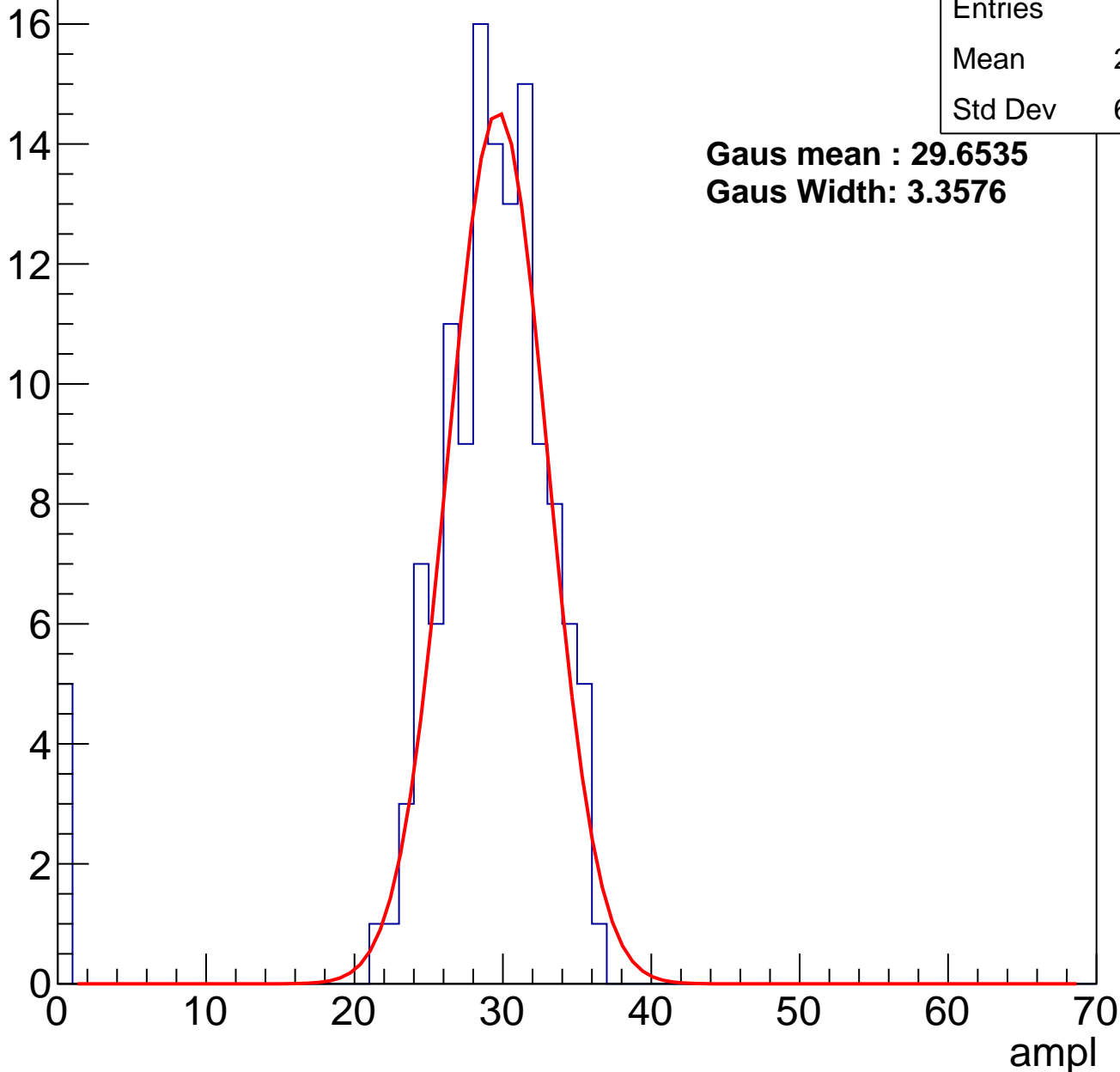
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	27.96
Std Dev	6.426

Gaus mean : 29.6535

Gaus Width: 3.3576

Entry



B1L001S, U16-ch29, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.49
Std Dev	3.269

Gaus mean : 36.0038

Gaus Width: 3.1743

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

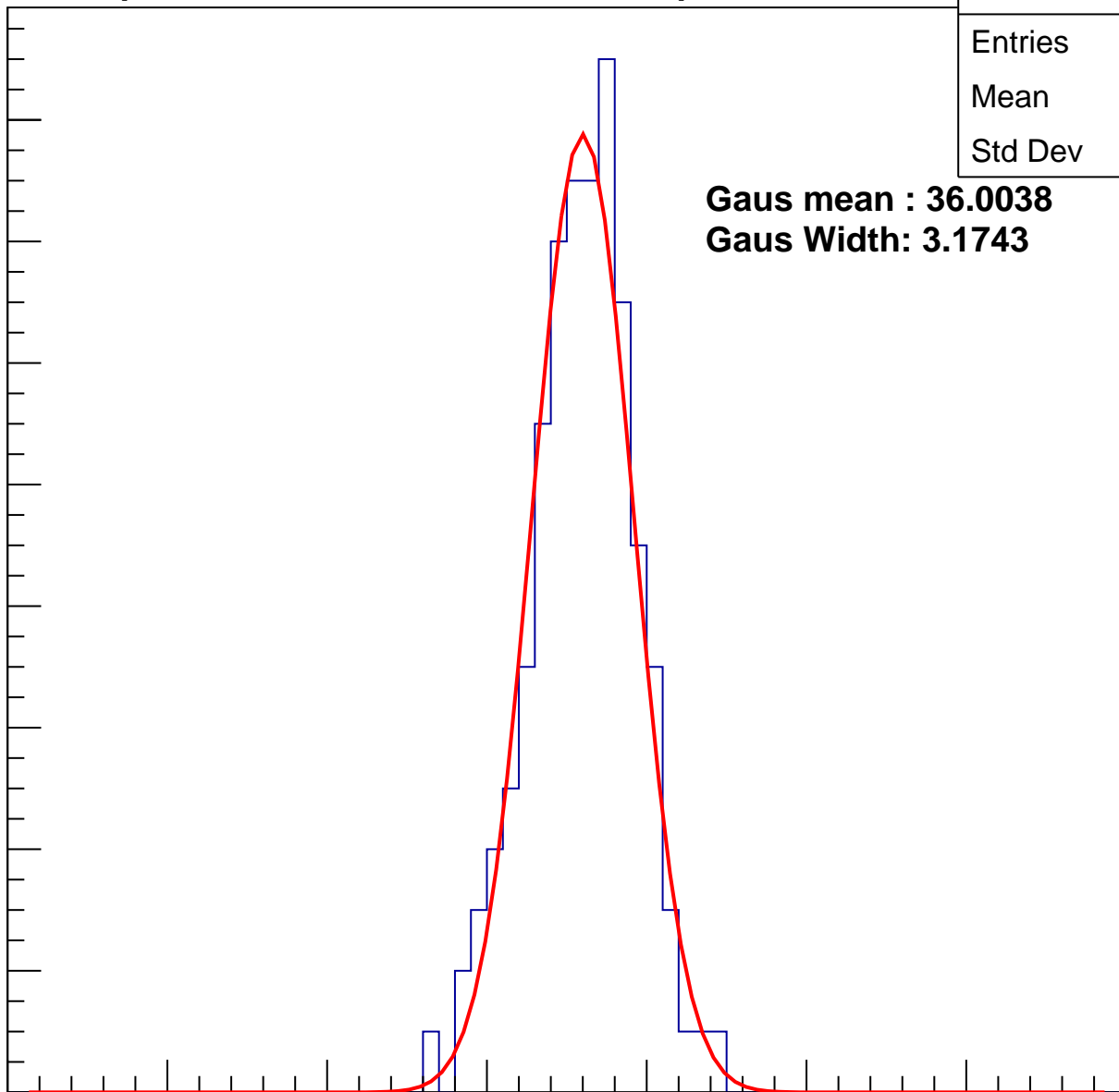
40

50

60

70

ampl



B1L001S, U16-ch29, adc2

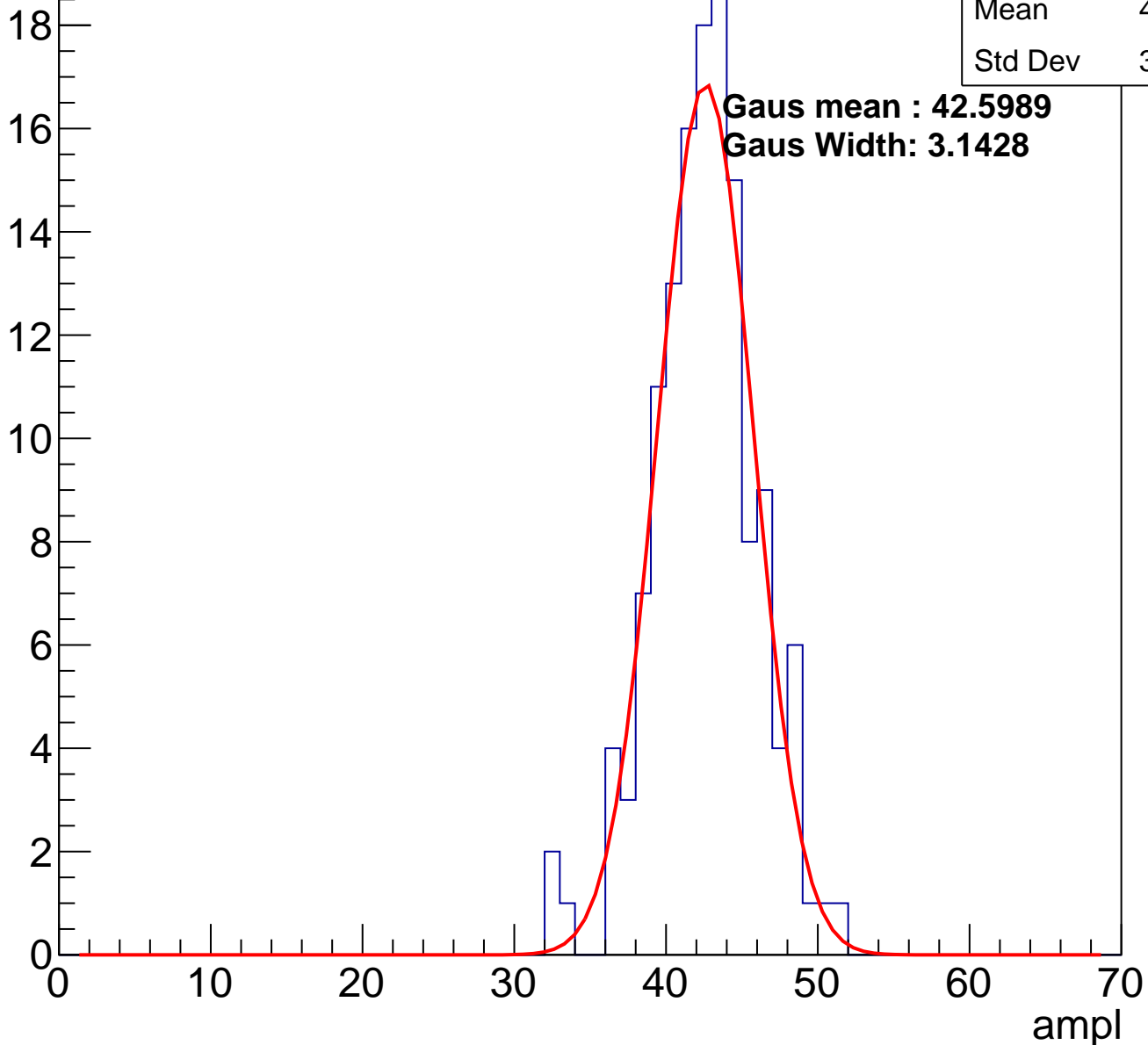
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	42.13
Std Dev	3.373

Gaus mean : 42.5989

Gaus Width: 3.1428

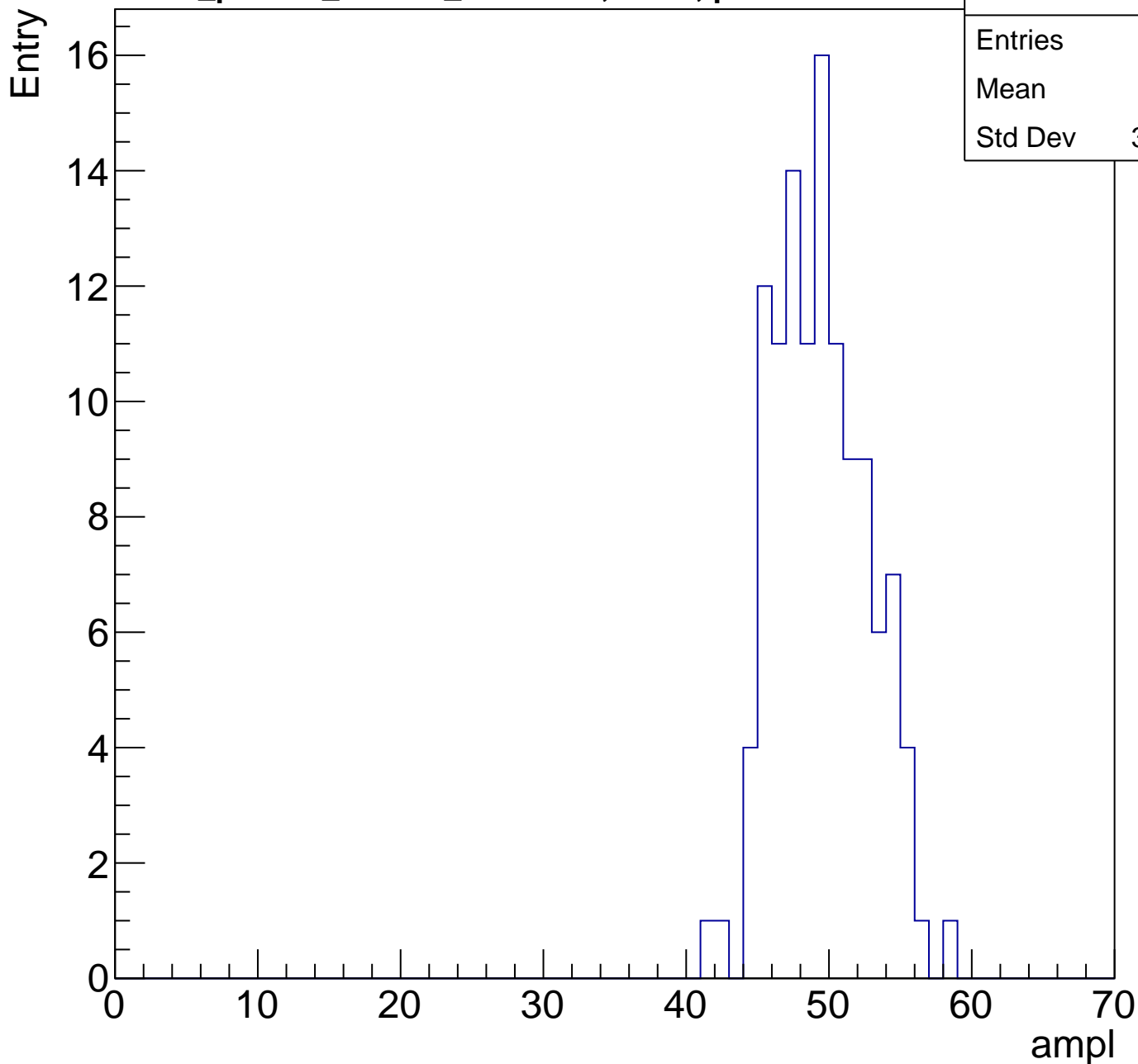
Entry



B1L001S, U16-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

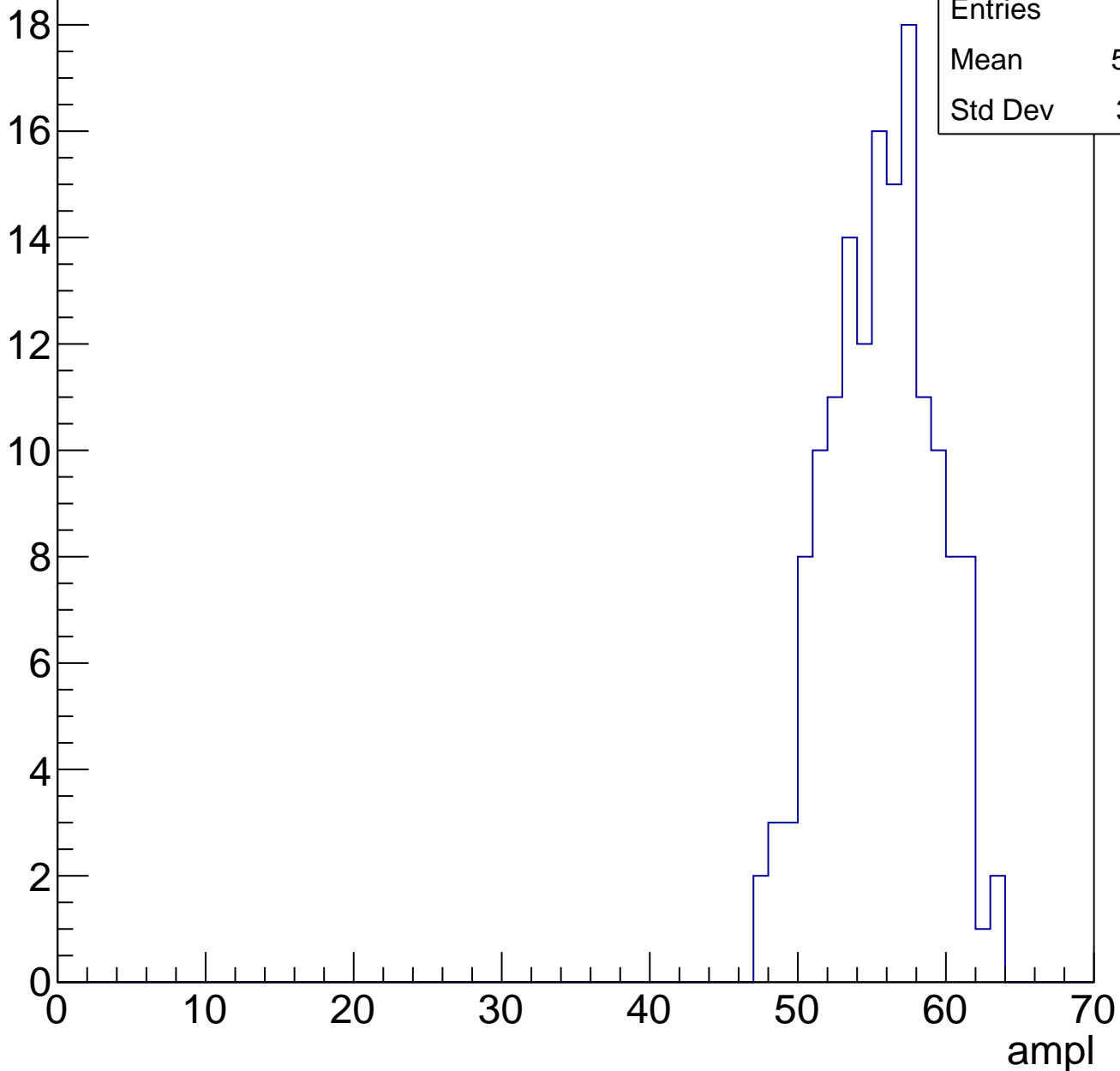
Entries	118
Mean	49
Std Dev	3.247



B1L001S, U16-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch29, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

69

Mean

59.23

Std Dev

7.47

ampl

0

10

20

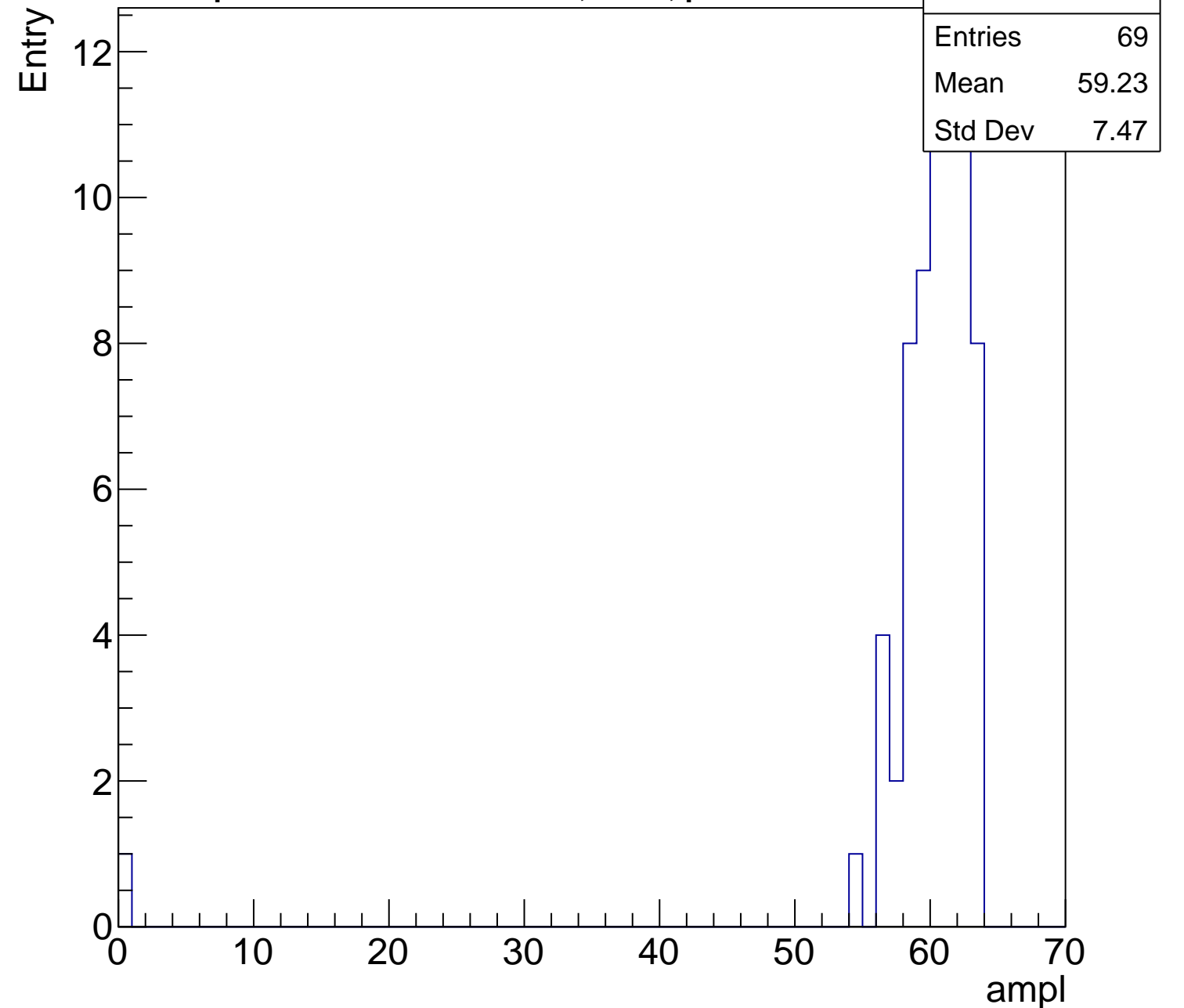
30

40

50

60

70

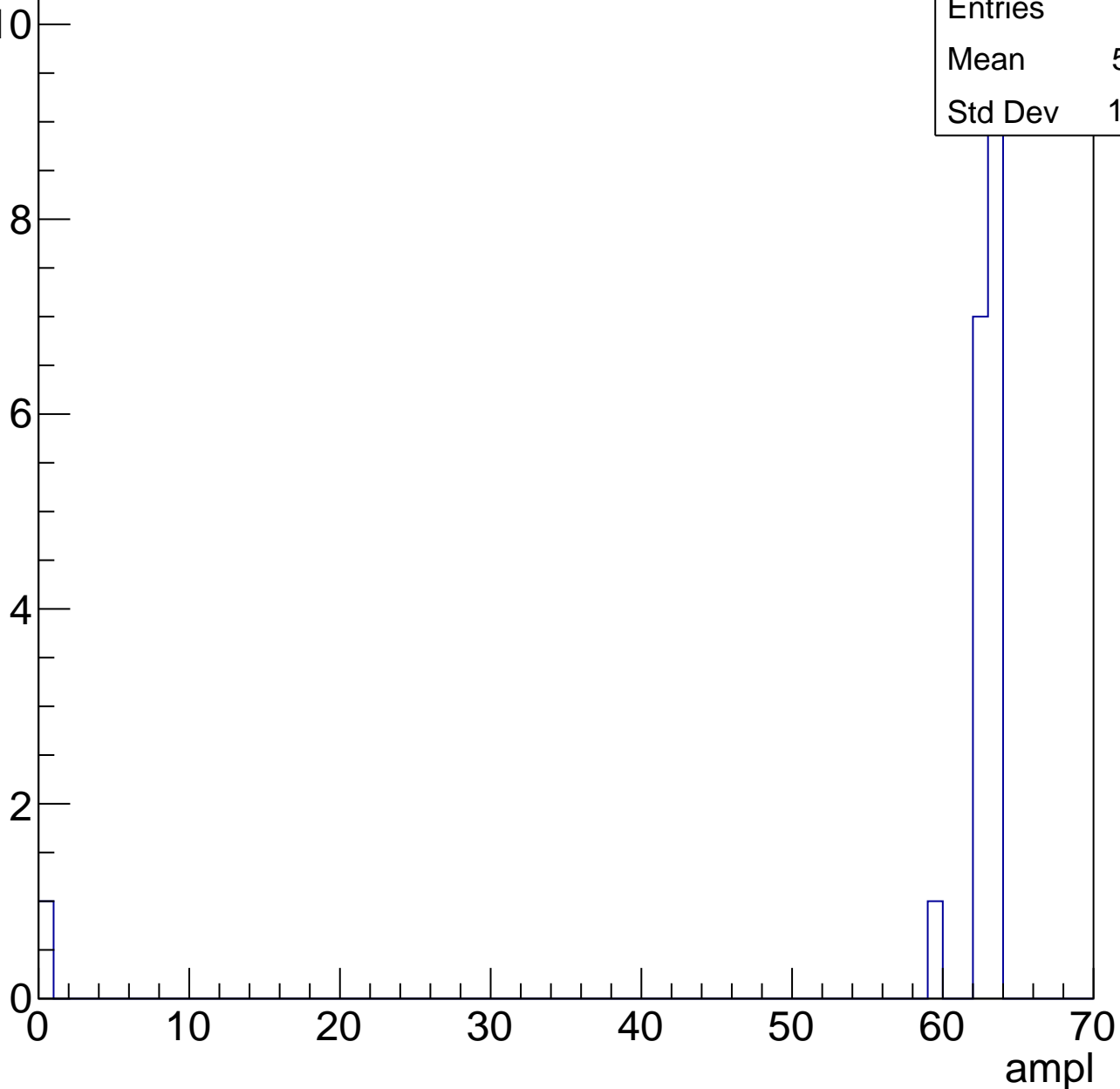


B1L001S, U16-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	59.11
Std Dev	13.96



B1L001S, U16-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch30, adc0

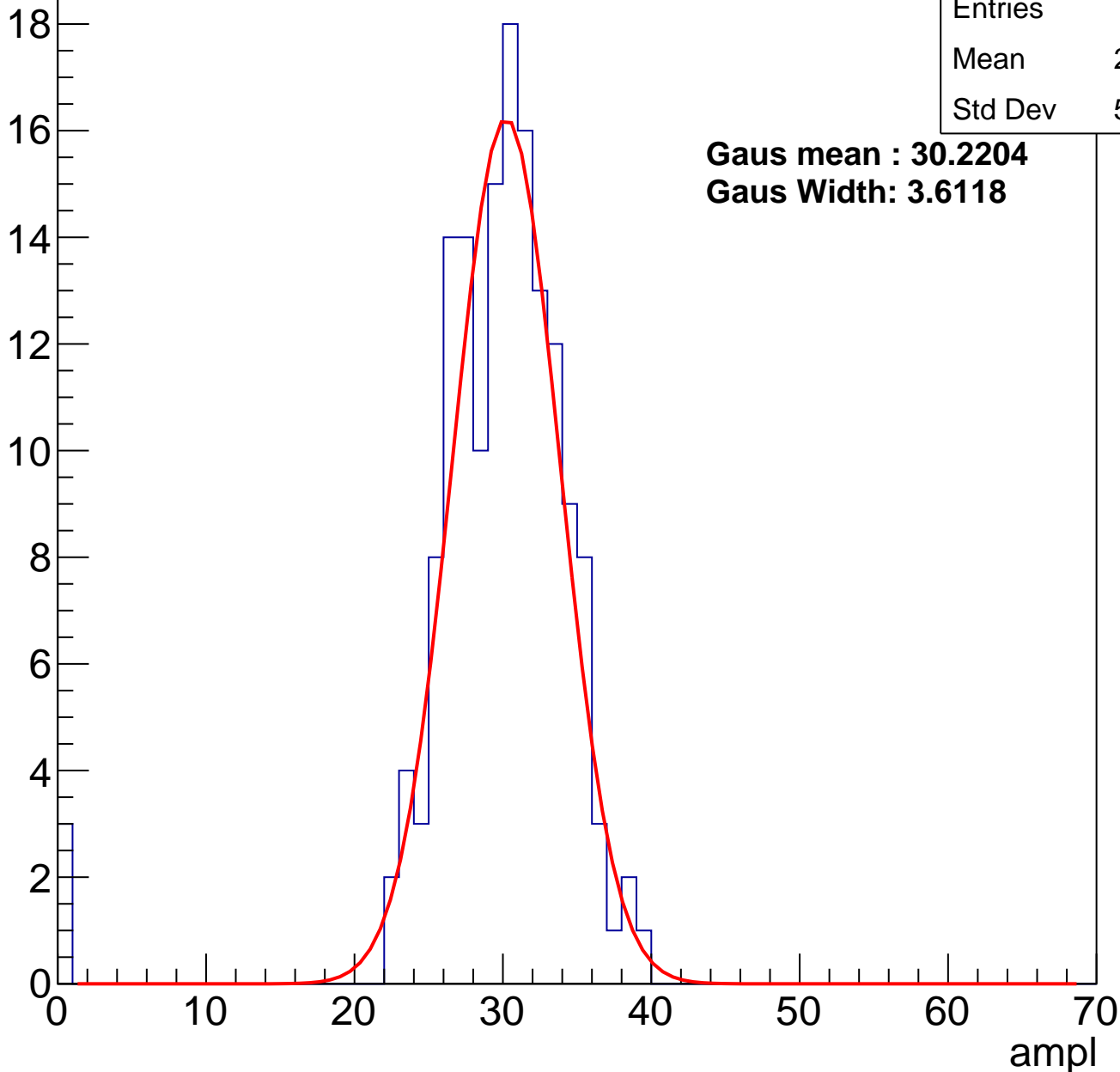
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	29.22
Std Dev	5.388

Gaus mean : 30.2204

Gaus Width: 3.6118

Entry



B1L001S, U16-ch30, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.83
Std Dev	3.222

Gaus mean : 37.1650

Gaus Width: 3.1794

Entry

16

14

12

10

8

6

4

2

0

0

10

20

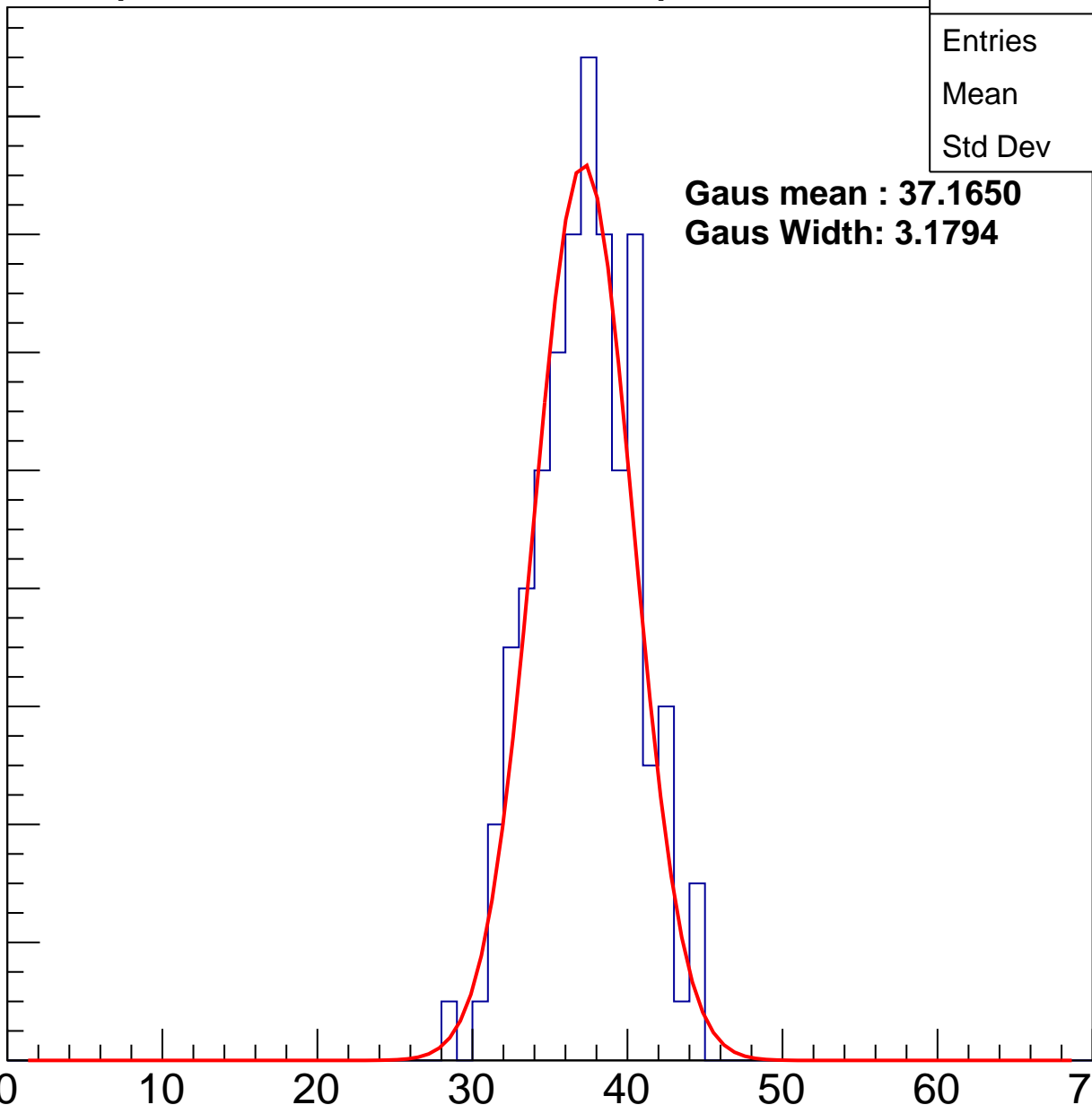
30

40

50

60

ampl



B1L001S, U16-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

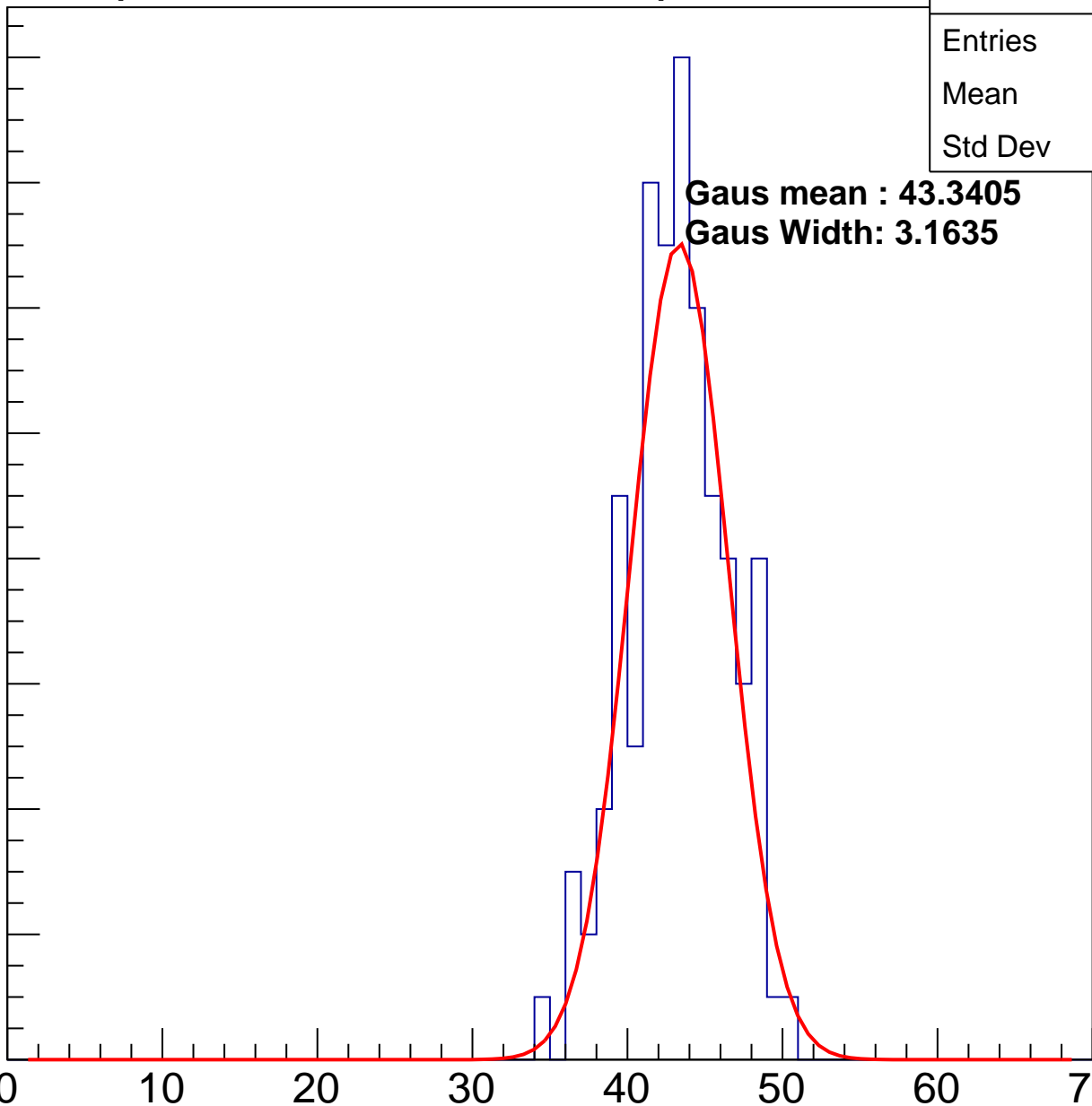
16
14
12
10
8
6
4
2
0

Entries	112
Mean	42.79
Std Dev	3.21

Gaus mean : 43.3405

Gaus Width: 3.1635

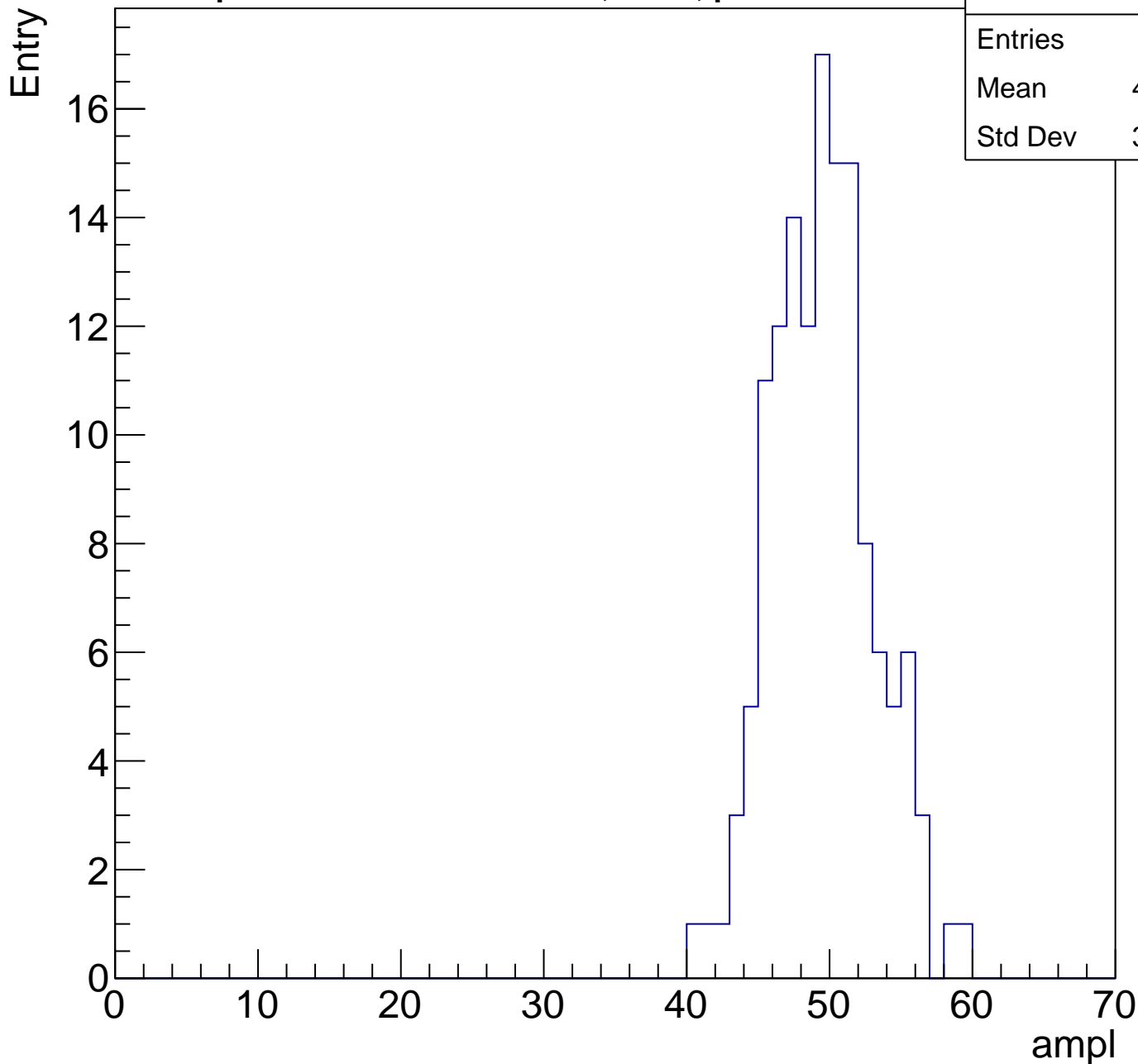
0 10 20 30 40 50 60 70
ampl



B1L001S, U16-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	49.05
Std Dev	3.513



B1L001S, U16-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

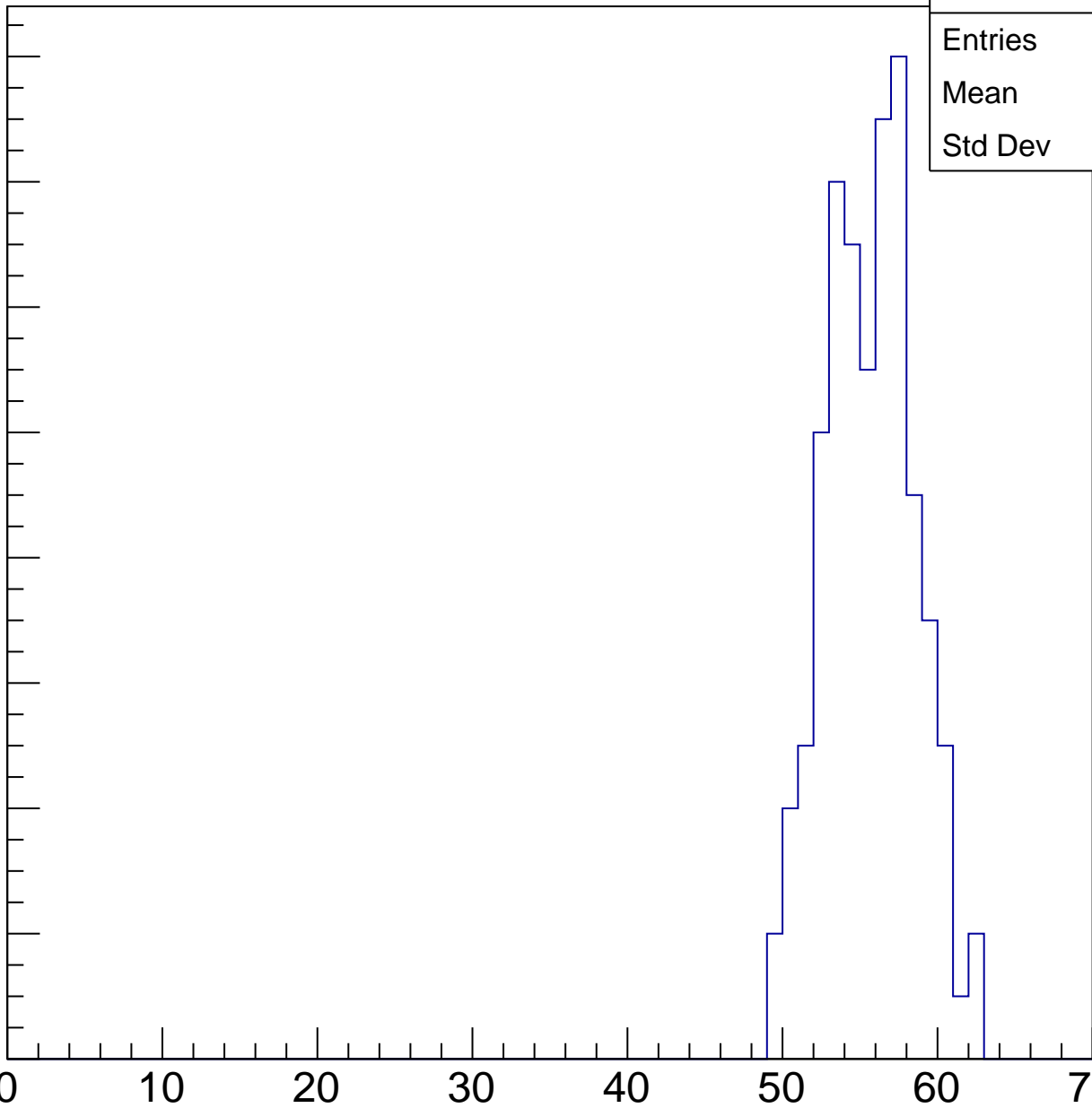
Entries	114
Mean	55.21
Std Dev	2.867

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch30, adc5

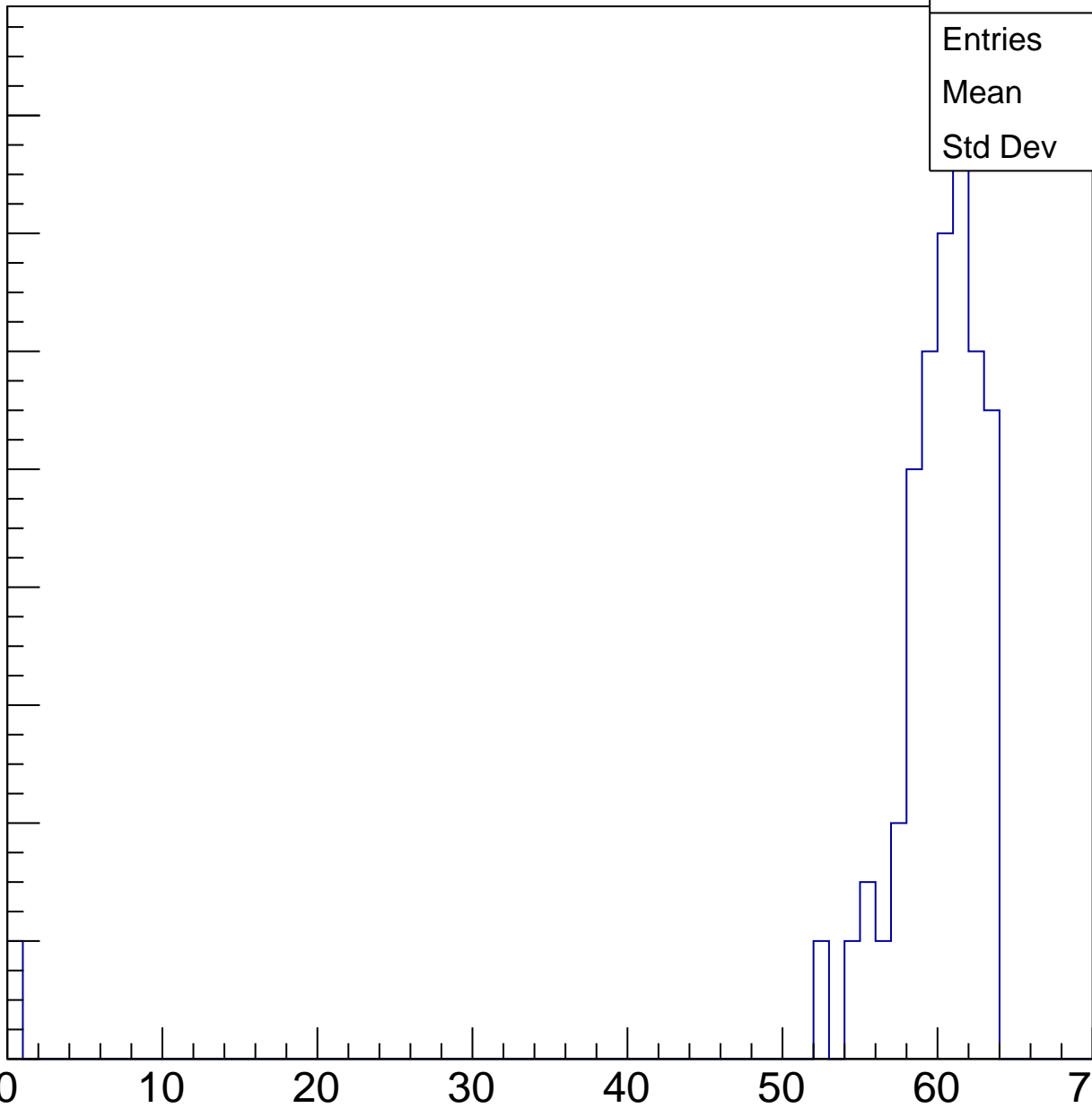
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.45
Std Dev	9.103

ampl

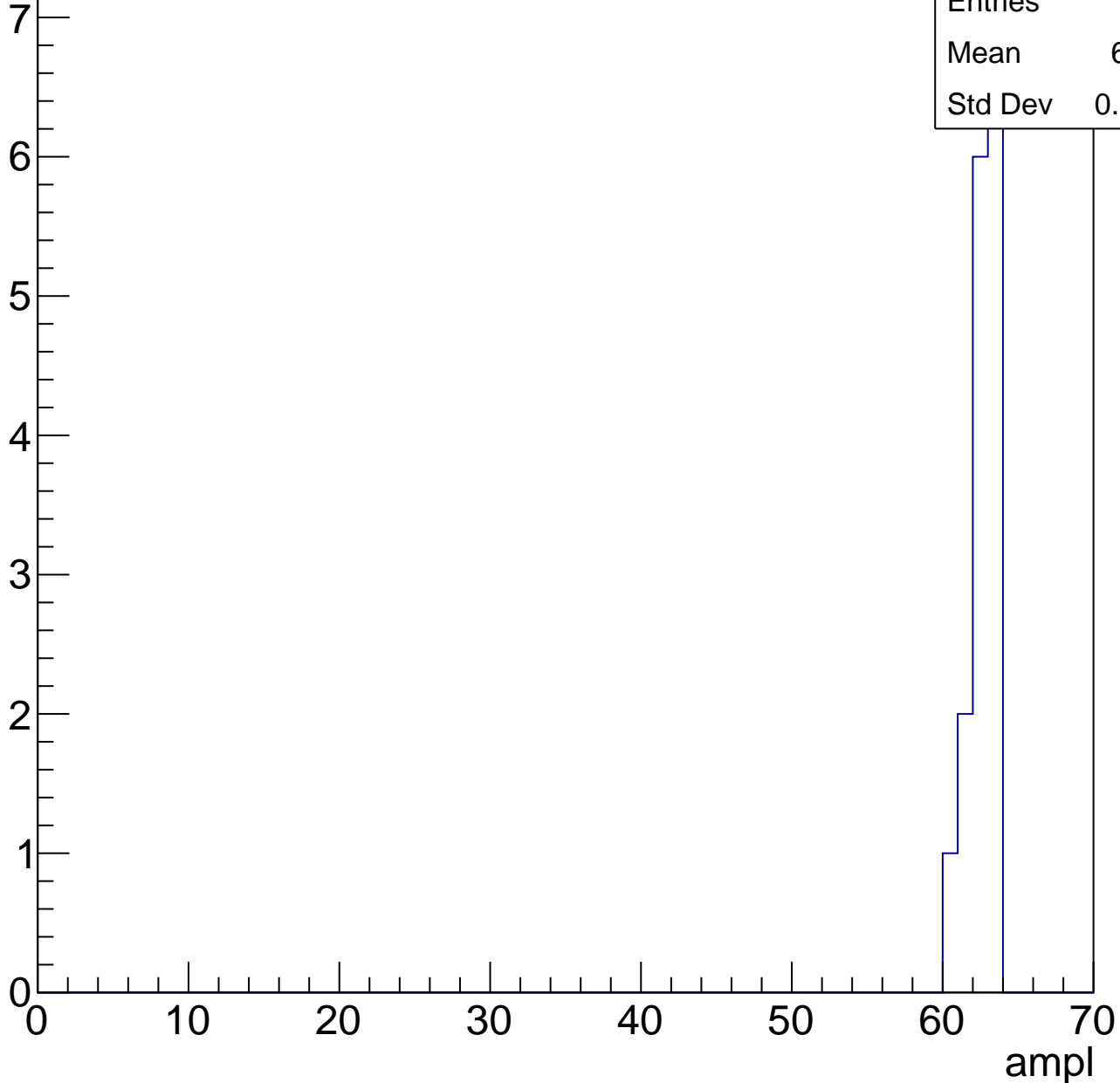


B1L001S, U16-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	62.19
Std Dev	0.8817



B1L001S, U16-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch31, adc0

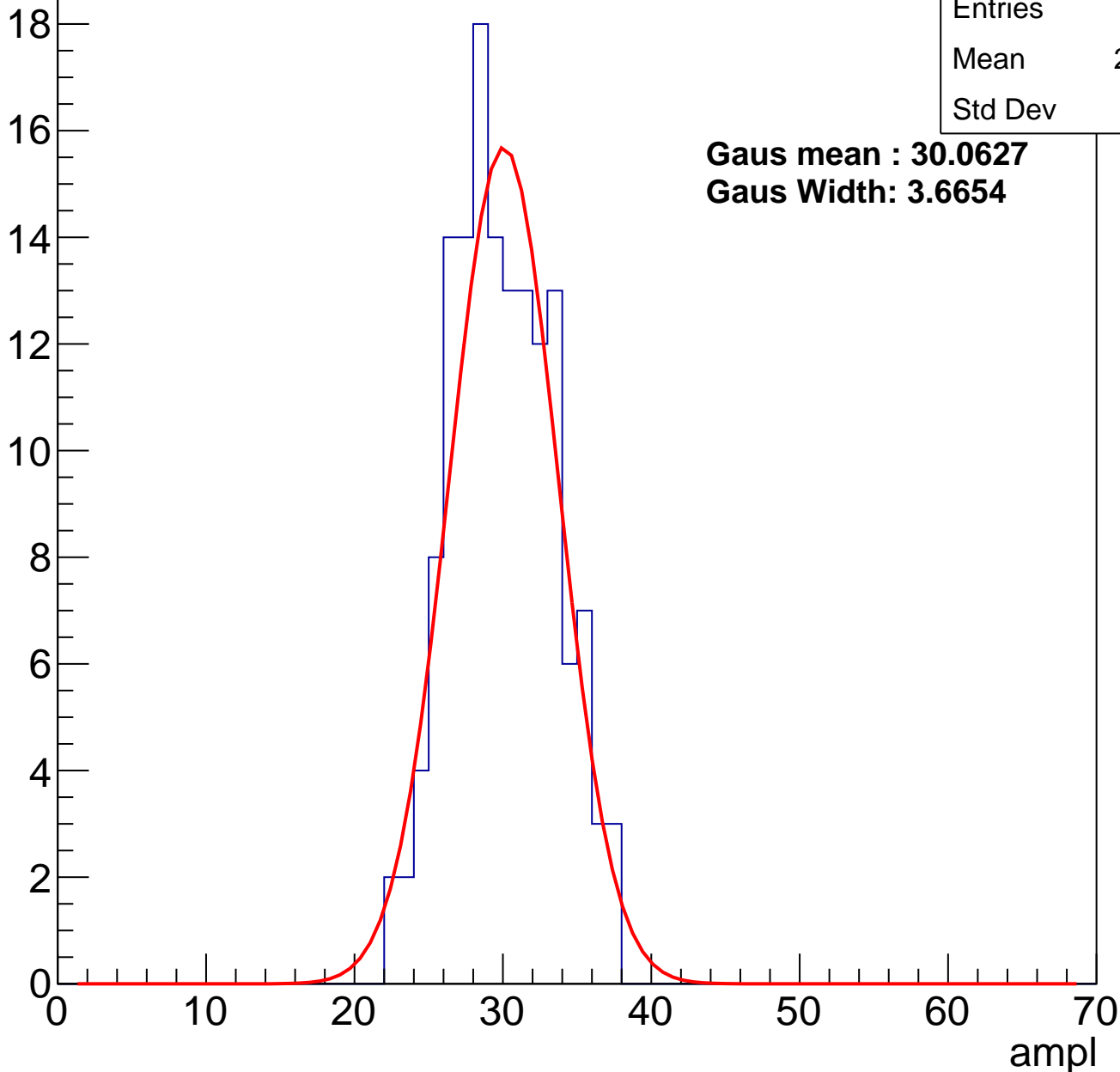
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.53
Std Dev	3.39

Gaus mean : 30.0627

Gaus Width: 3.6654

Entry



B1L001S, U16-ch31, adc1

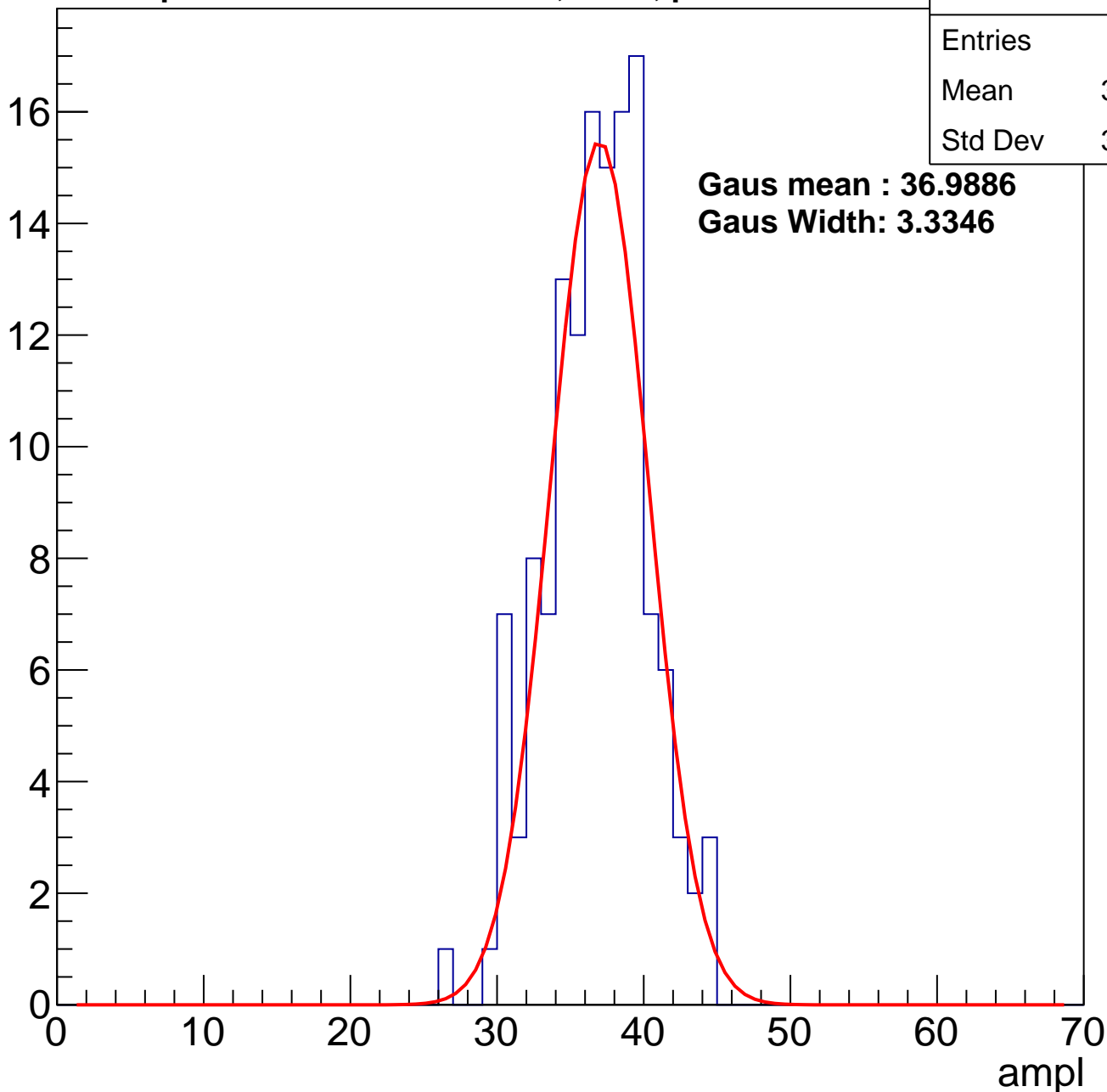
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	36.34
Std Dev	3.425

Gaus mean : 36.9886

Gaus Width: 3.3346

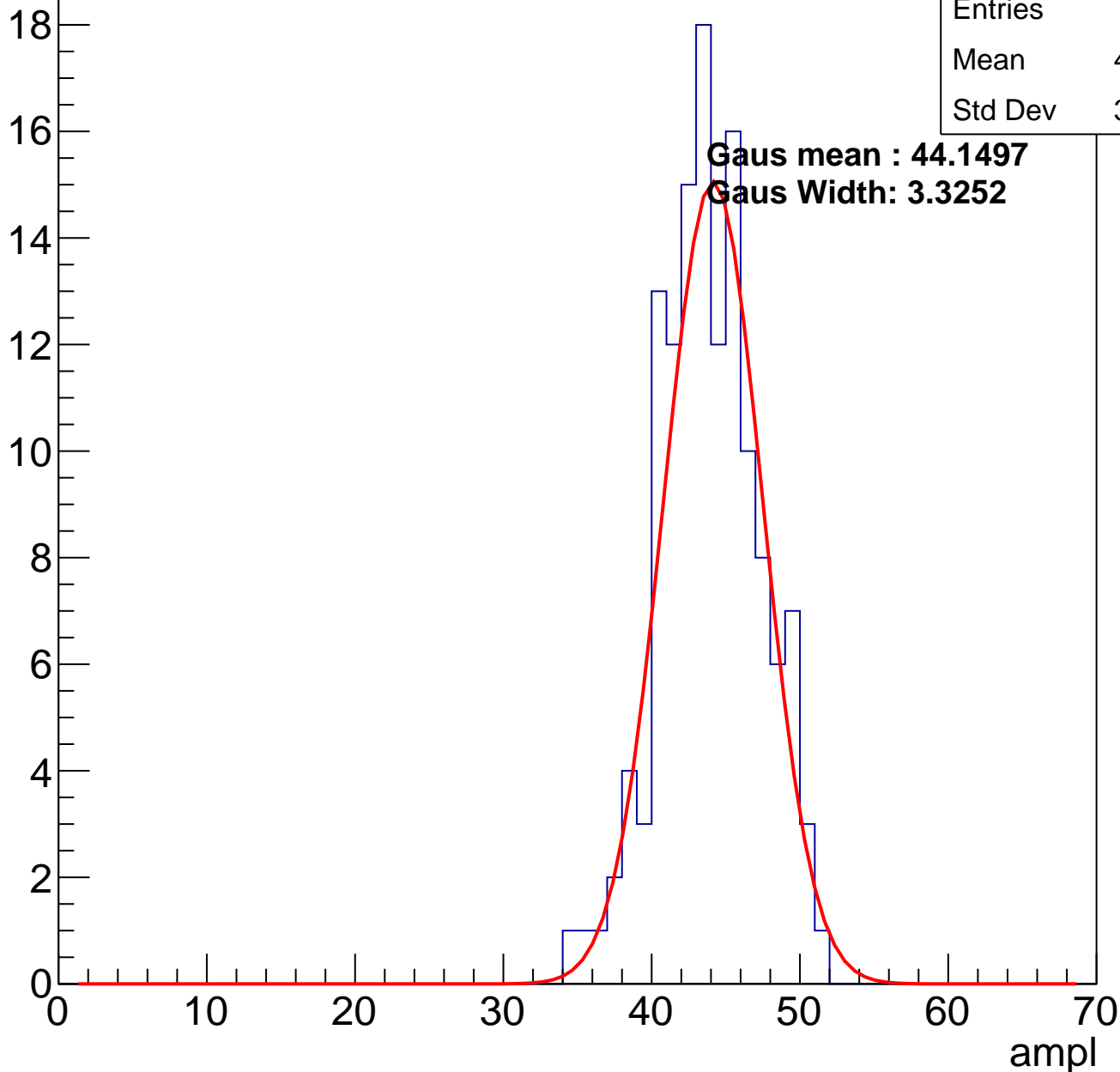
Entry



B1L001S, U16-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch31, adc3

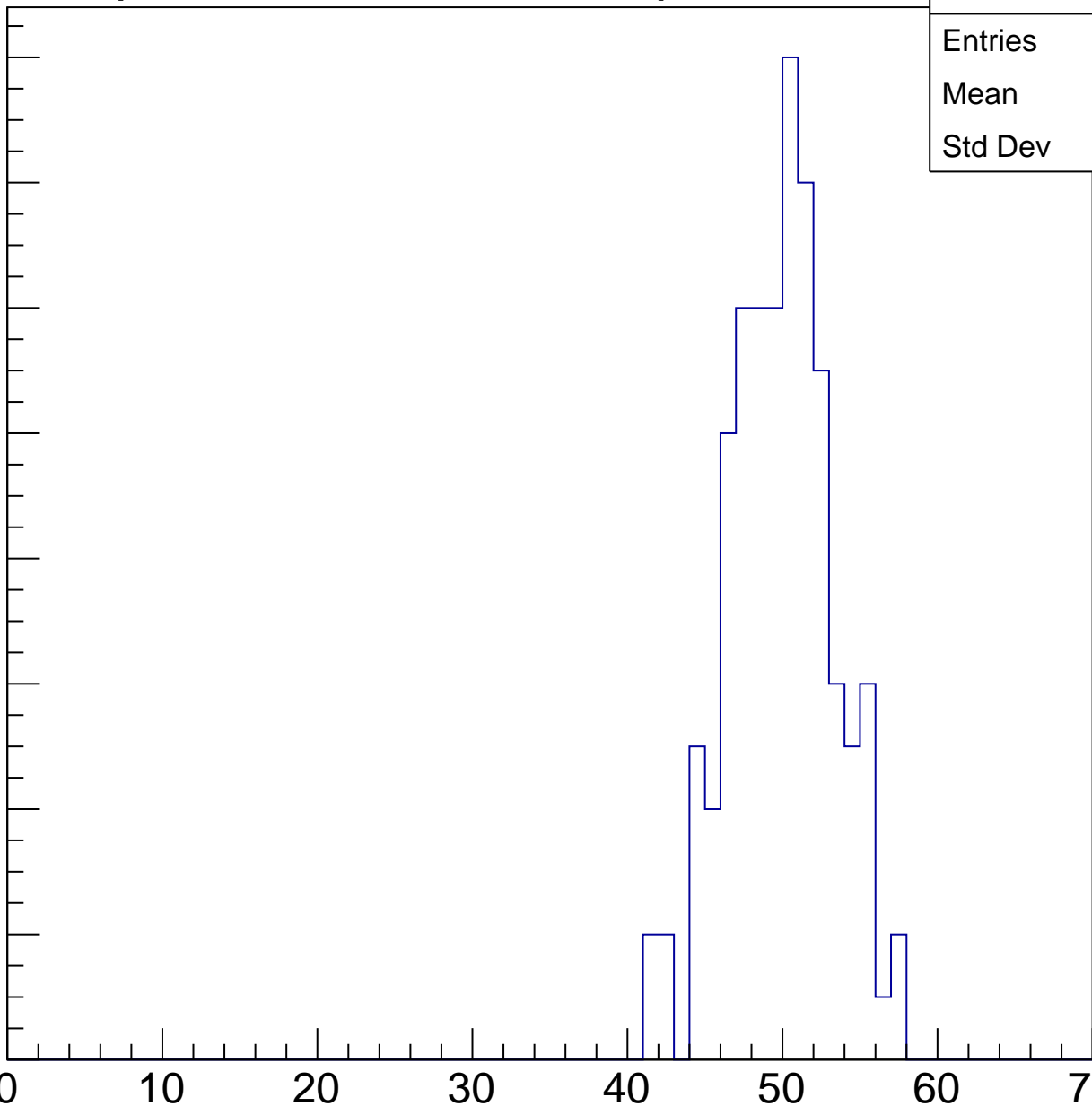
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	49.4
Std Dev	3.33

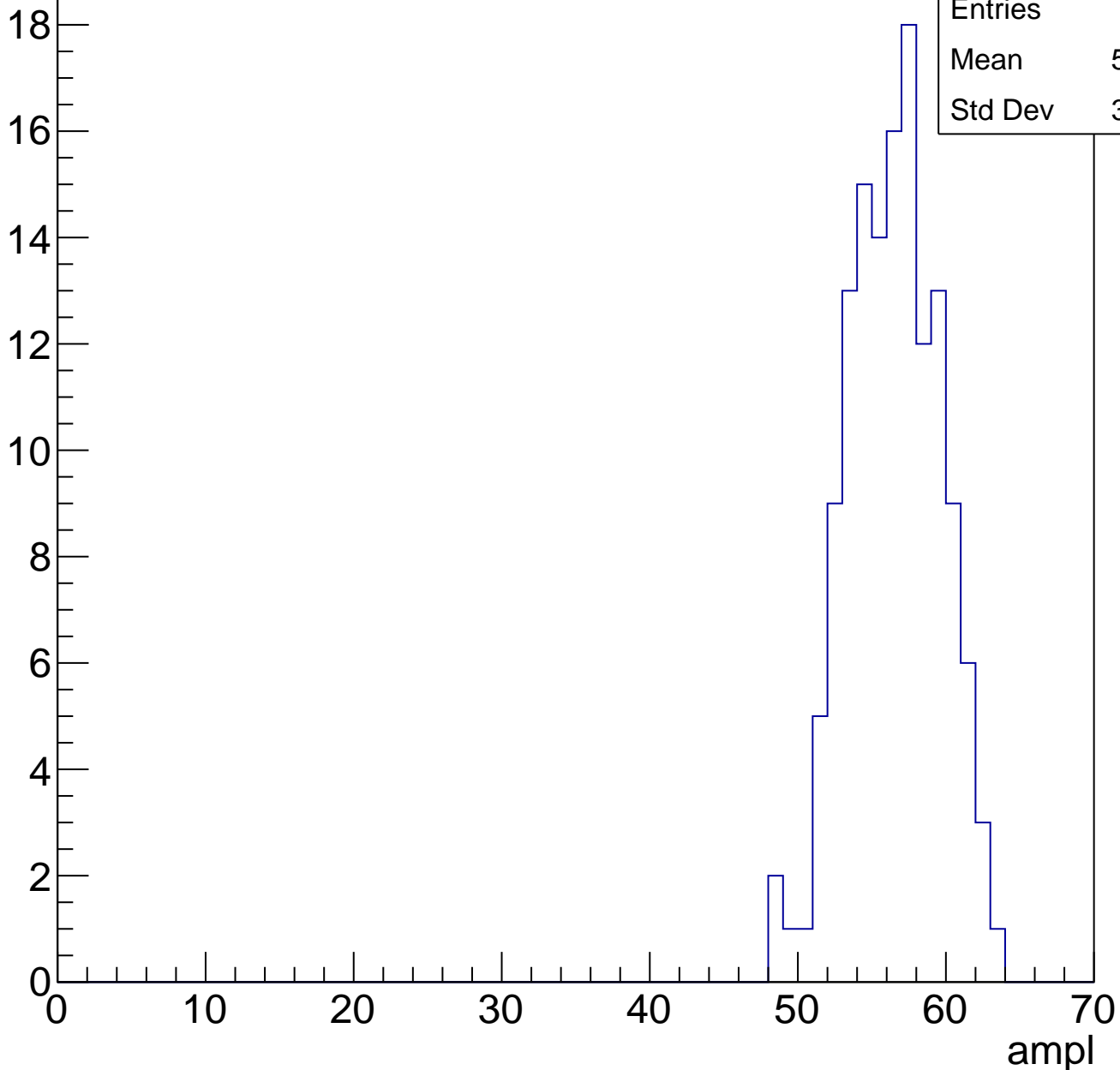
ampl



B1L001S, U16-ch31, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch31, adc5

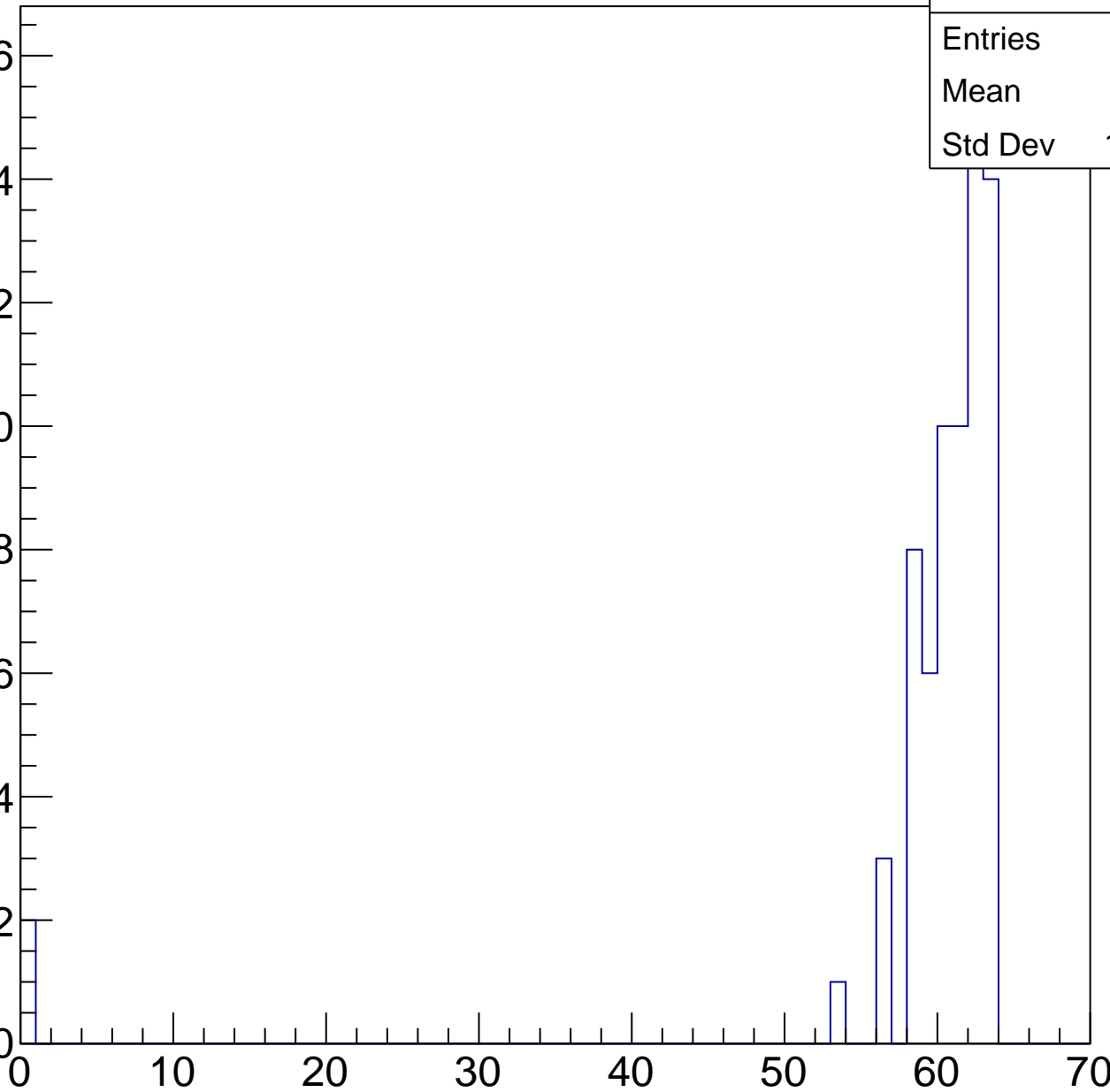
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	70
Mean	58.9
Std Dev	10.32

ampl

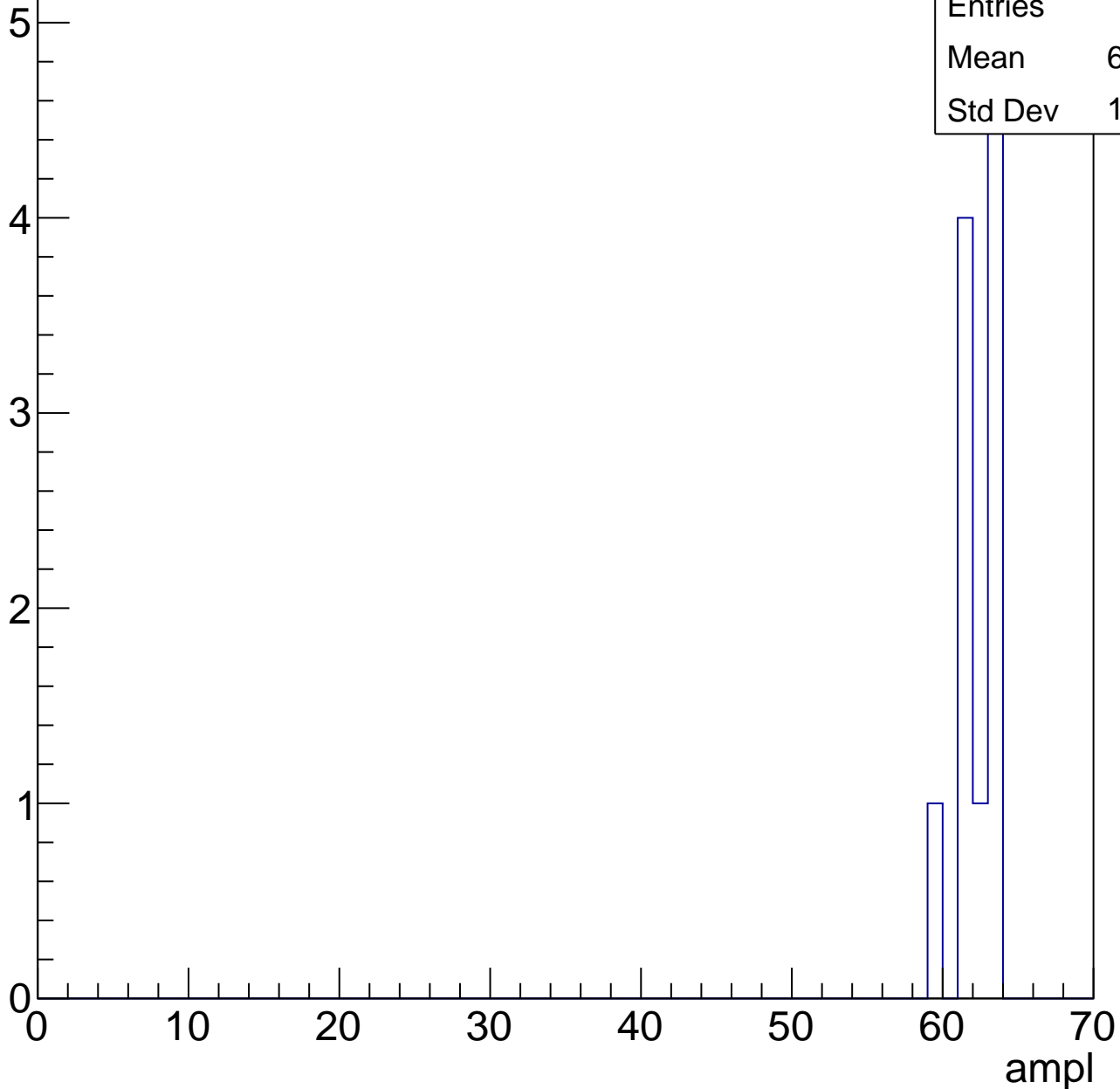


B1L001S, U16-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

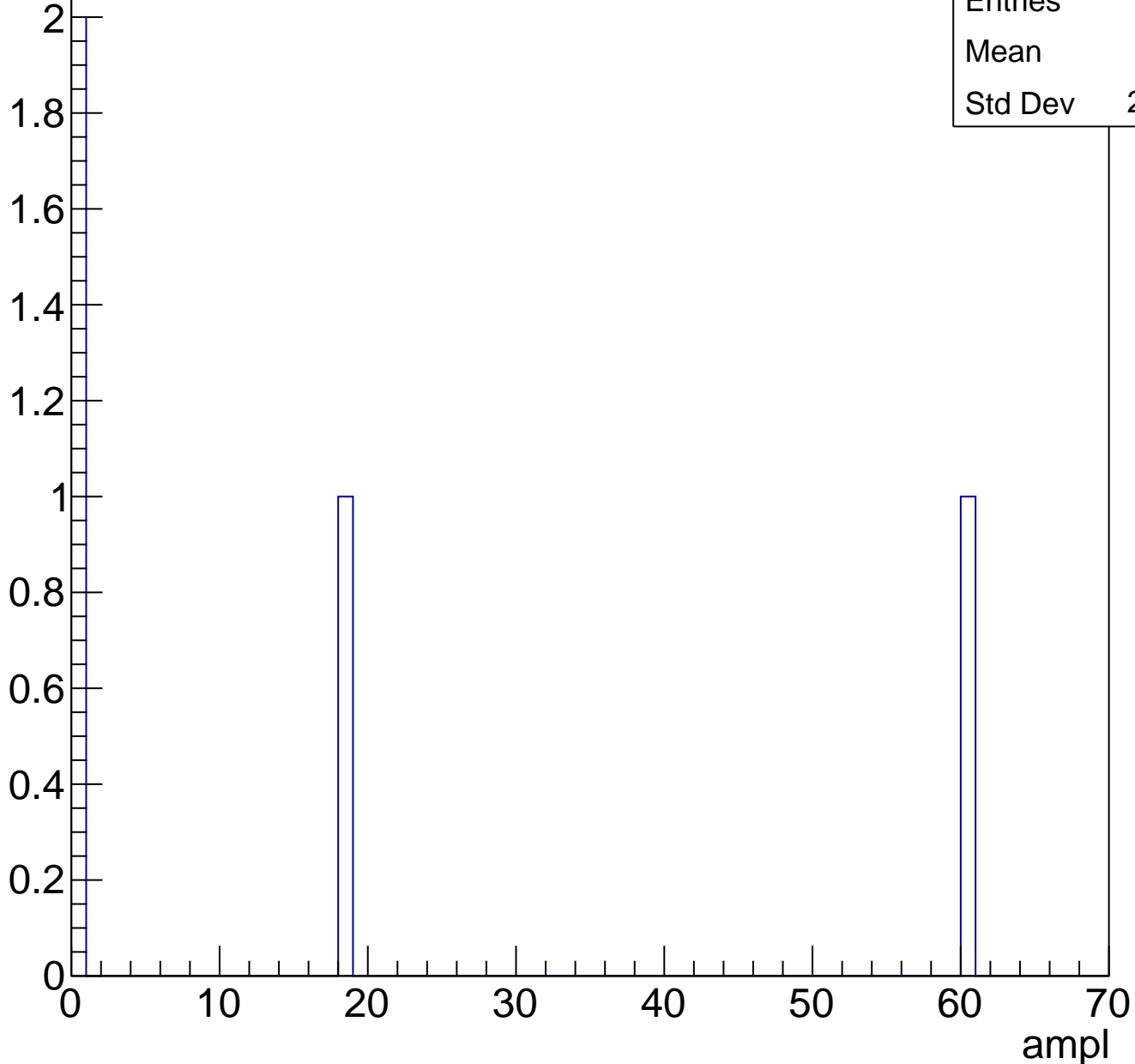
Entries	11
Mean	61.82
Std Dev	1.266



B1L001S, U16-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	19.5
Std Dev	24.51

B1L001S, U16-ch32, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

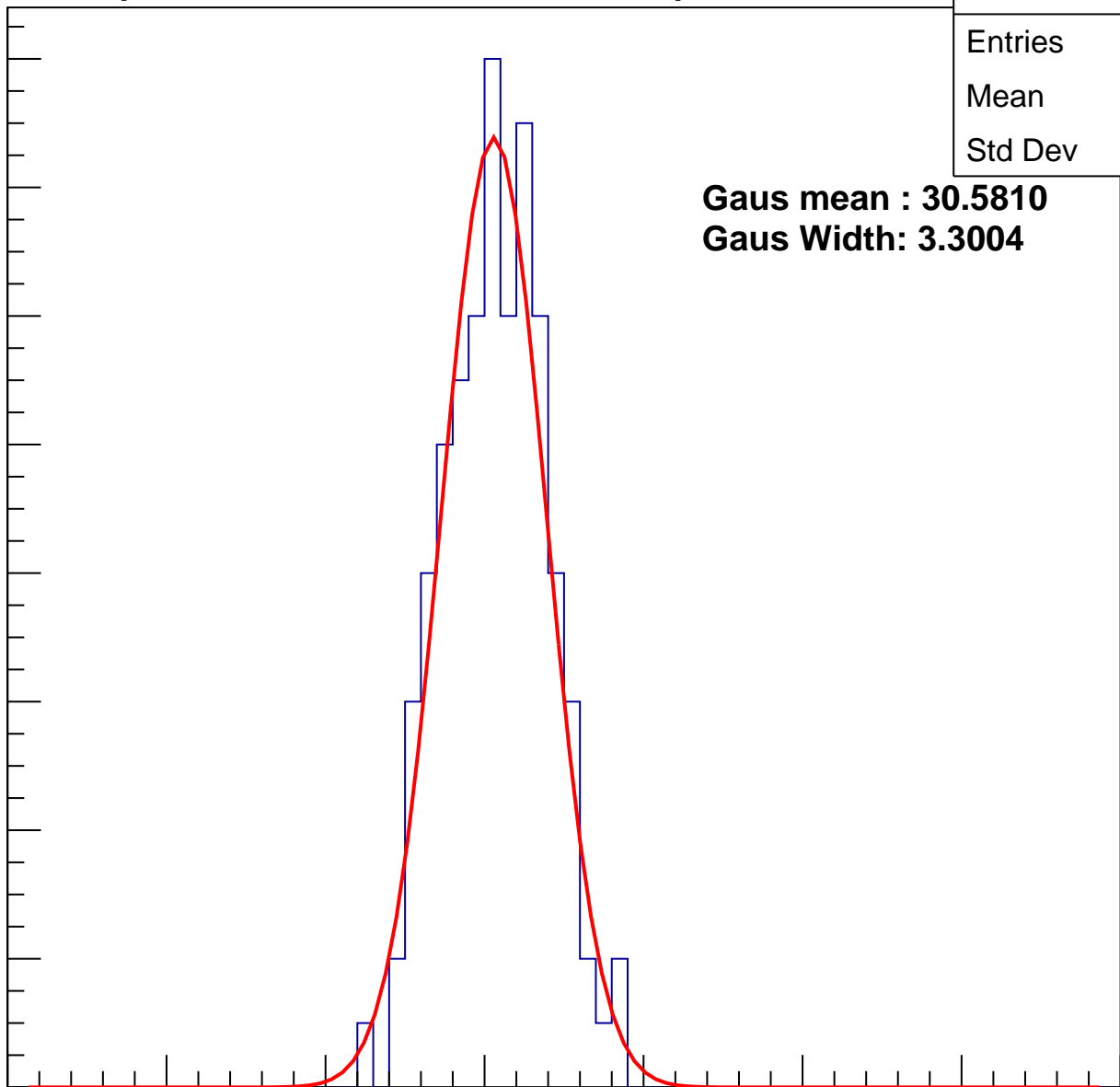
Entries	124
Mean	30.23
Std Dev	3.188

Gaus mean : 30.5810

Gaus Width: 3.3004

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch32, adc1

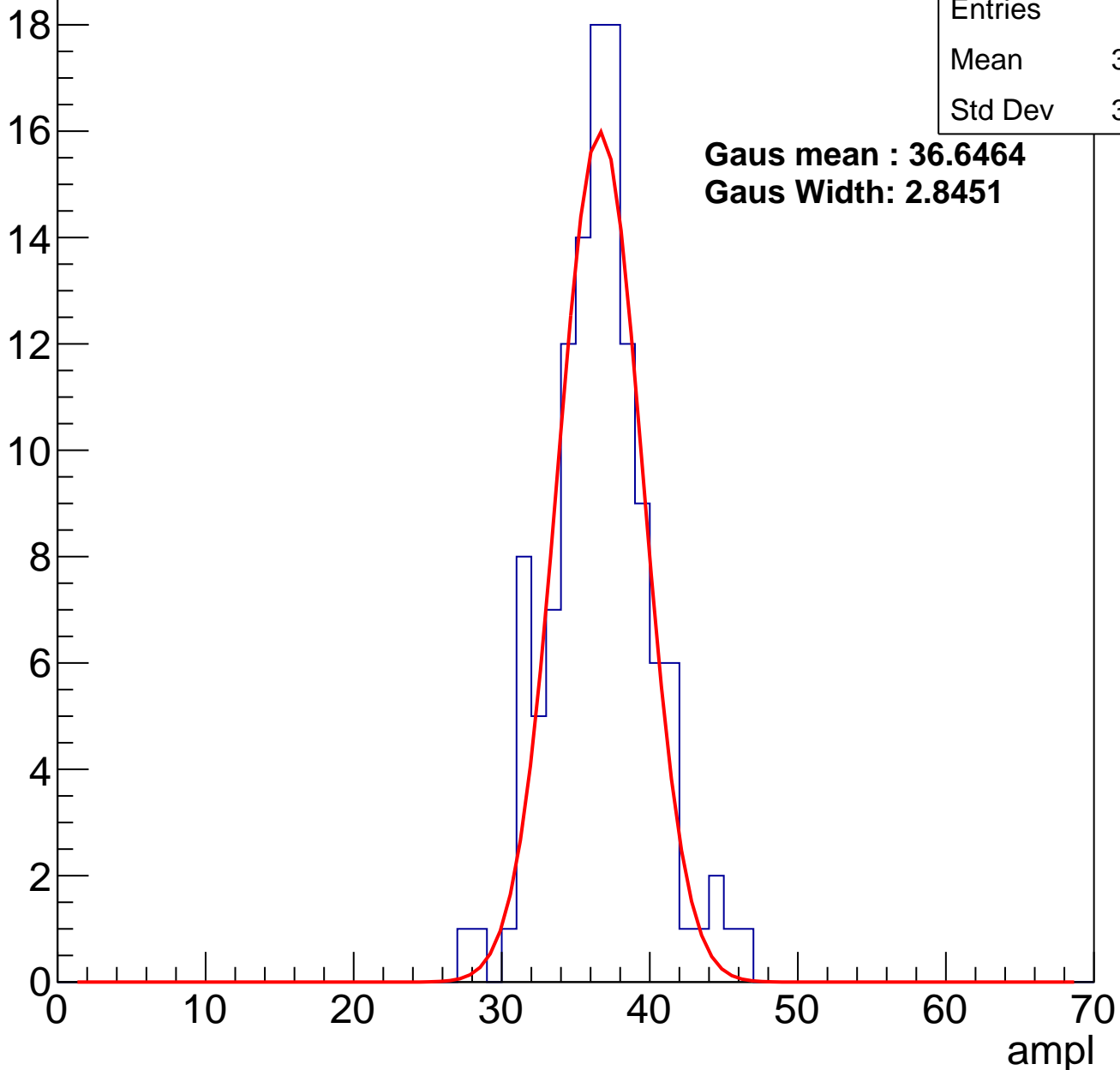
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.23
Std Dev	3.319

Gaus mean : 36.6464

Gaus Width: 2.8451

Entry



B1L001S, U16-ch32, adc2

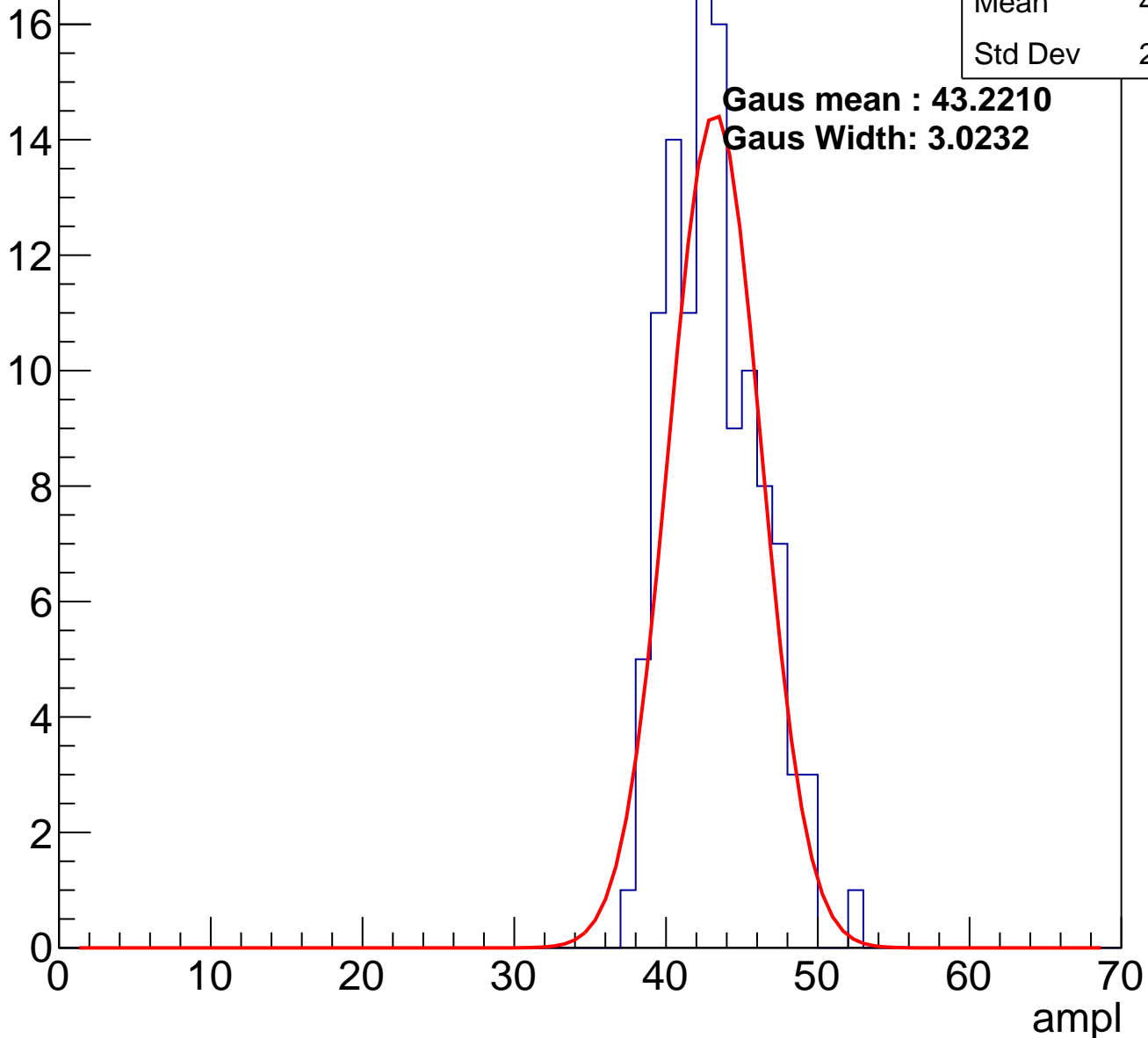
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	116
Mean	42.72
Std Dev	2.953

Gaus mean : 43.2210

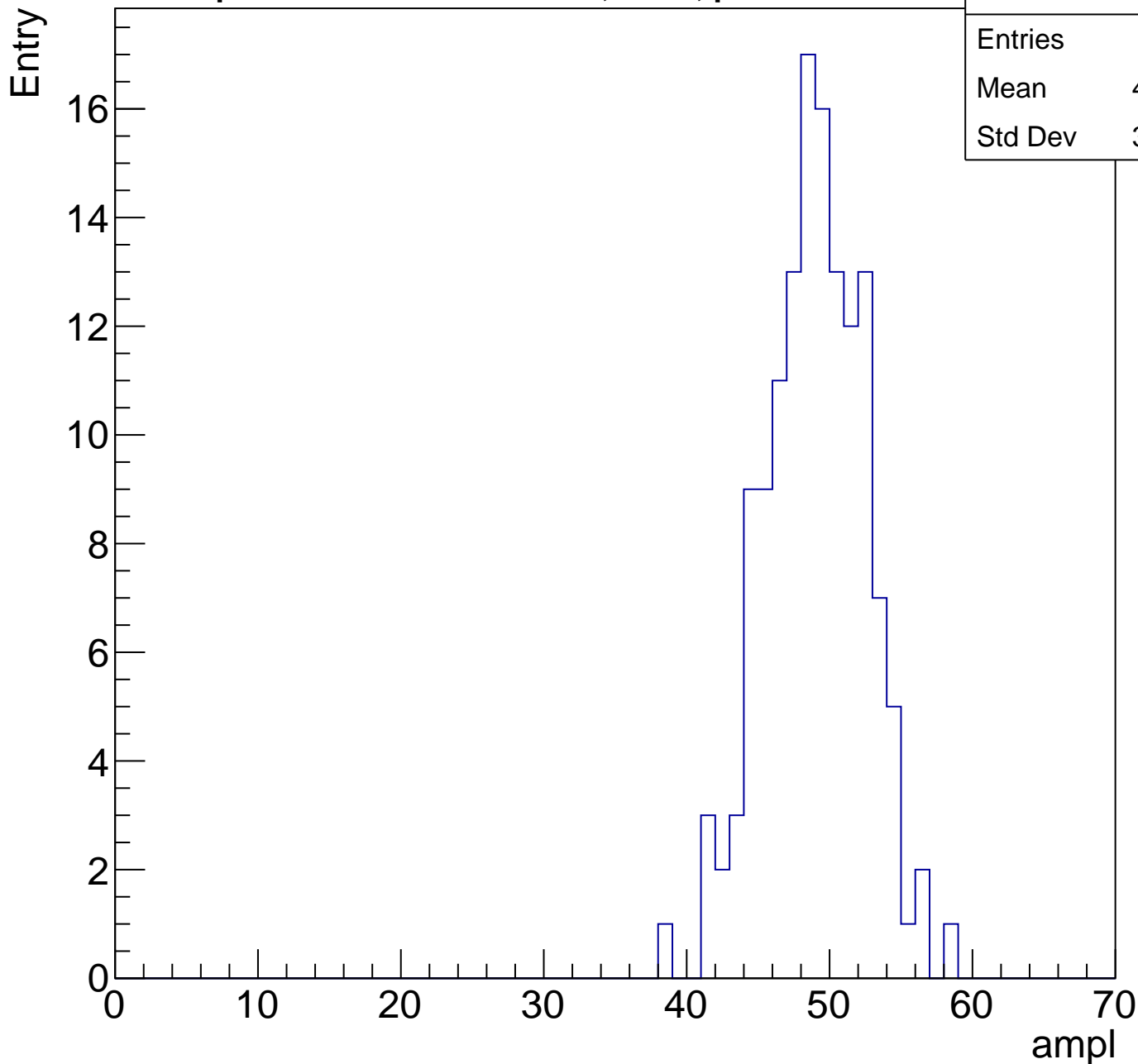
Gaus Width: 3.0232



B1L001S, U16-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	48.52
Std Dev	3.462



B1L001S, U16-ch32, adc4

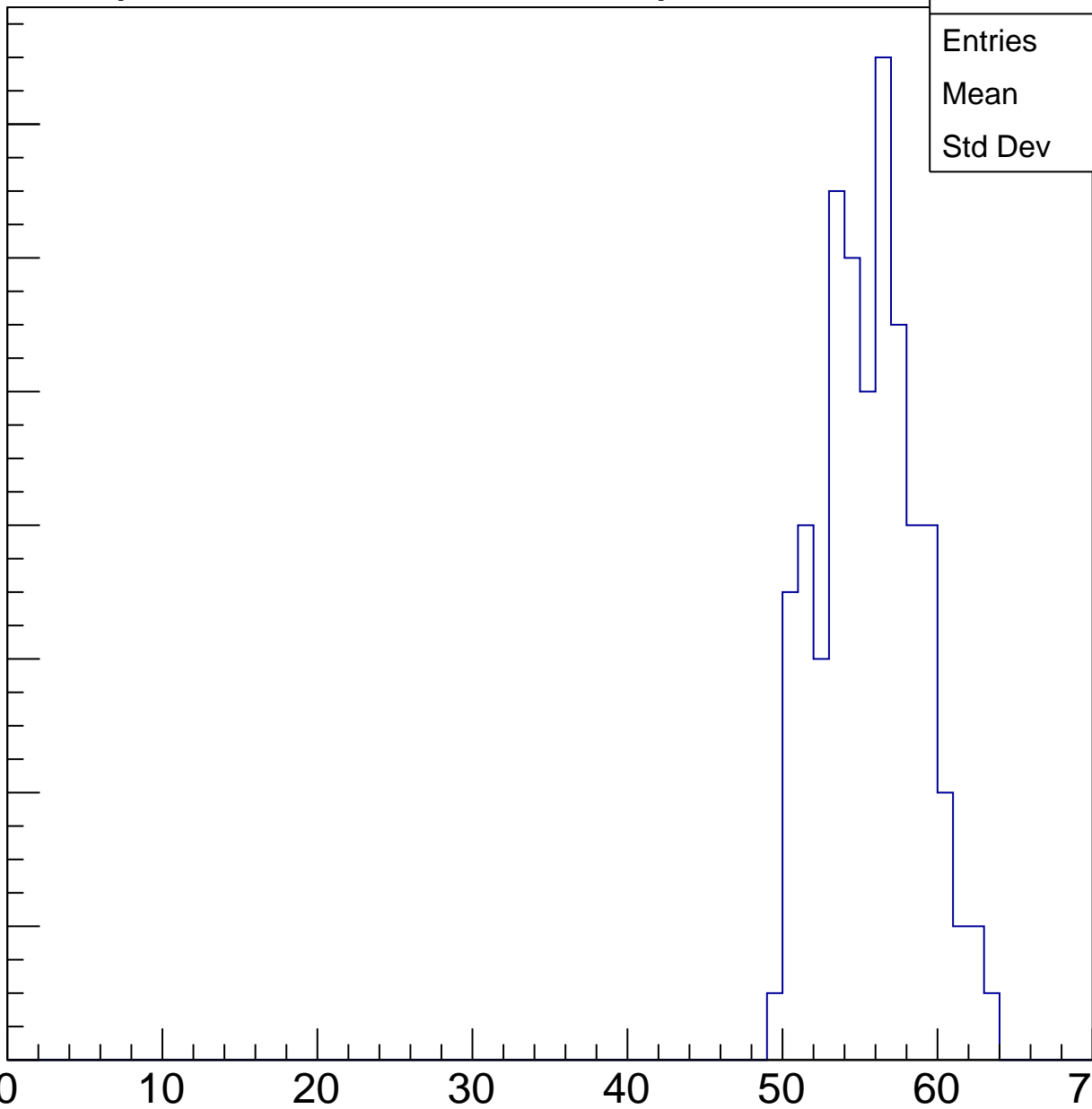
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	55.17
Std Dev	3.114

ampl



B1L001S, U16-ch32, adc5

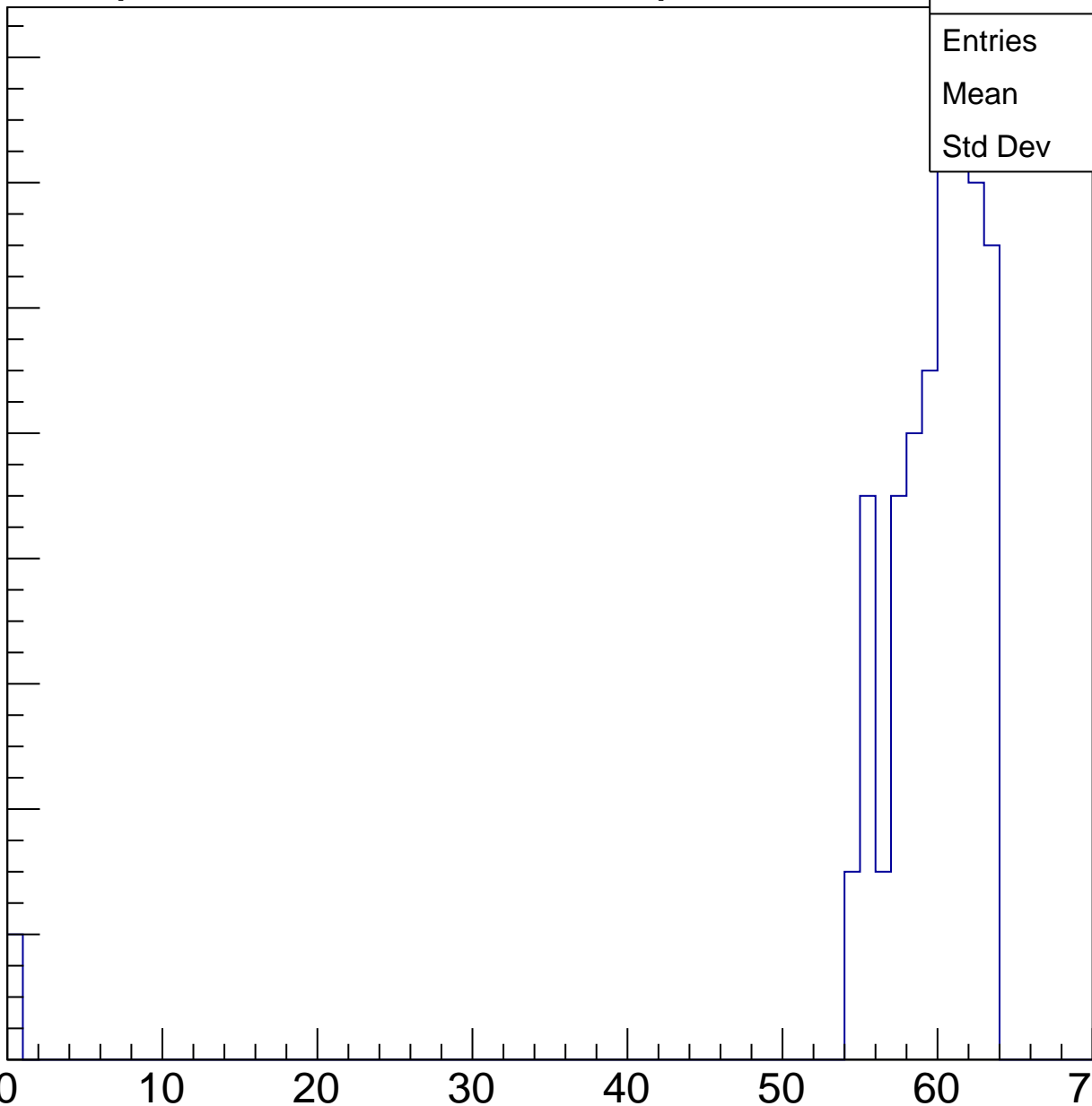
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	58.4
Std Dev	8.484

ampl

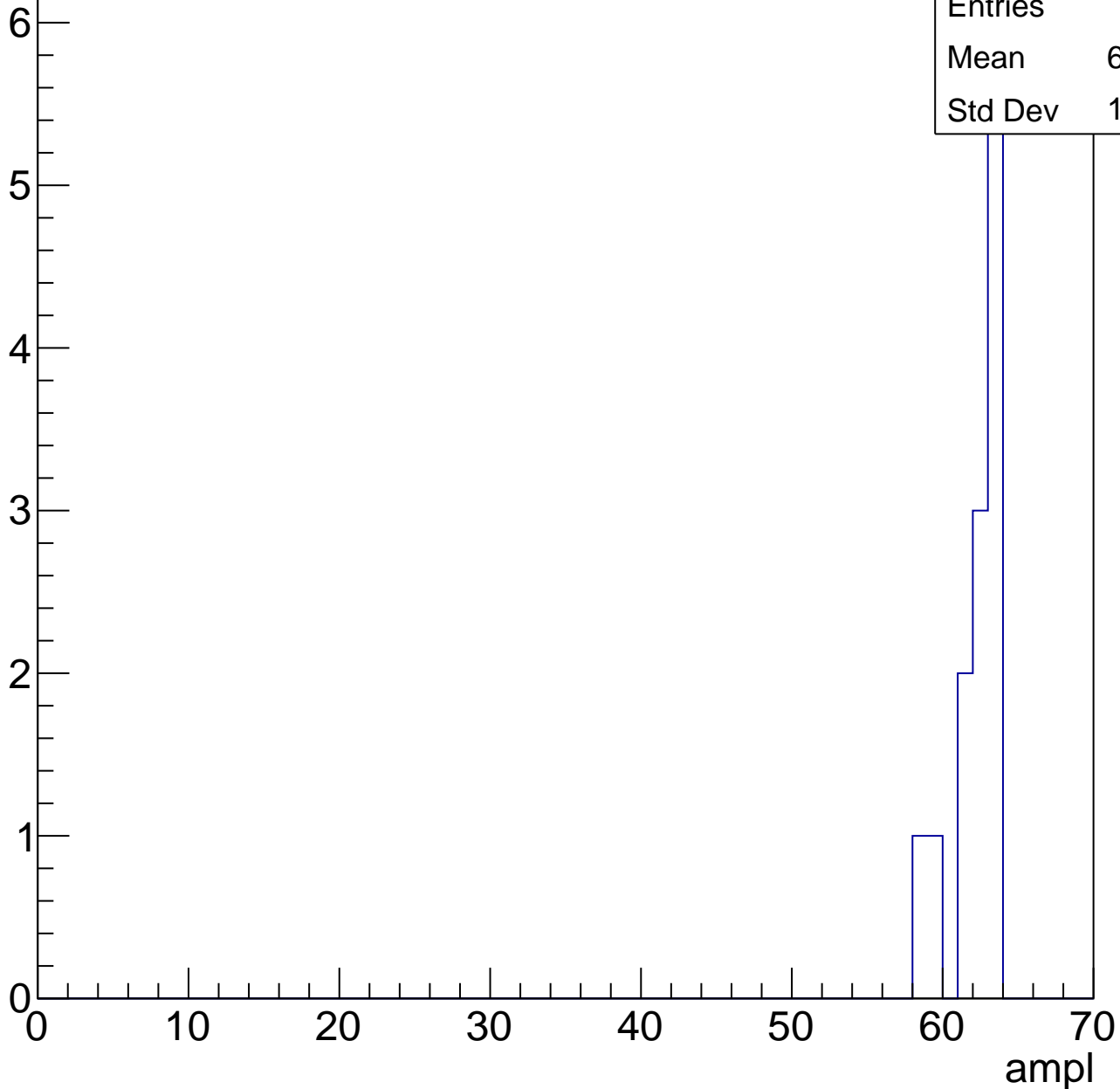


B1L001S, U16-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	61.77
Std Dev	1.576



B1L001S, U16-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L001S, U16-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	28.97
Std Dev	4.982

Gaus mean : 29.8701

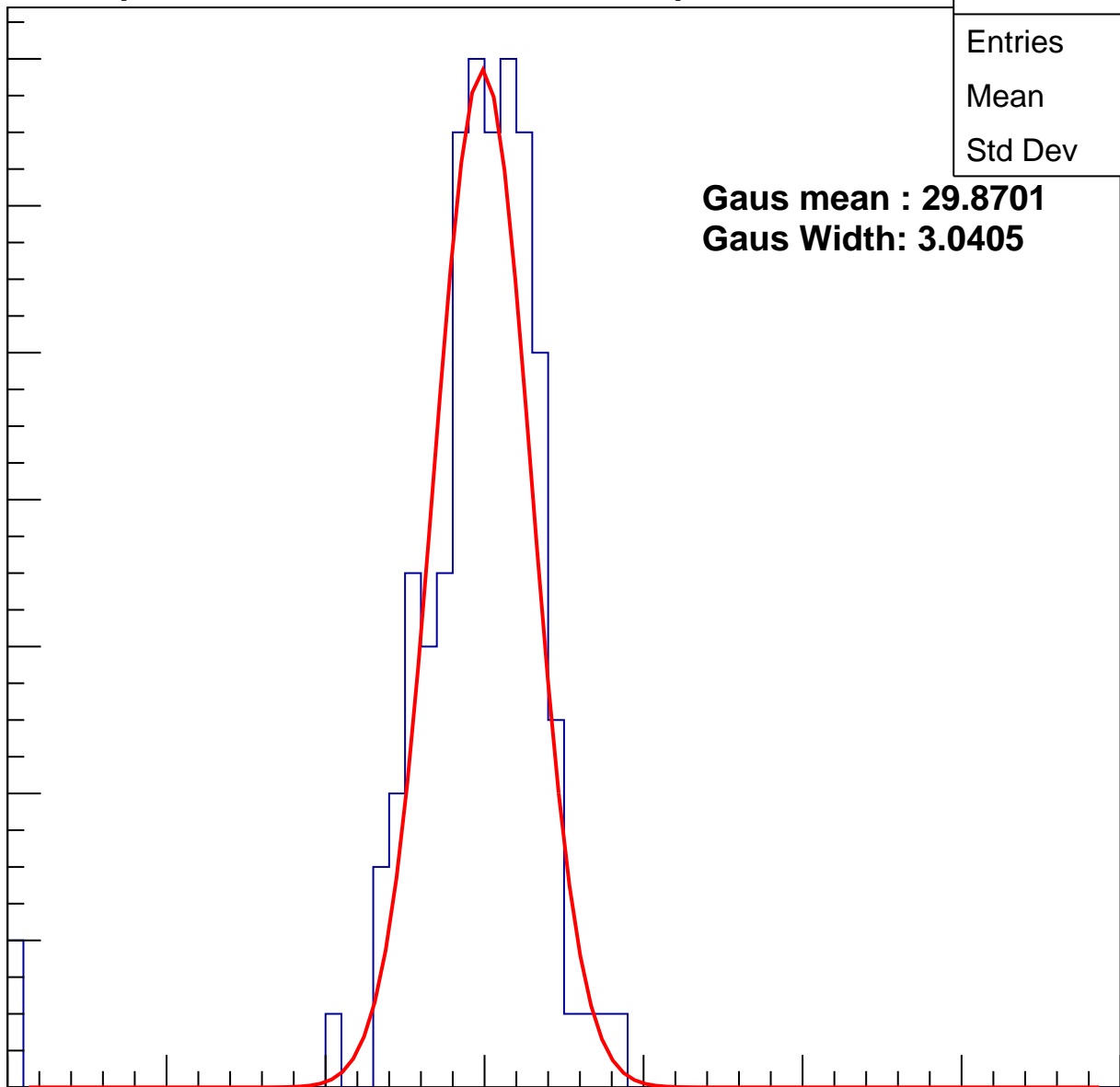
Gaus Width: 3.0405

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



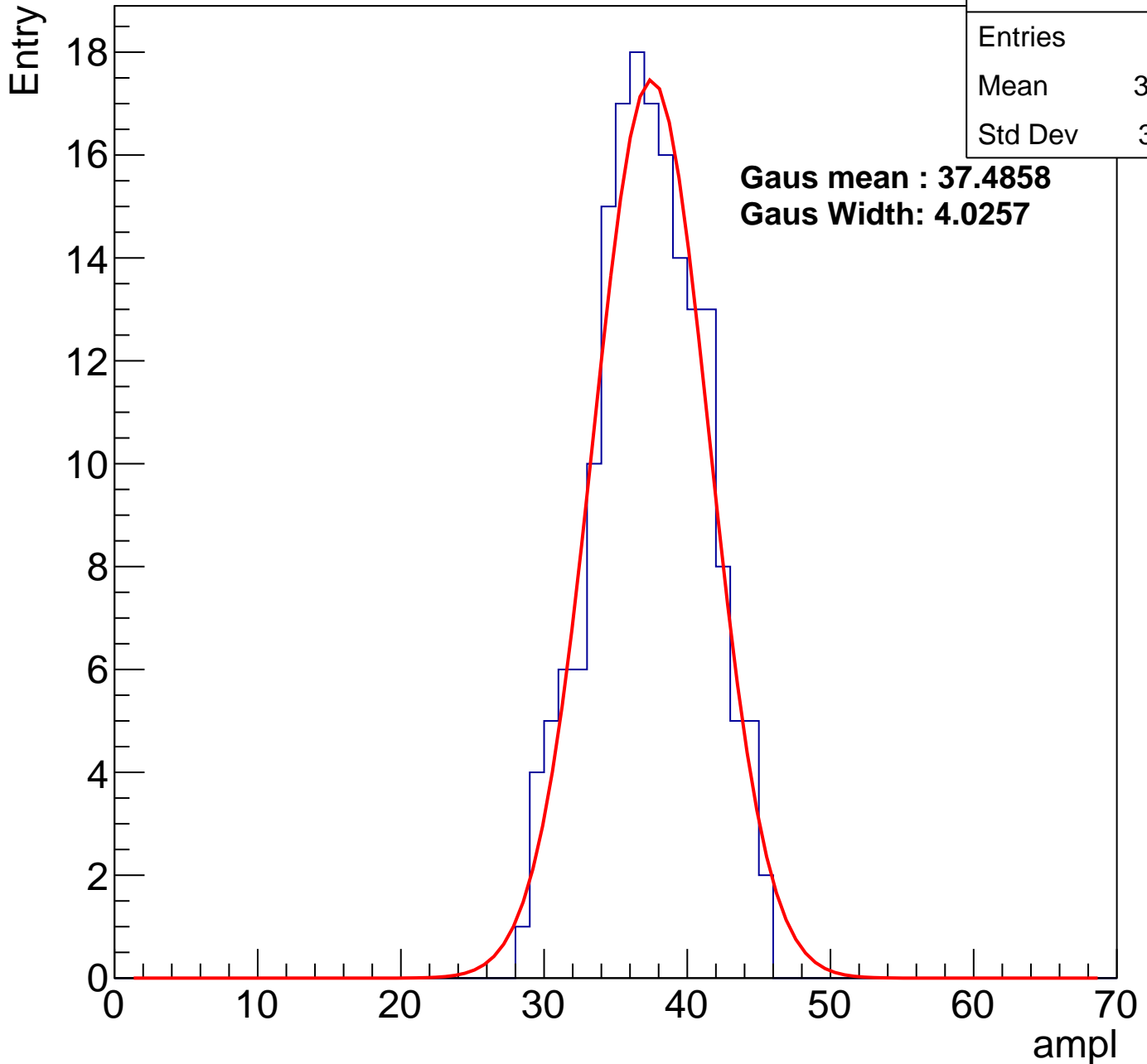
B1L001S, U16-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	175
Mean	36.87
Std Dev	3.761

Gaus mean : 37.4858

Gaus Width: 4.0257



B1L001S, U16-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries 108

Mean 43.58

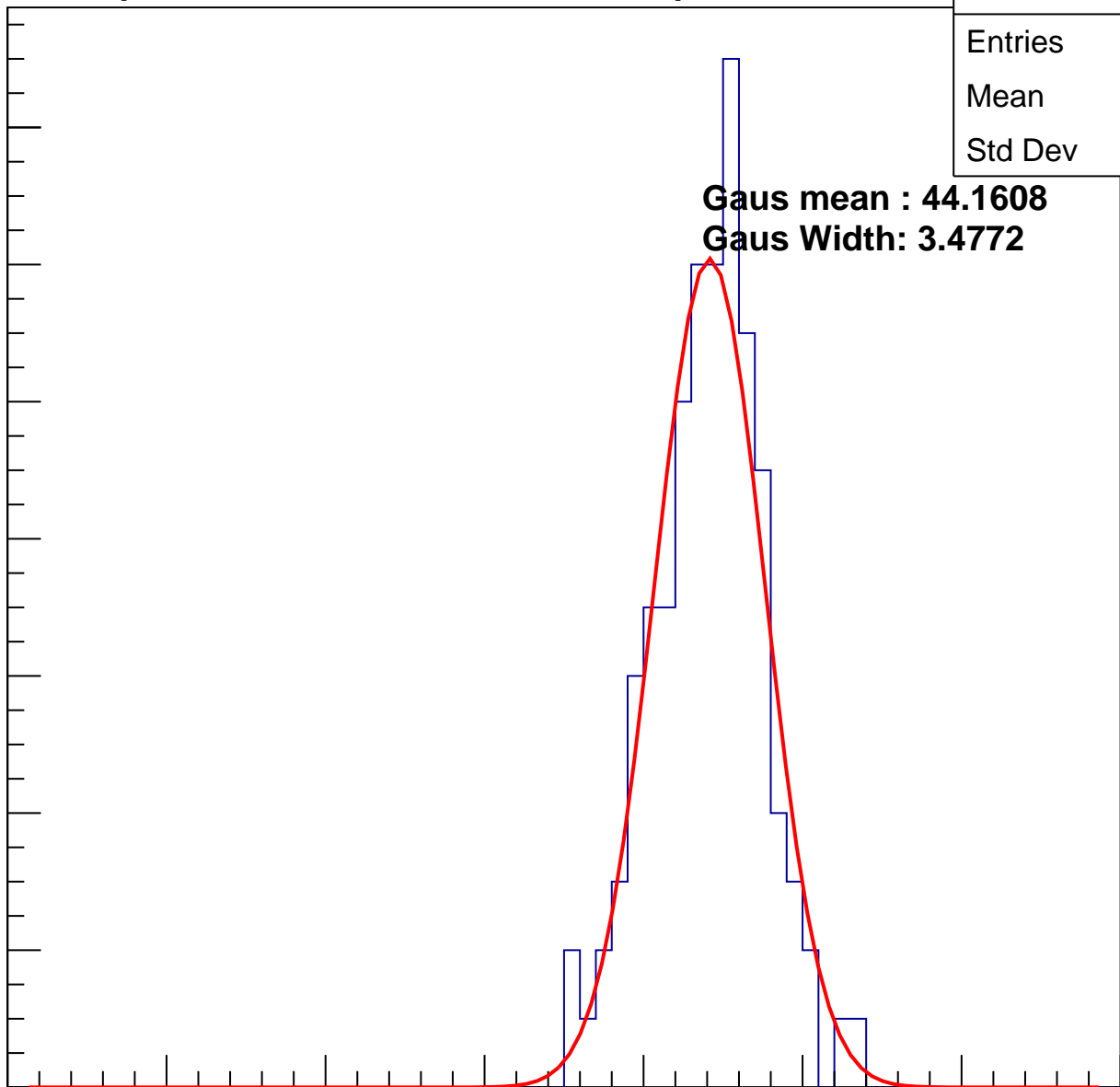
Std Dev 3.451

Gaus mean : 44.1608

Gaus Width: 3.4772

0 10 20 30 40 50 60 70

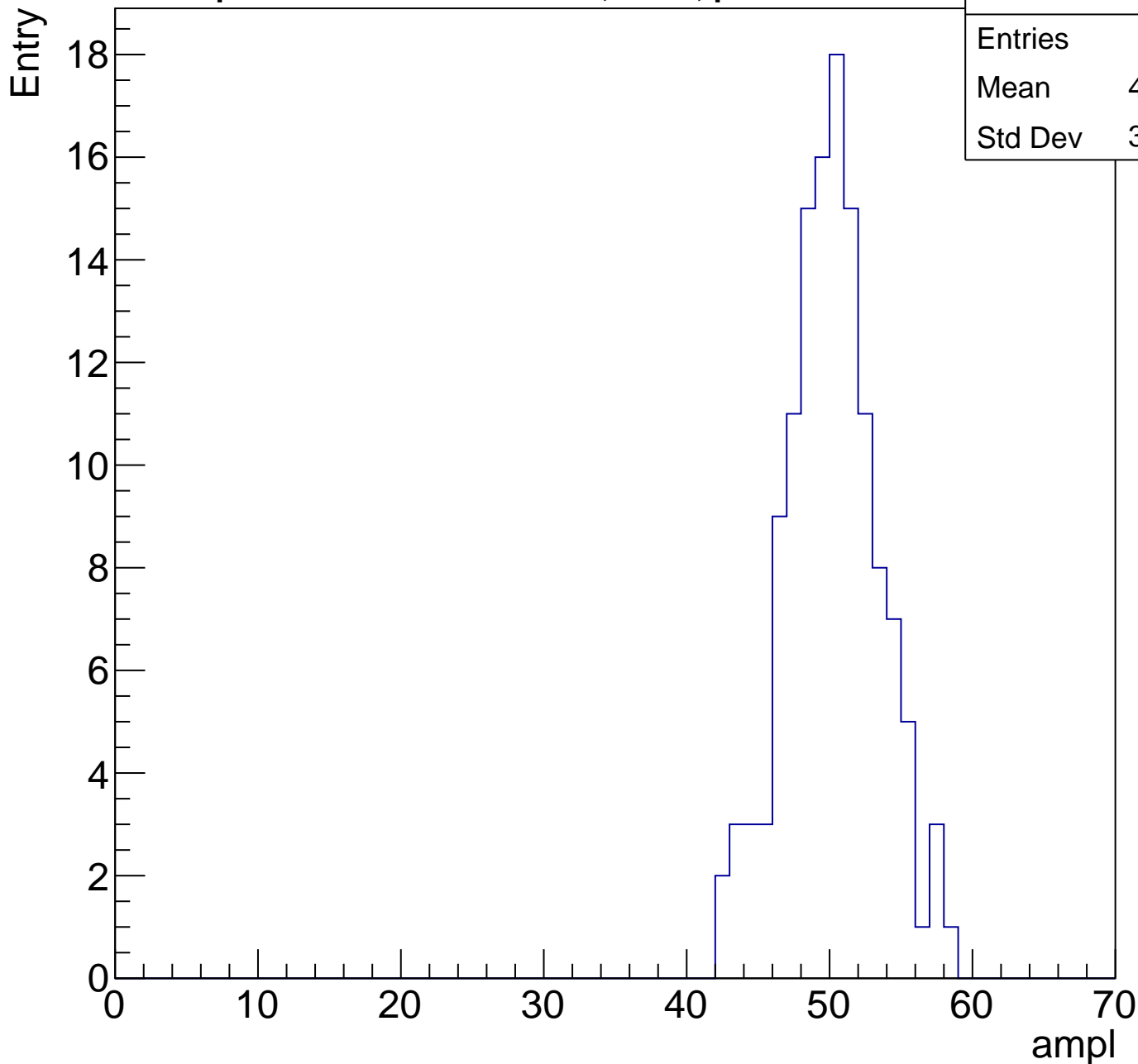
ampl



B1L001S, U16-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	49.73
Std Dev	3.248



B1L001S, U16-ch33, adc4

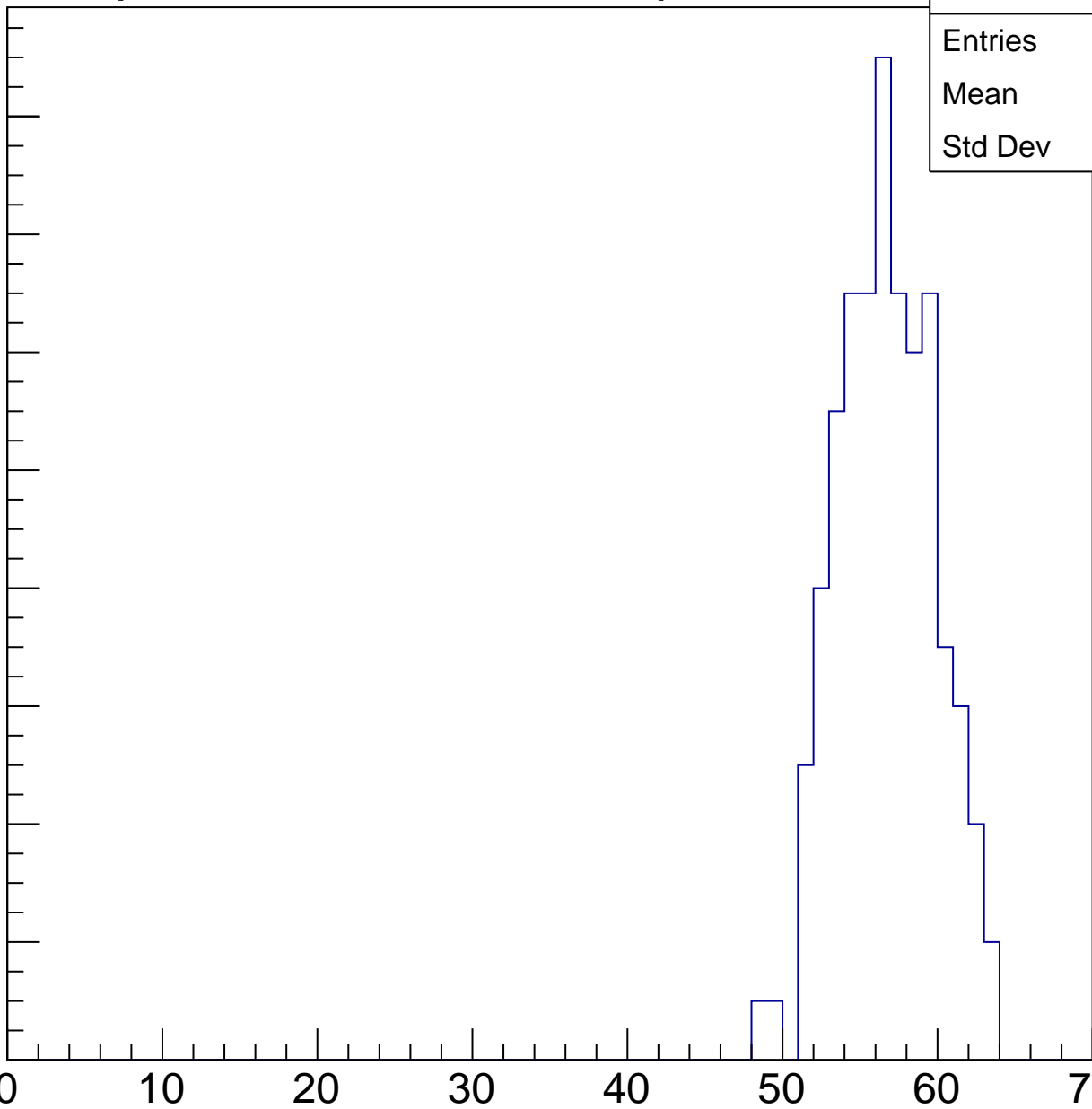
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	56.22
Std Dev	3.093

ampl



B1L001S, U16-ch33, adc5

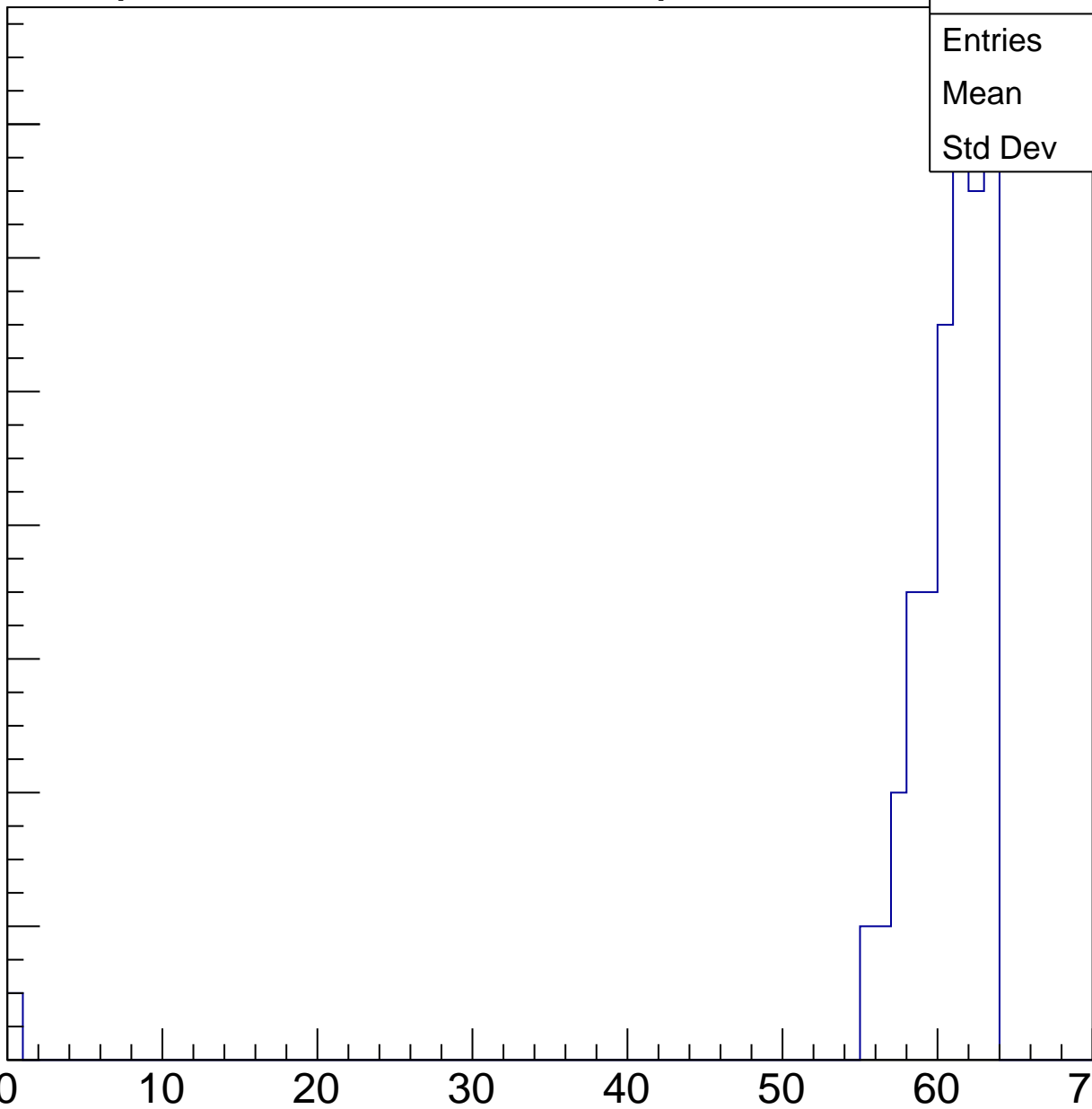
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.66
Std Dev	7.205

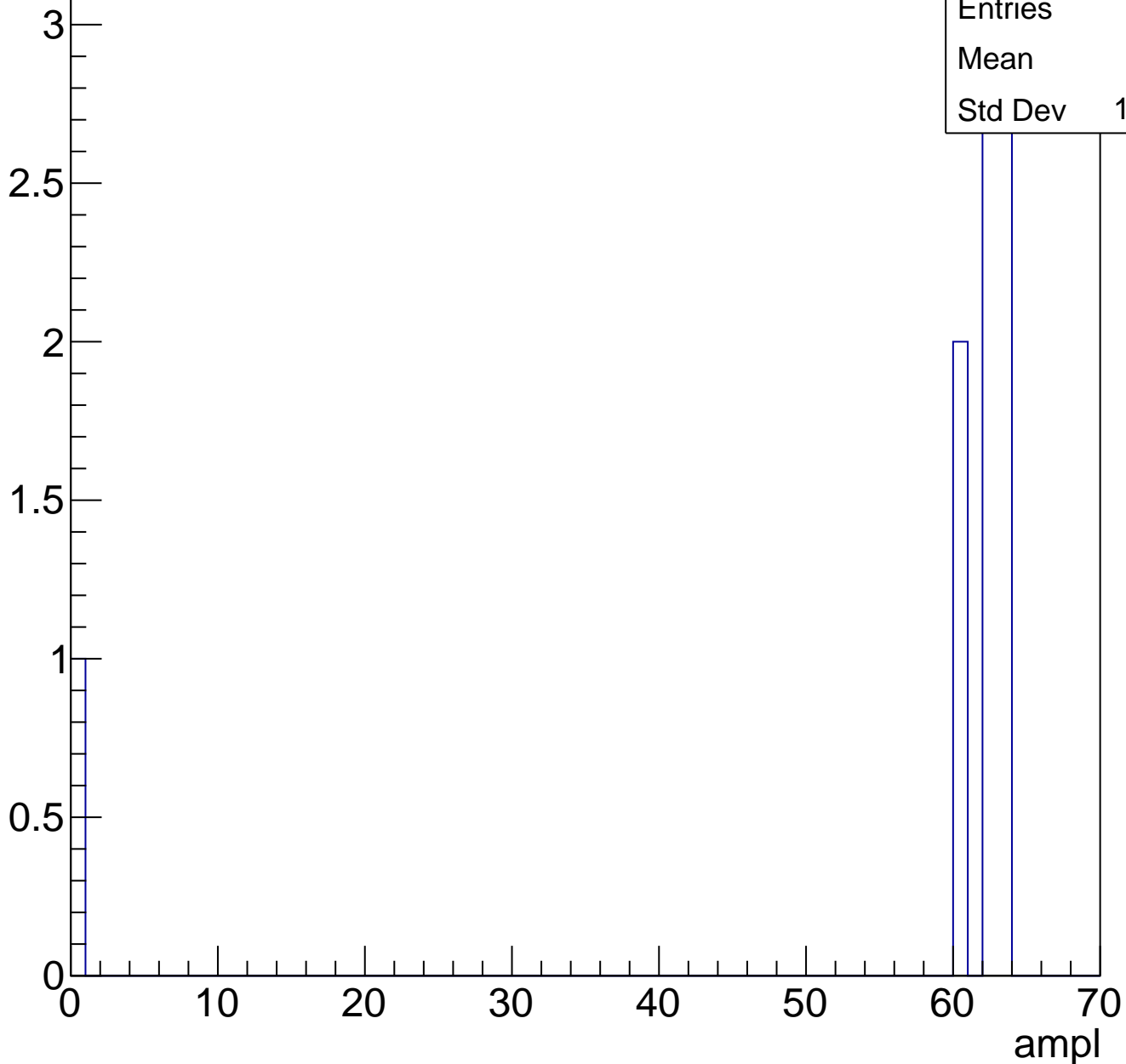
ampl



B1L001S, U16-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U16-ch34, adc0

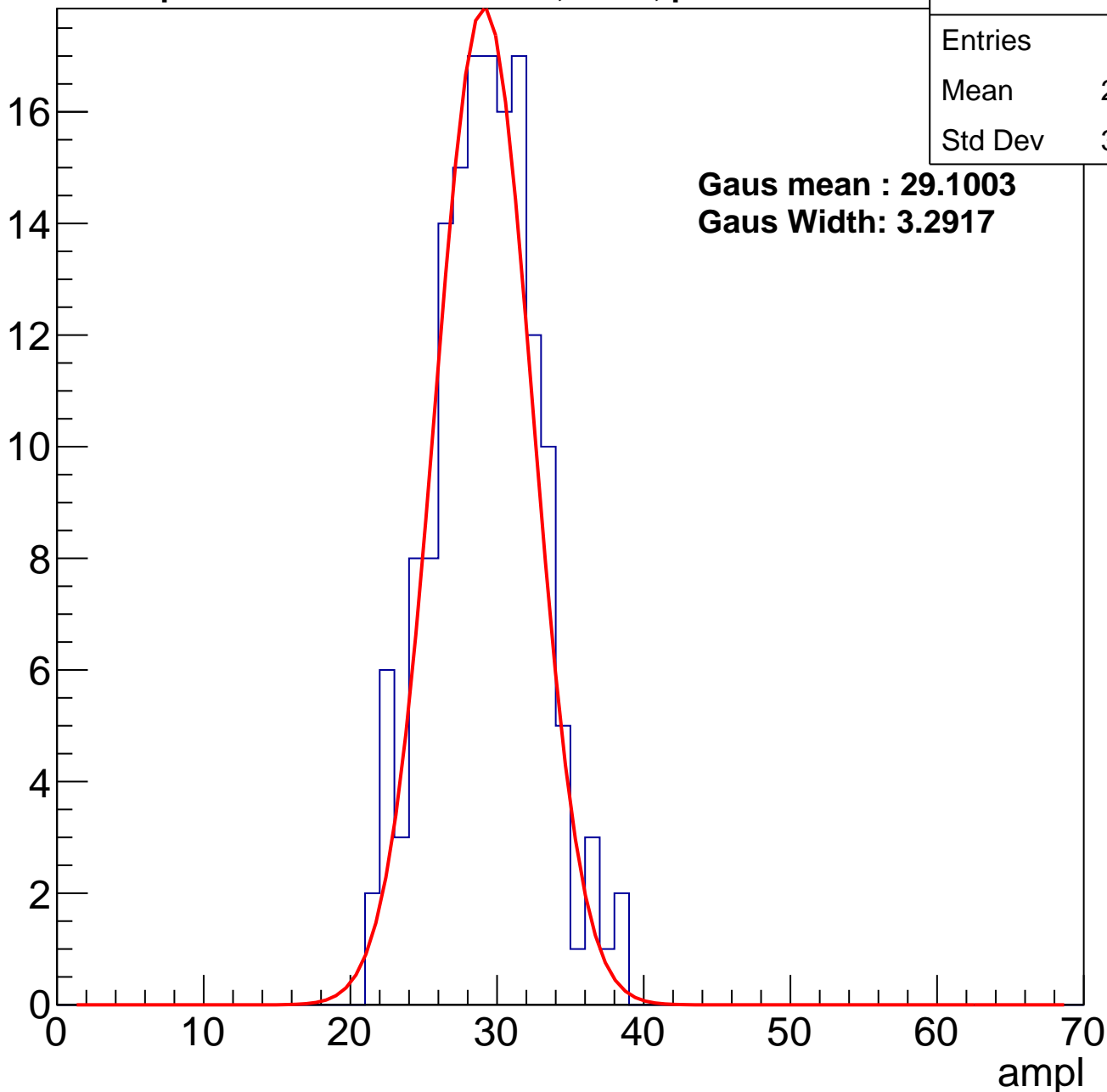
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	28.79
Std Dev	3.515

Gaus mean : 29.1003

Gaus Width: 3.2917

Entry



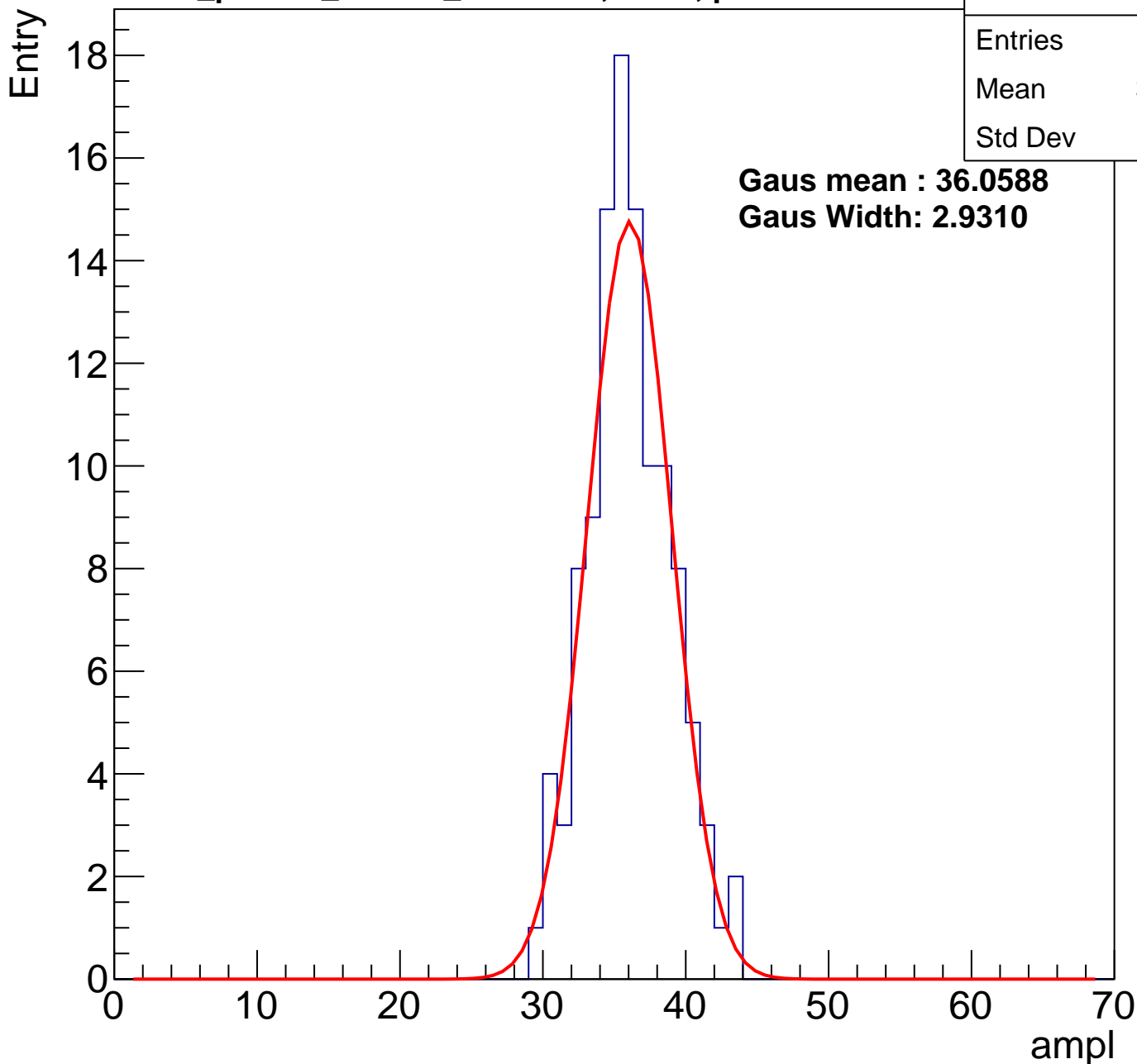
B1L001S, U16-ch34, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	35.61
Std Dev	2.91

Gaus mean : 36.0588

Gaus Width: 2.9310



B1L001S, U16-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	42.32
Std Dev	3.509

Gaus mean : 42.4118

Gaus Width: 3.1391

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

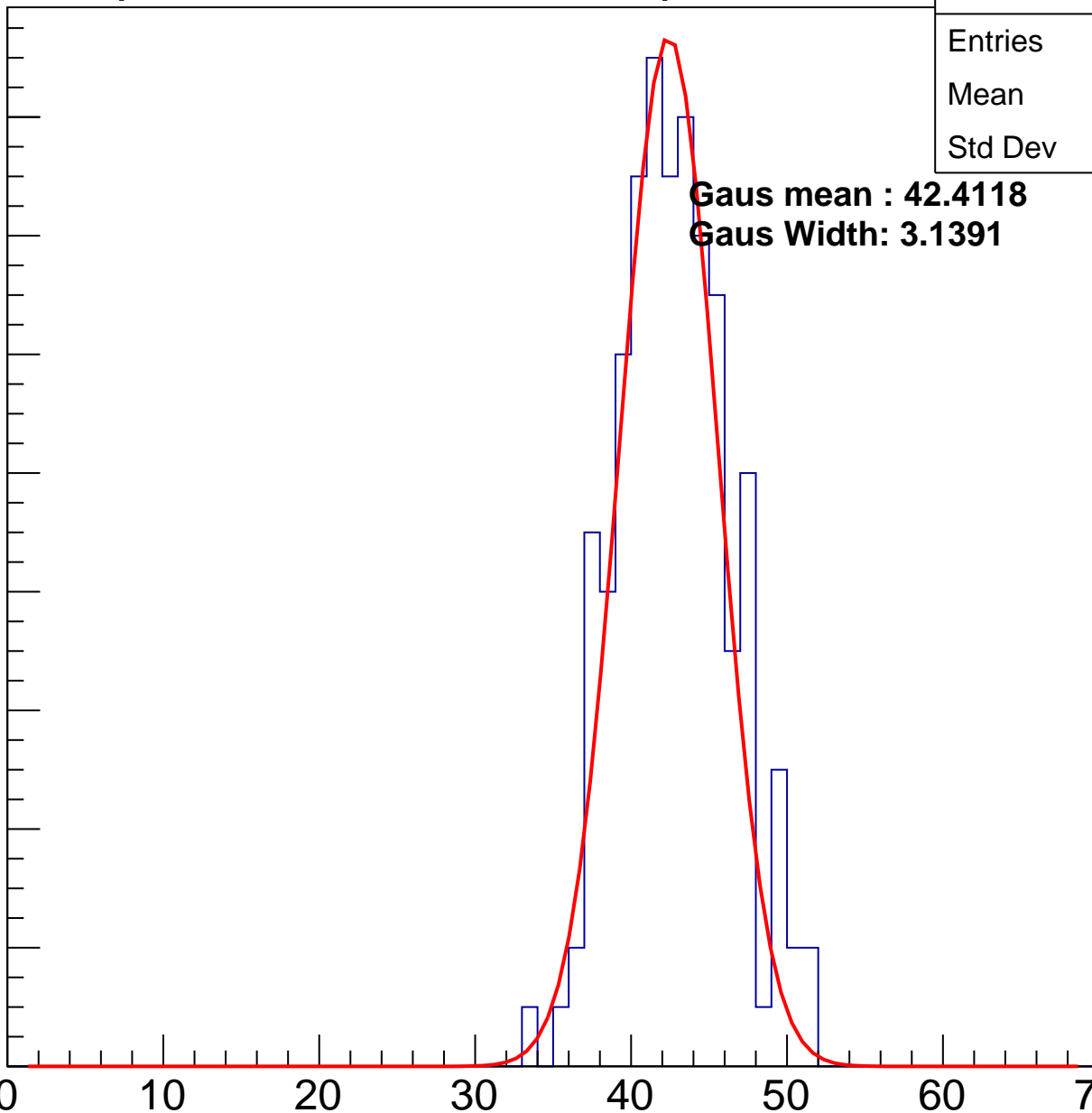
40

50

60

70

ampl



B1L001S, U16-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	48.47
Std Dev	3.254

Entry

12

10

8

6

4

2

0

0

10

20

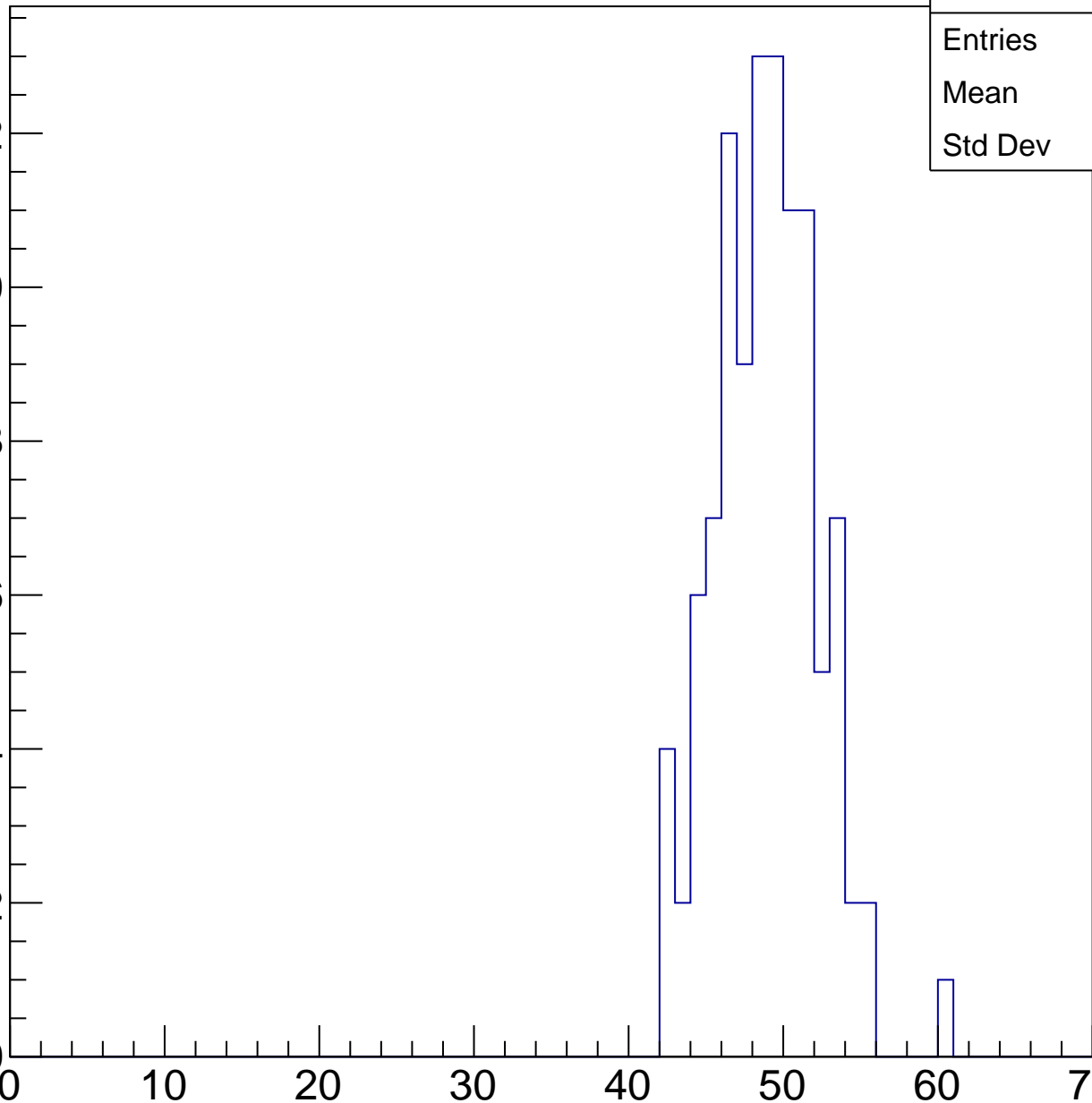
30

40

50

60

ampl



B1L001S, U16-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

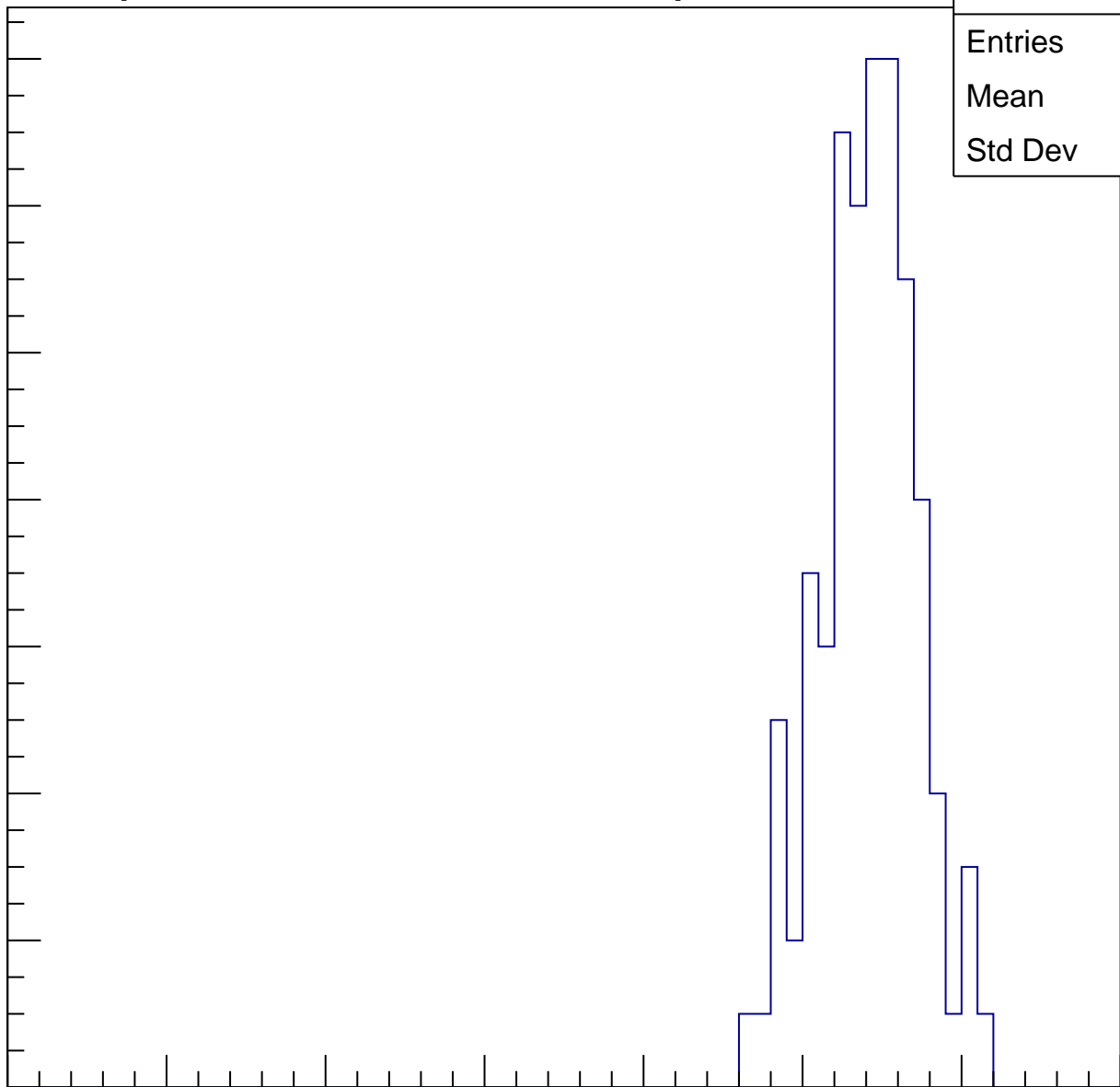
Entries	103
Mean	53.68
Std Dev	3.018

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

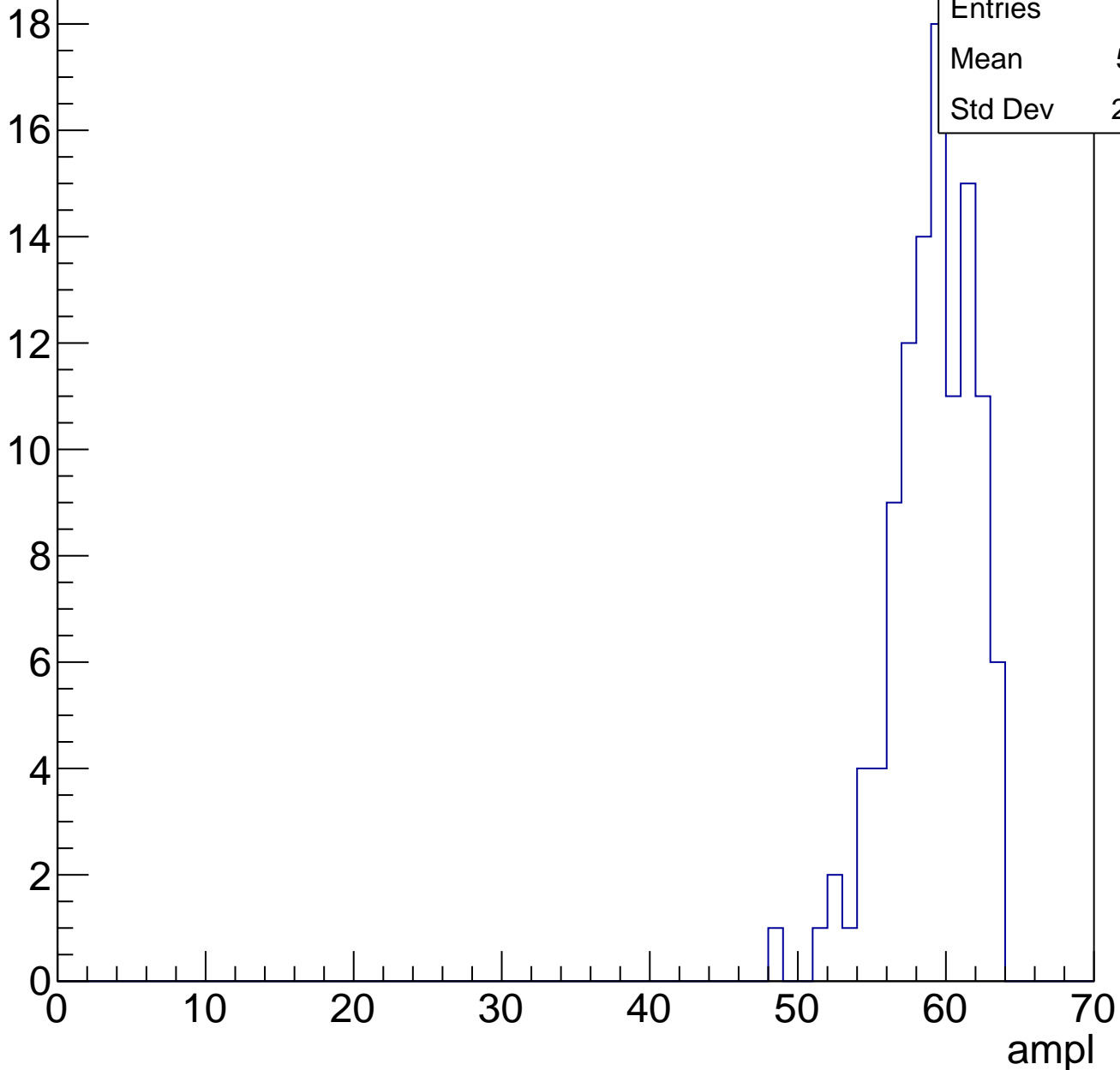


B1L001S, U16-ch34, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	58.61
Std Dev	2.835

Entry



B1L001S, U16-ch34, adc6

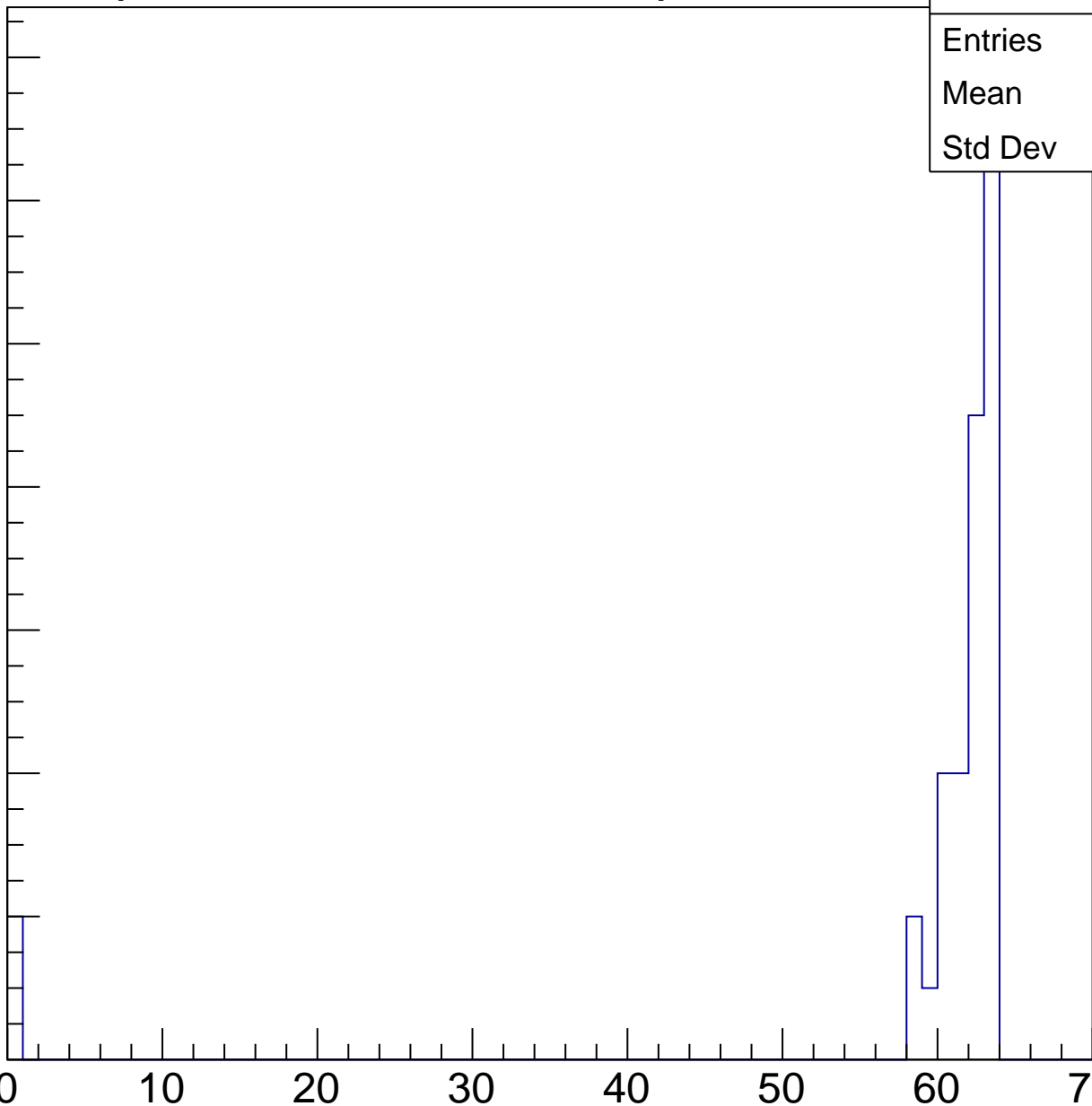
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	36
Mean	58.31
Std Dev	14.21

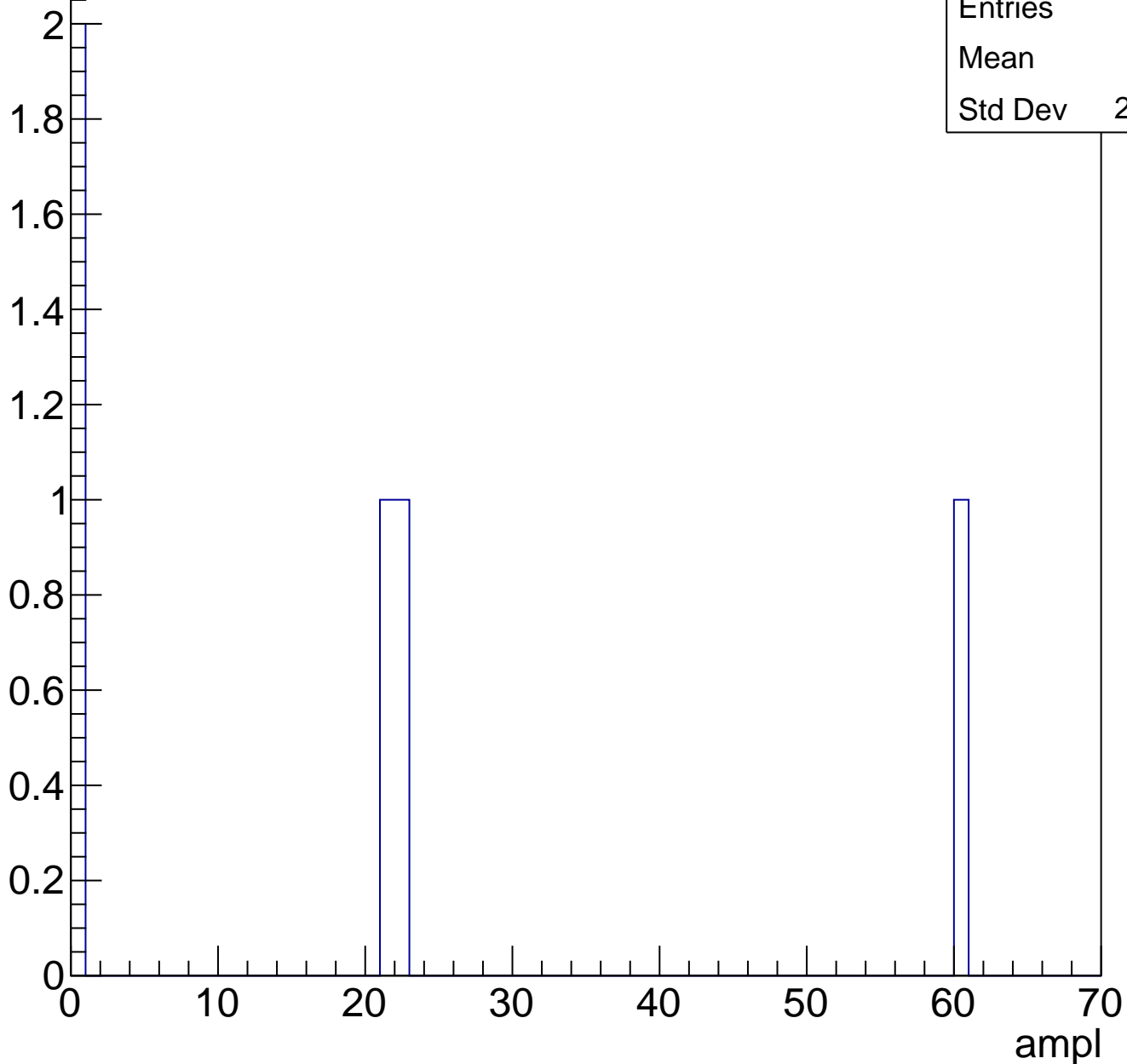
ampl



B1L001S, U16-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	20.6
Std Dev	21.92

B1L001S, U16-ch35, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.72
Std Dev	3.194

Gaus mean : 29.3200

Gaus Width: 3.3262

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch35, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	35.13
Std Dev	2.98

Gaus mean : 35.6024

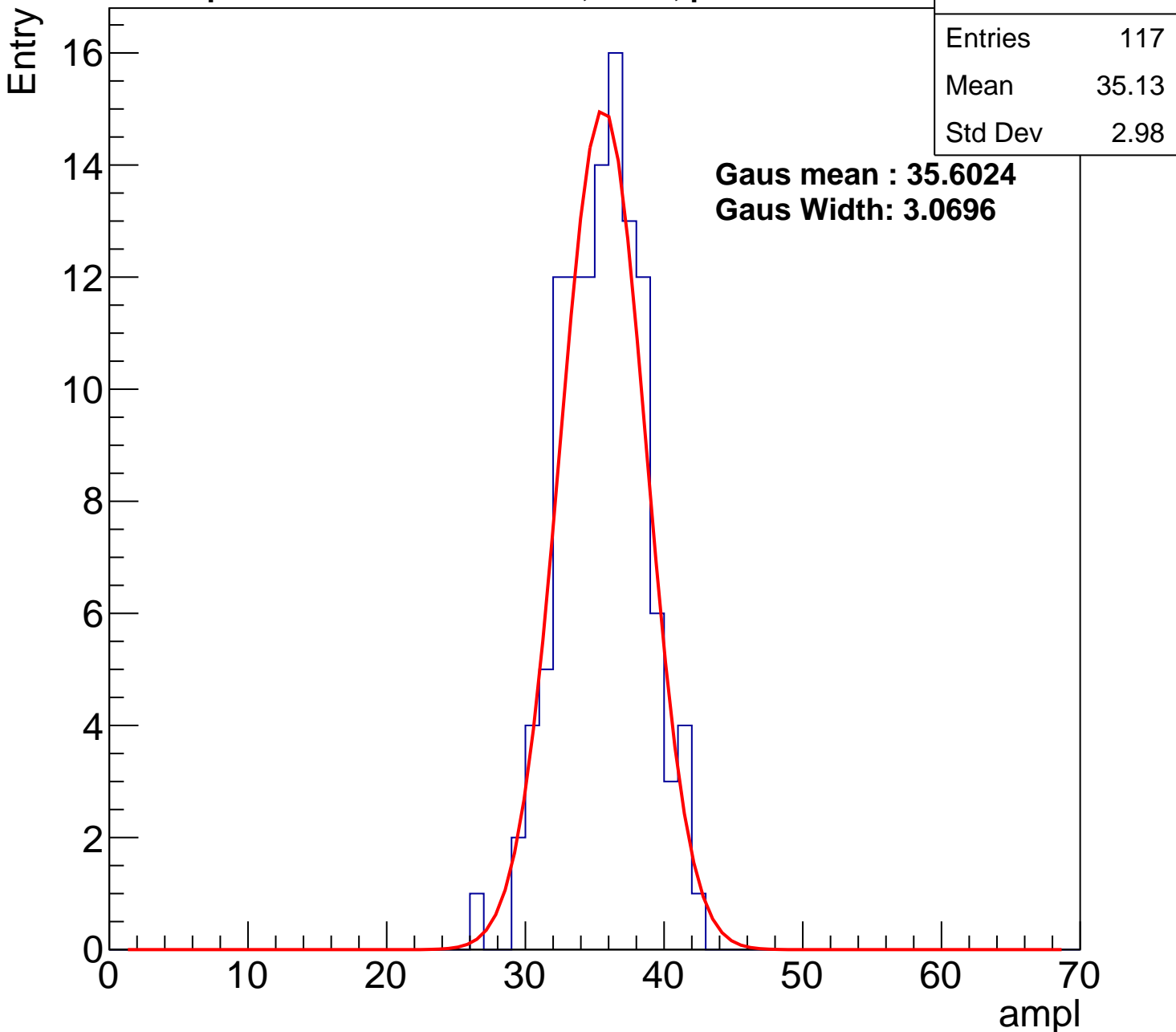
Gaus Width: 3.0696

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

127

Mean

41.21

Std Dev

3.116

Gaus mean : 41.5851

Gaus Width: 3.0139

0

10

20

30

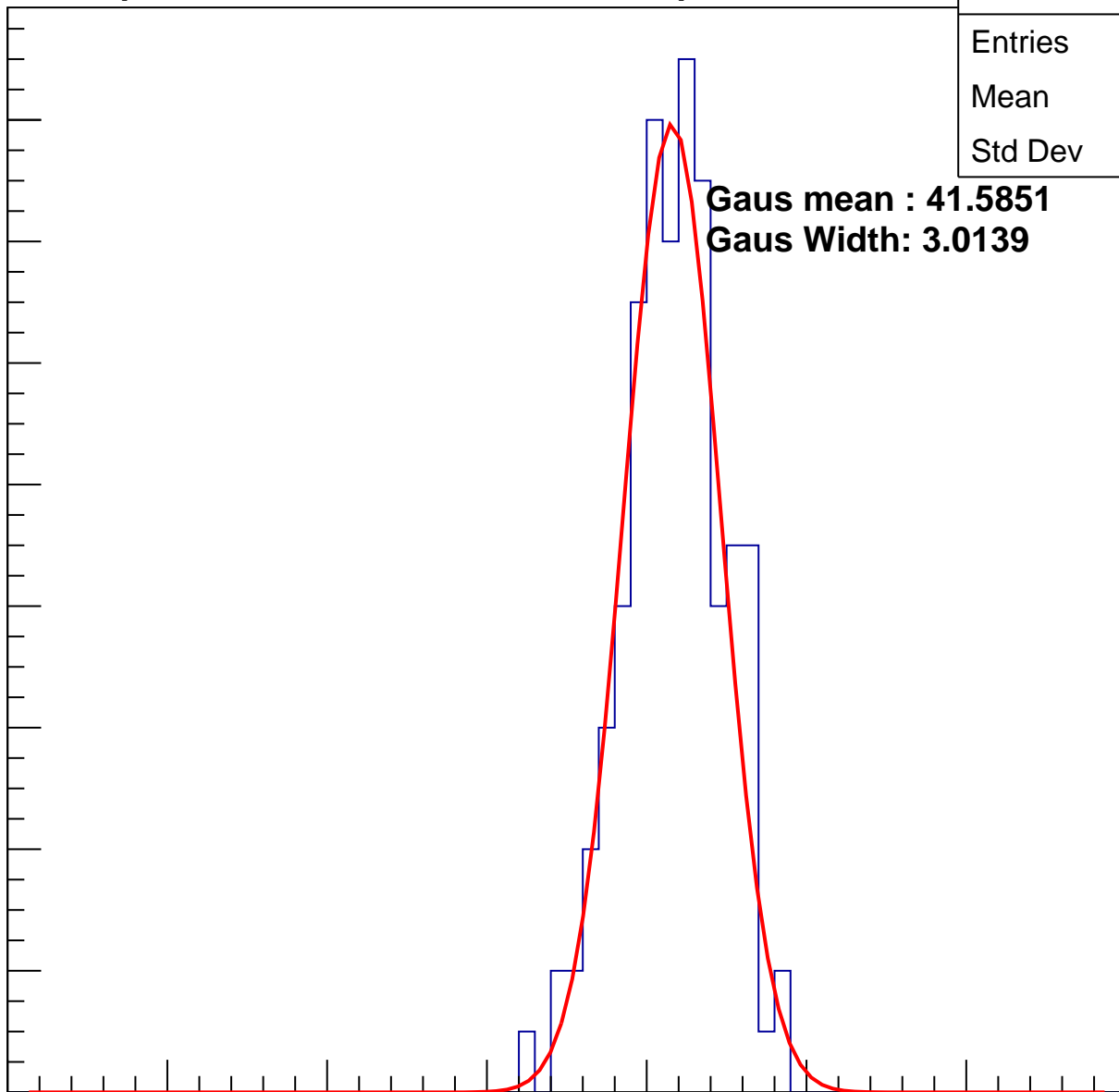
40

50

60

70

ampl



B1L001S, U16-ch35, adc3

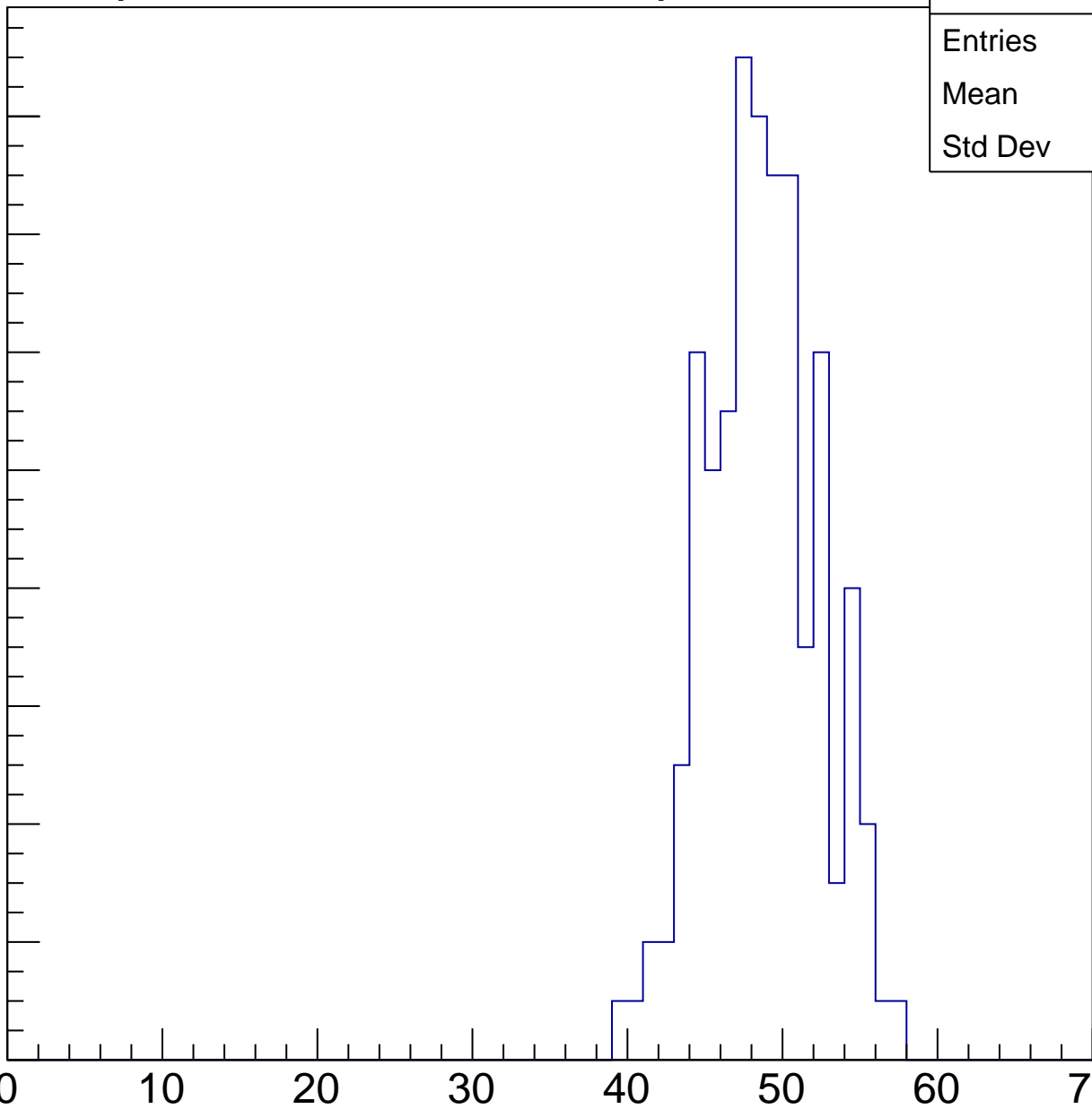
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	143
Mean	48.26
Std Dev	3.555

ampl



B1L001S, U16-ch35, adc4

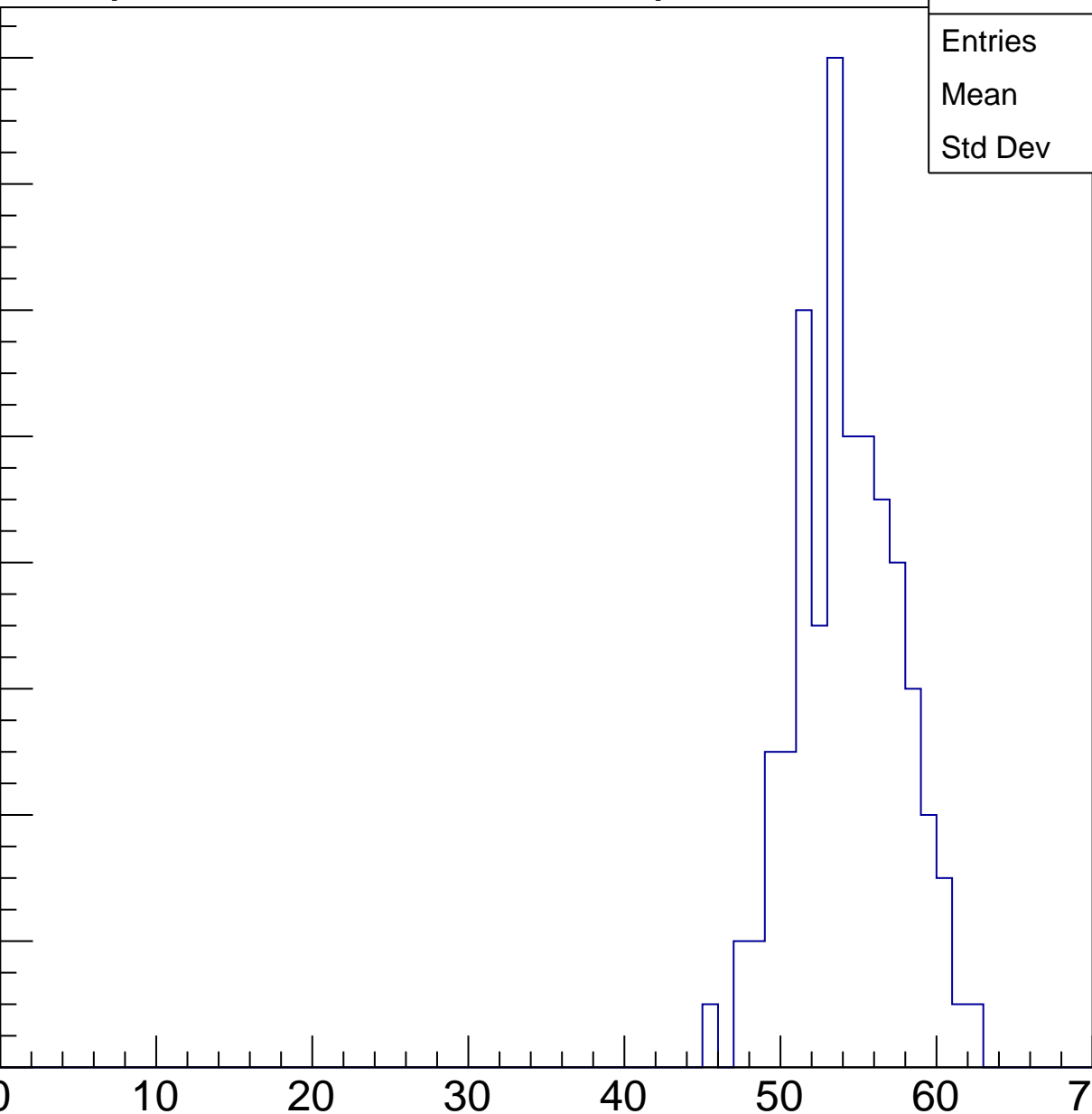
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	53.83
Std Dev	3.343

ampl



B1L001S, U16-ch35, adc5

calib_packv5_042523_0143.root, FC#2, port C2

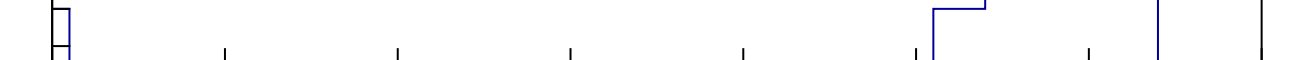
Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	58.06
Std Dev	6.424

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	38
Mean	61.58
Std Dev	1.664

0

2

4

6

8

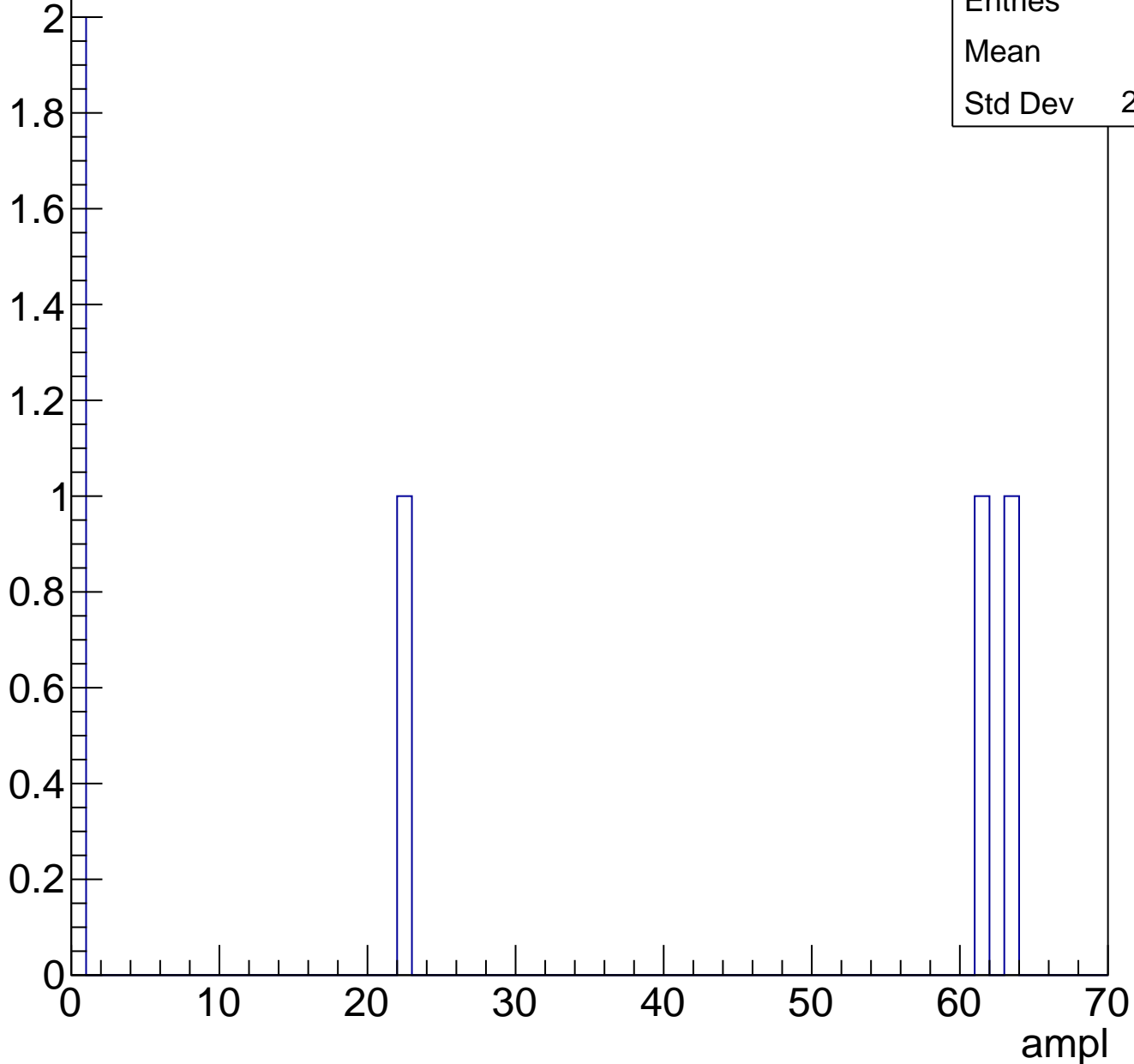
10

12

B1L001S, U16-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	29.2
Std Dev	27.97

B1L001S, U16-ch36, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	27.92
Std Dev	3.204

Gaus mean : 28.5739

Gaus Width: 3.6072

Entry

12

10

8

6

4

2

0

0

10

20

30

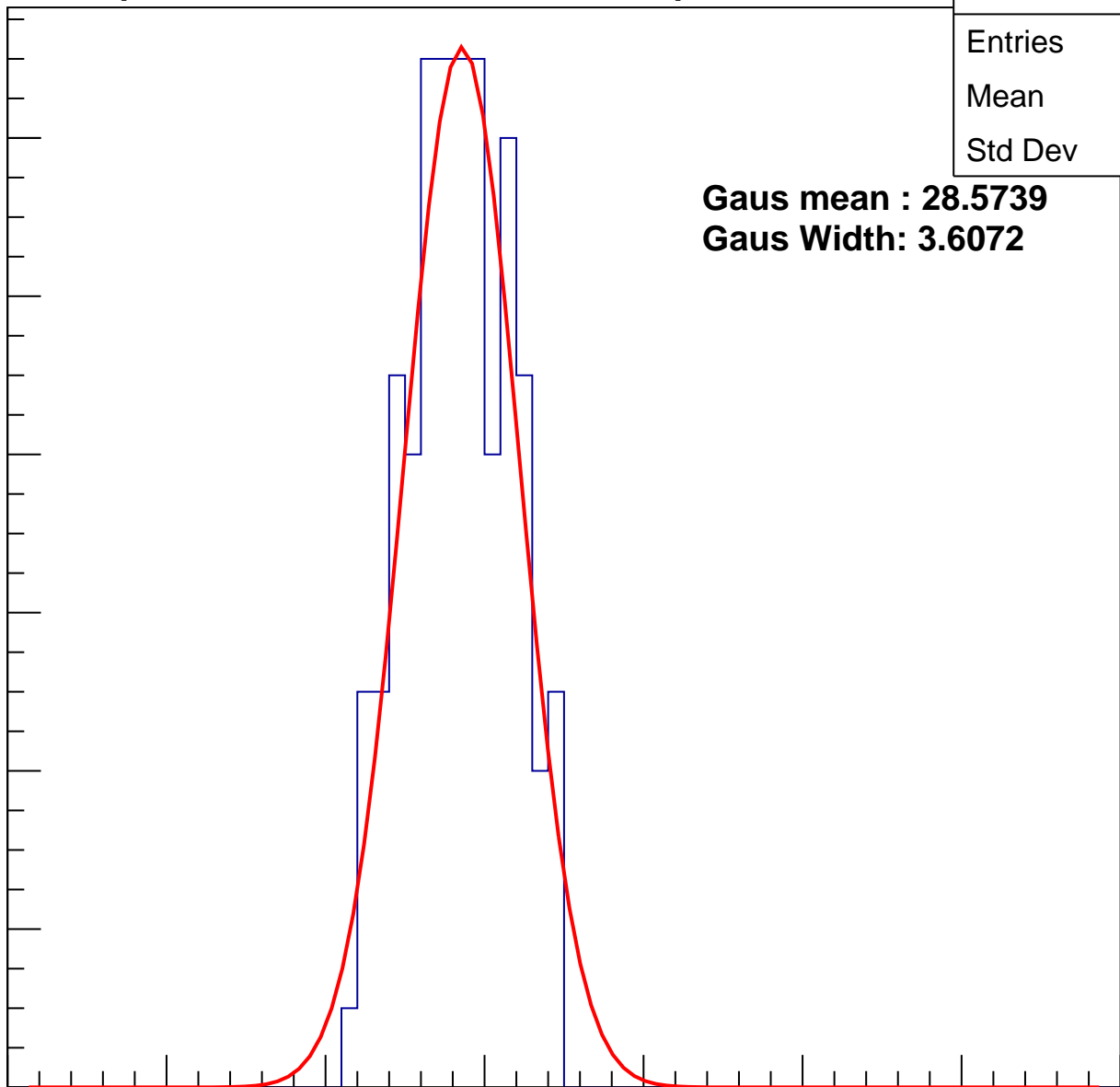
40

50

60

70

ampl



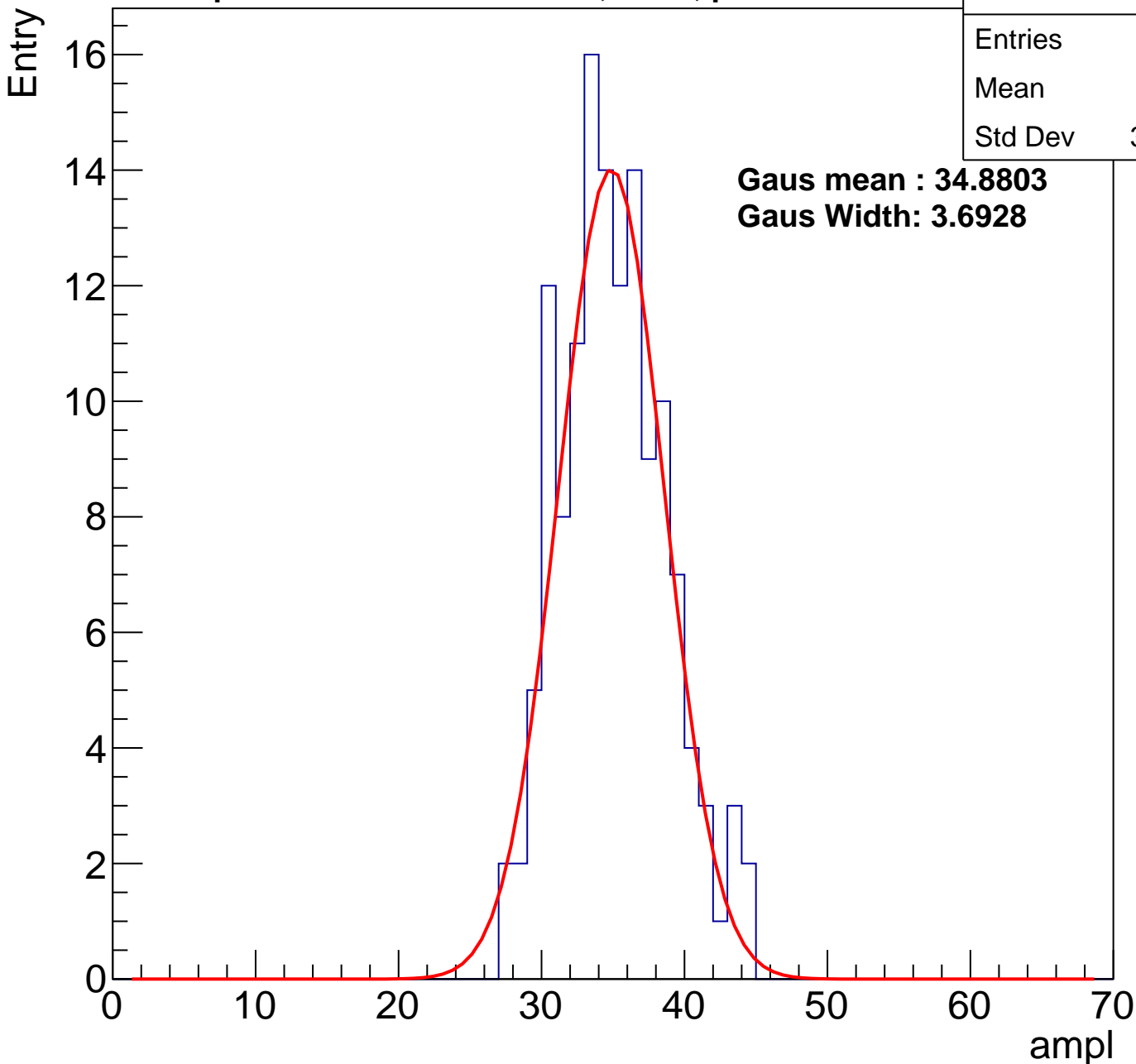
B1L001S, U16-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	34.6
Std Dev	3.704

Gaus mean : 34.8803

Gaus Width: 3.6928



B1L001S, U16-ch36, adc2

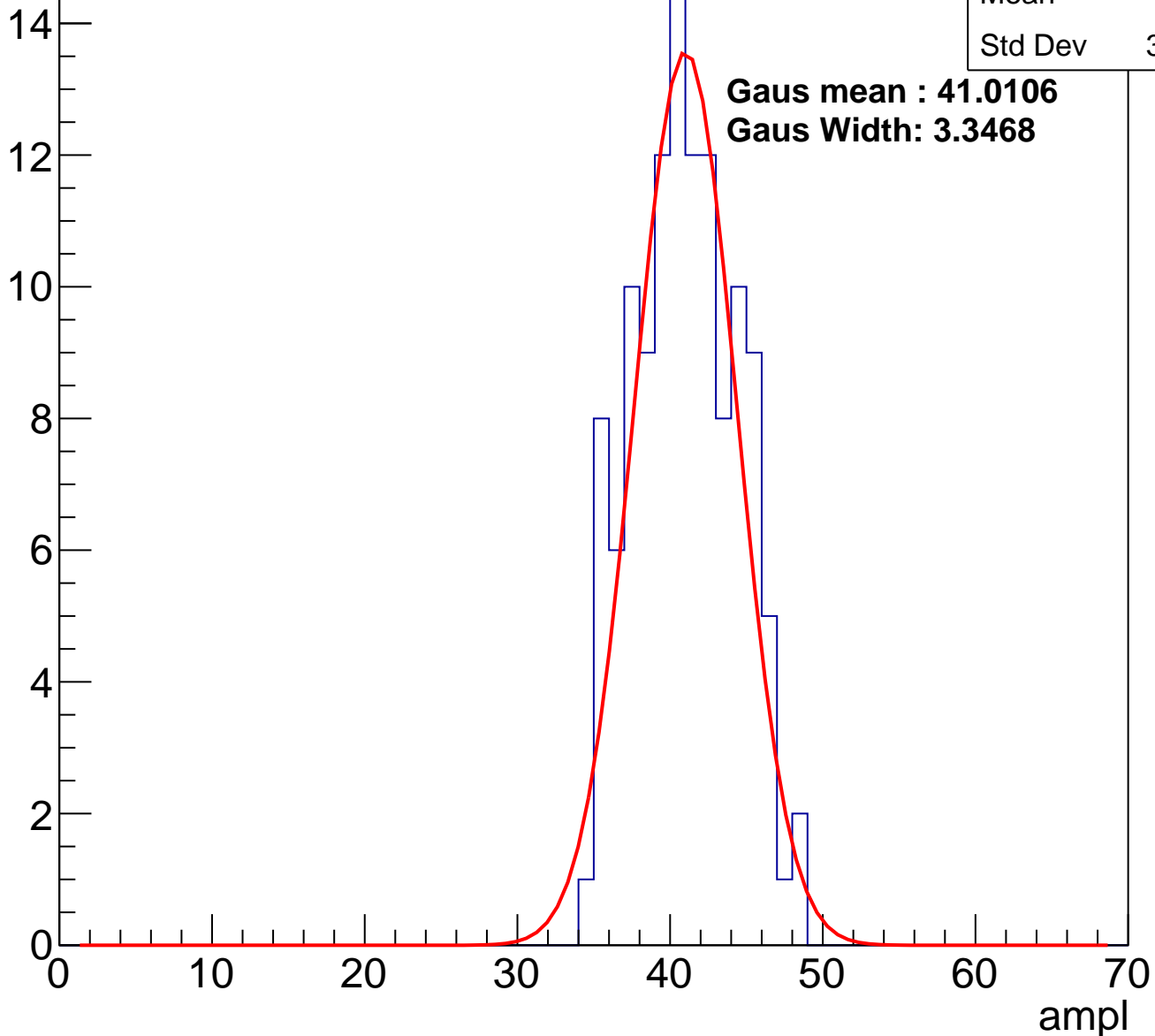
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	40.57
Std Dev	3.298

Gaus mean : 41.0106

Gaus Width: 3.3468

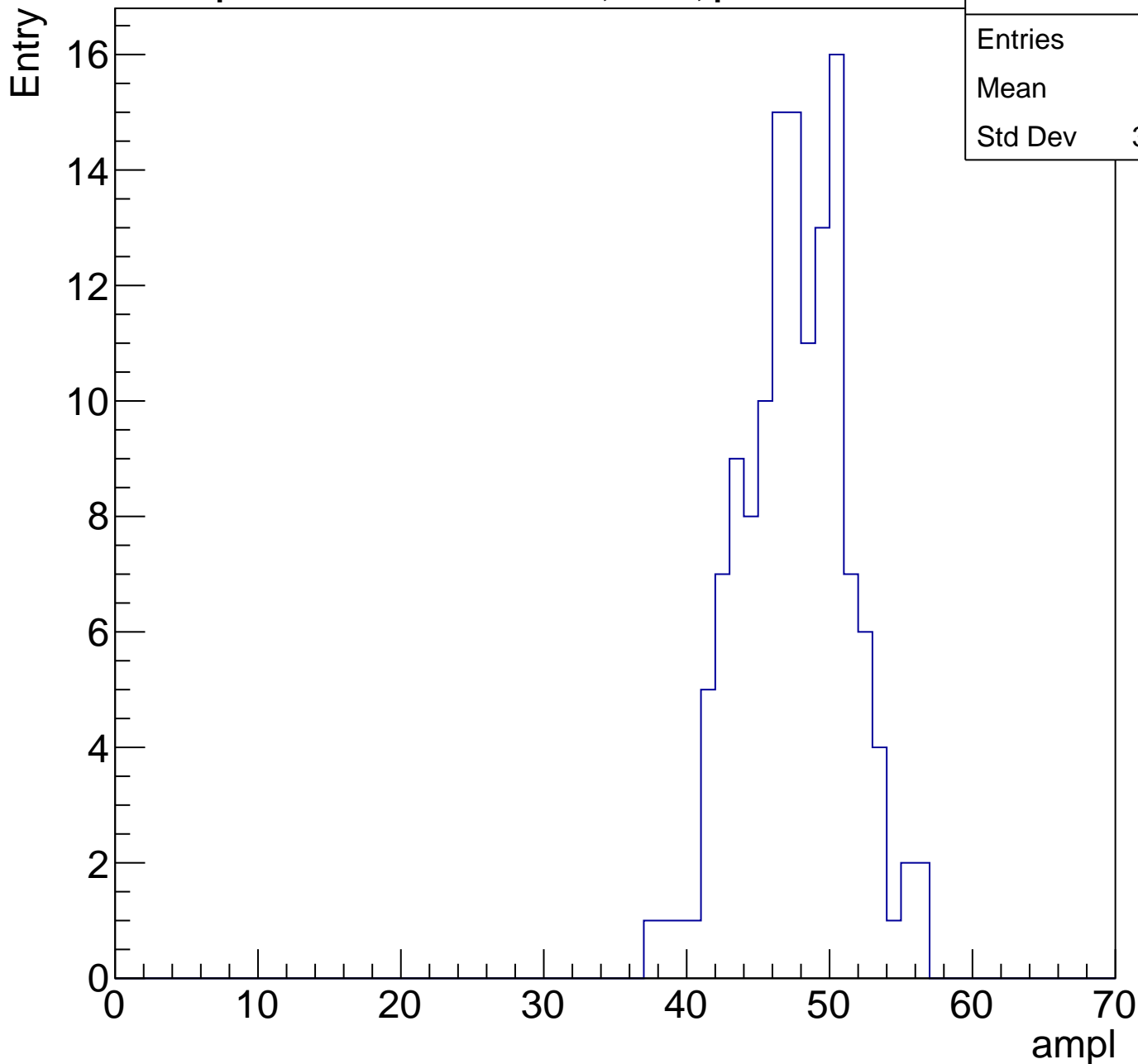
Entry



B1L001S, U16-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

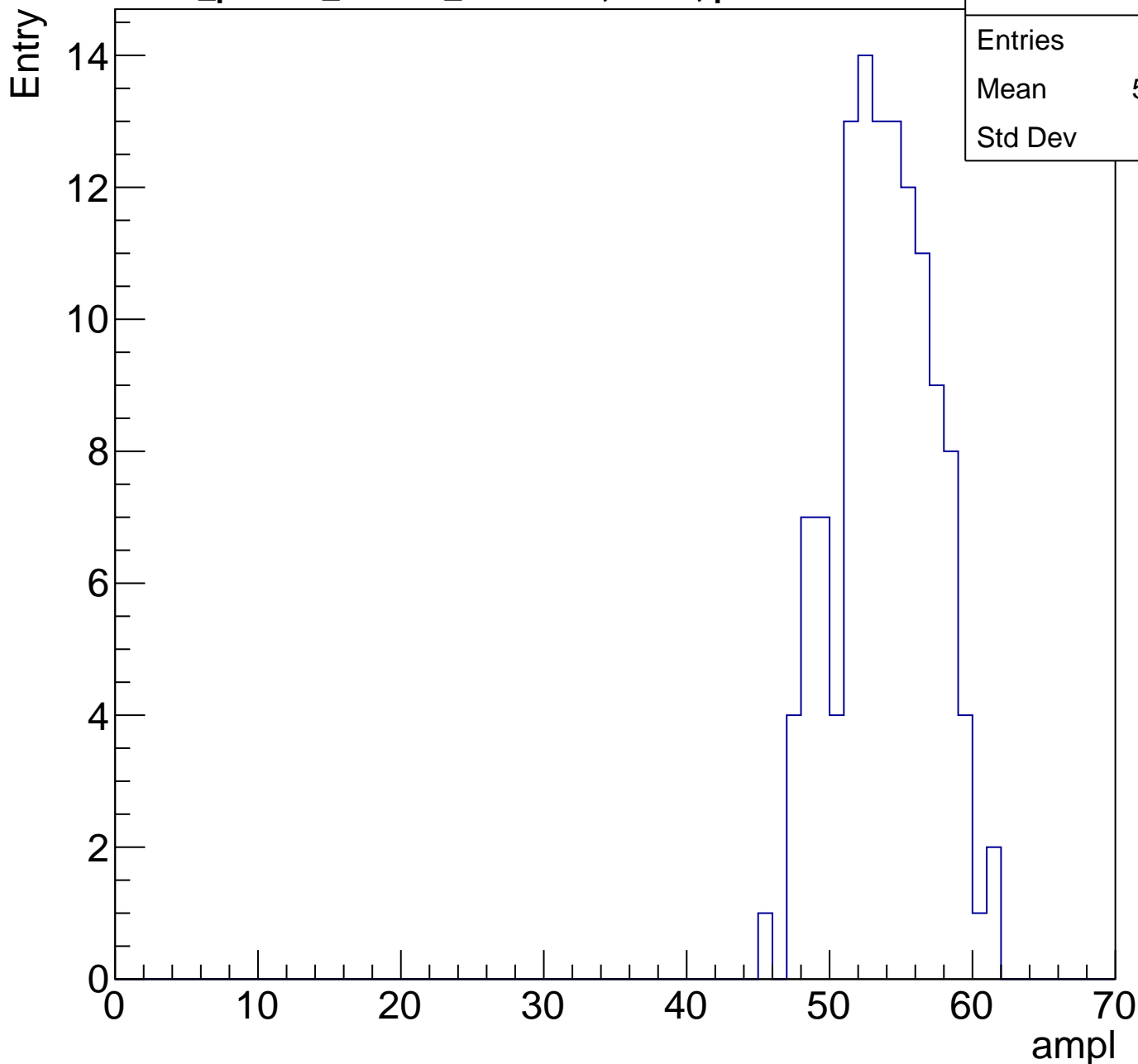
Entries	135
Mean	47.1
Std Dev	3.722



B1L001S, U16-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	53.37
Std Dev	3.38



B1L001S, U16-ch36, adc5

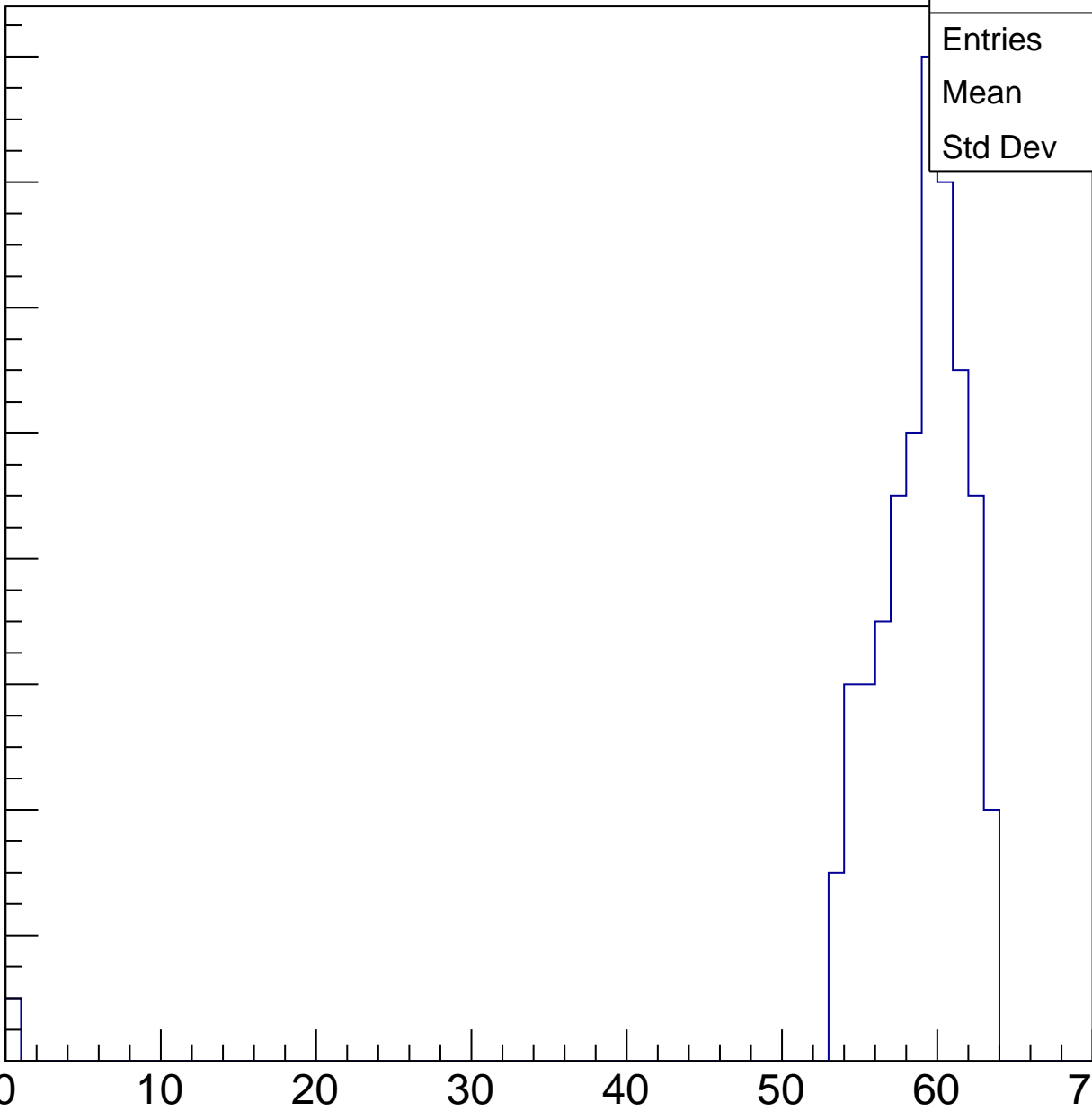
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	57.95
Std Dev	6.488

ampl



B1L001S, U16-ch36, adc6

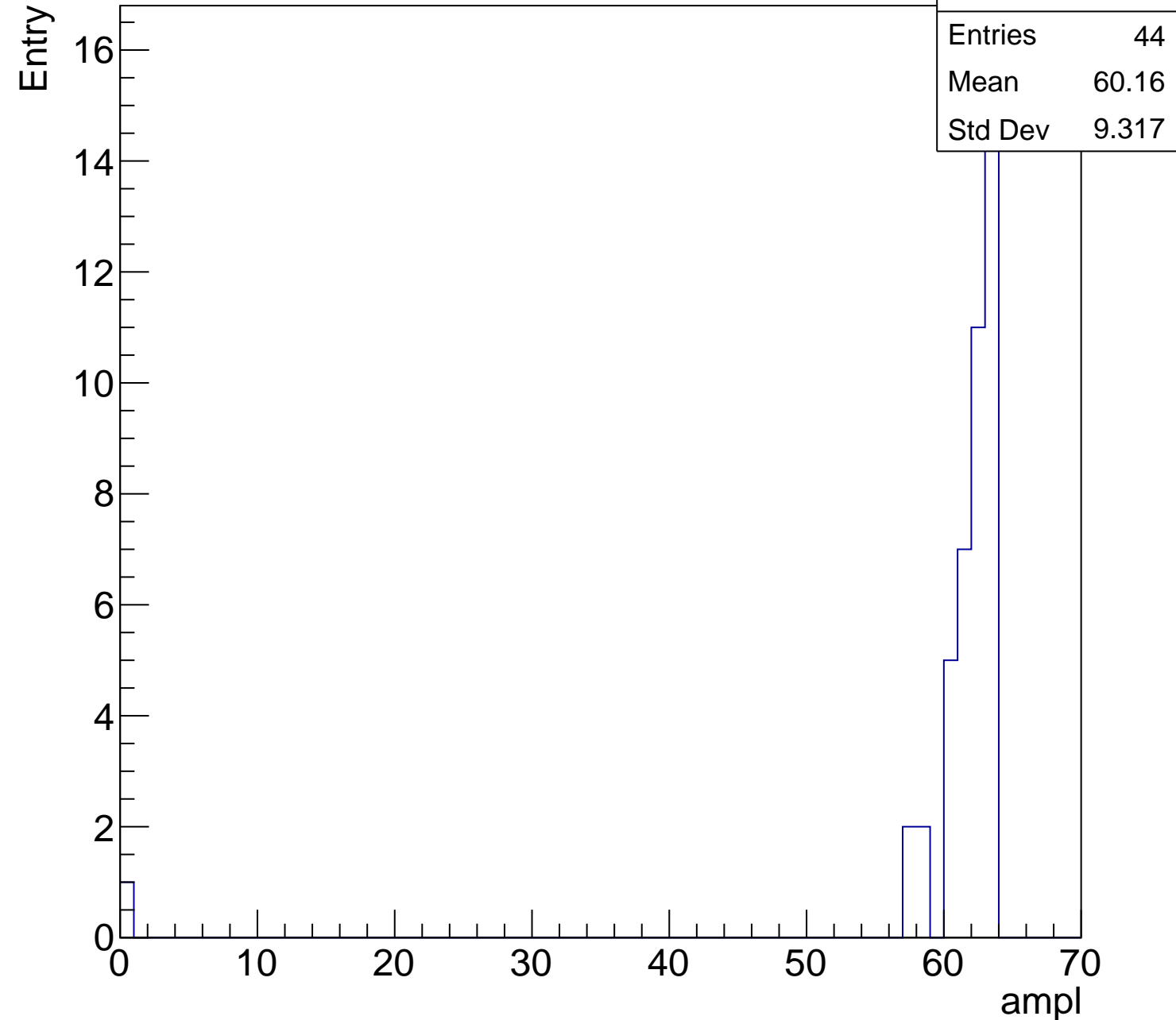
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	44
Mean	60.16
Std Dev	9.317

ampl



B1L001S, U16-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



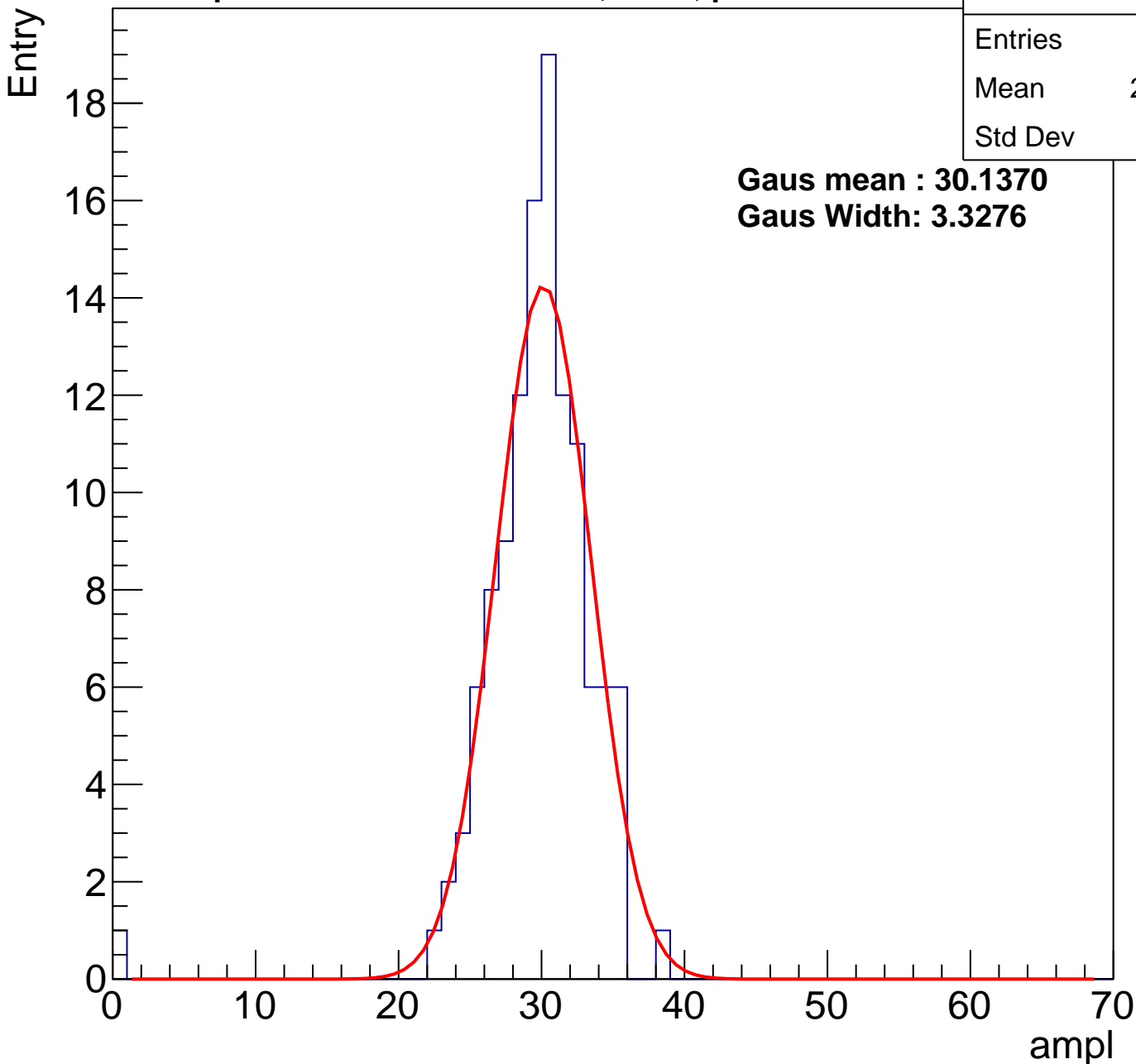
B1L001S, U16-ch37, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.29
Std Dev	4.05

Gaus mean : 30.1370

Gaus Width: 3.3276



B1L001S, U16-ch37, adc1

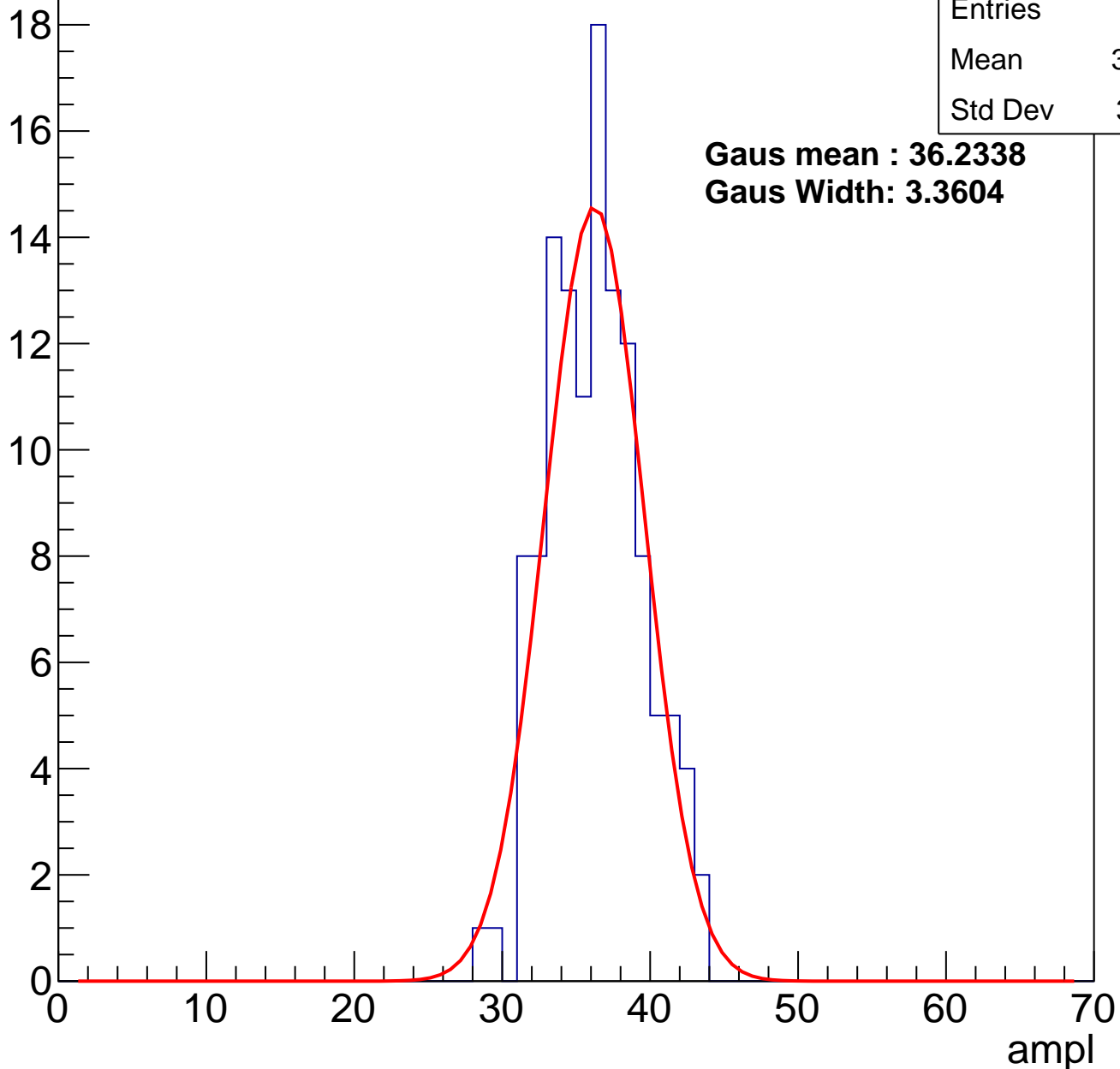
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.82
Std Dev	3.131

Gaus mean : 36.2338

Gaus Width: 3.3604

Entry



B1L001S, U16-ch37, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

128

Mean

41.75

Std Dev

3.48

Gaus mean : 42.2273

Gaus Width: 3.3531

0

10

20

30

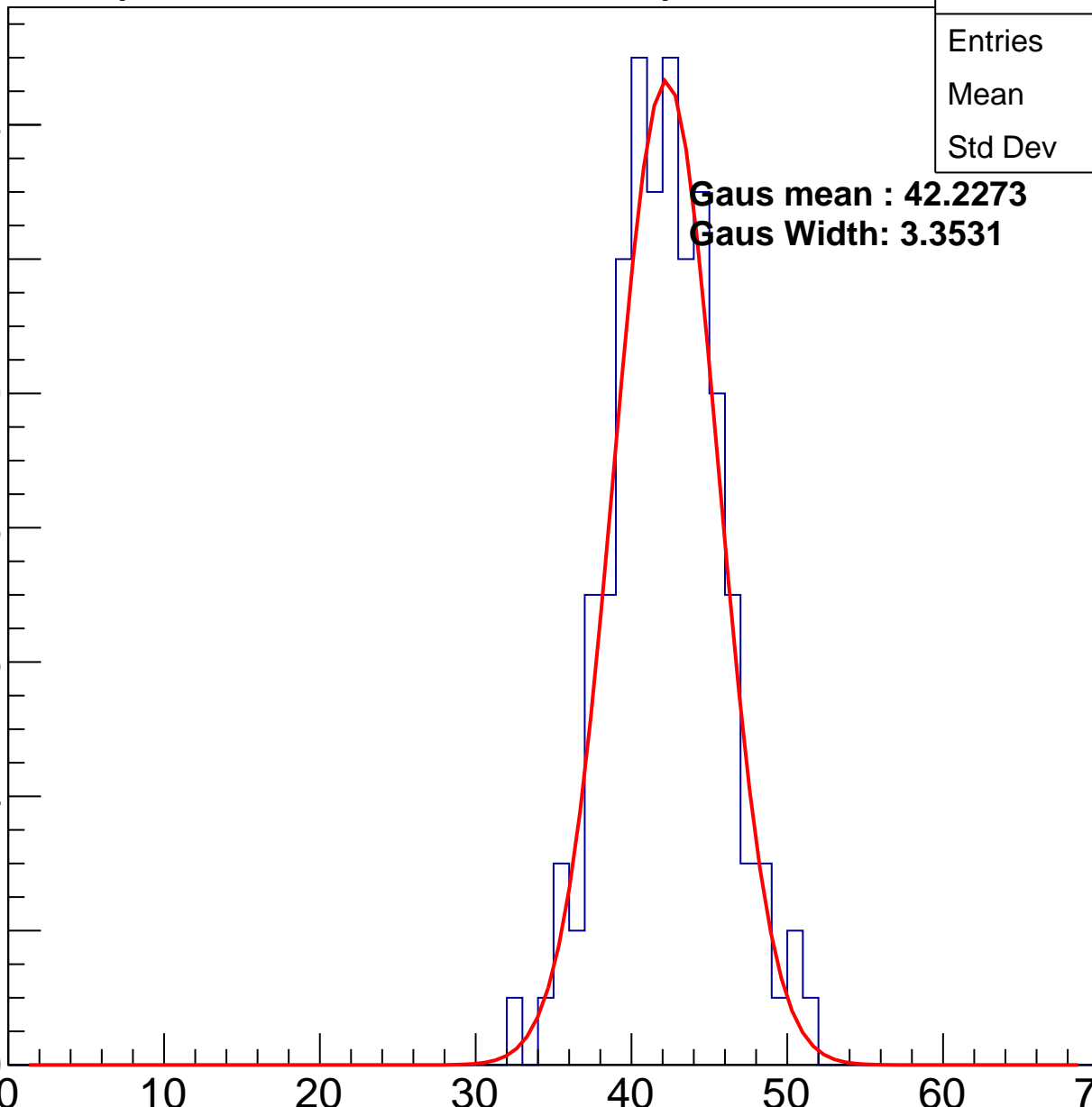
40

50

60

70

ampl



B1L001S, U16-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	48.29
Std Dev	3.153

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

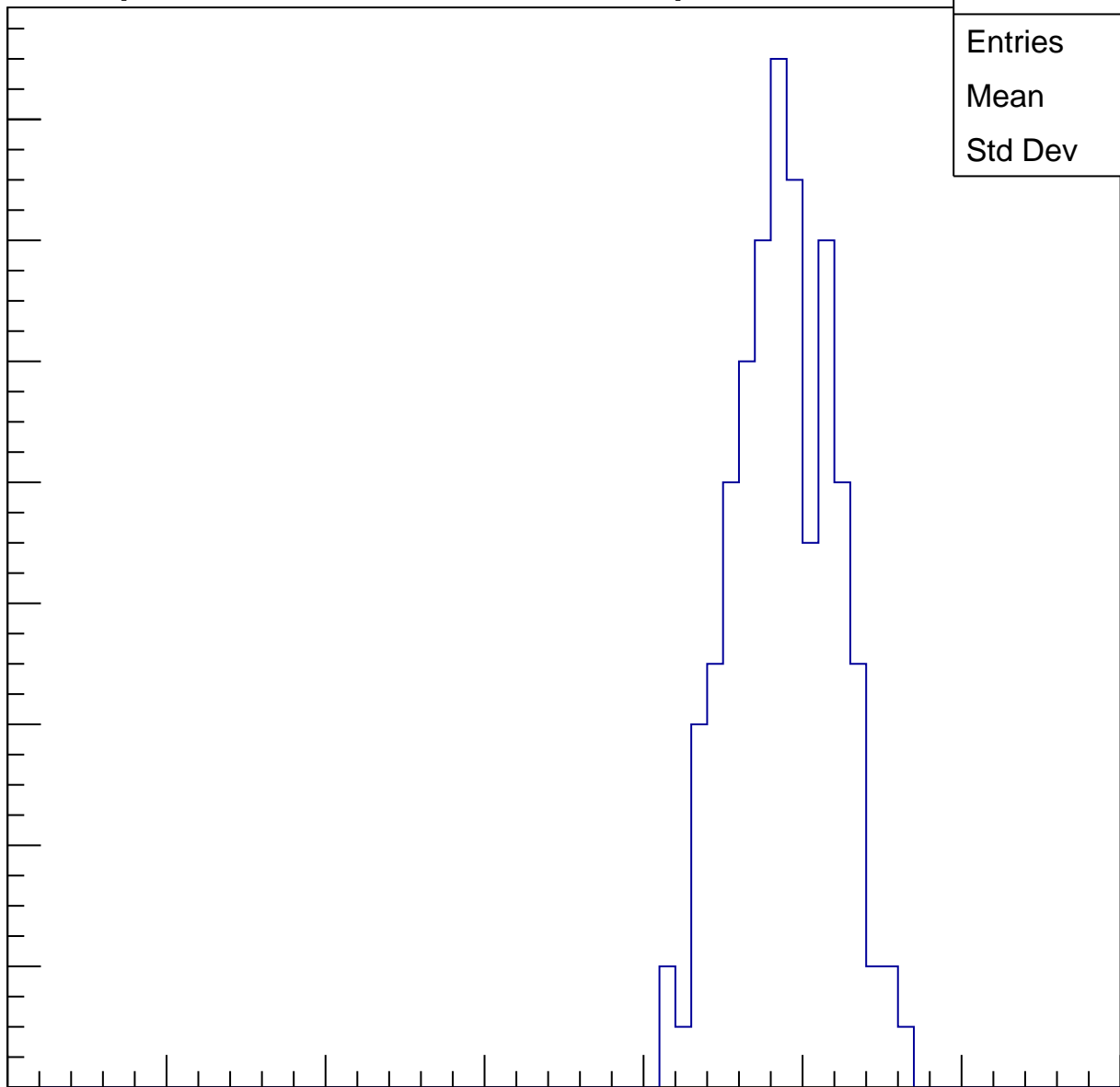
40

50

60

70

ampl



B1L001S, U16-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

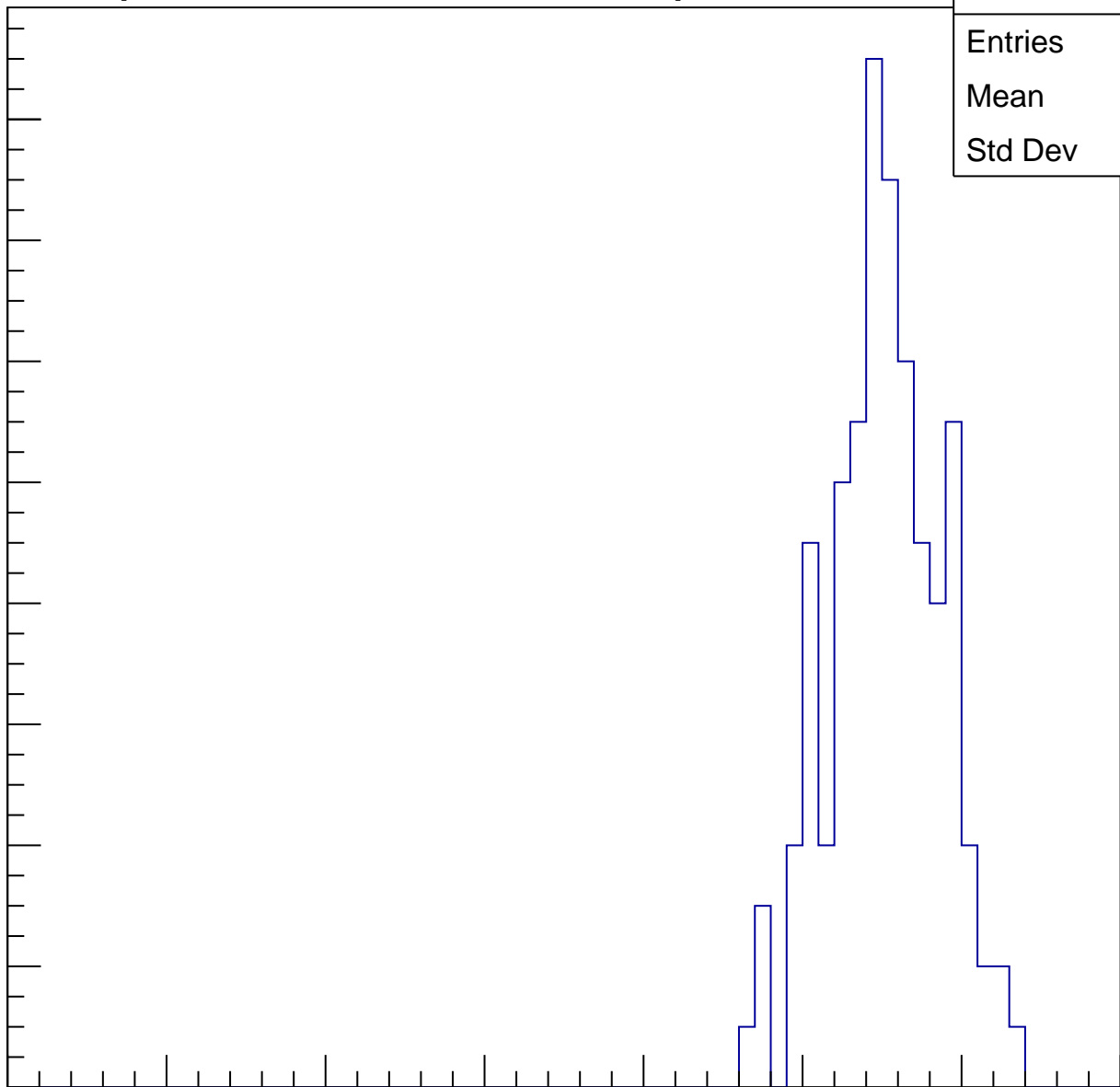
Entries	123
Mean	54.72
Std Dev	3.465

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch37, adc5

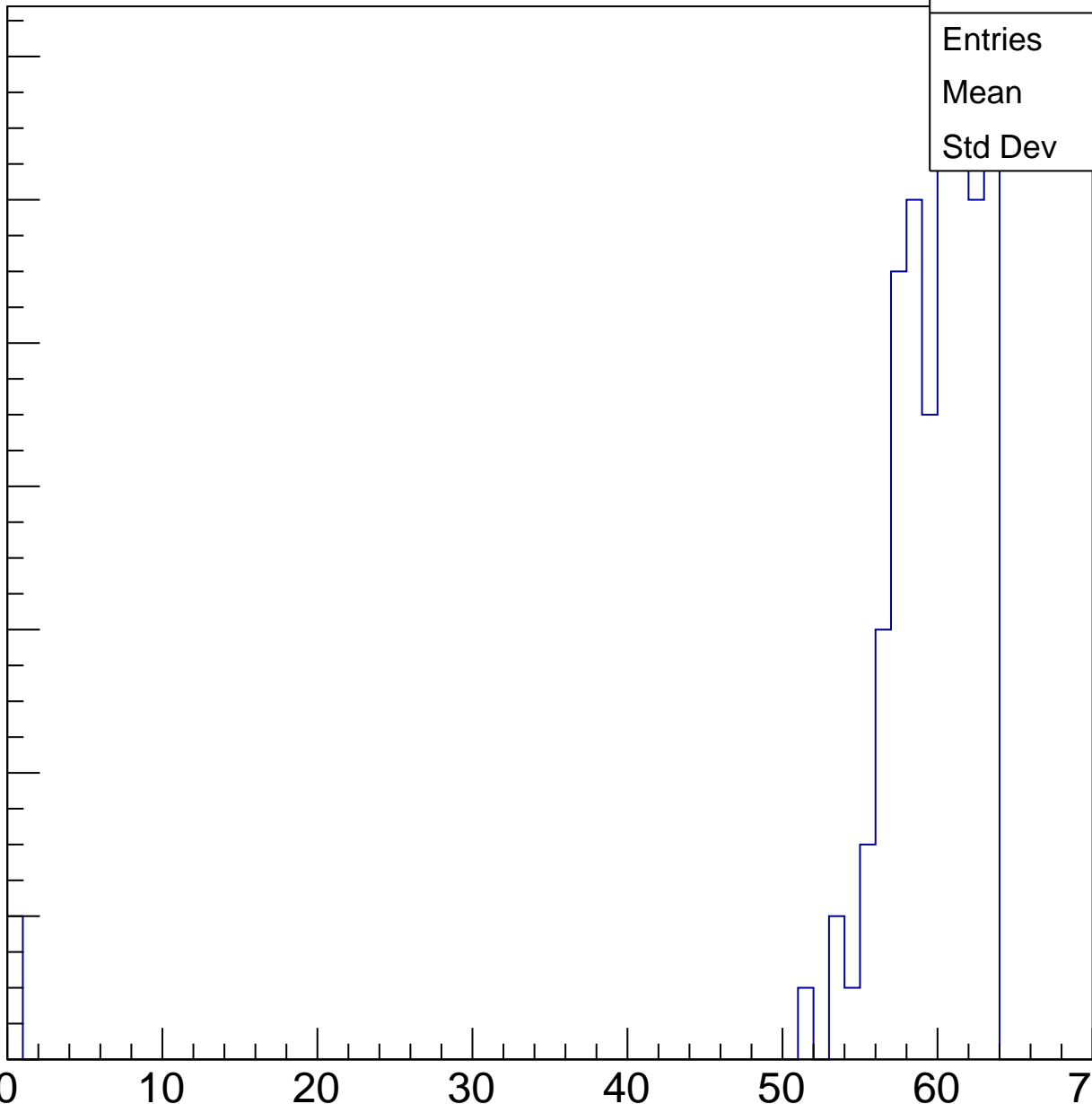
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	58.21
Std Dev	8.764

ampl

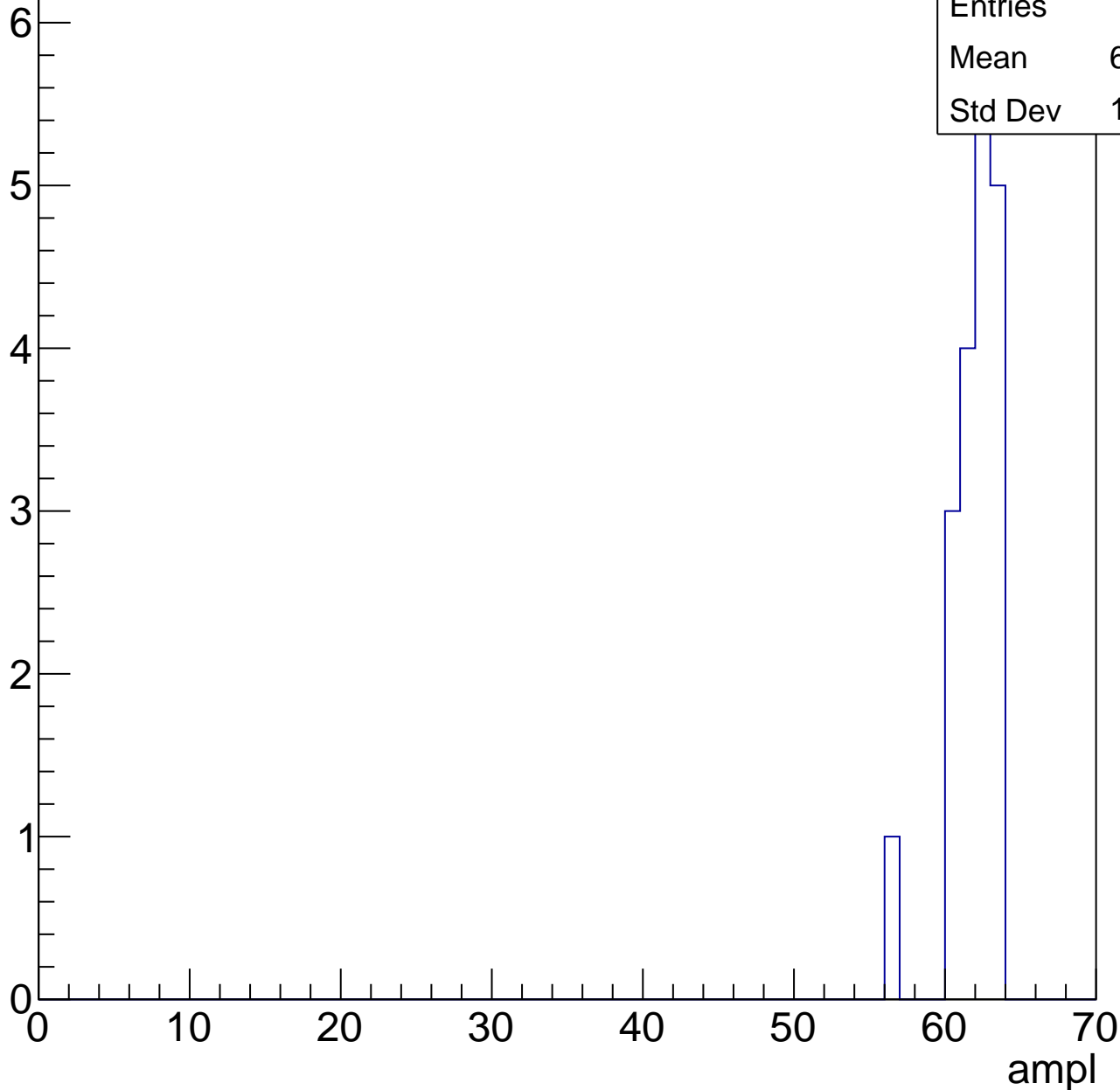


B1L001S, U16-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.42
Std Dev	1.632



B1L001S, U16-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	30.2
Std Dev	5.221

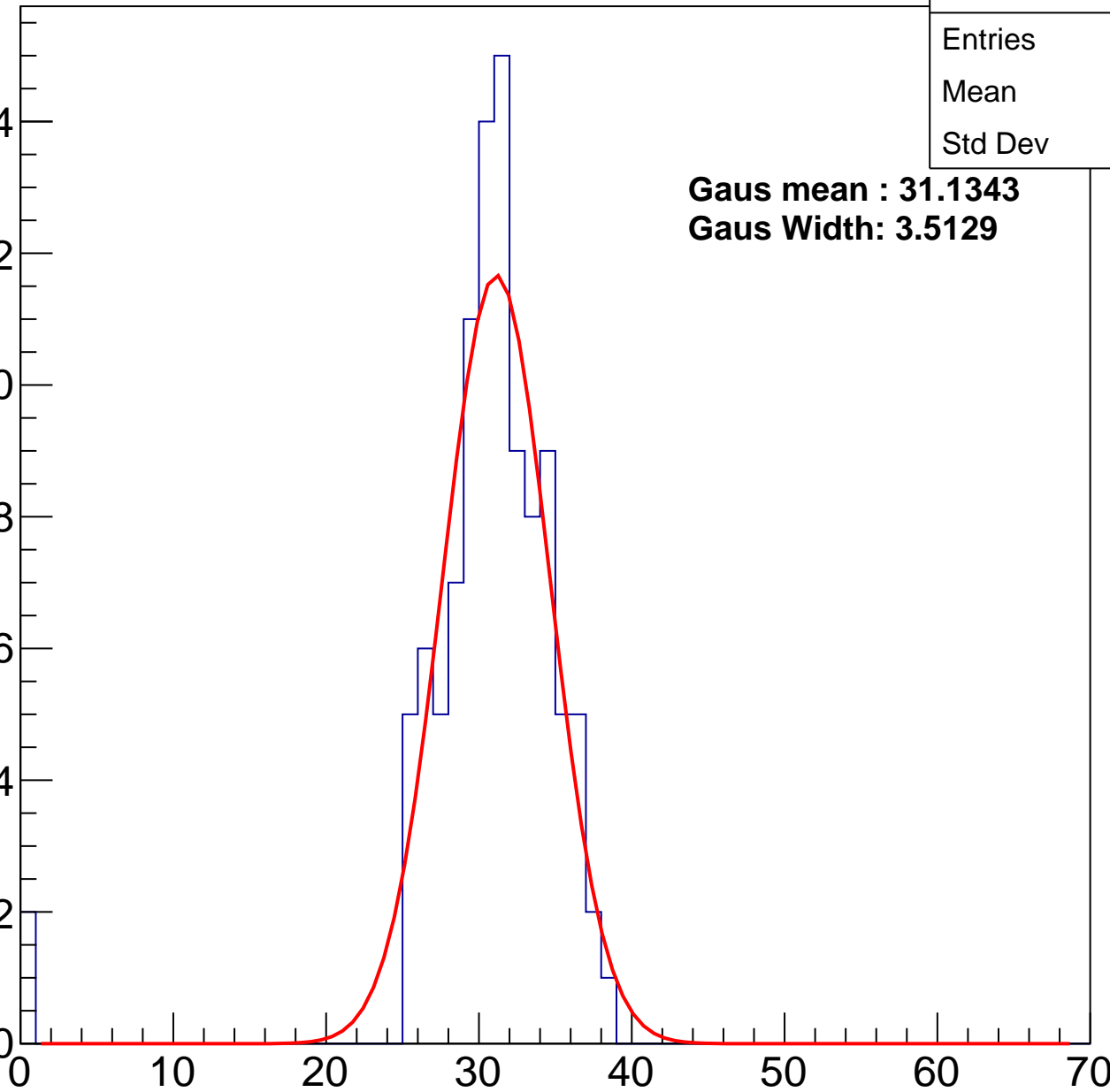
Gaus mean : 31.1343

Gaus Width: 3.5129

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch38, adc1

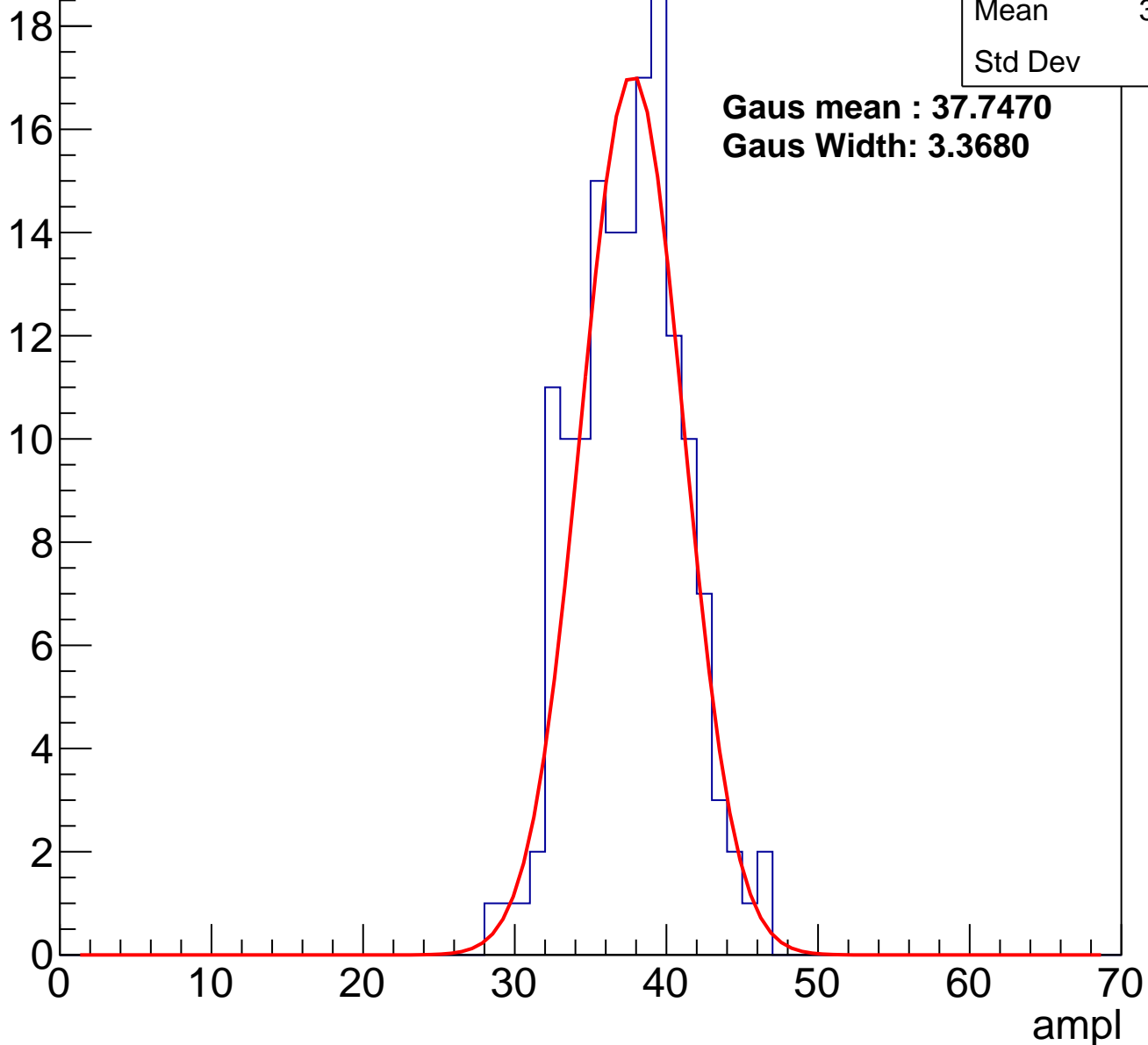
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	37.12
Std Dev	3.47

Gaus mean : 37.7470

Gaus Width: 3.3680

Entry



B1L001S, U16-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

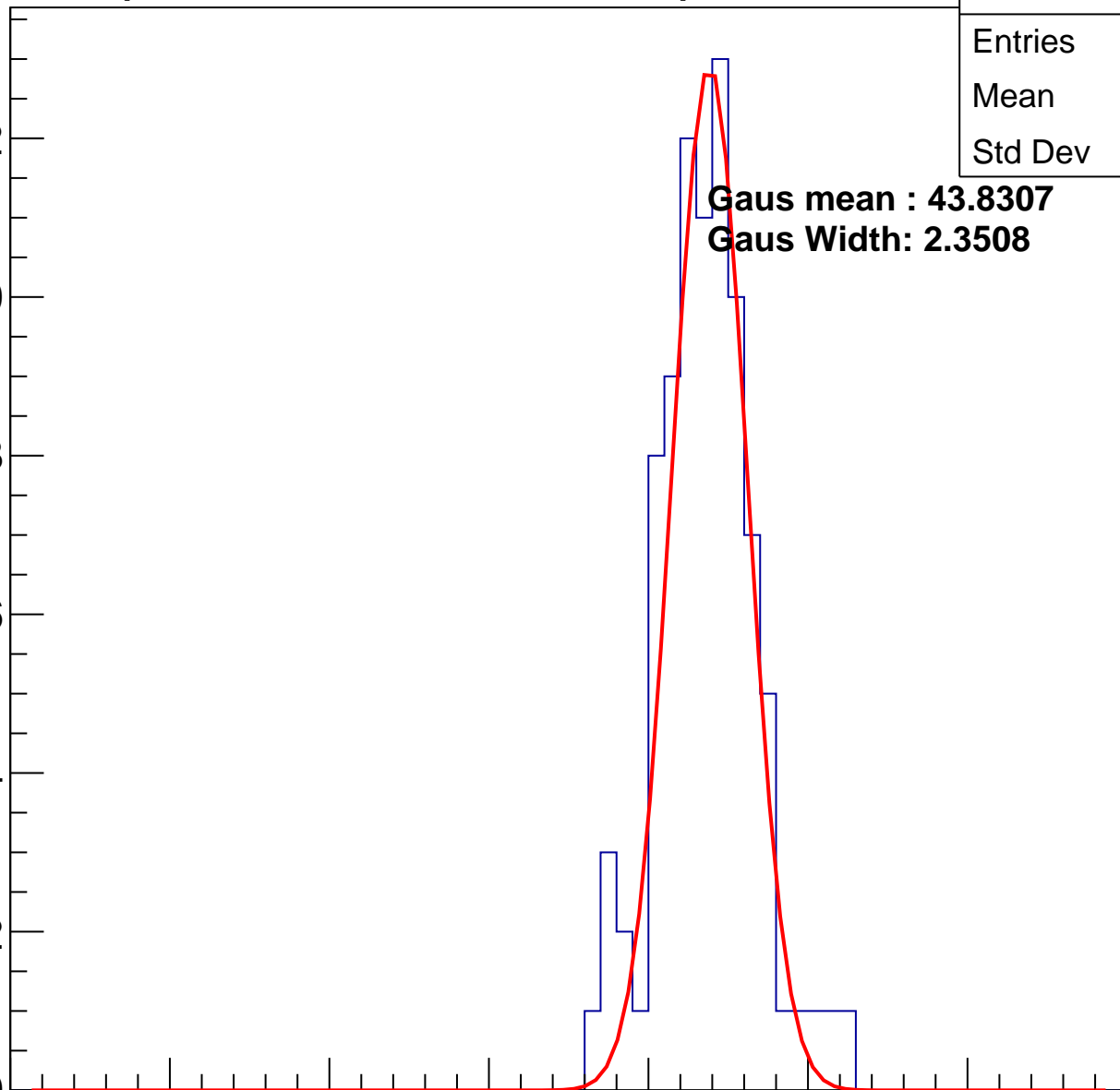
70

ampl

Entries	87
Mean	43.18
Std Dev	3.015

Gaus mean : 43.8307

Gaus Width: 2.3508



B1L001S, U16-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	49.1
Std Dev	3.506

Entry

16

14

12

10

8

6

4

2

0

0

10

20

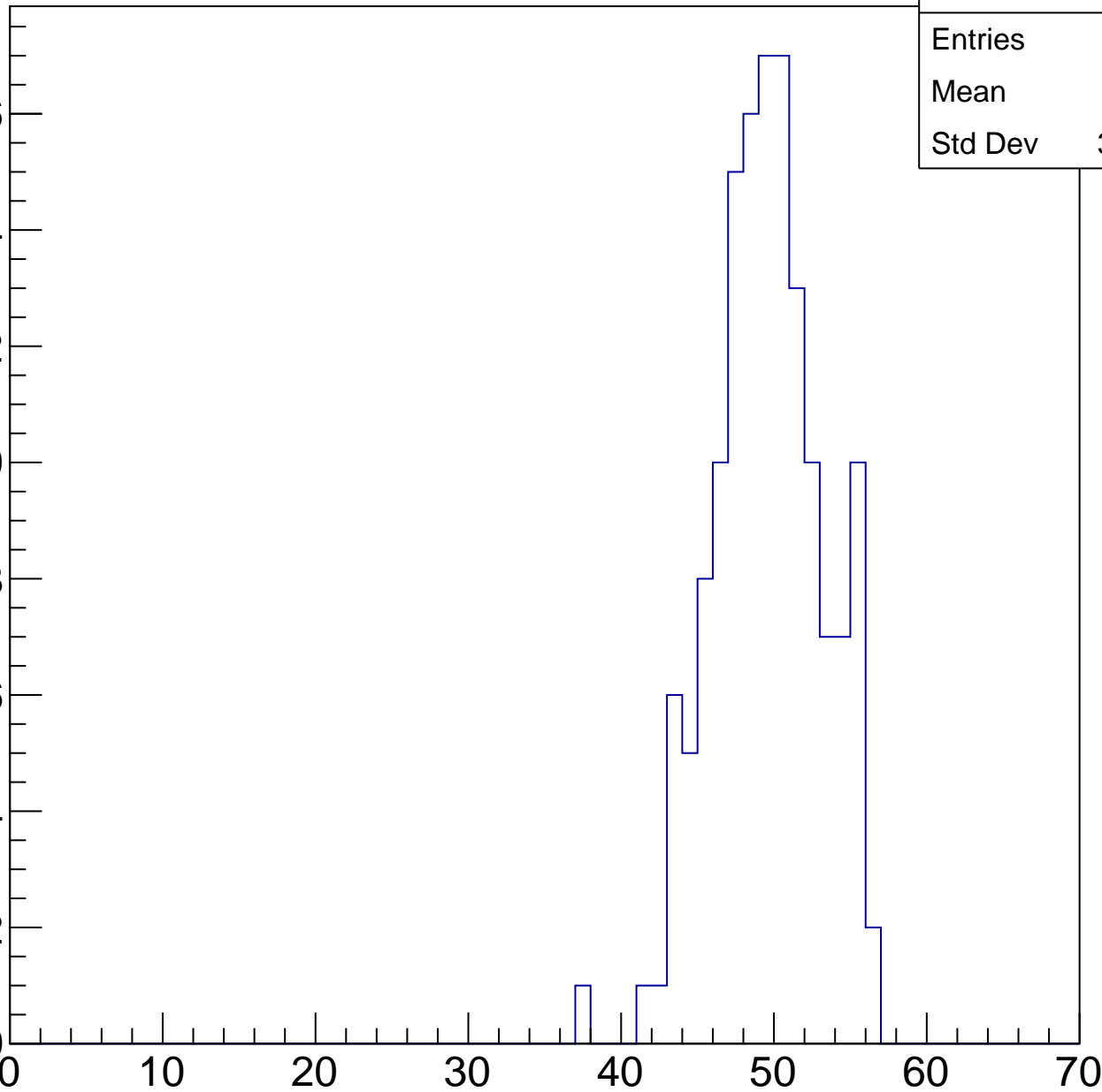
30

40

50

60

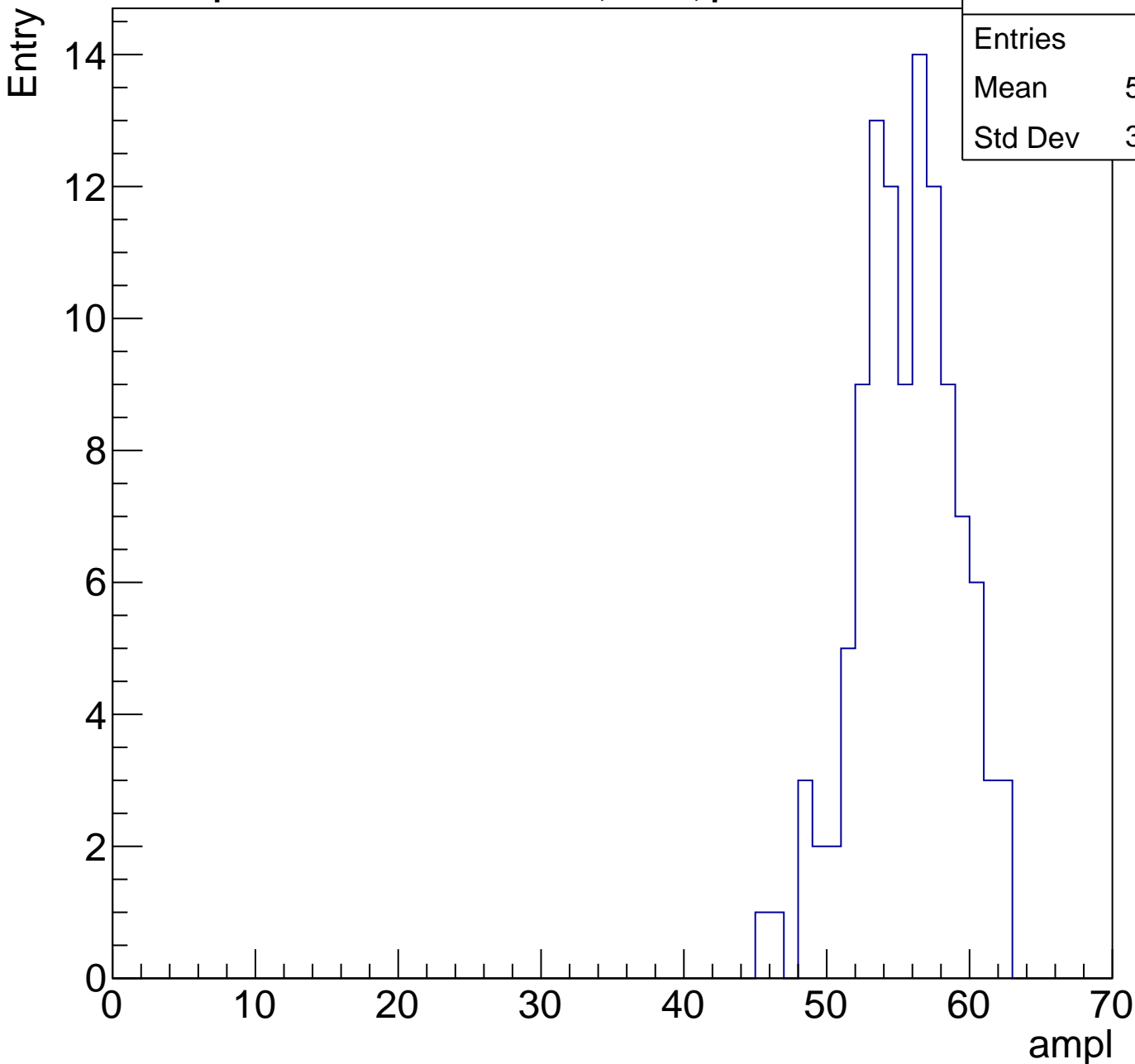
ampl



B1L001S, U16-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

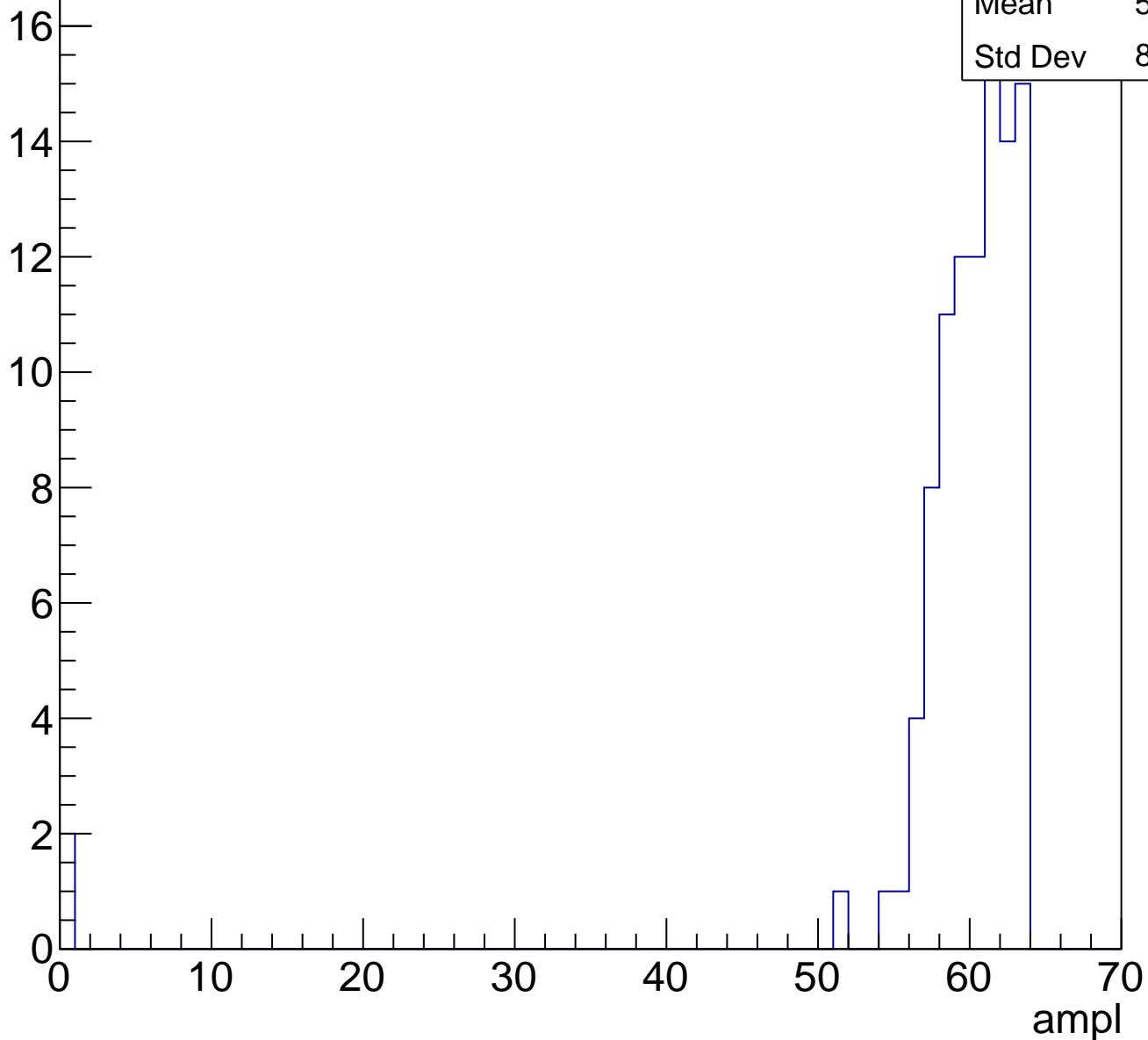
Entries	111
Mean	55.14
Std Dev	3.465



B1L001S, U16-ch38, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

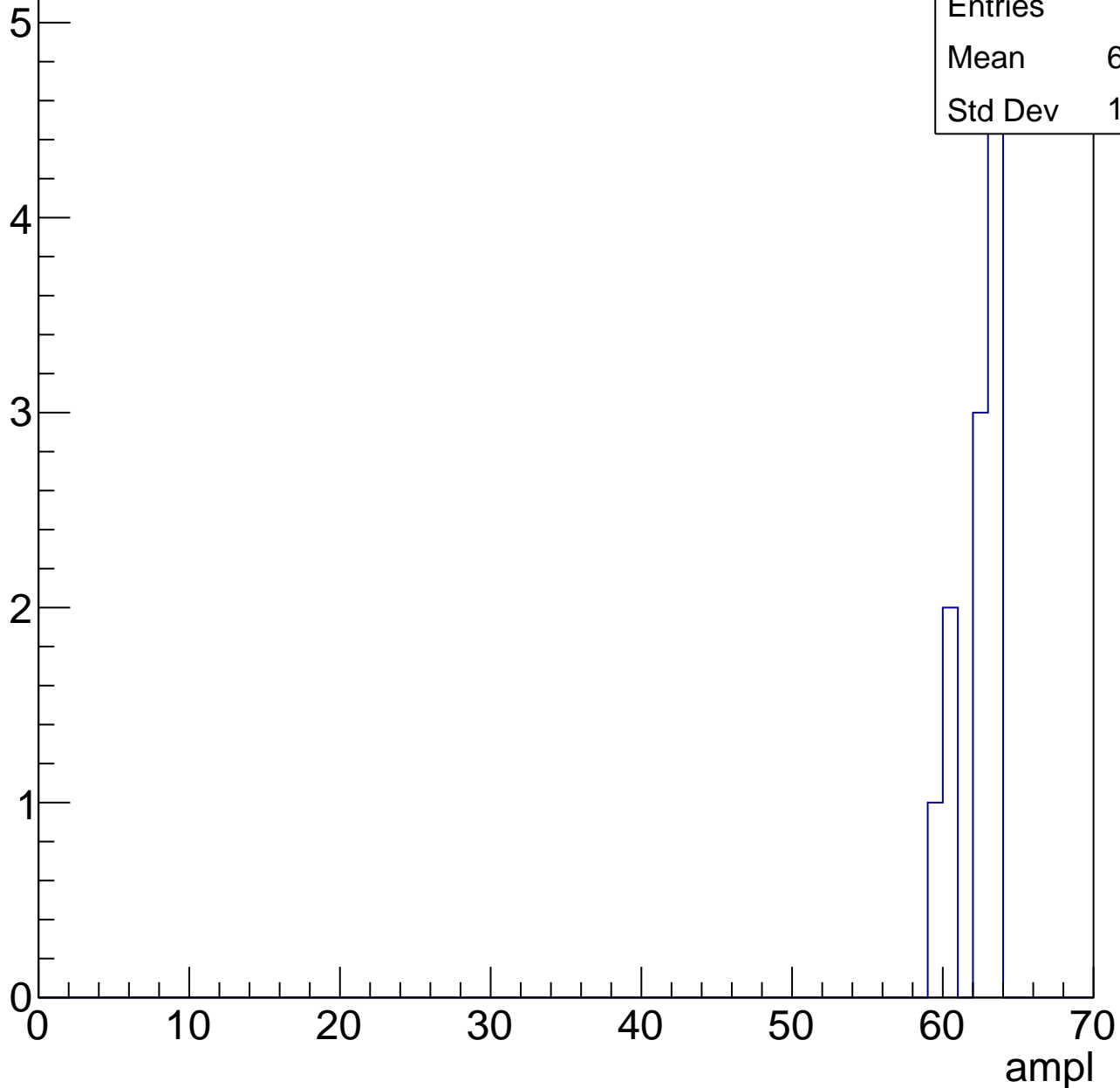


B1L001S, U16-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	61.82
Std Dev	1.402



B1L001S, U16-ch38, adc7

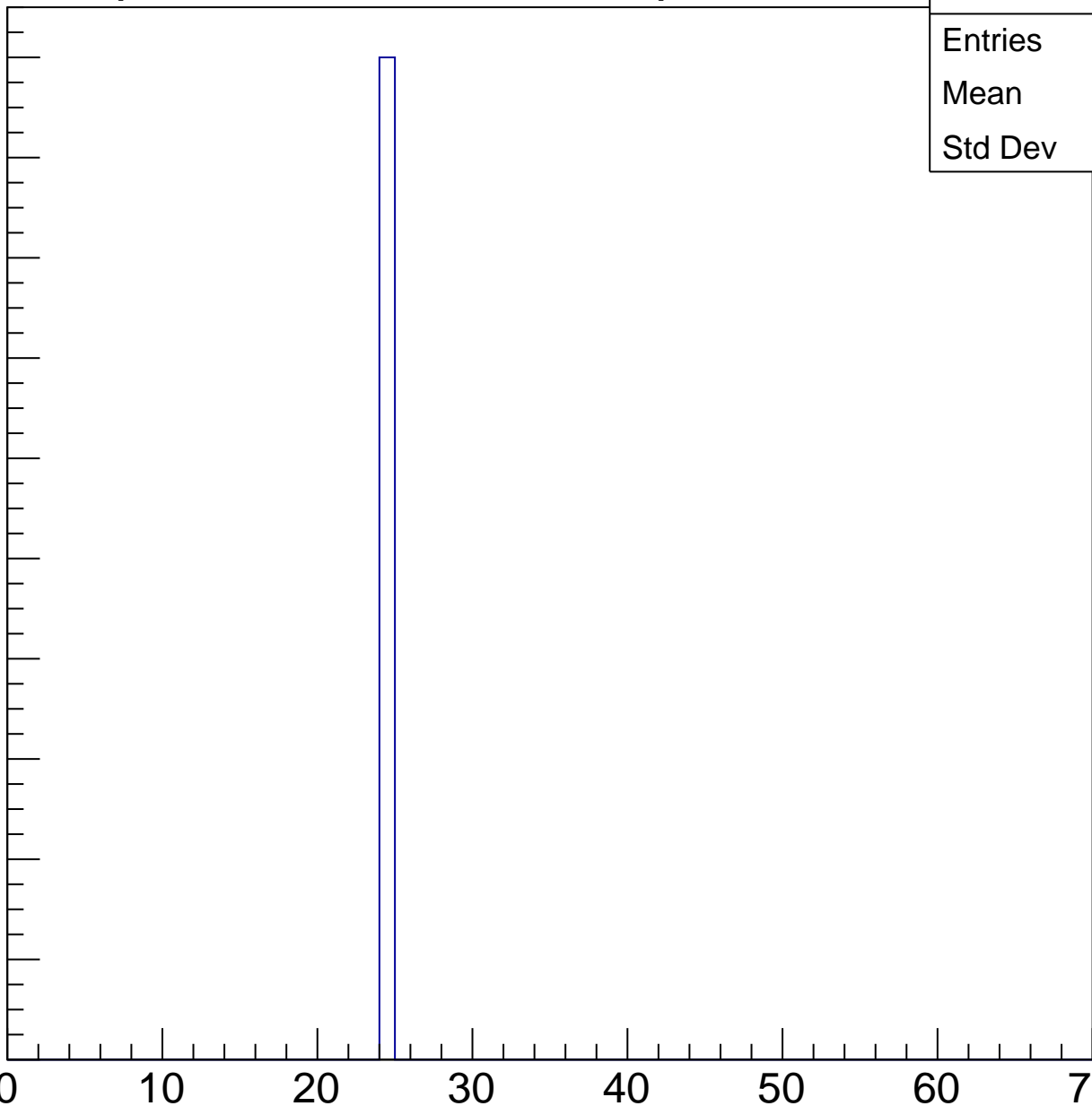
calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	24
Std Dev	0

ampl



B1L001S, U16-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	29.82
Std Dev	3.254

Gaus mean : 30.1698

Gaus Width: 3.2209

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

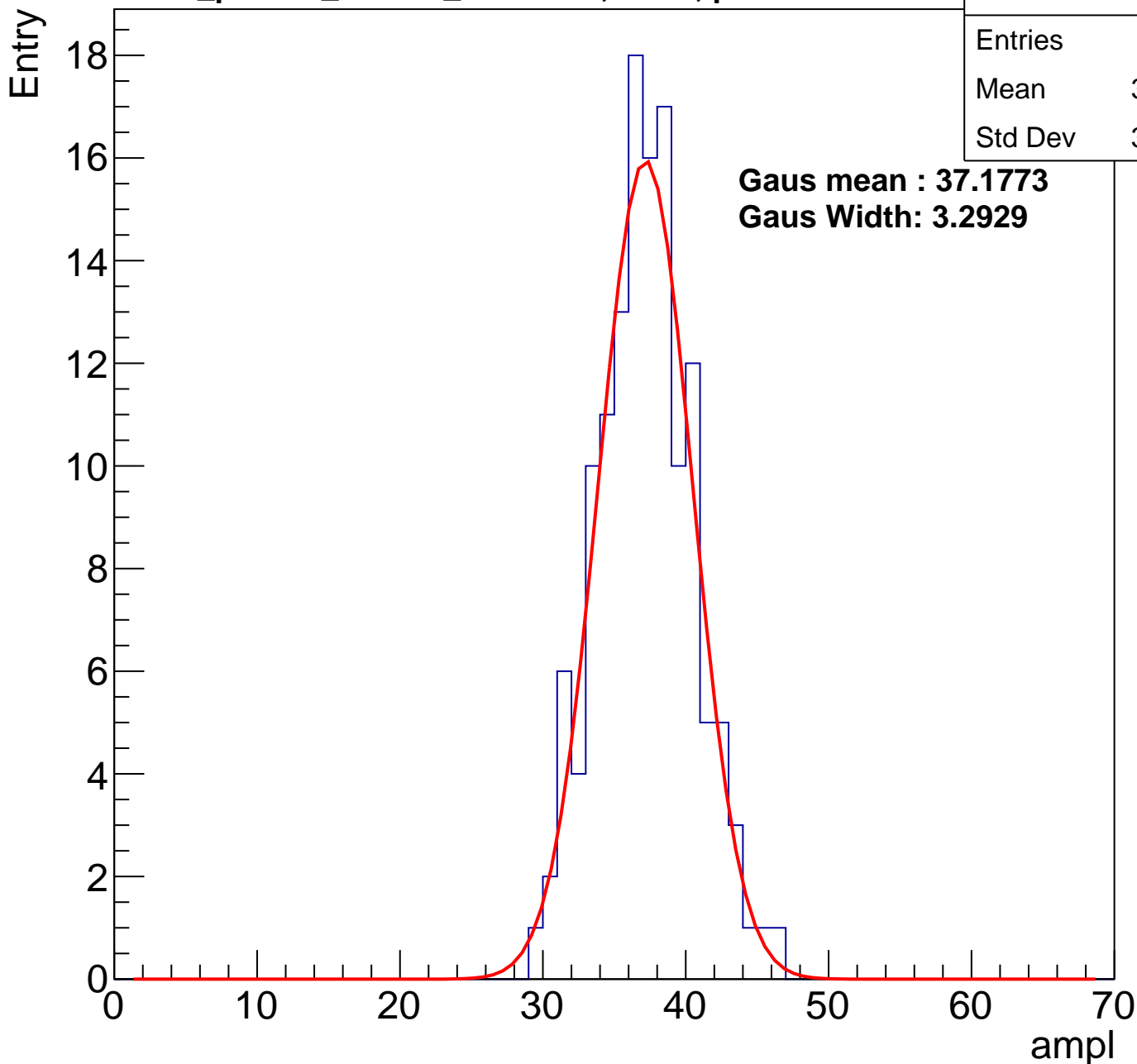
B1L001S, U16-ch39, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.74
Std Dev	3.288

Gaus mean : 37.1773

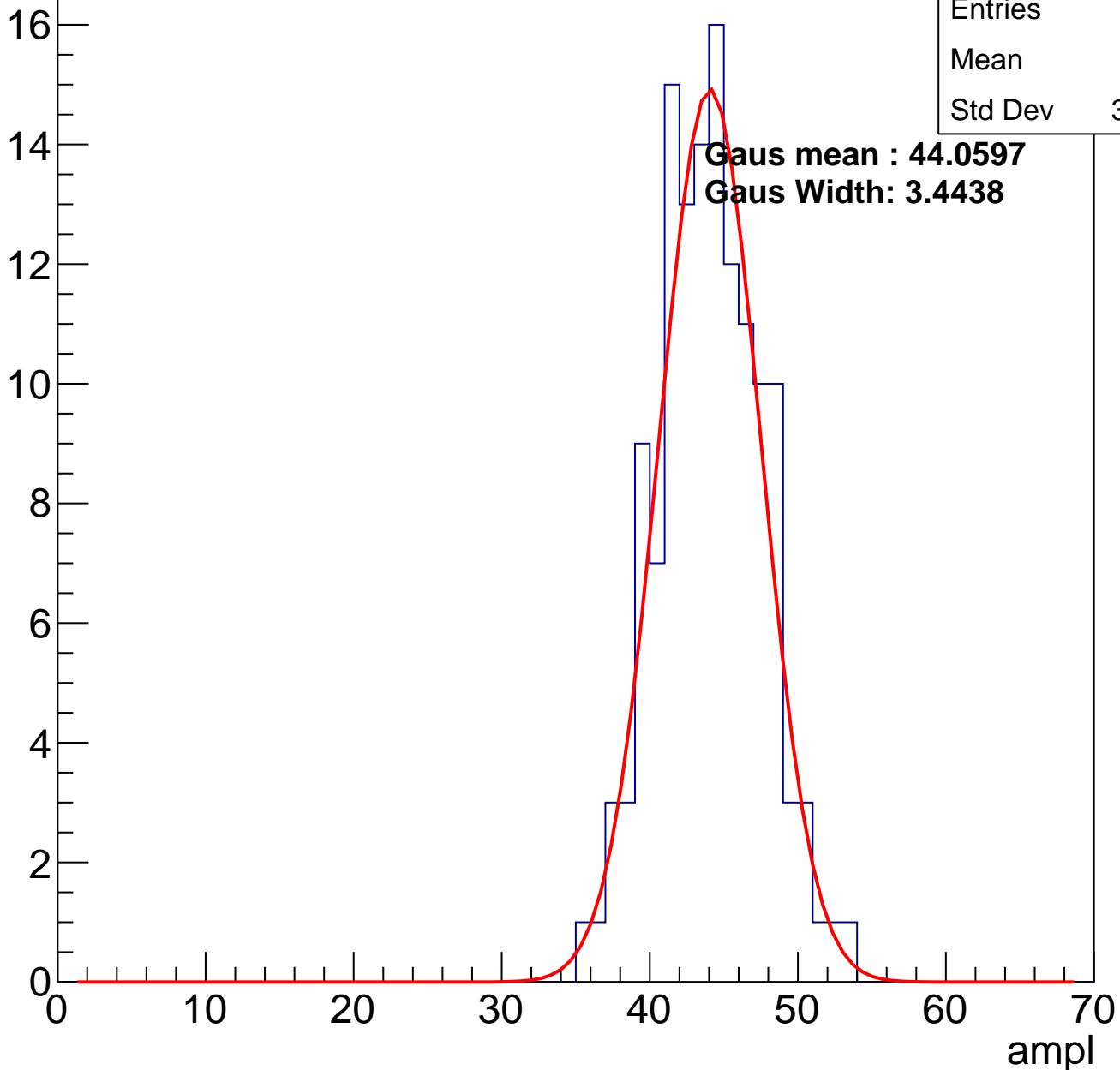
Gaus Width: 3.2929



B1L001S, U16-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

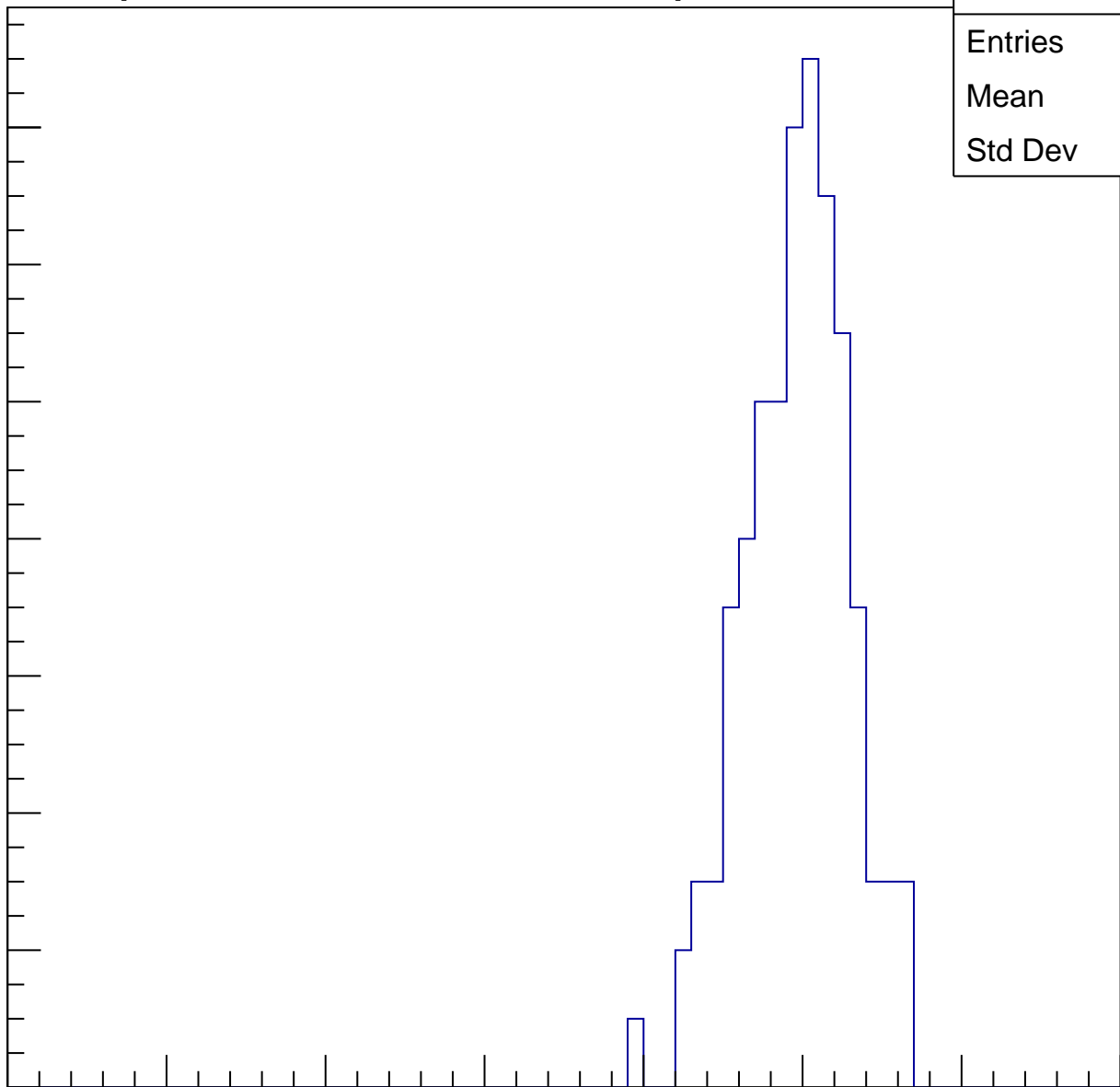
Entries	113
Mean	49.15
Std Dev	3.286

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

148

Mean

56.11

Std Dev

3.148

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch39, adc5

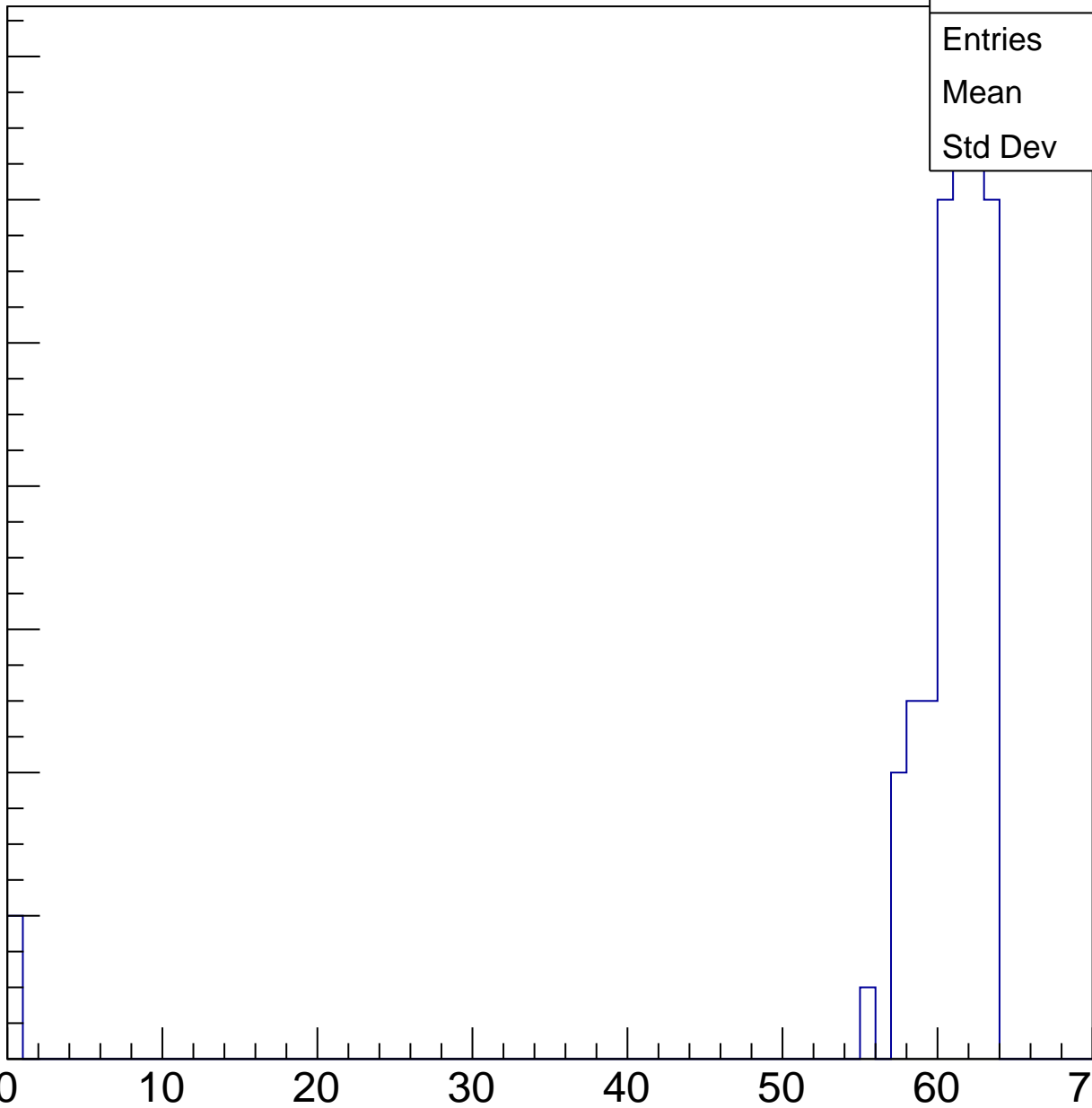
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.9
Std Dev	10.42

ampl

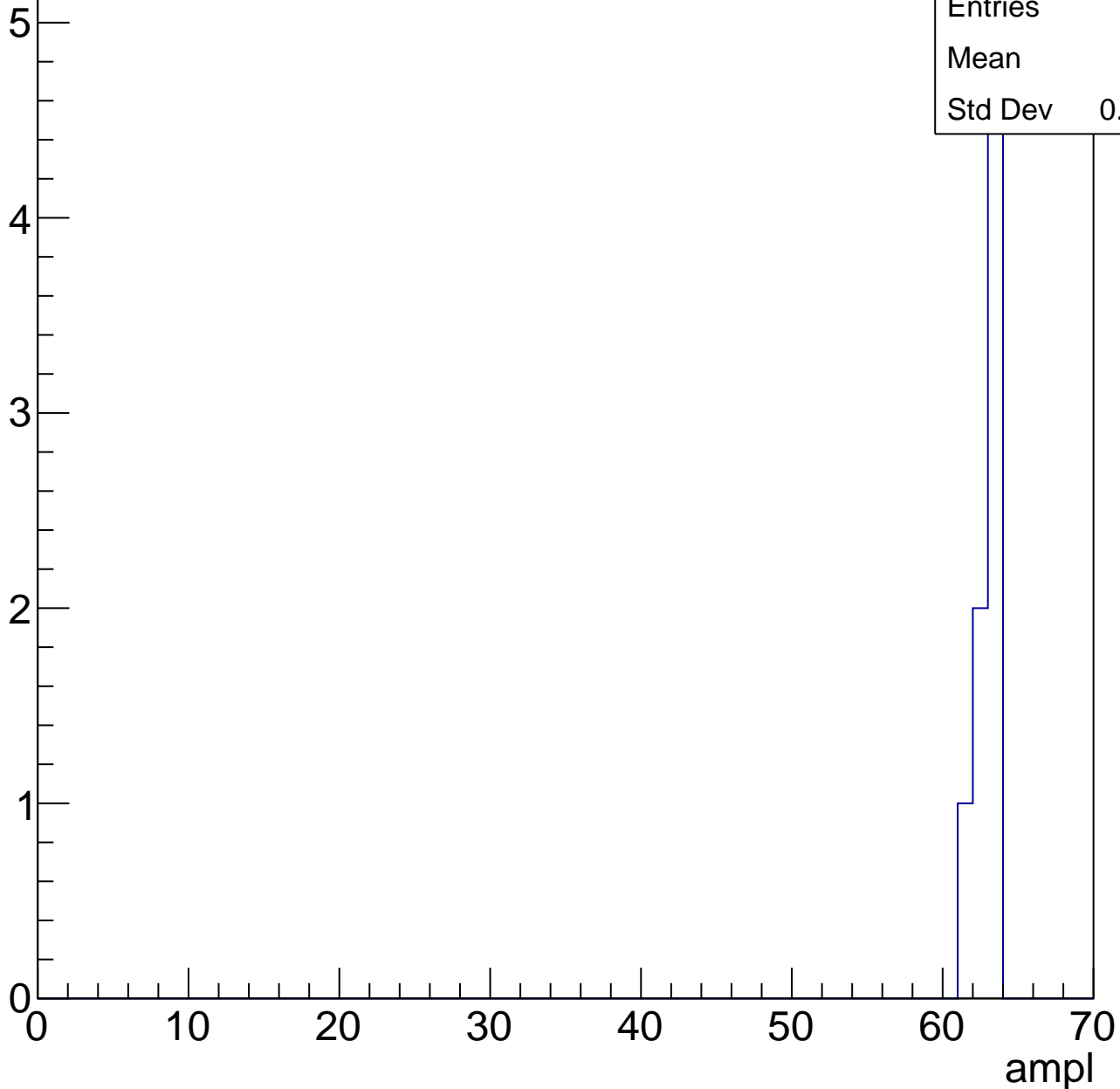


B1L001S, U16-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

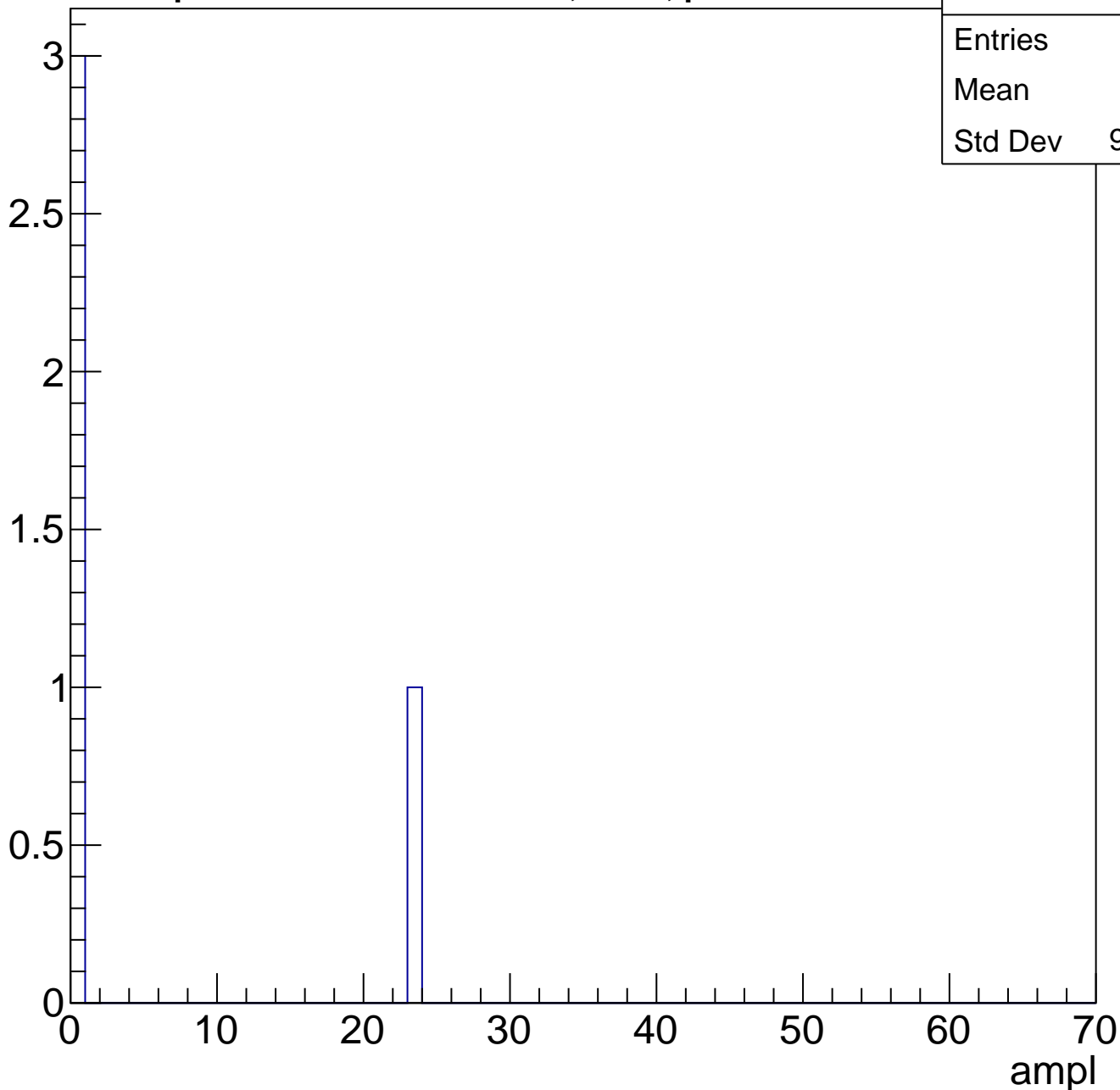
Entries	8
Mean	62.5
Std Dev	0.7071



B1L001S, U16-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	5.75
Std Dev	9.959

B1L001S, U16-ch40, adc0

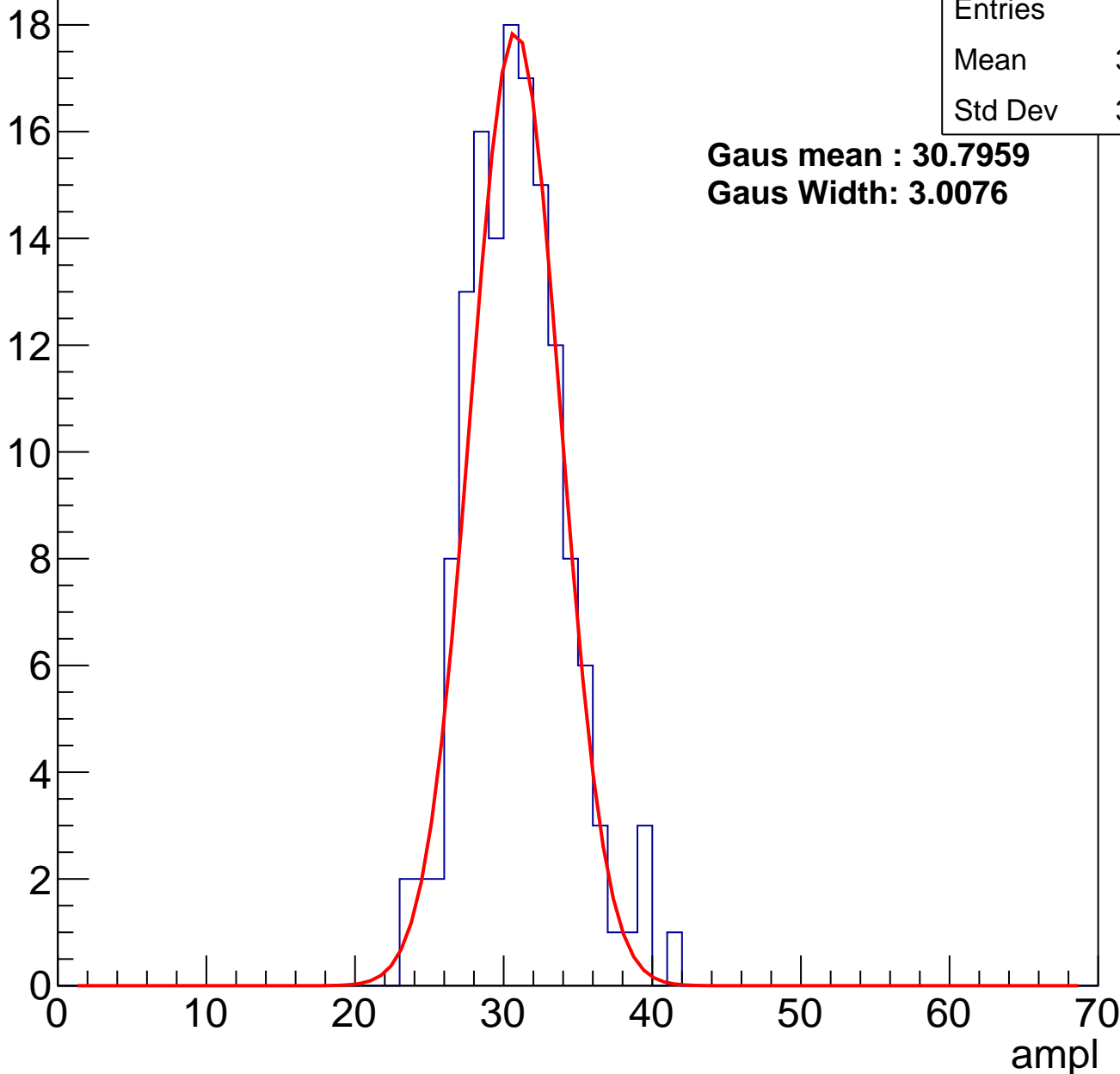
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.44
Std Dev	3.318

Gaus mean : 30.7959

Gaus Width: 3.0076

Entry



B1L001S, U16-ch40, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	36.34
Std Dev	3.11

Gaus mean : 36.8037

Gaus Width: 2.8807

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

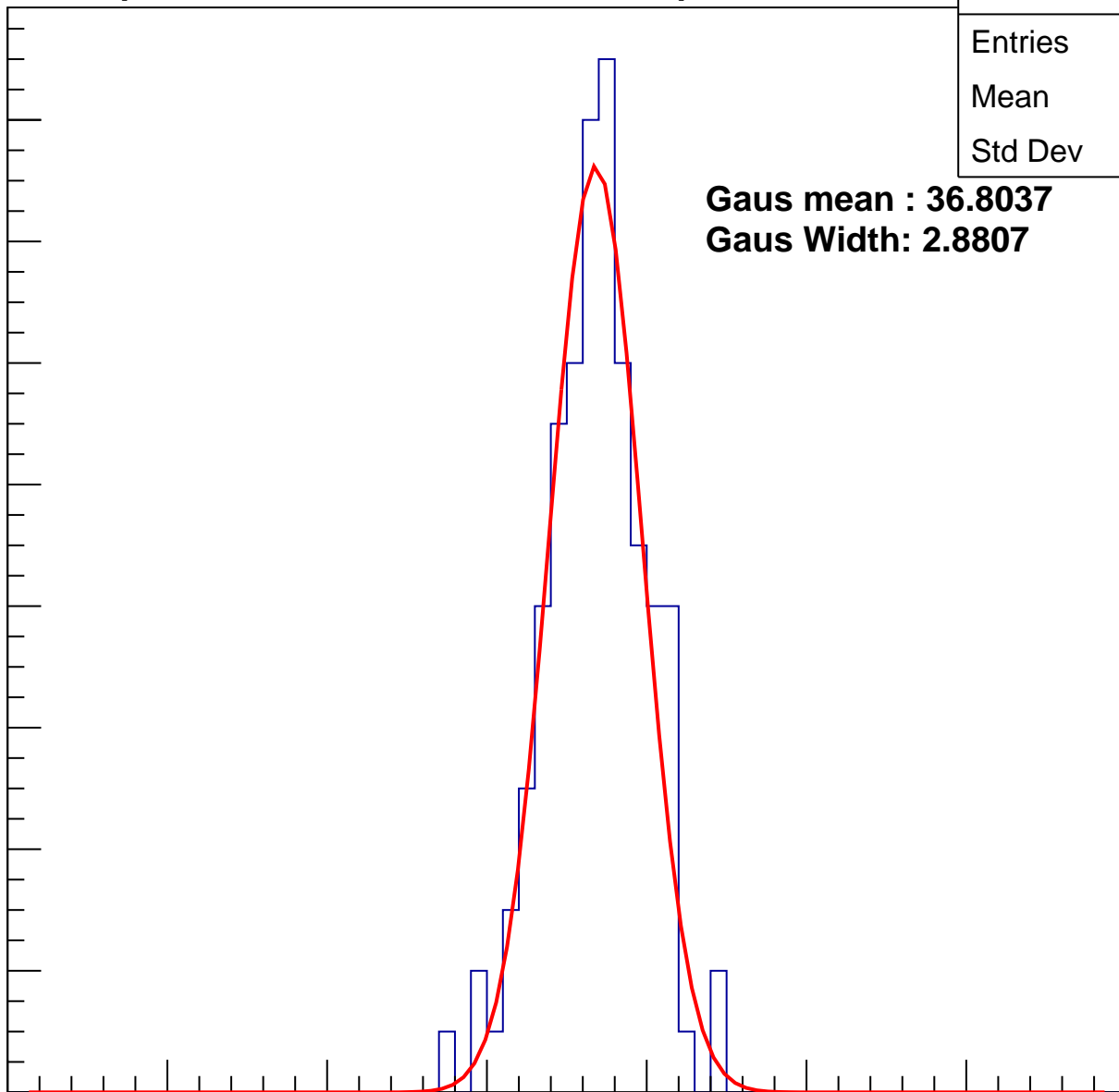
40

50

60

70

ampl



B1L001S, U16-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

123

Mean

42.6

Std Dev

3.307

Gaus mean : 43.0917

Gaus Width: 3.0648

0

10

20

30

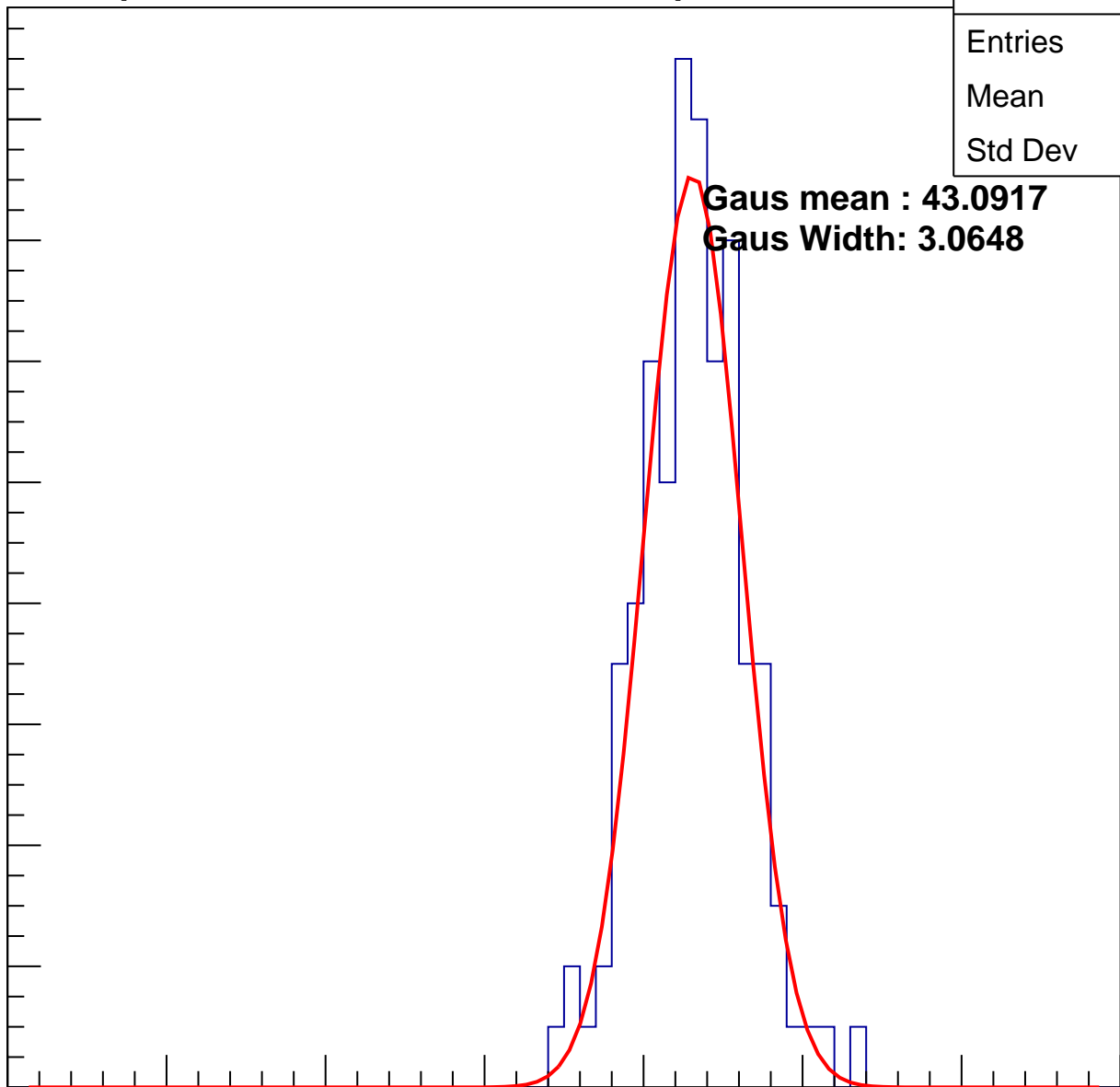
40

50

60

70

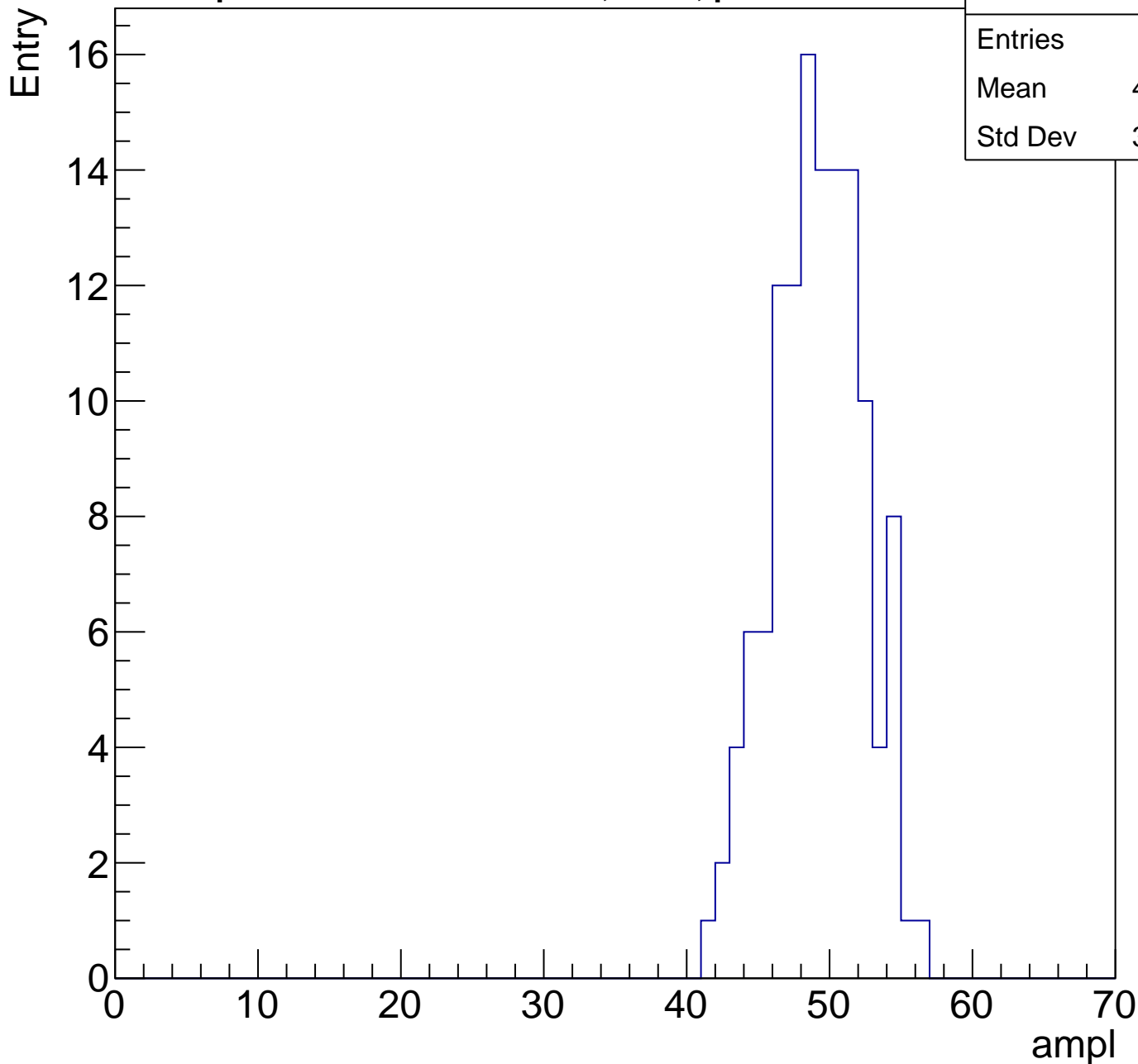
ampl



B1L001S, U16-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

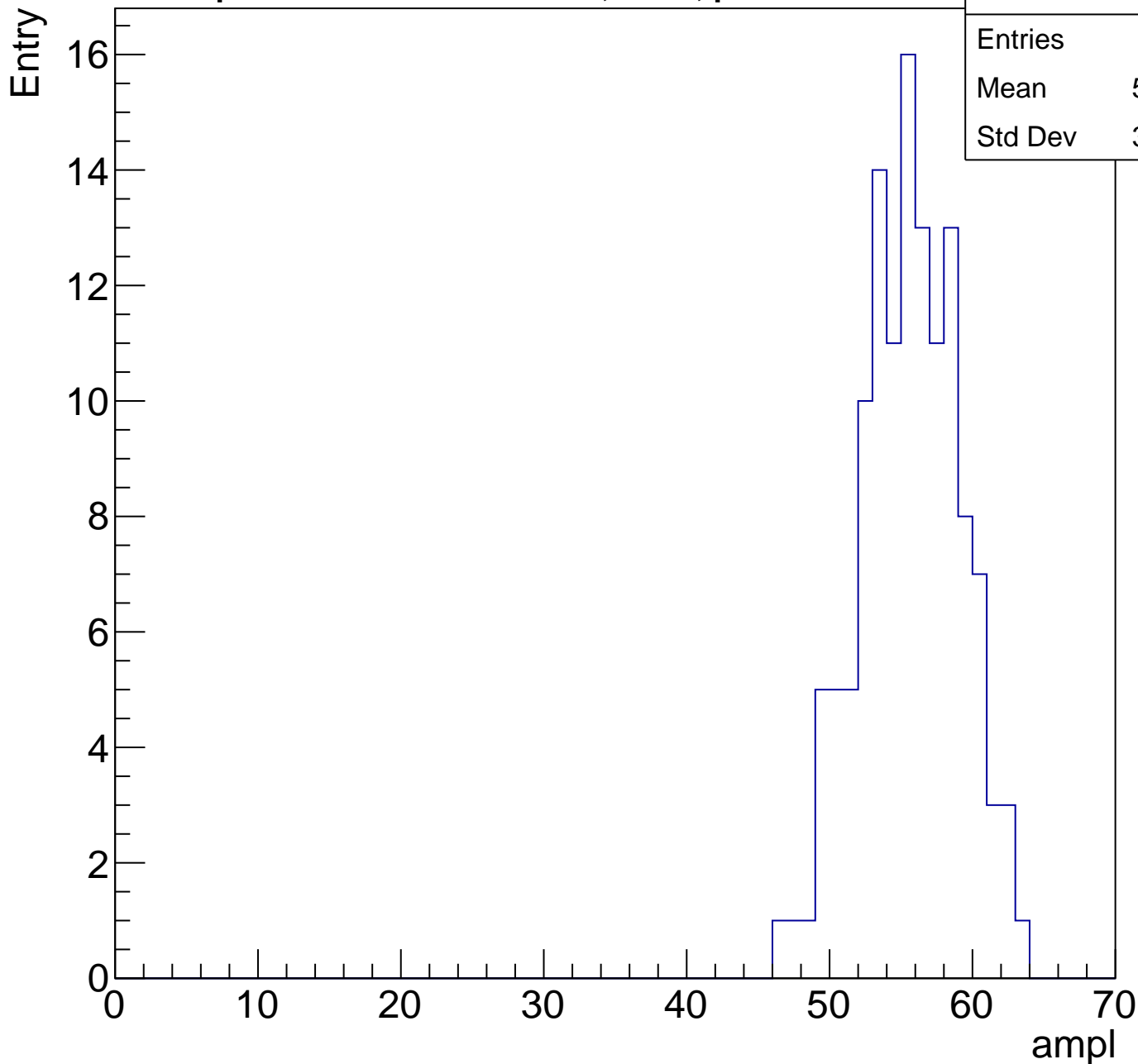
Entries	125
Mean	48.72
Std Dev	3.128



B1L001S, U16-ch40, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	55.16
Std Dev	3.456



B1L001S, U16-ch40, adc5

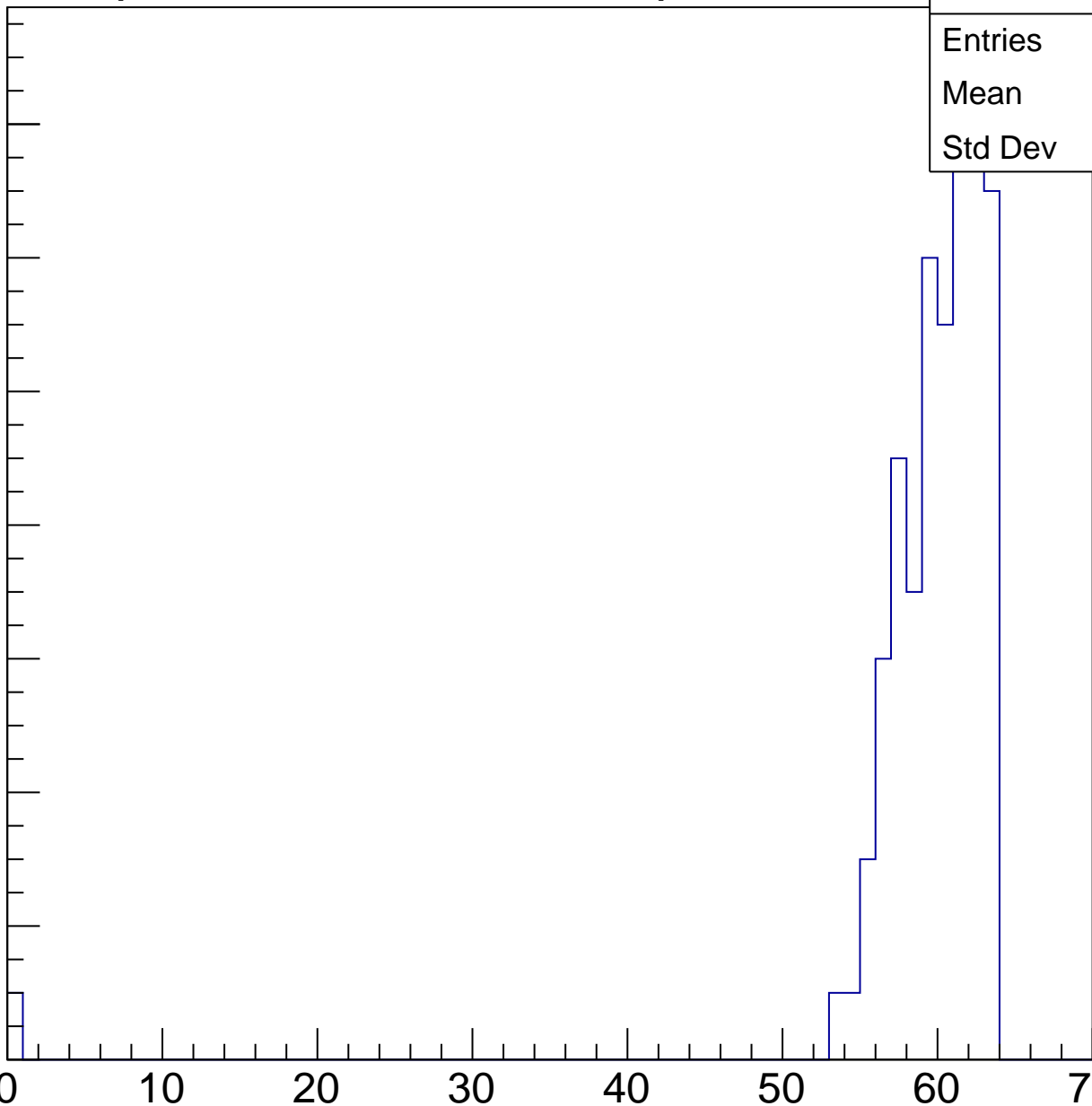
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	59.14
Std Dev	6.604

ampl

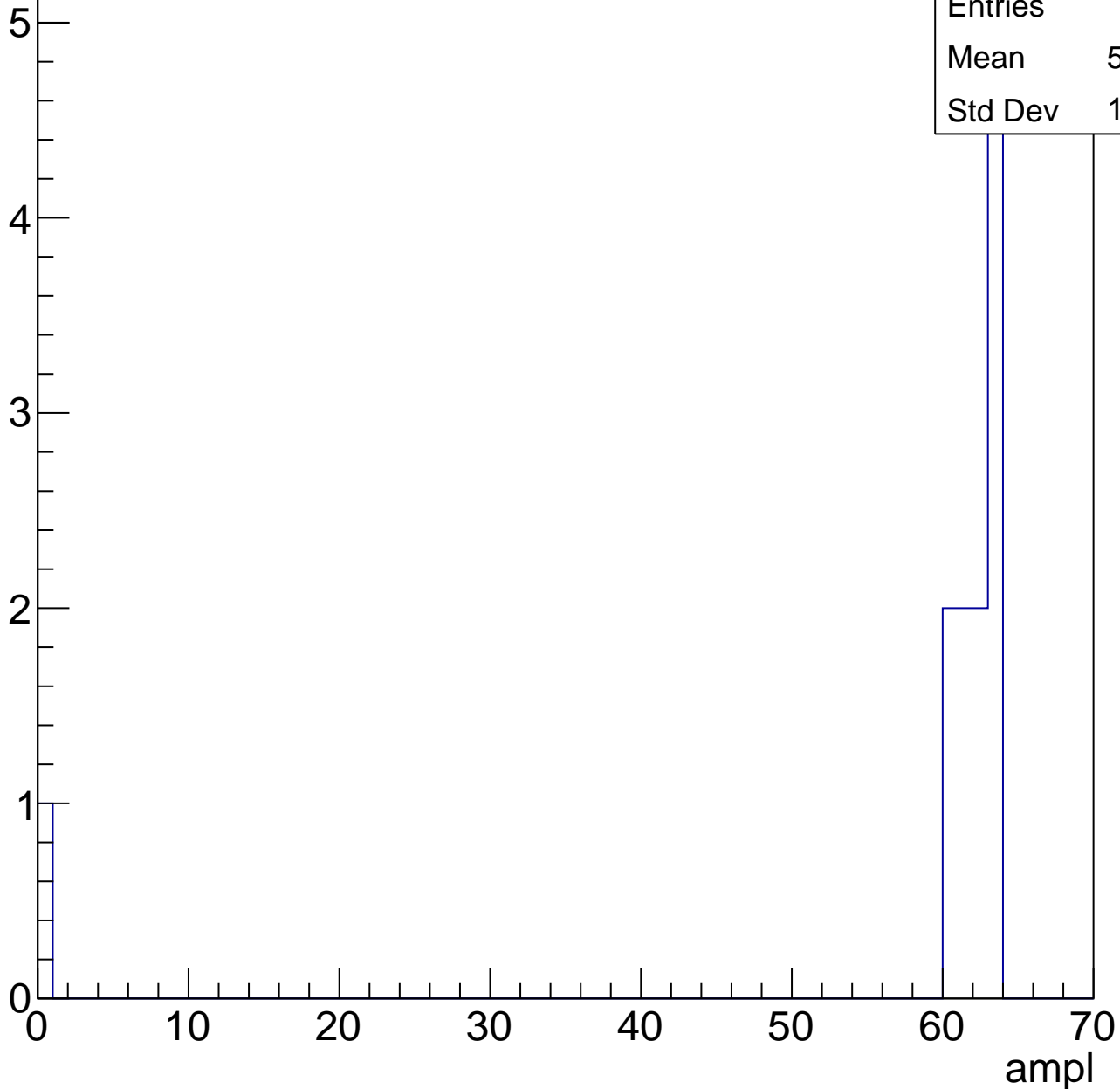


B1L001S, U16-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	56.75
Std Dev	17.15



B1L001S, U16-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



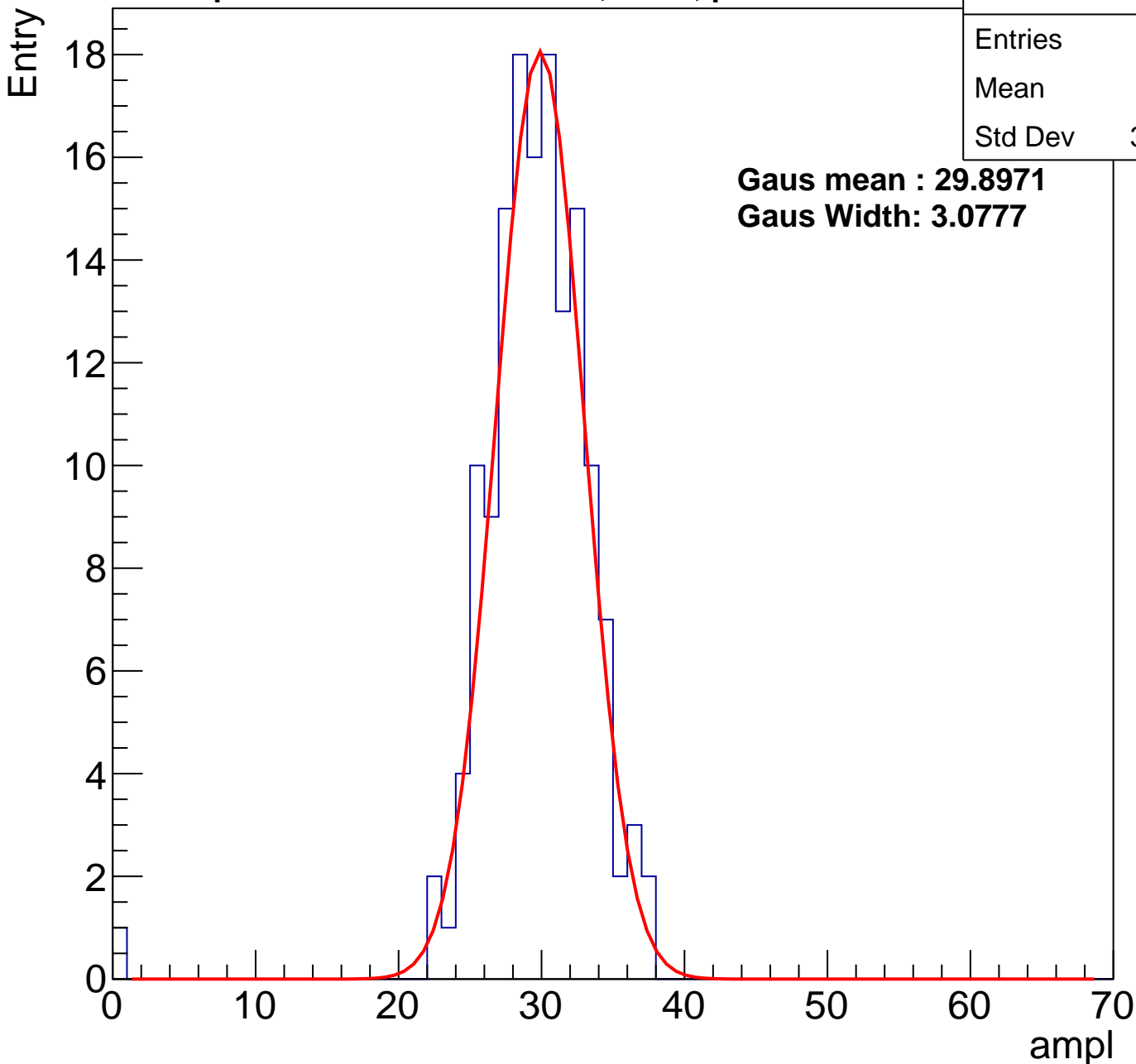
B1L001S, U16-ch41, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.2
Std Dev	3.948

Gaus mean : 29.8971

Gaus Width: 3.0777



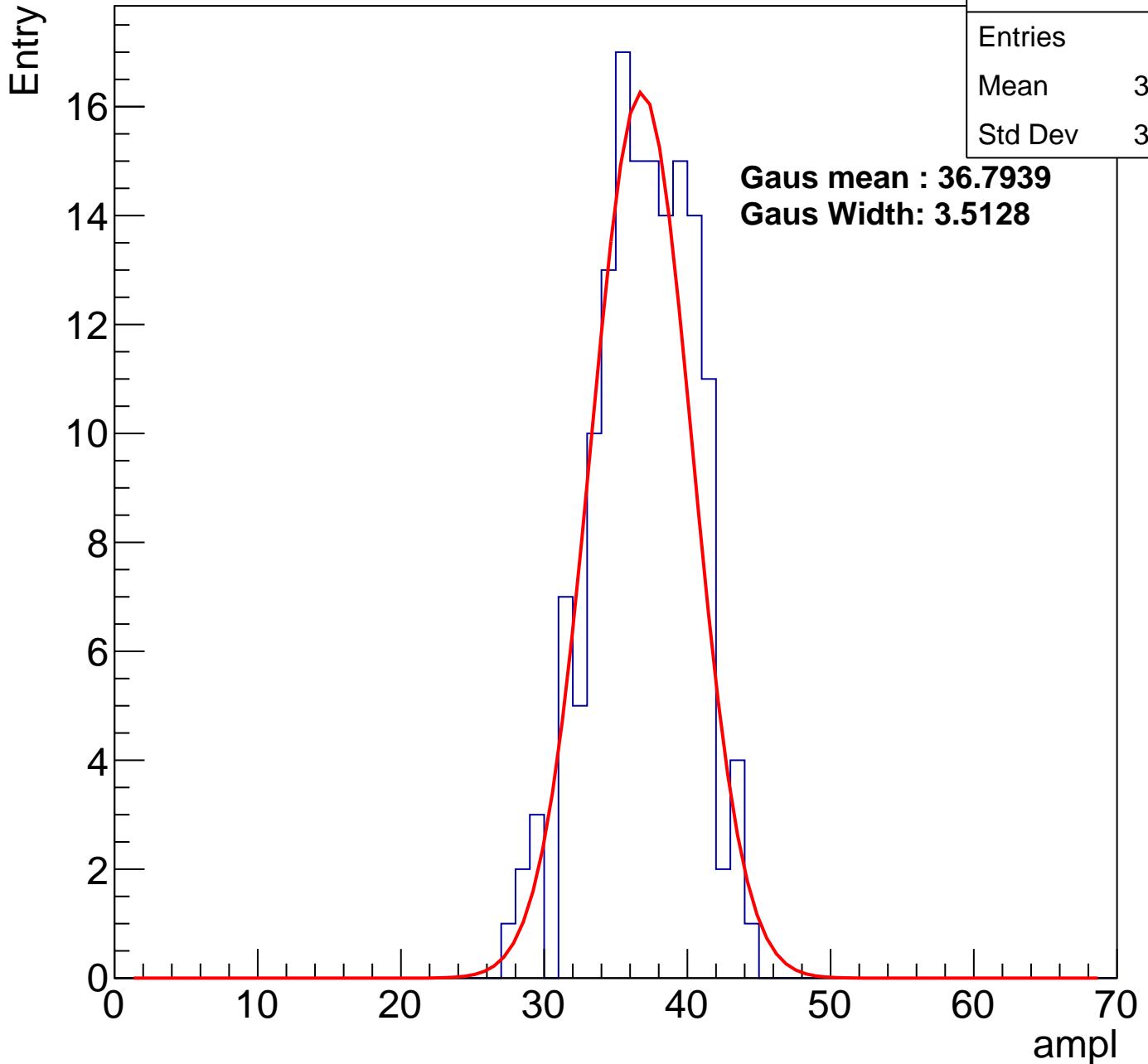
B1L001S, U16-ch41, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.49
Std Dev	3.448

Gaus mean : 36.7939

Gaus Width: 3.5128



B1L001S, U16-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

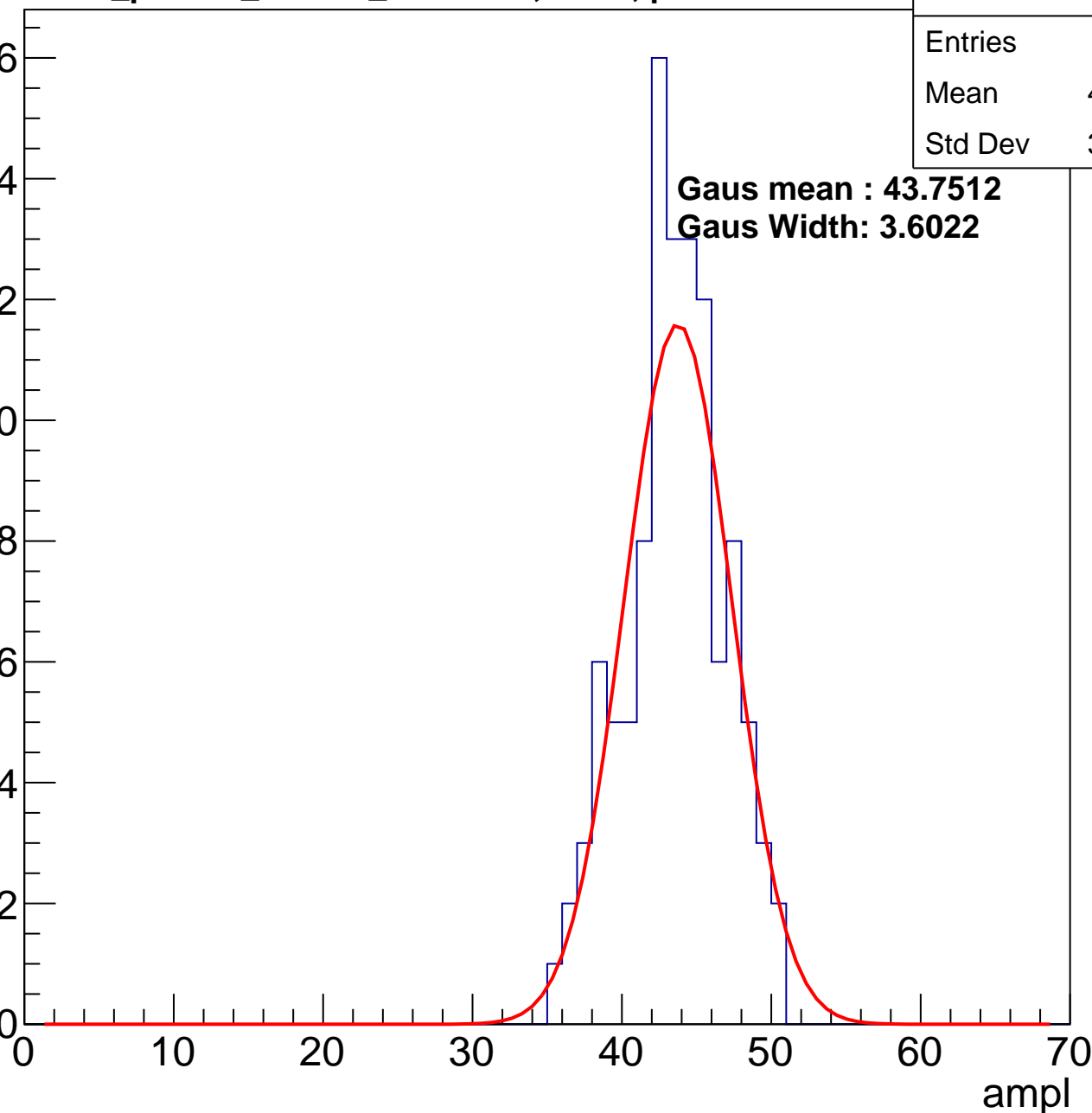
Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	43.06
Std Dev	3.287

Gaus mean : 43.7512

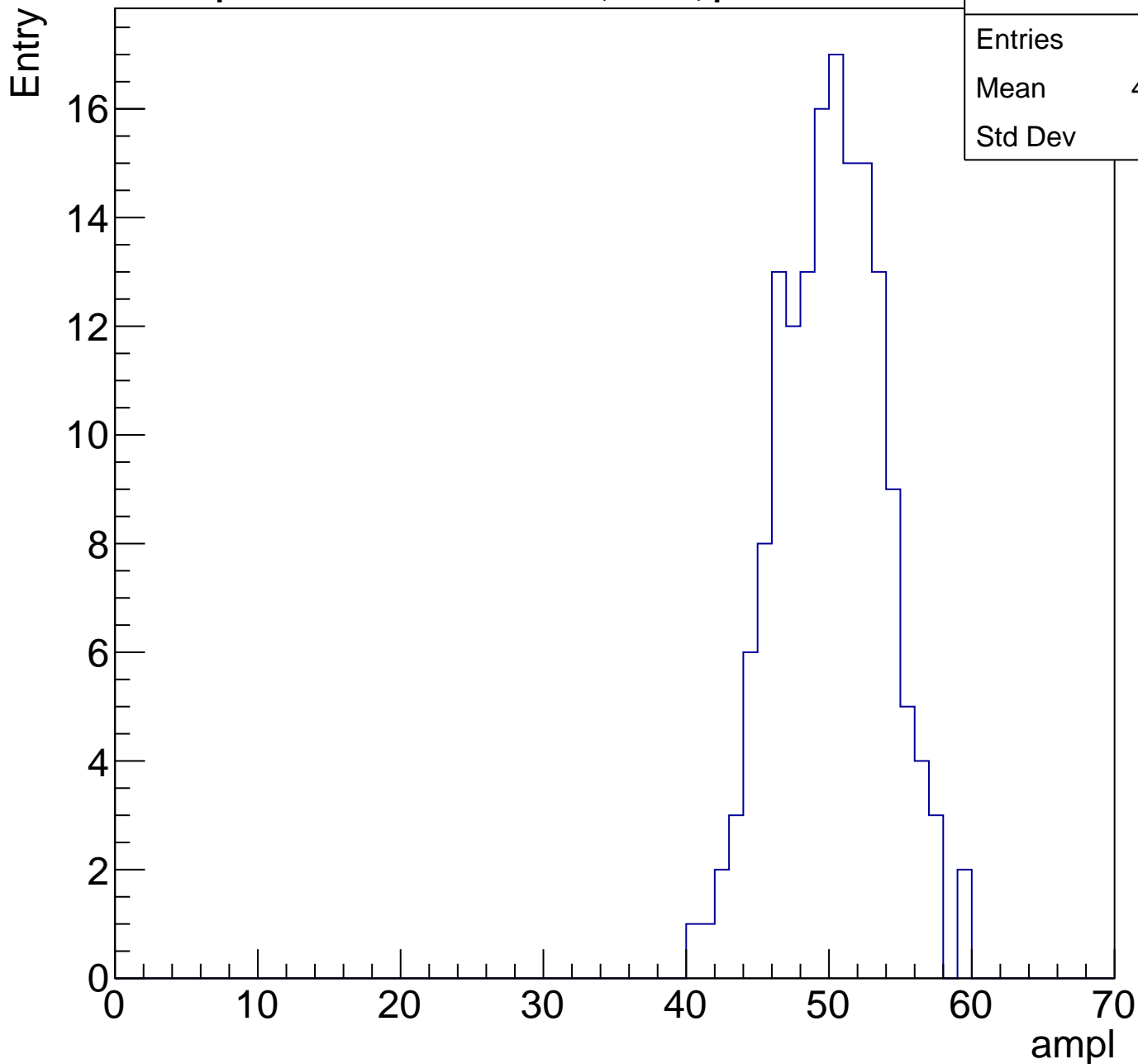
Gaus Width: 3.6022



B1L001S, U16-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	49.66
Std Dev	3.69



B1L001S, U16-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

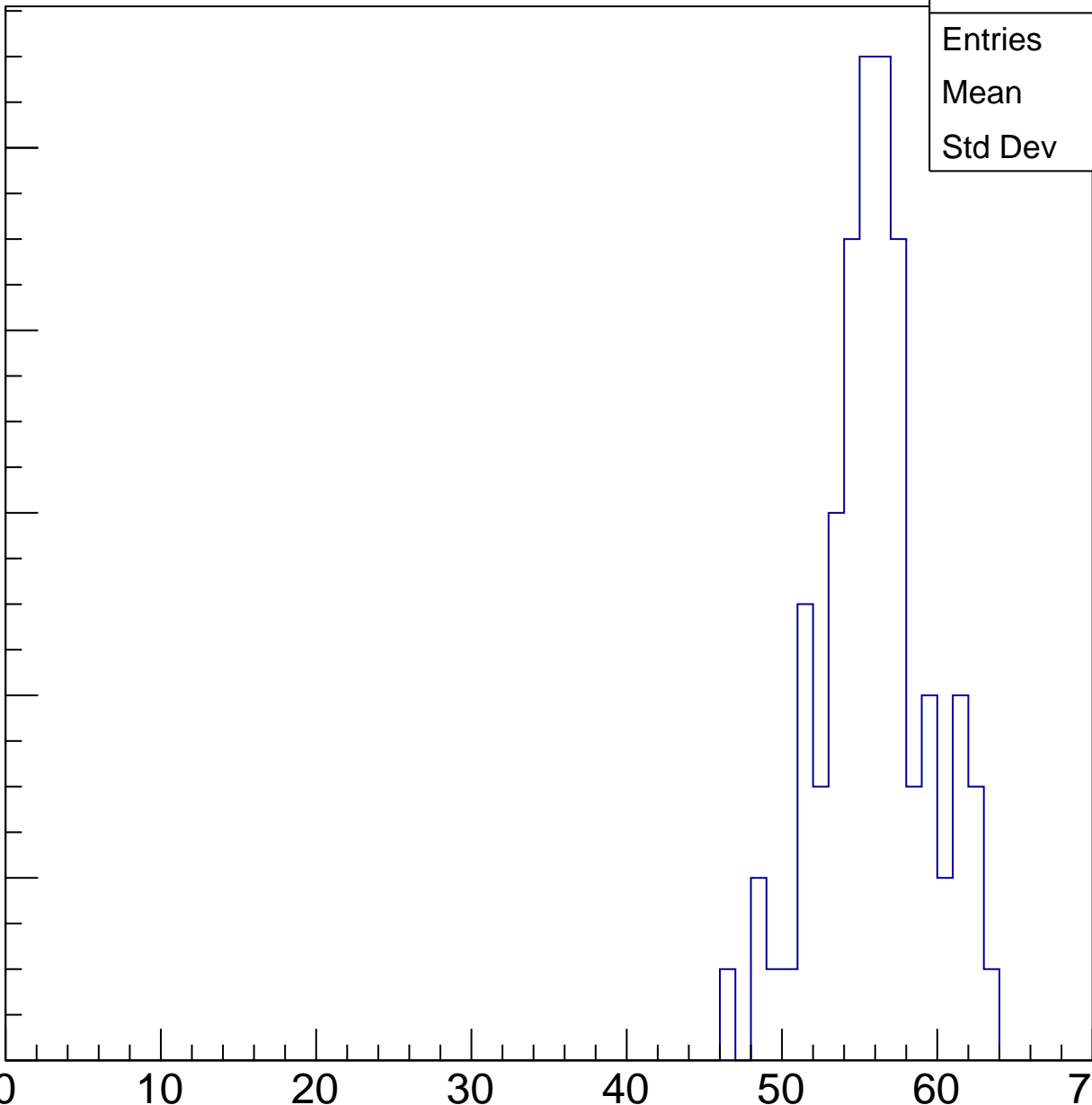
40

50

60

ampl

Entries	76
Mean	55.43
Std Dev	3.458



B1L001S, U16-ch41, adc5

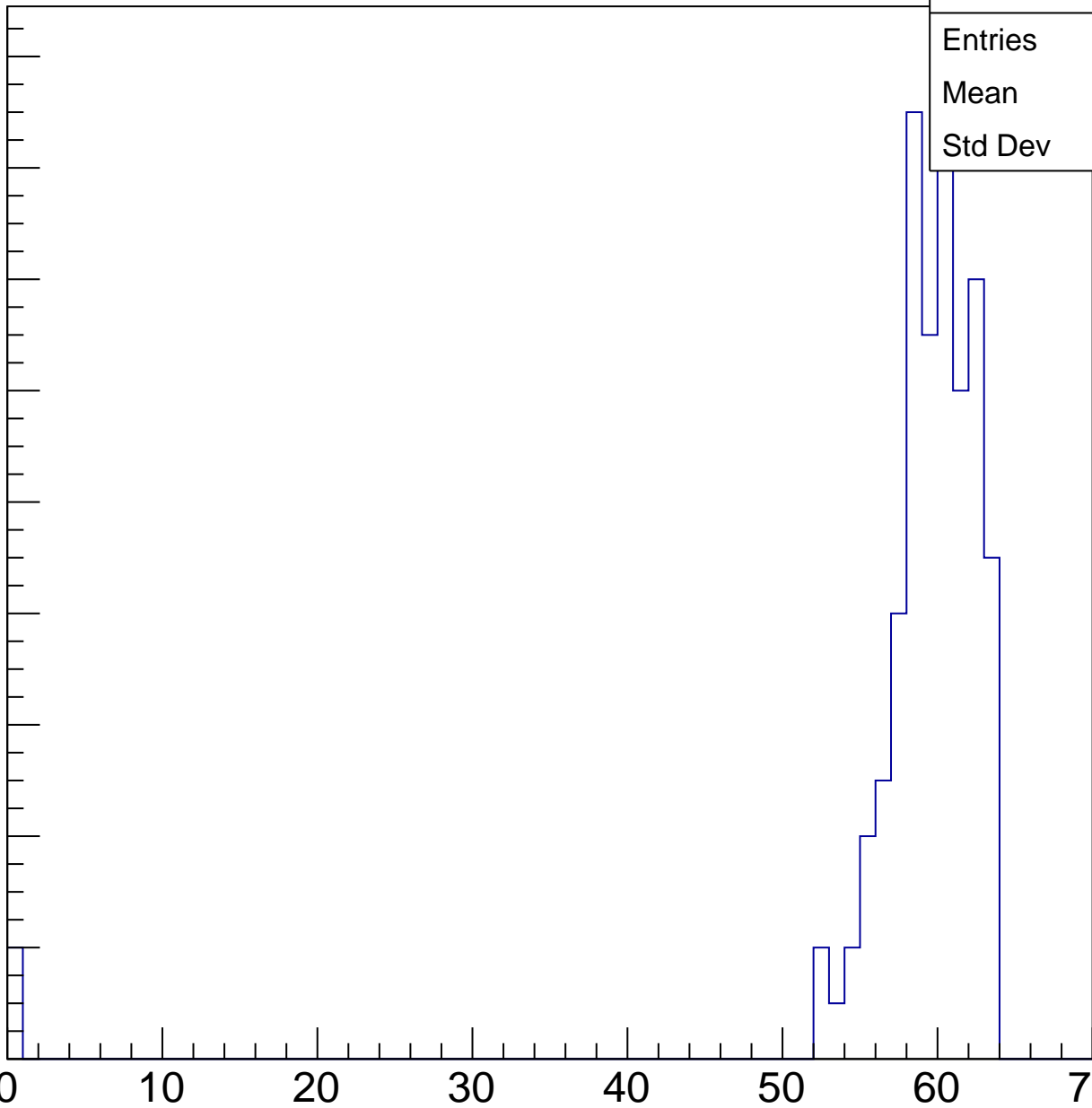
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.14
Std Dev	8.406

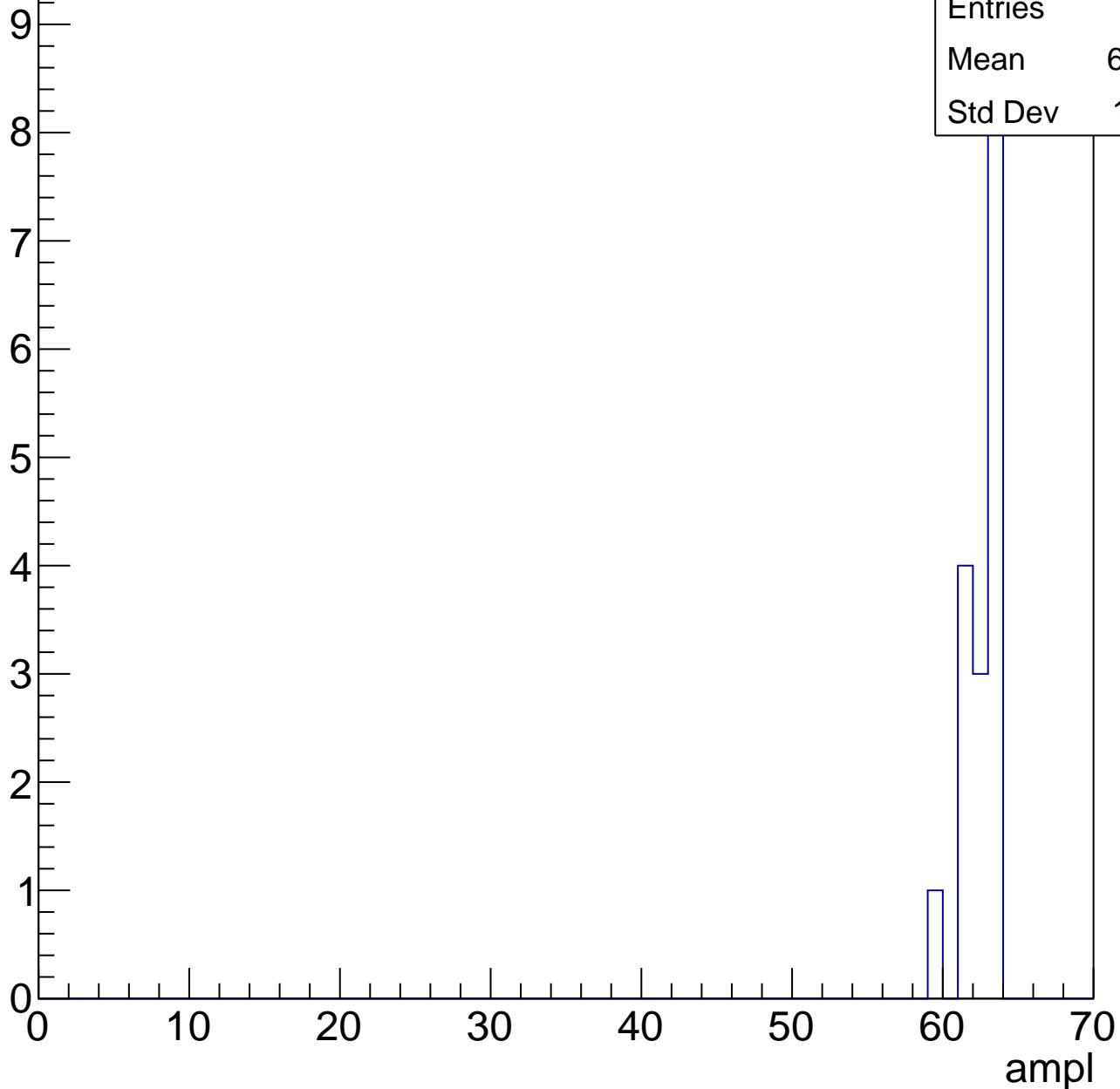
ampl



B1L001S, U16-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl

B1L001S, U16-ch42, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	29.99
Std Dev	4.776

Gaus mean : 31.2007

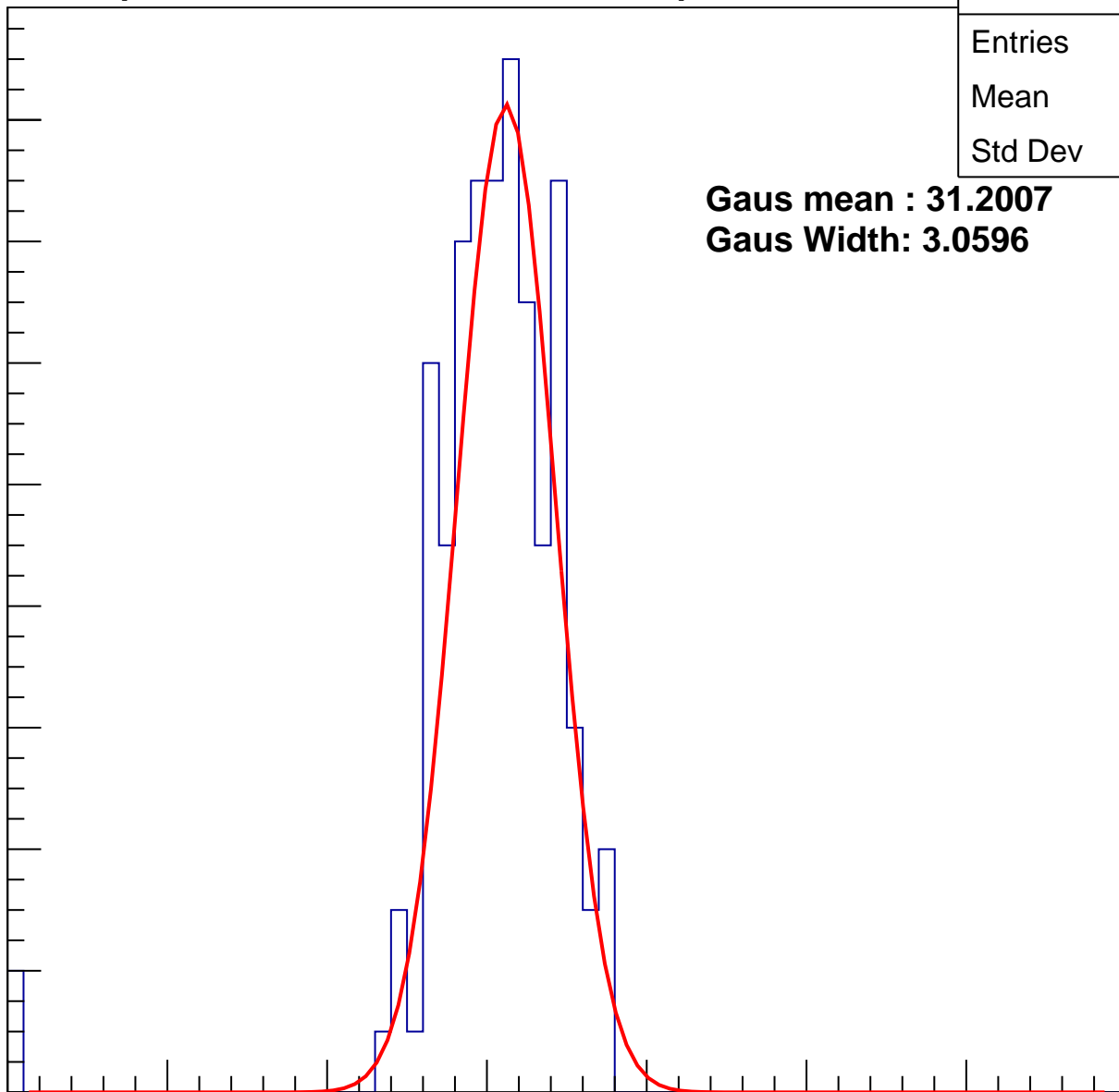
Gaus Width: 3.0596

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

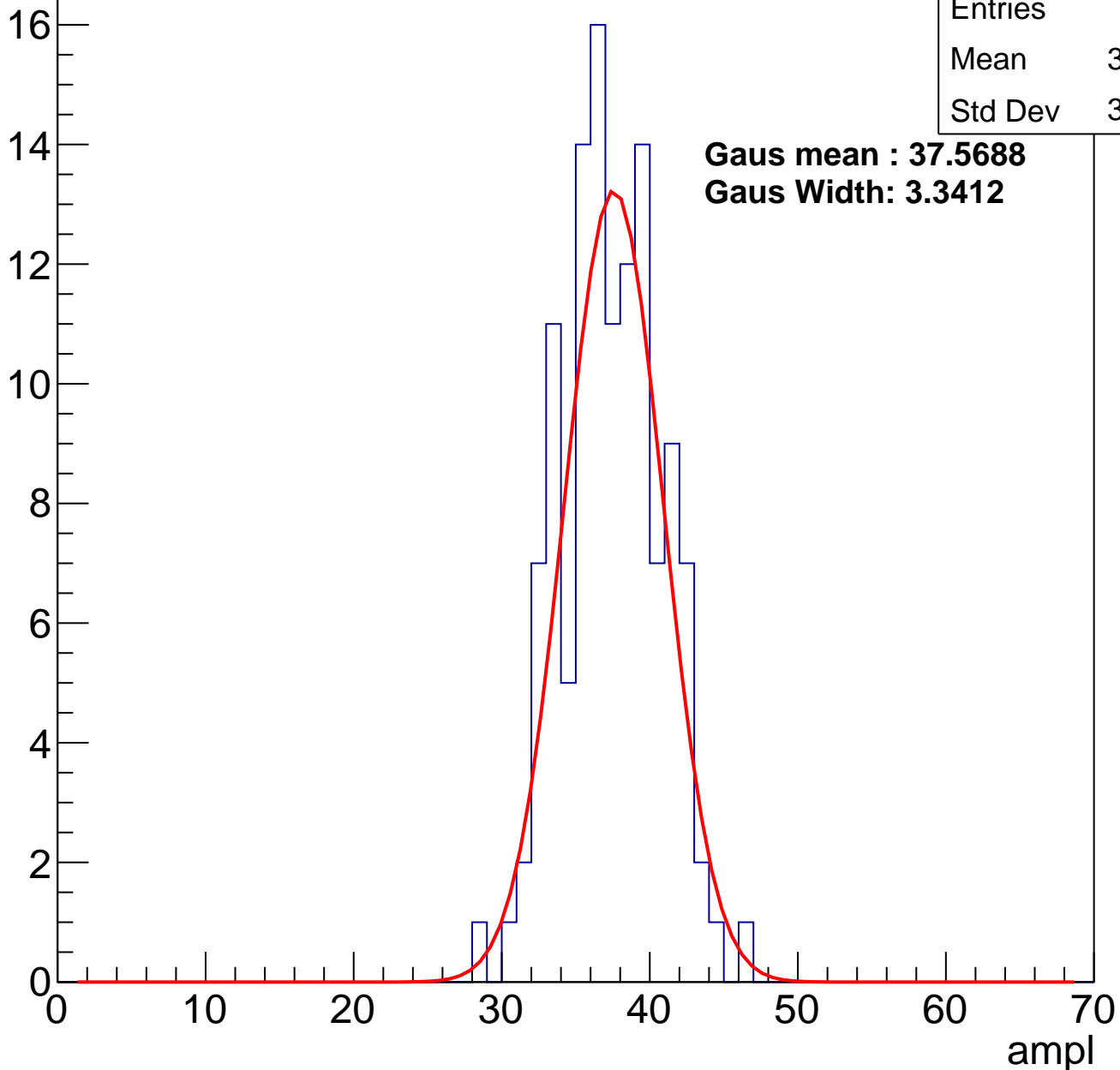
ampl



B1L001S, U16-ch42, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	42.95
Std Dev	3.257

Gaus mean : 43.3759

Gaus Width: 3.2820

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

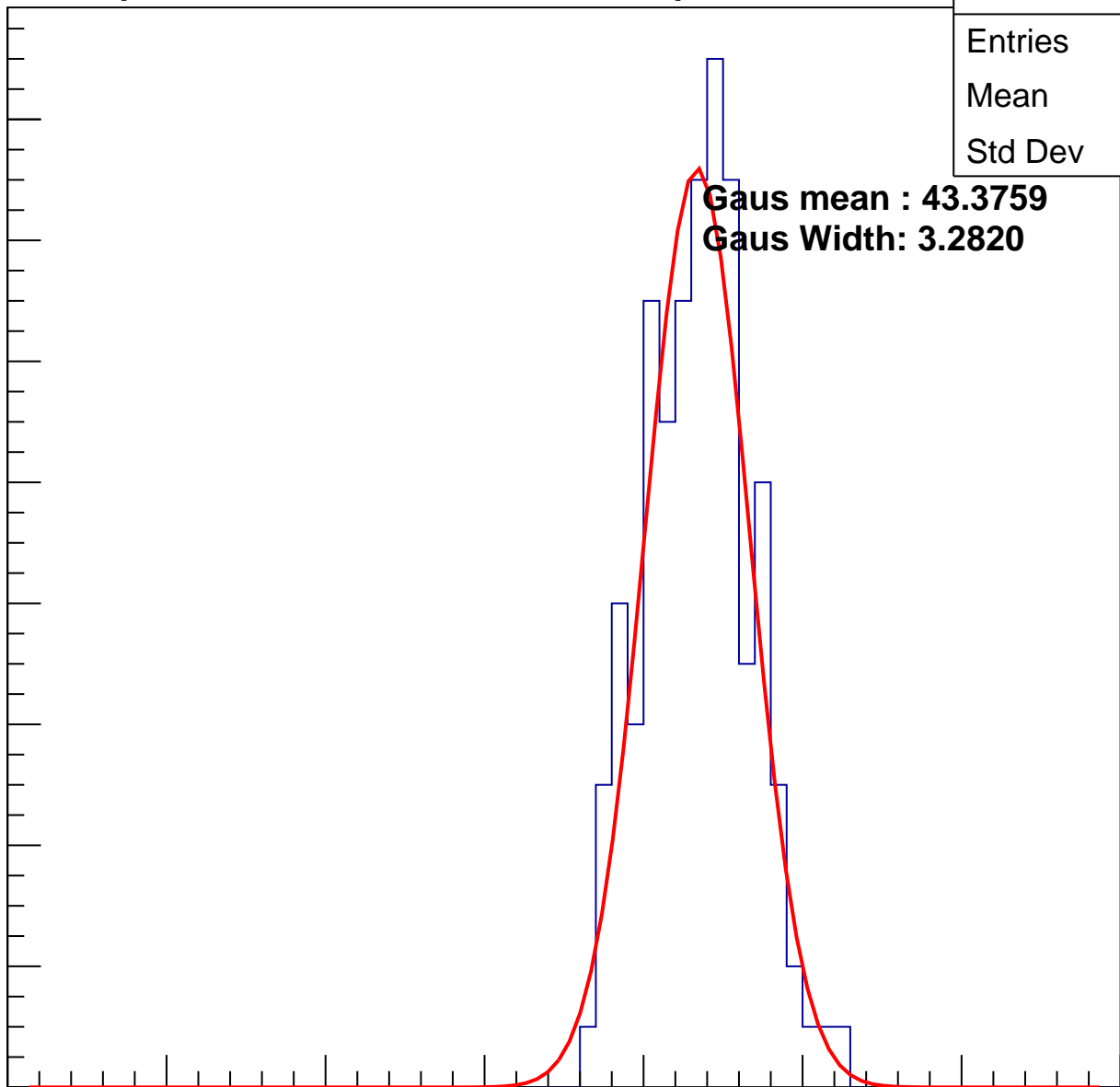
40

50

60

70

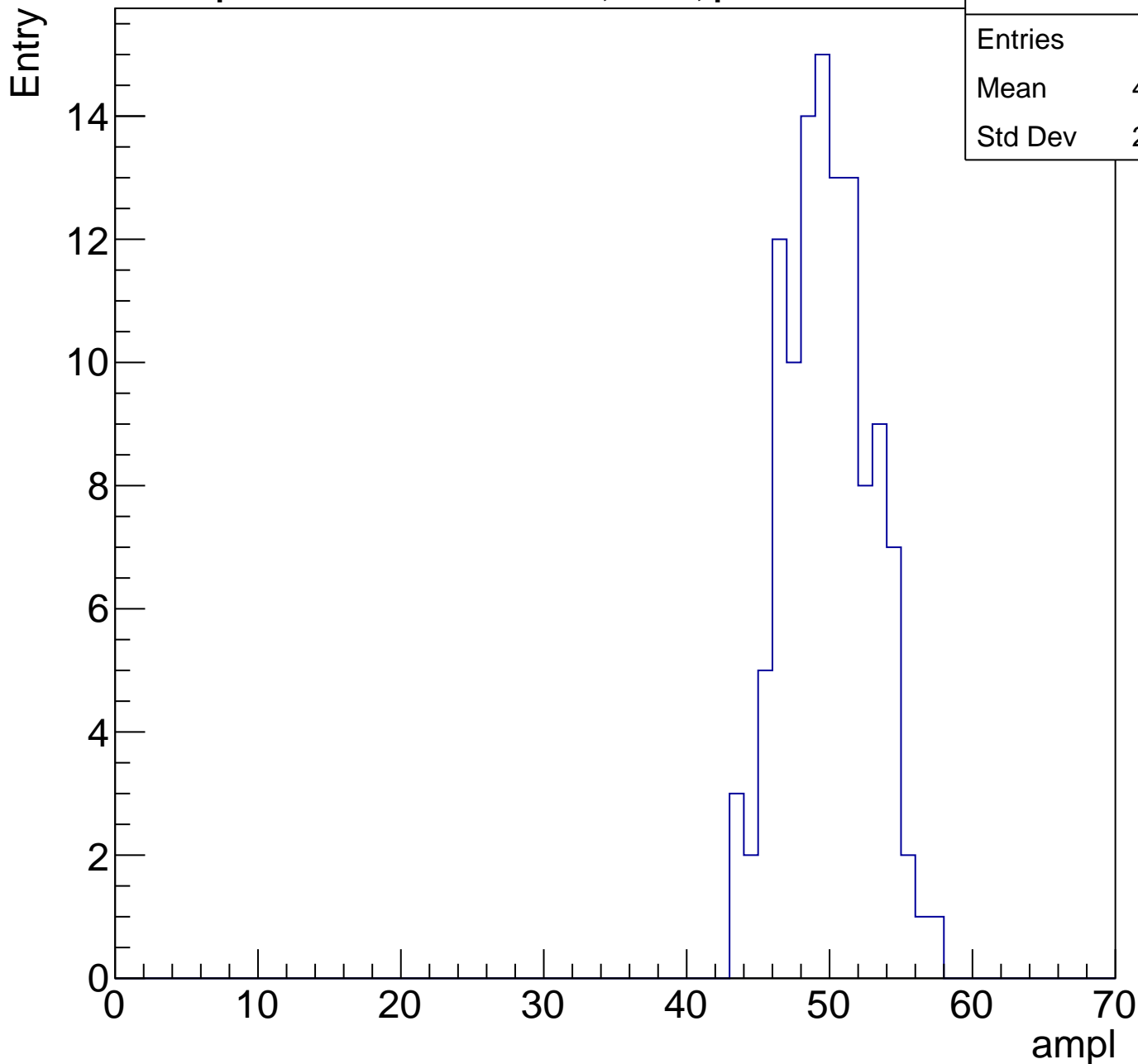
ampl



B1L001S, U16-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

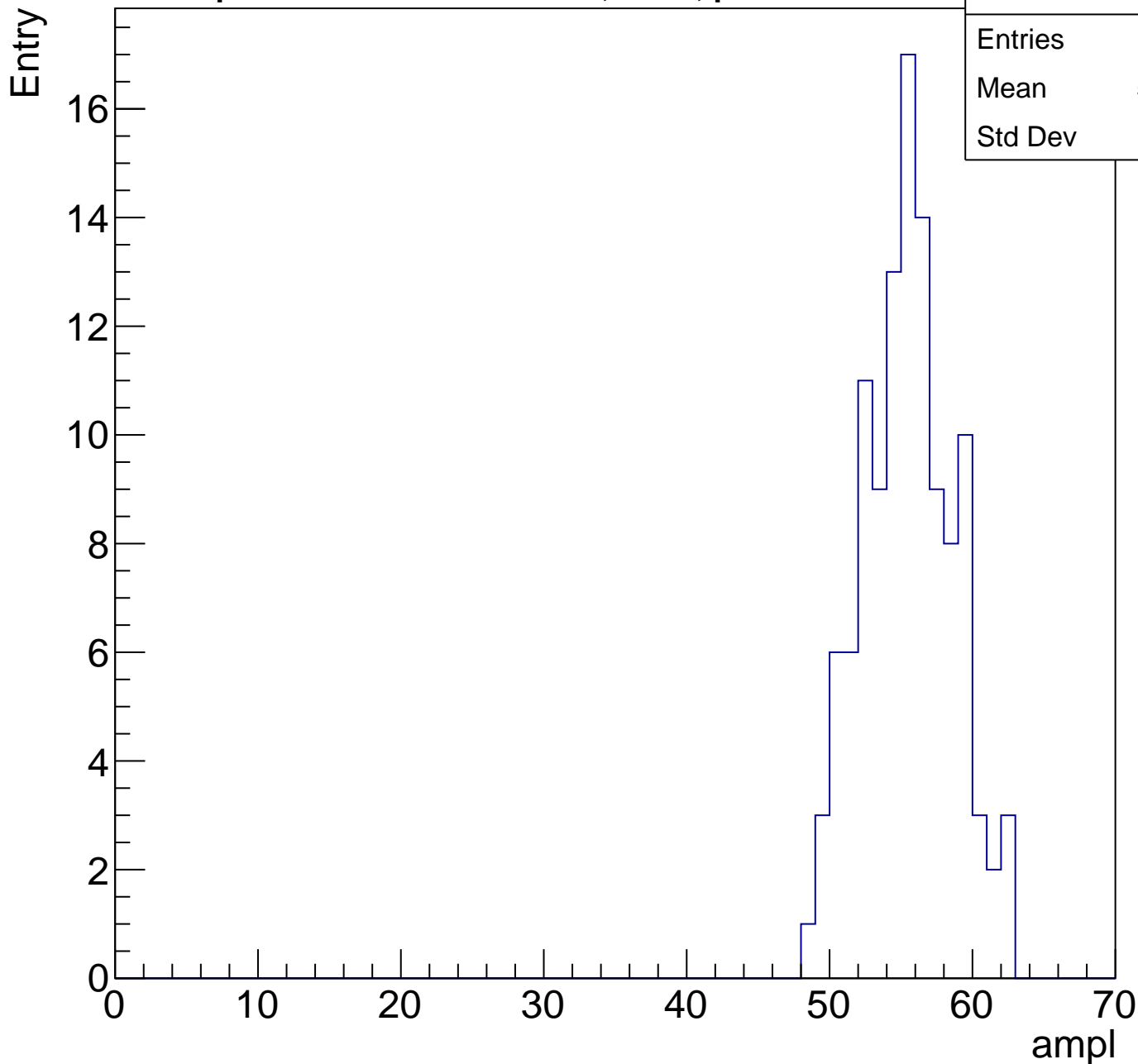
Entries	115
Mean	49.37
Std Dev	2.985



B1L001S, U16-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	55.01
Std Dev	3.13



B1L001S, U16-ch42, adc5

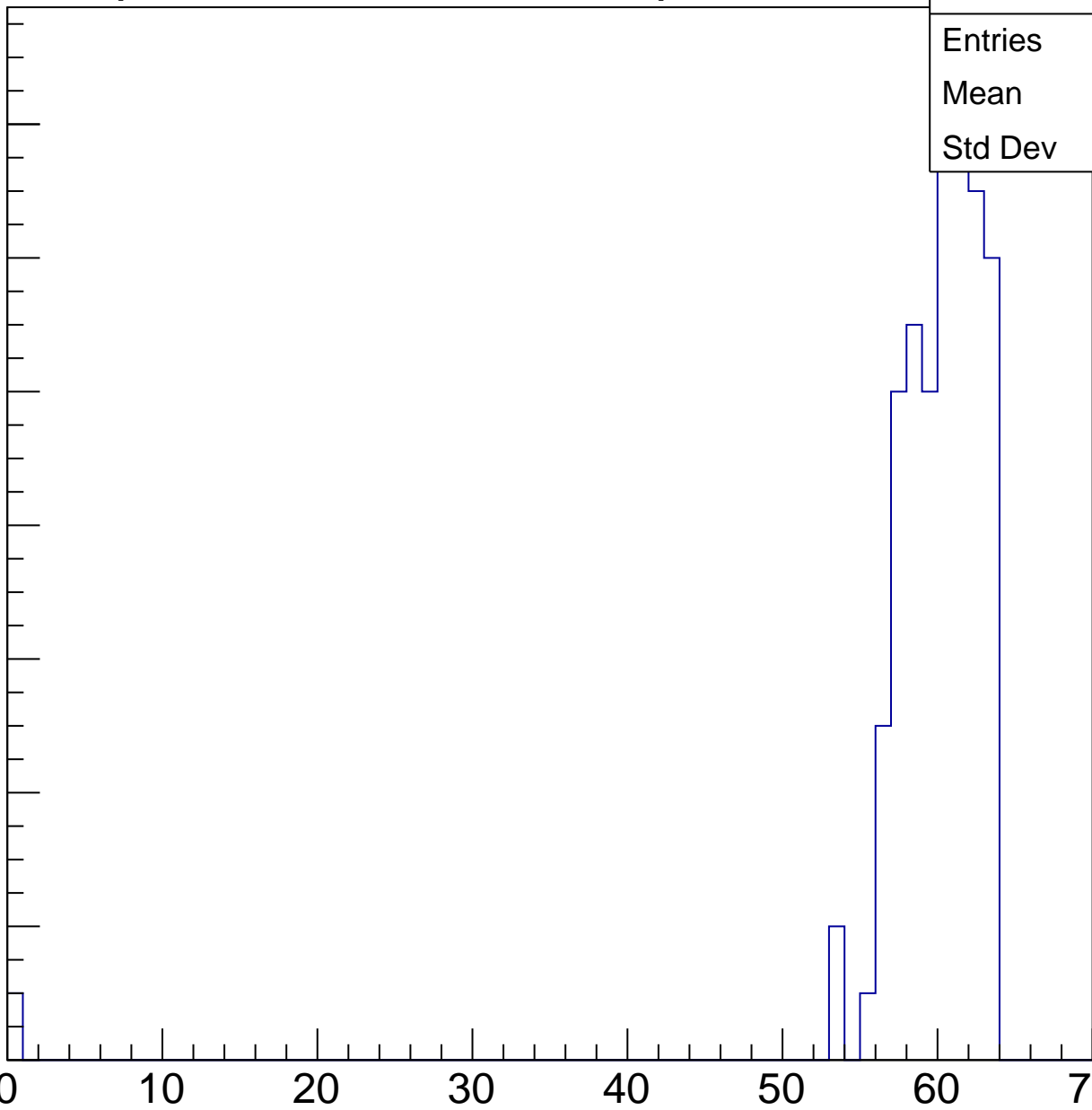
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	59.12
Std Dev	6.527

ampl

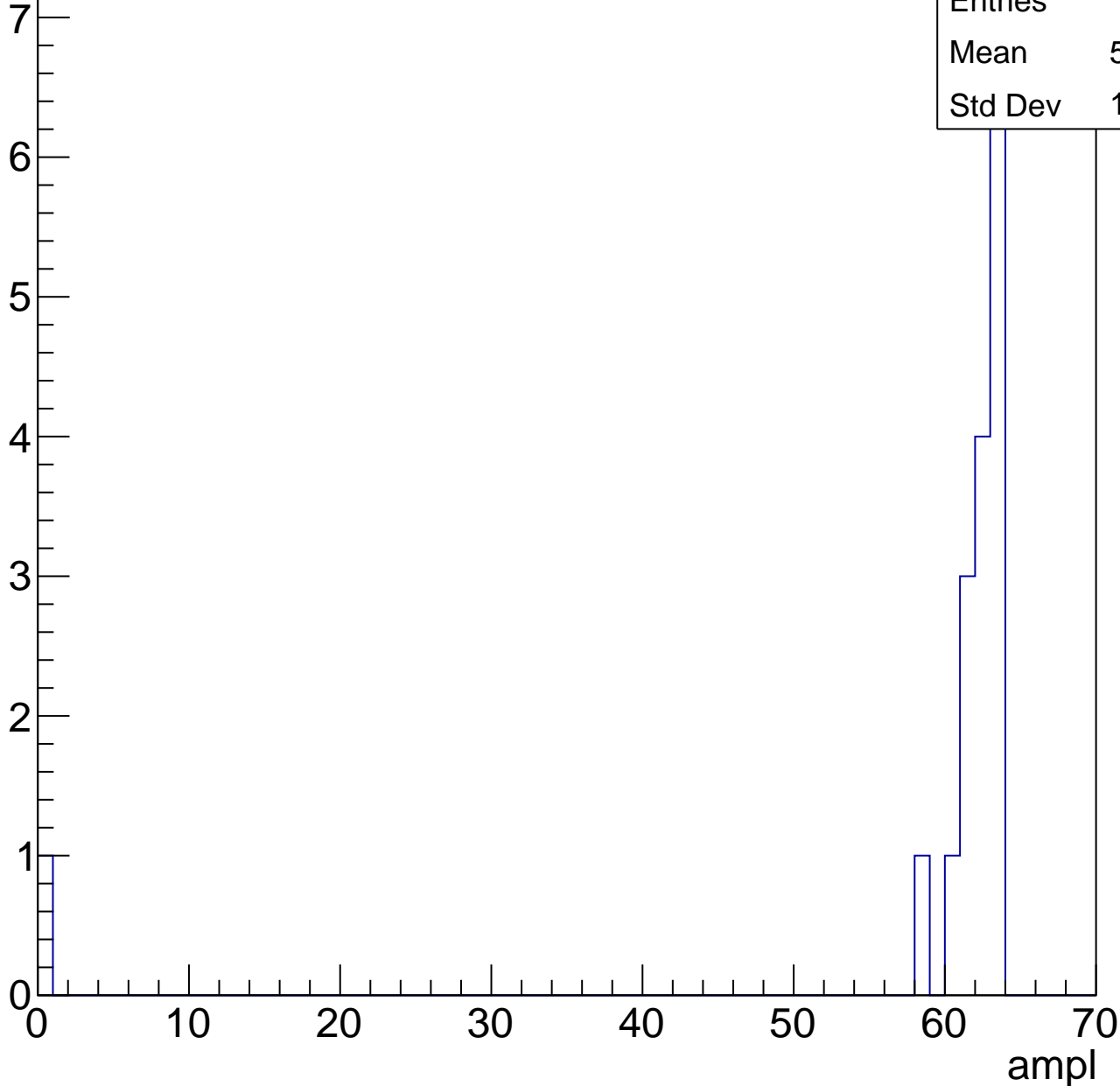


B1L001S, U16-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

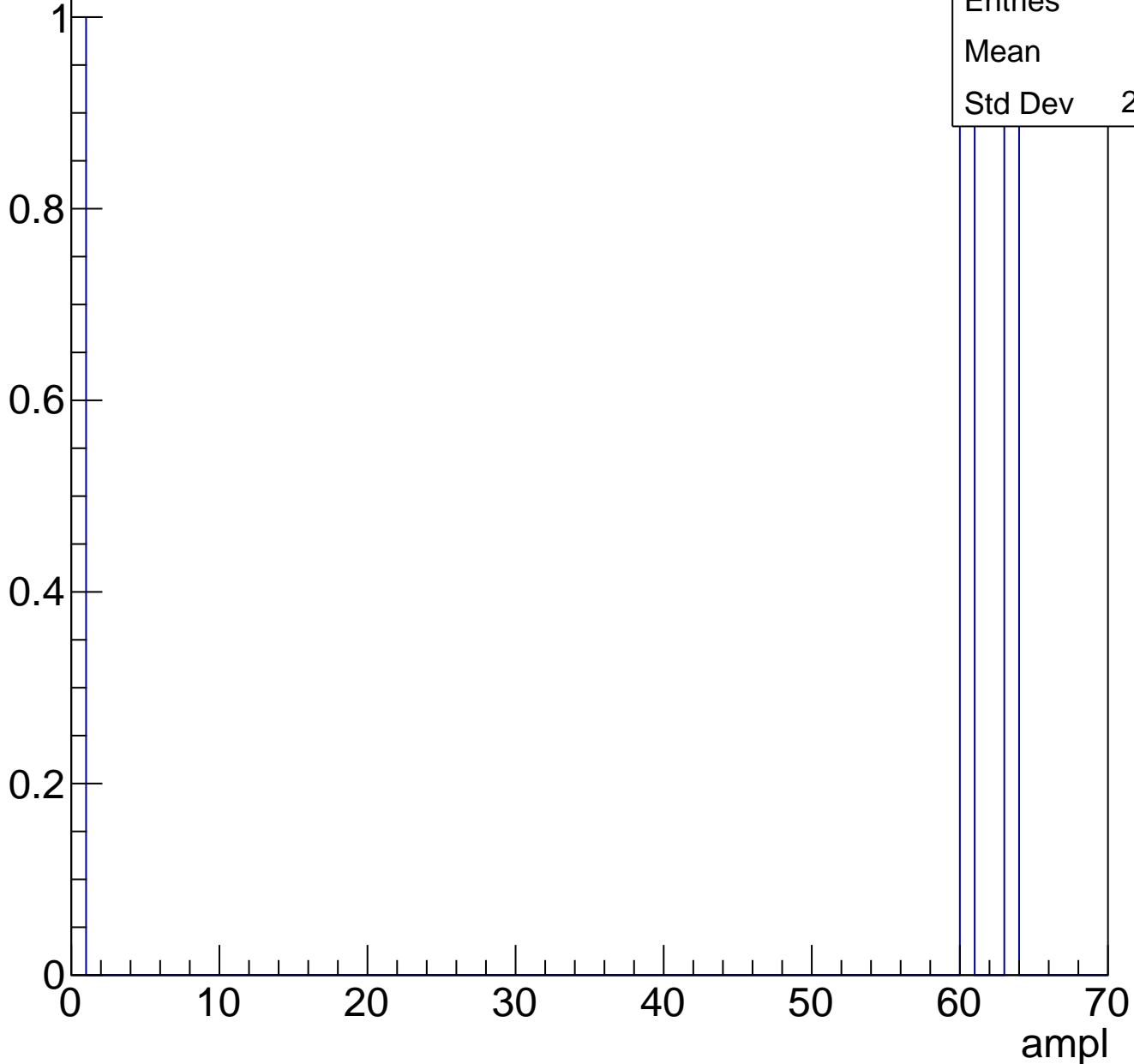
Entries	17
Mean	58.24
Std Dev	14.62



B1L001S, U16-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch43, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

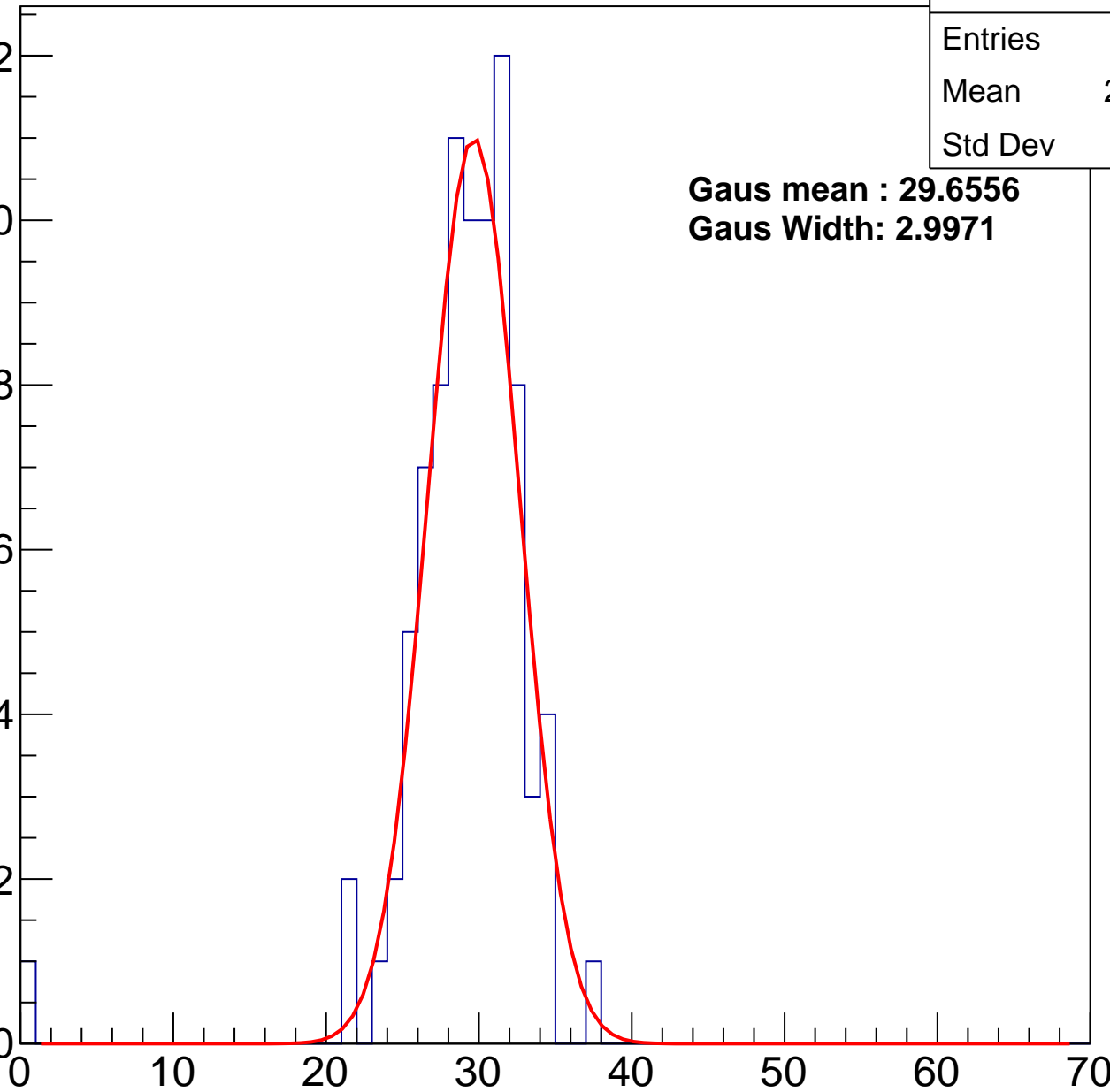
12
10
8
6
4
2
0

Entries	85
Mean	28.64
Std Dev	4.3

Gaus mean : 29.6556

Gaus Width: 2.9971

ampl



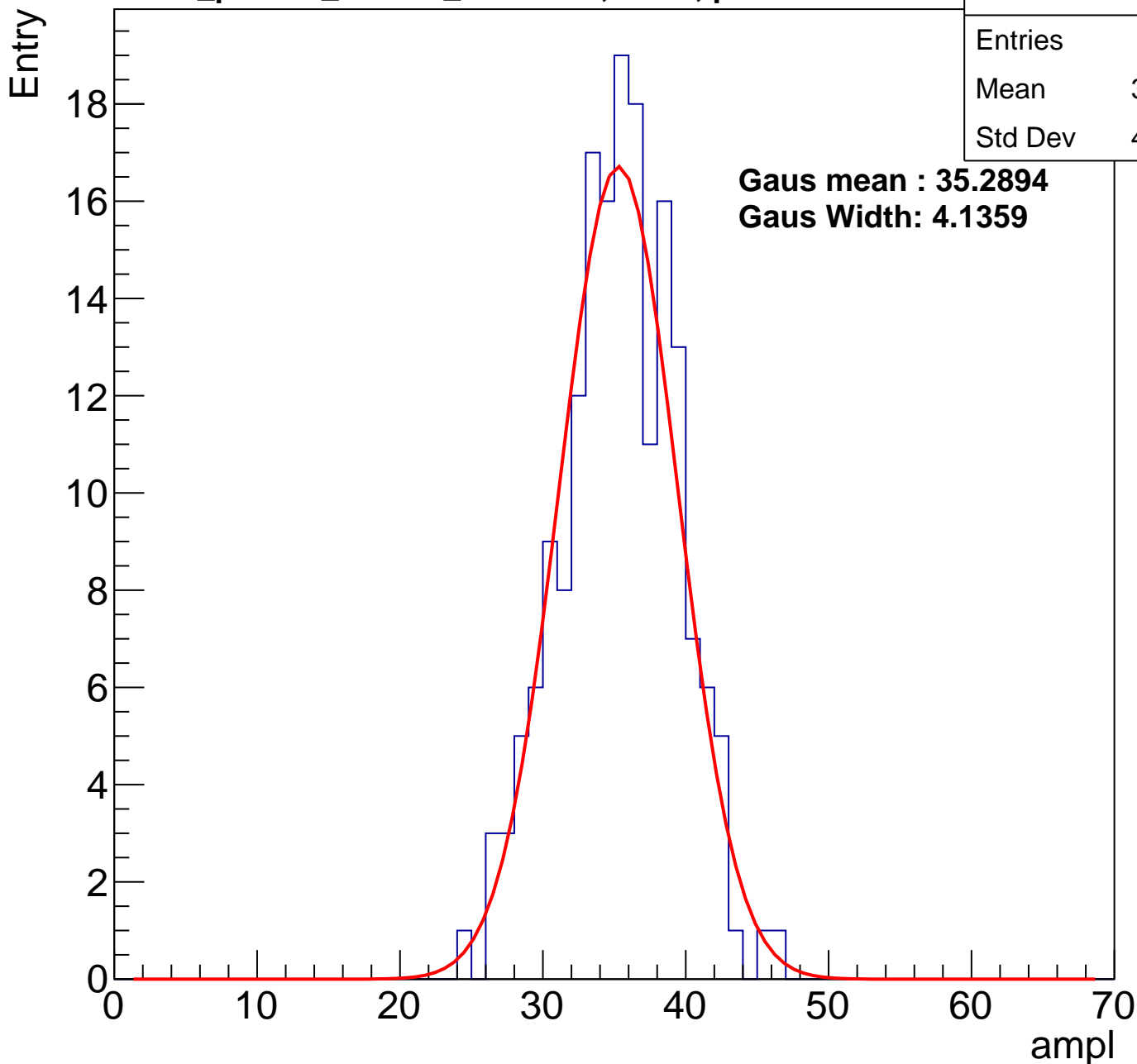
B1L001S, U16-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	178
Mean	34.88
Std Dev	4.033

Gaus mean : 35.2894

Gaus Width: 4.1359



B1L001S, U16-ch43, adc2

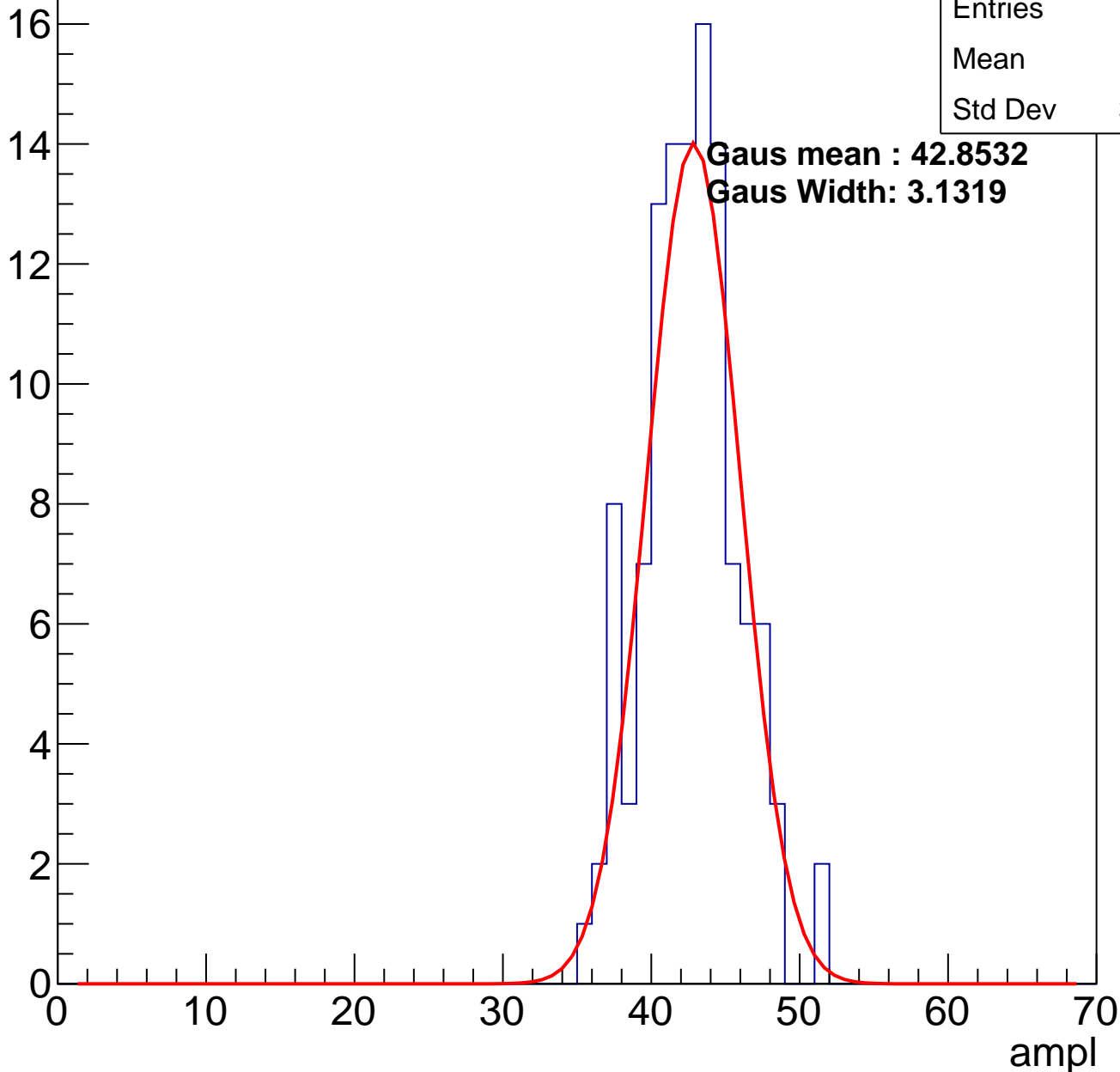
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	42.2
Std Dev	3.141

Gaus mean : 42.8532

Gaus Width: 3.1319

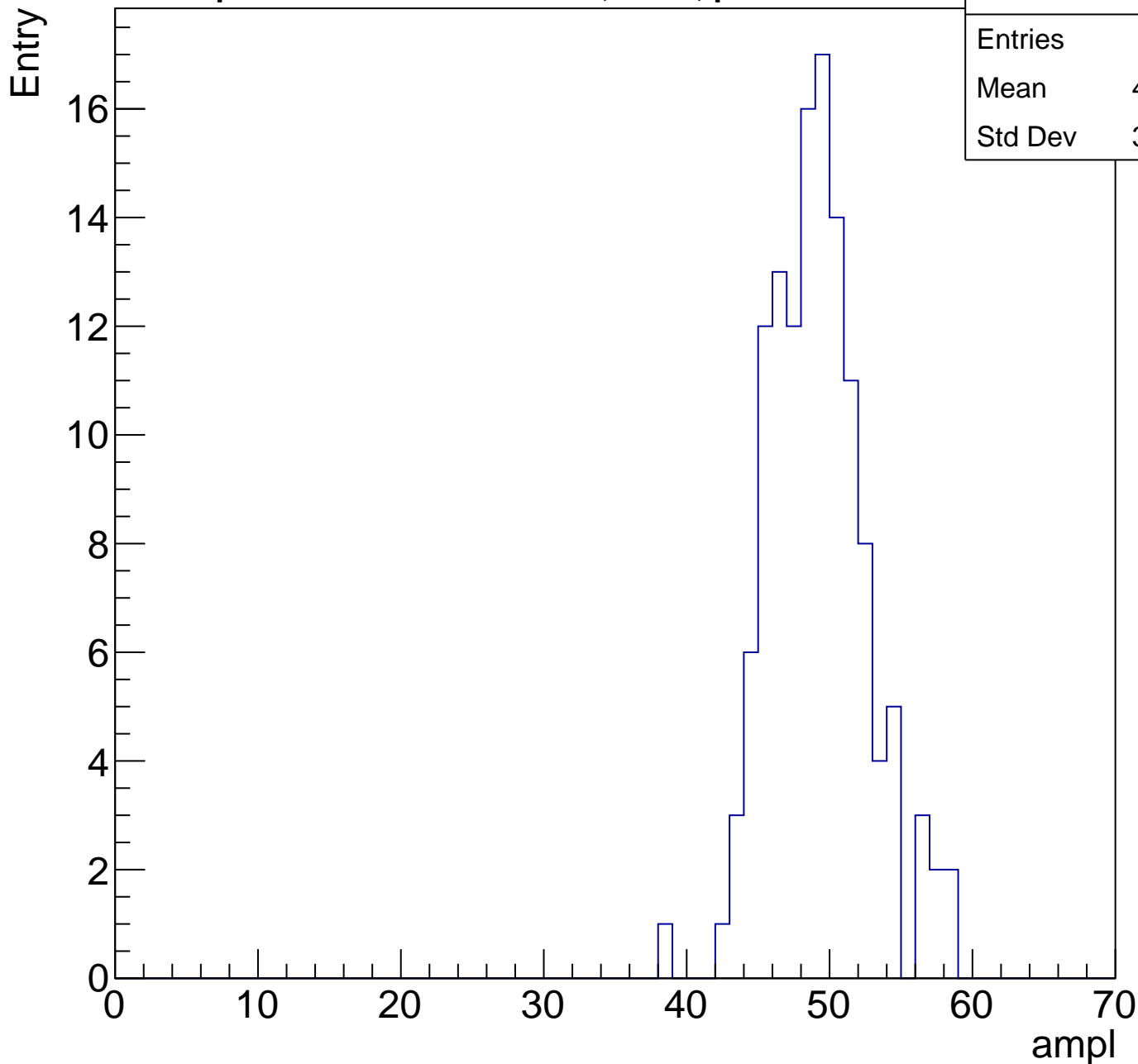
Entry



B1L001S, U16-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	48.72
Std Dev	3.449



B1L001S, U16-ch43, adc4

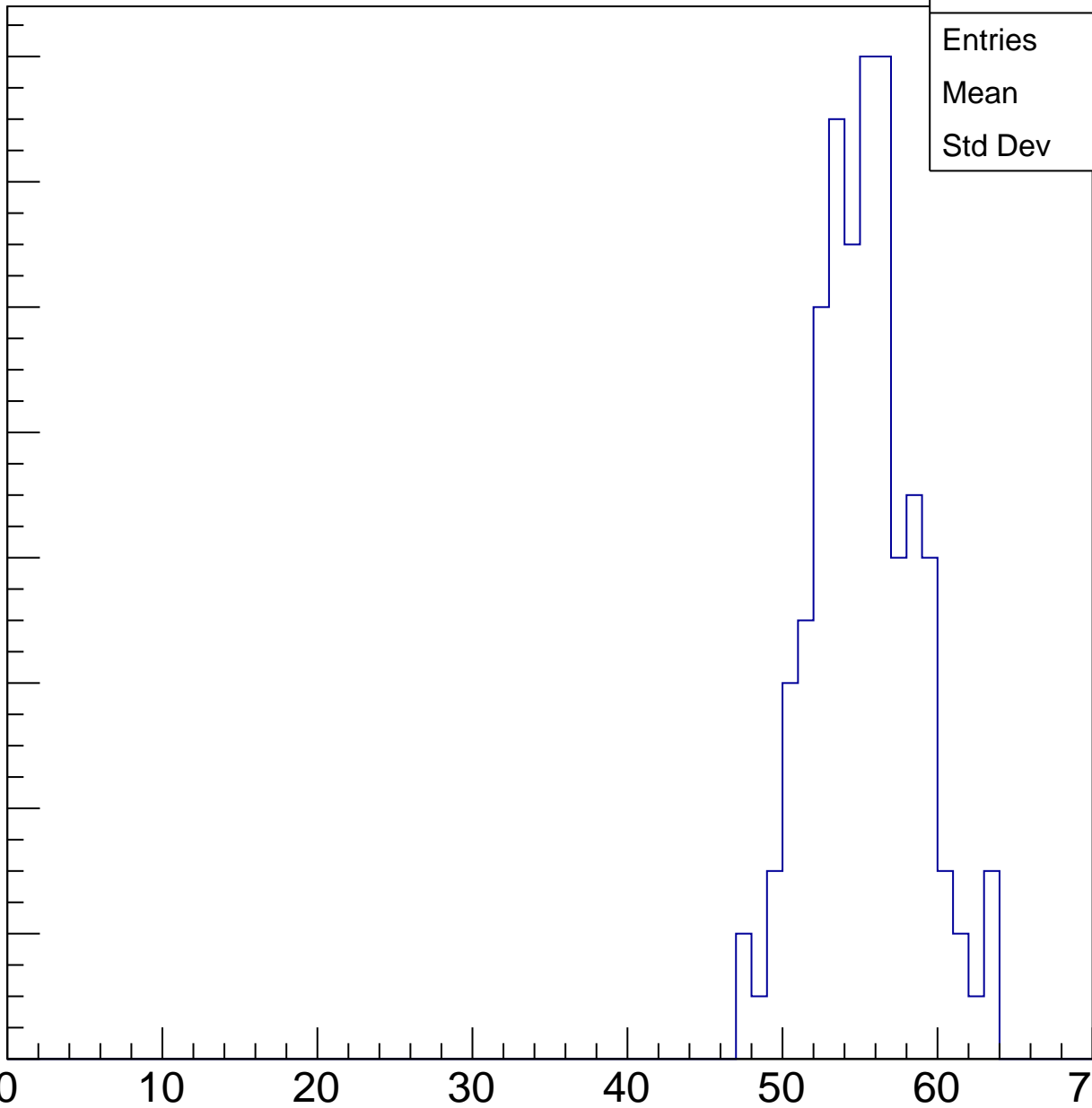
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	54.77
Std Dev	3.306

ampl



B1L001S, U16-ch43, adc5

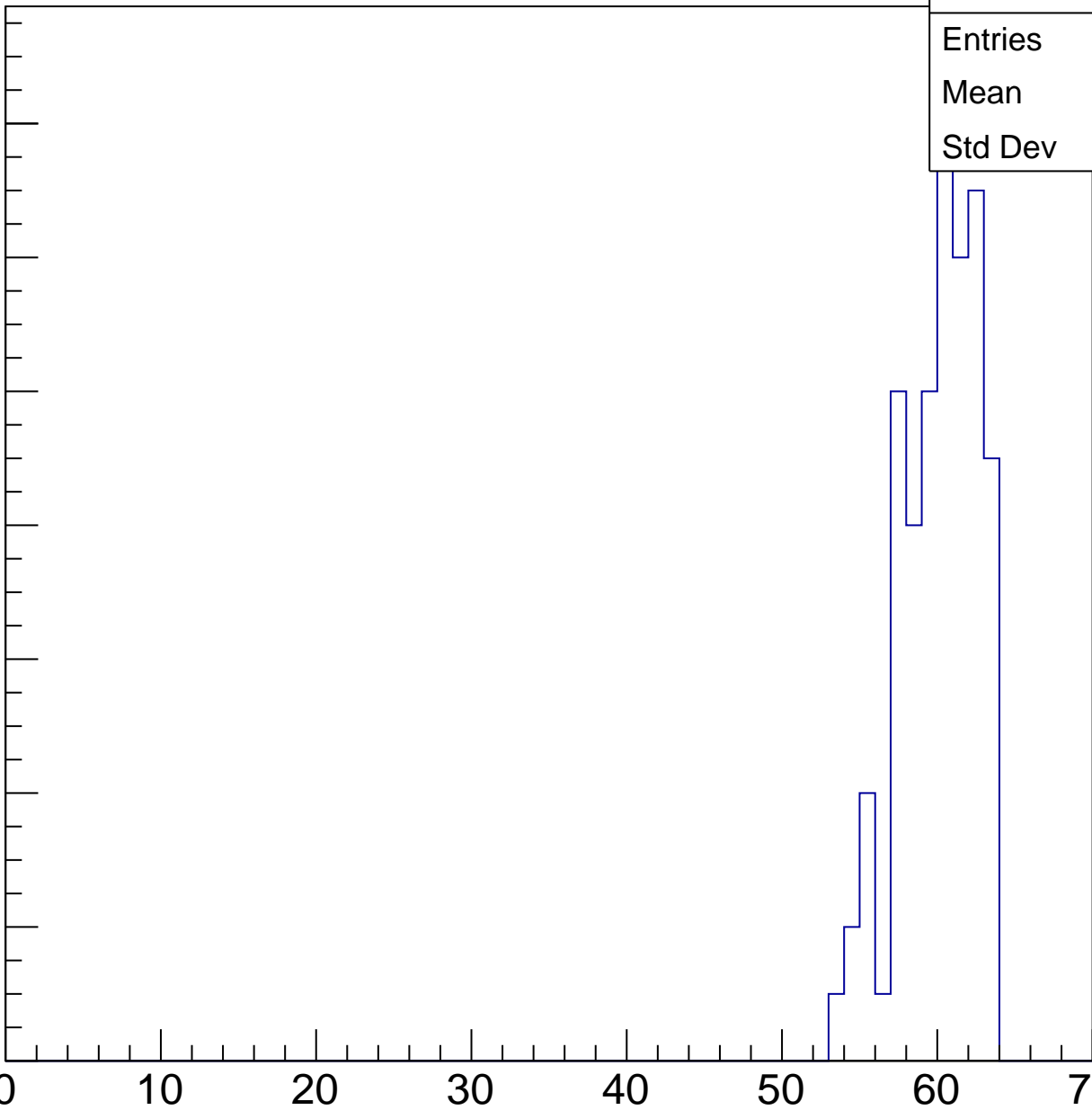
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	59.6
Std Dev	2.426

ampl



B1L001S, U16-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

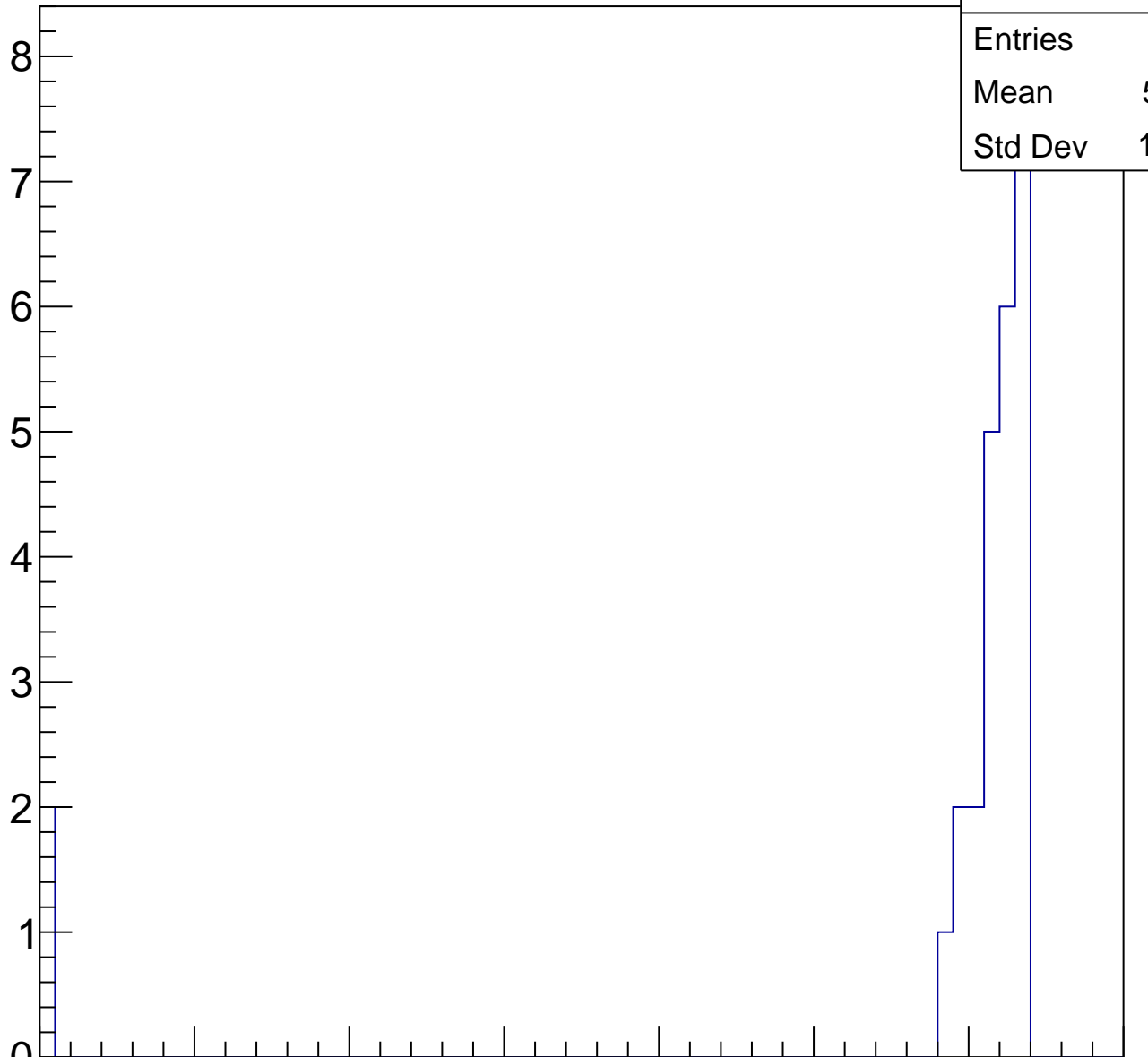
Entry

8
7
6
5
4
3
2
1
0

Entries	26
Mean	56.81
Std Dev	16.46

ampl

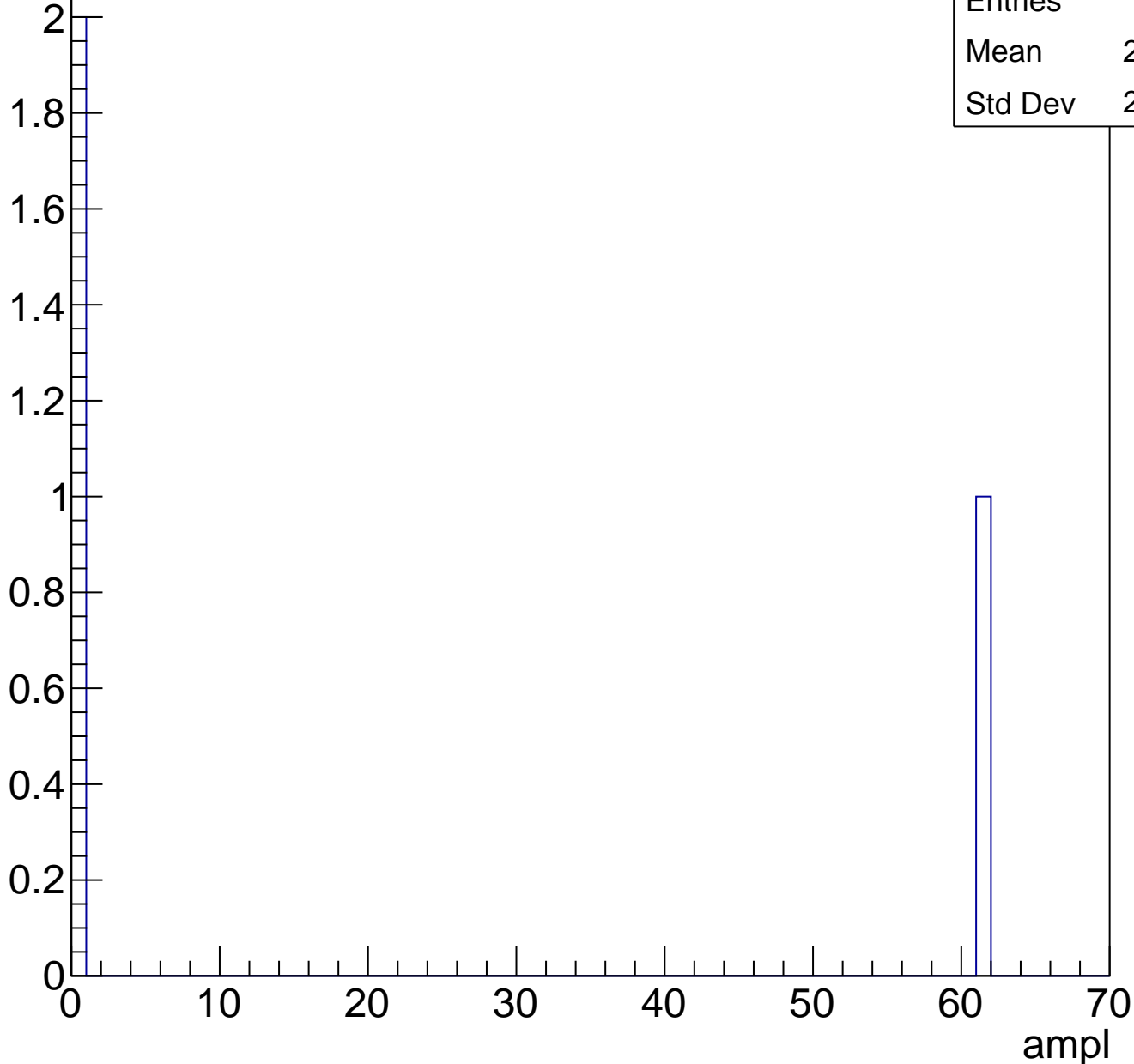
0 10 20 30 40 50 60 70



B1L001S, U16-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch44, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	30.23
Std Dev	4.252

Gaus mean : 30.7954

Gaus Width: 3.8708

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

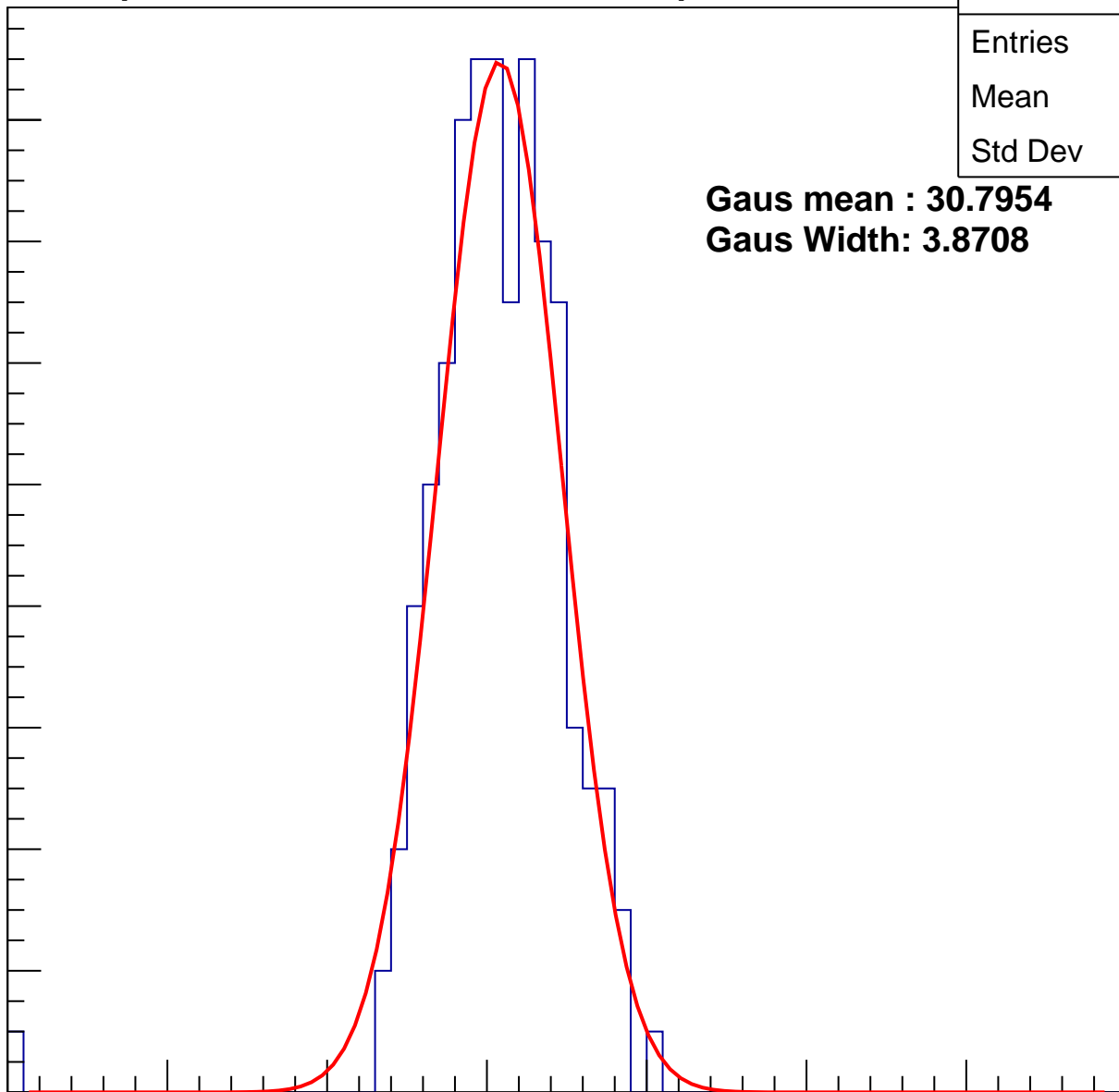
40

50

60

70

ampl



B1L001S, U16-ch44, adc1

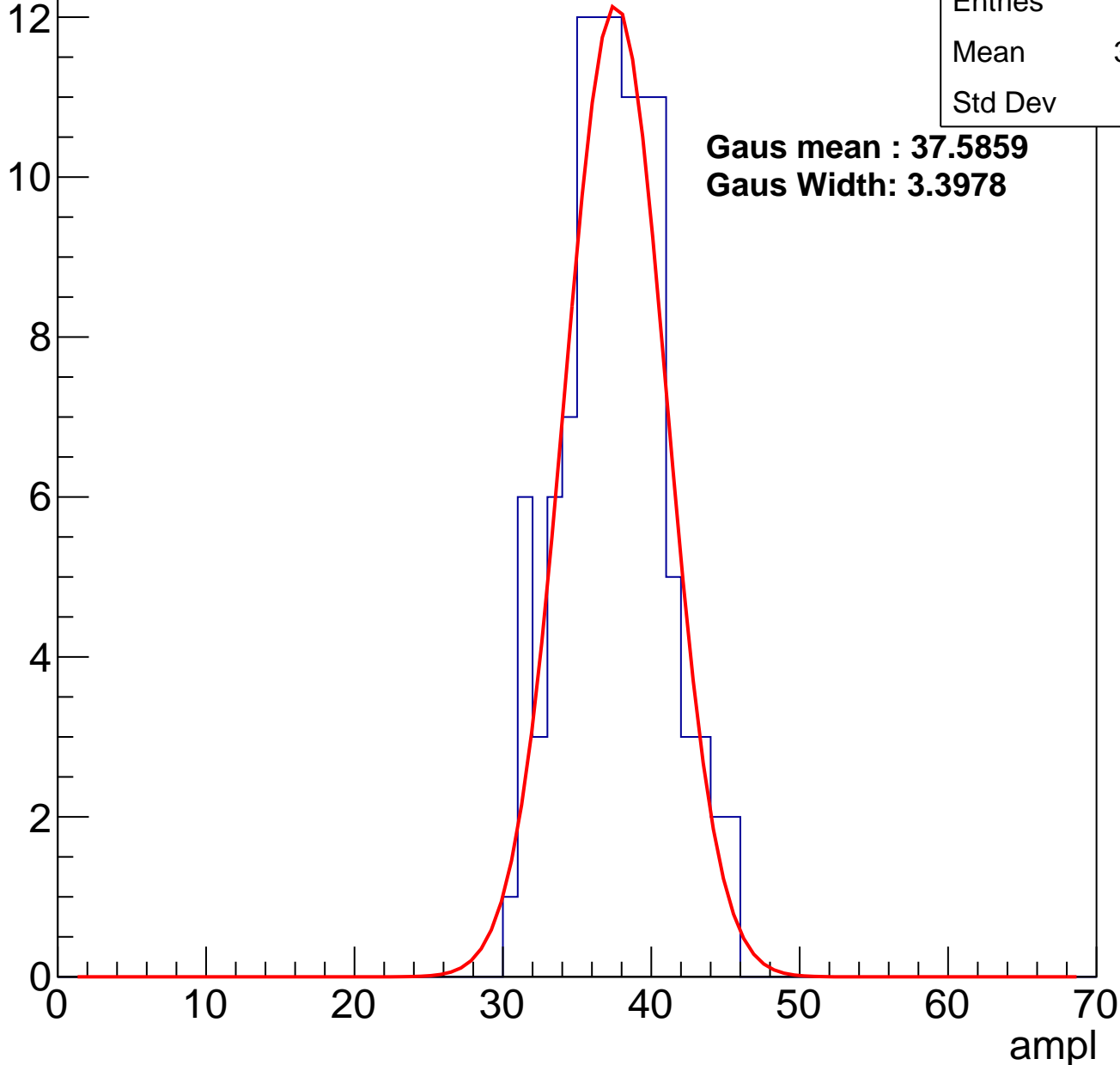
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	37.09
Std Dev	3.35

Gaus mean : 37.5859

Gaus Width: 3.3978

Entry



B1L001S, U16-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

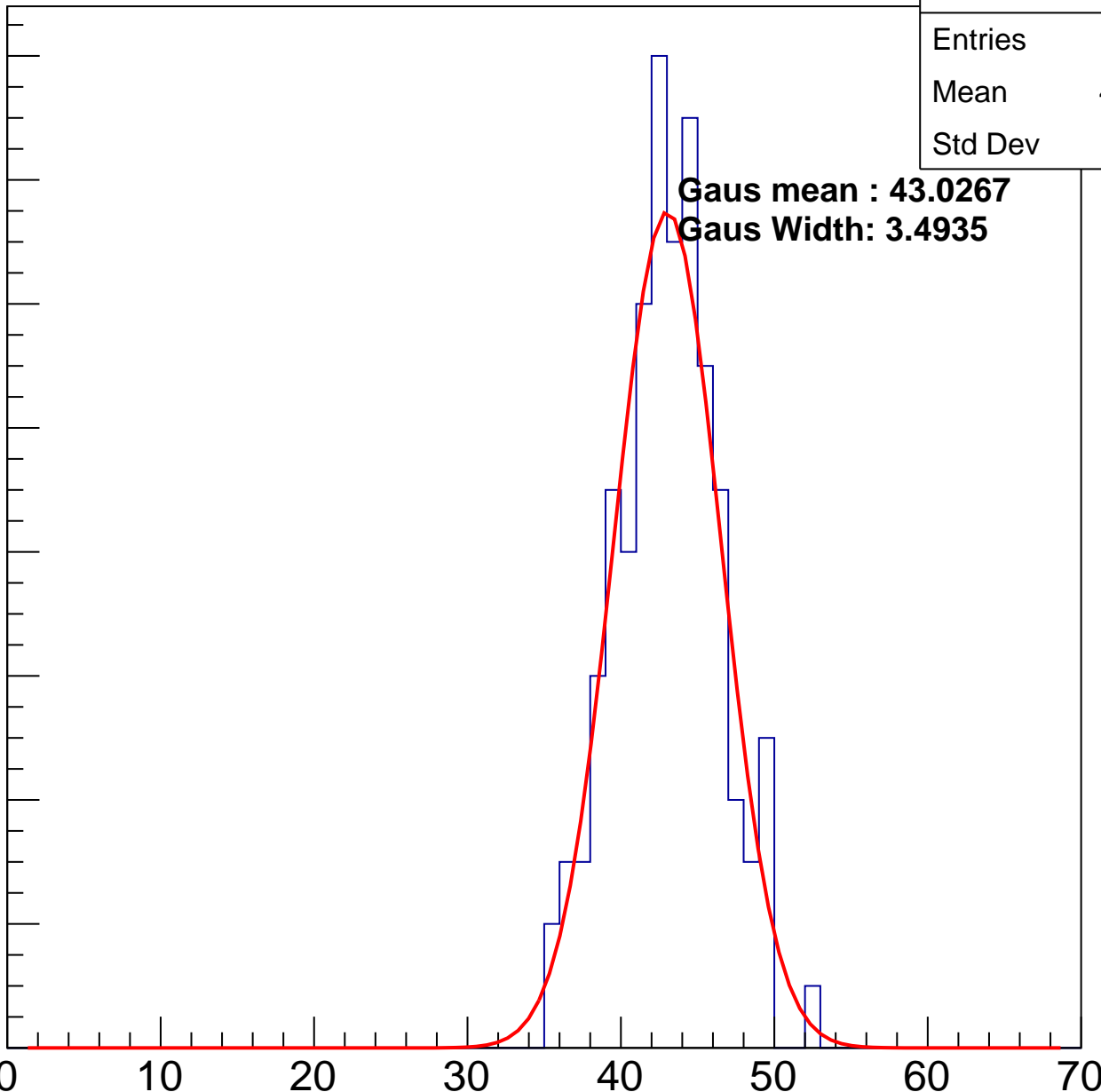
16
14
12
10
8
6
4
2
0

Entries	120
Mean	42.58
Std Dev	3.331

Gaus mean : 43.0267

Gaus Width: 3.4935

ampl

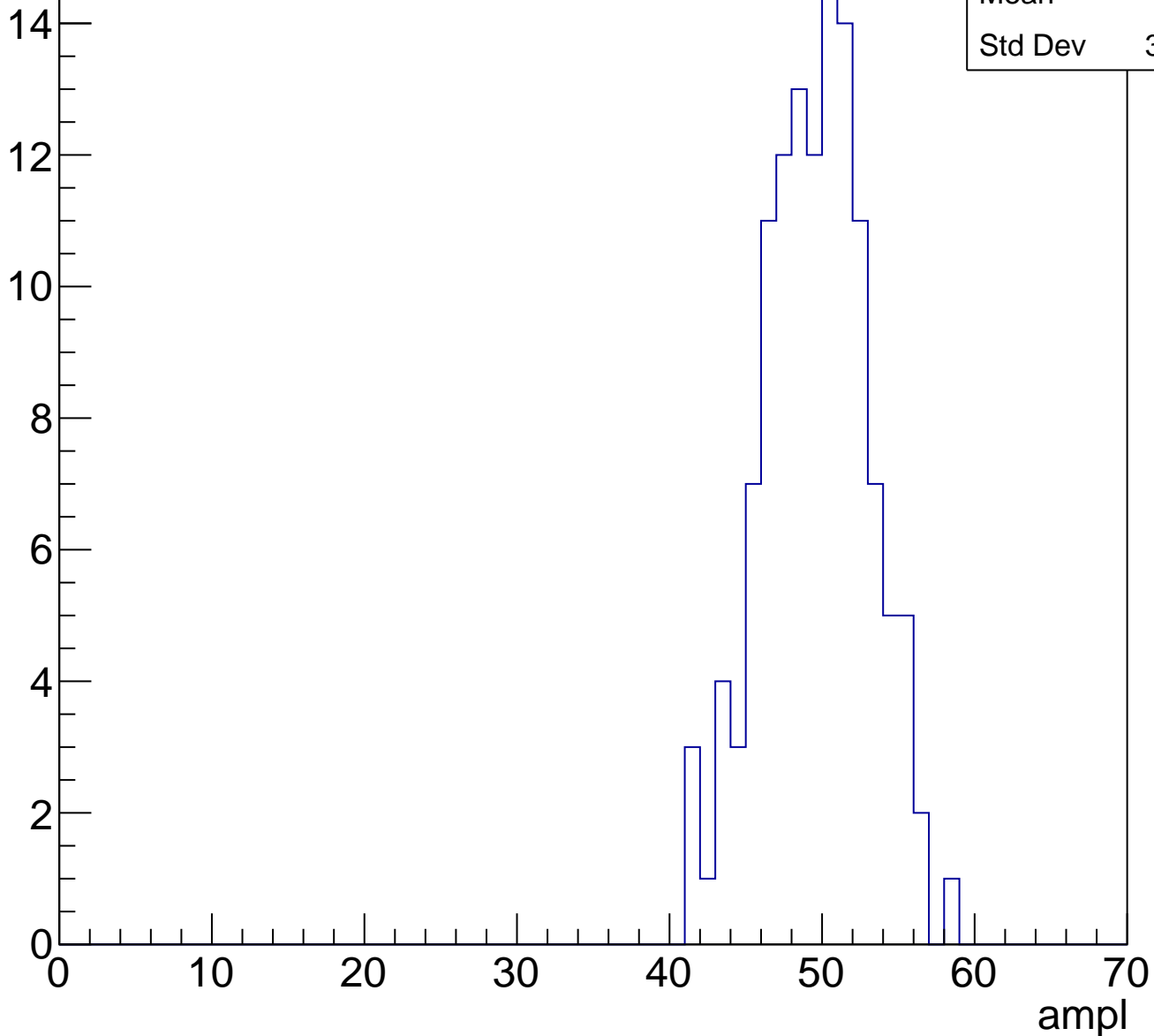


B1L001S, U16-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	49.11
Std Dev	3.446

Entry



B1L001S, U16-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	54.31
Std Dev	5.853

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

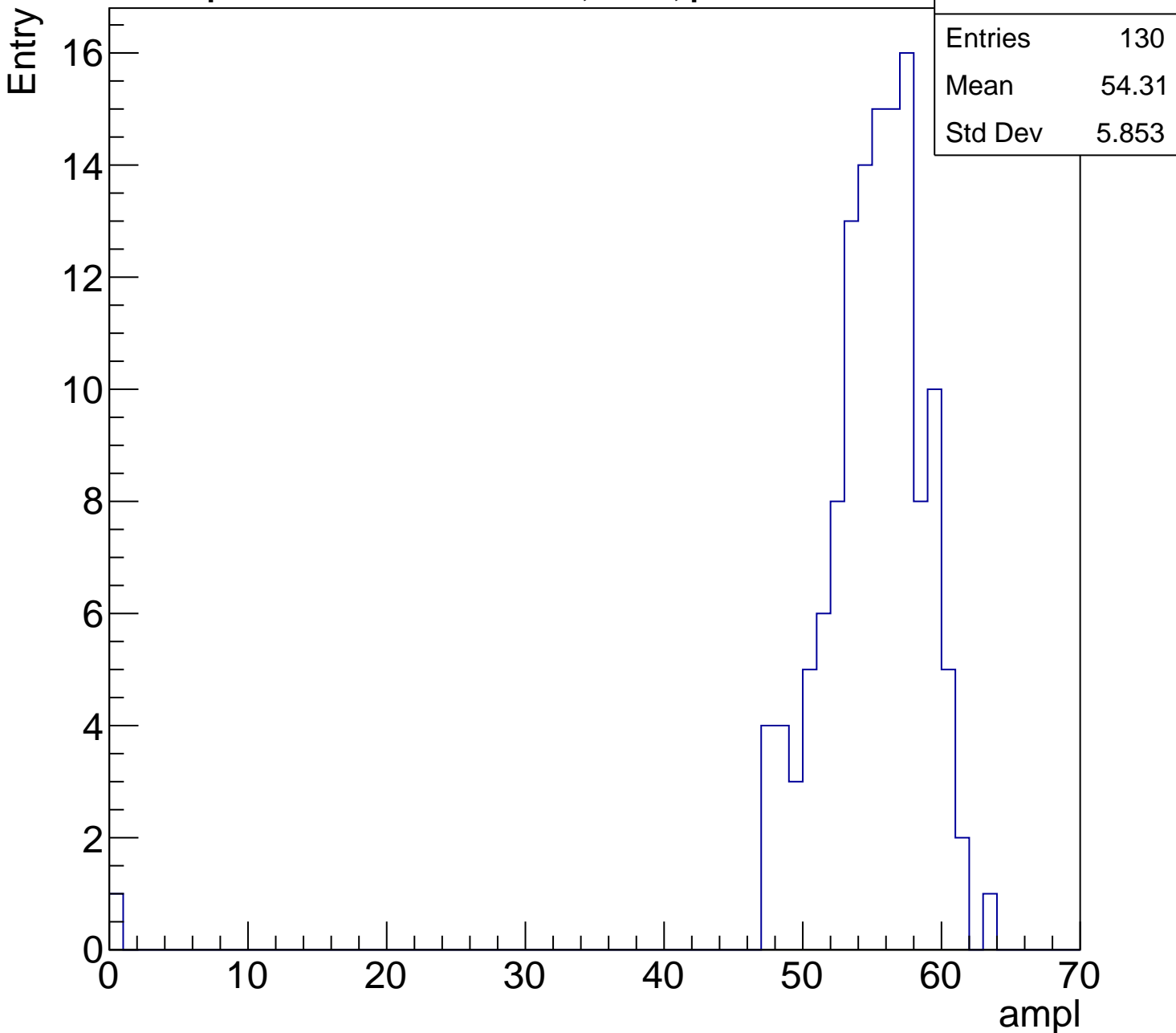
40

50

60

70

ampl



B1L001S, U16-ch44, adc5

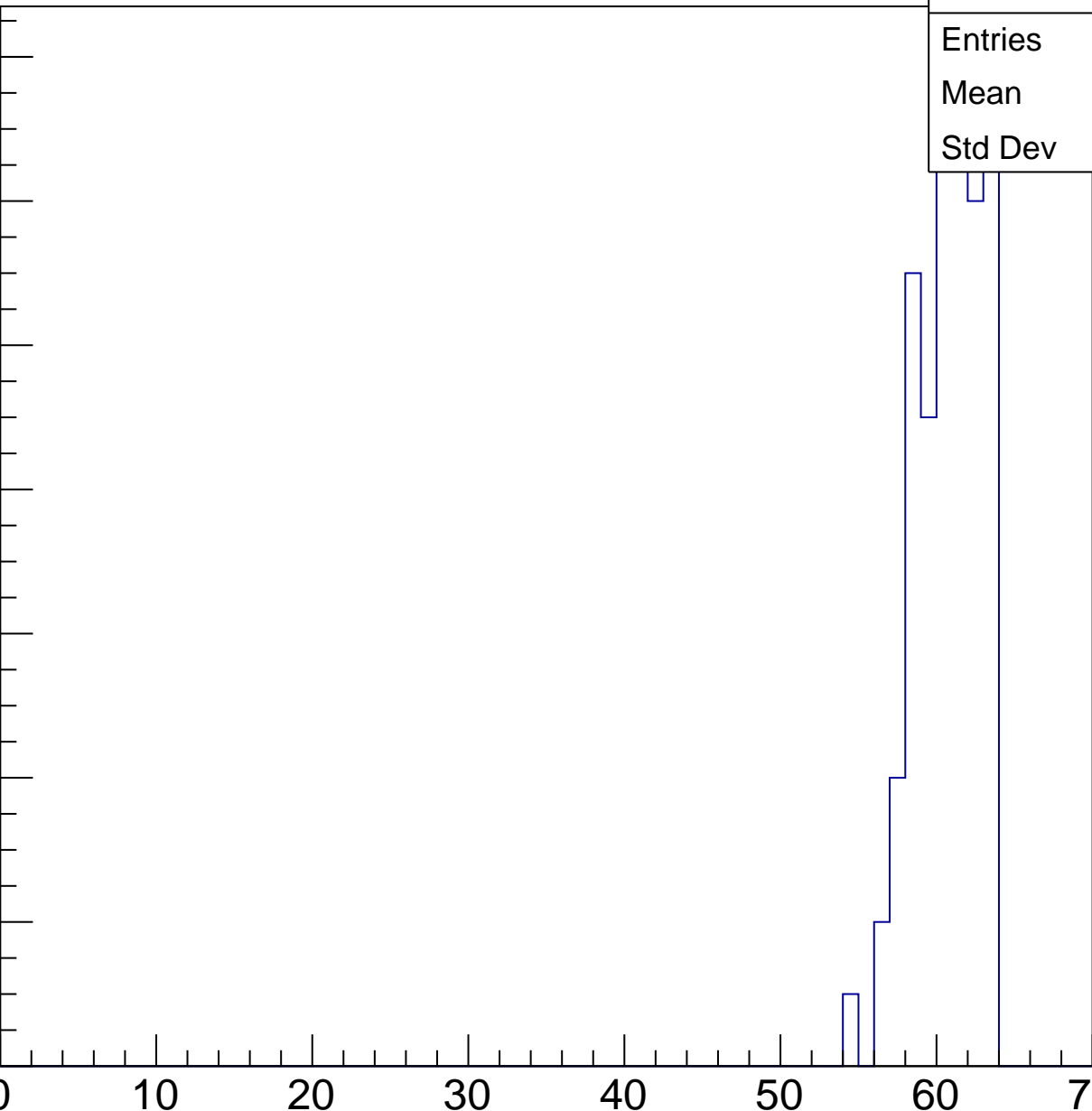
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	60.25
Std Dev	2.034

ampl

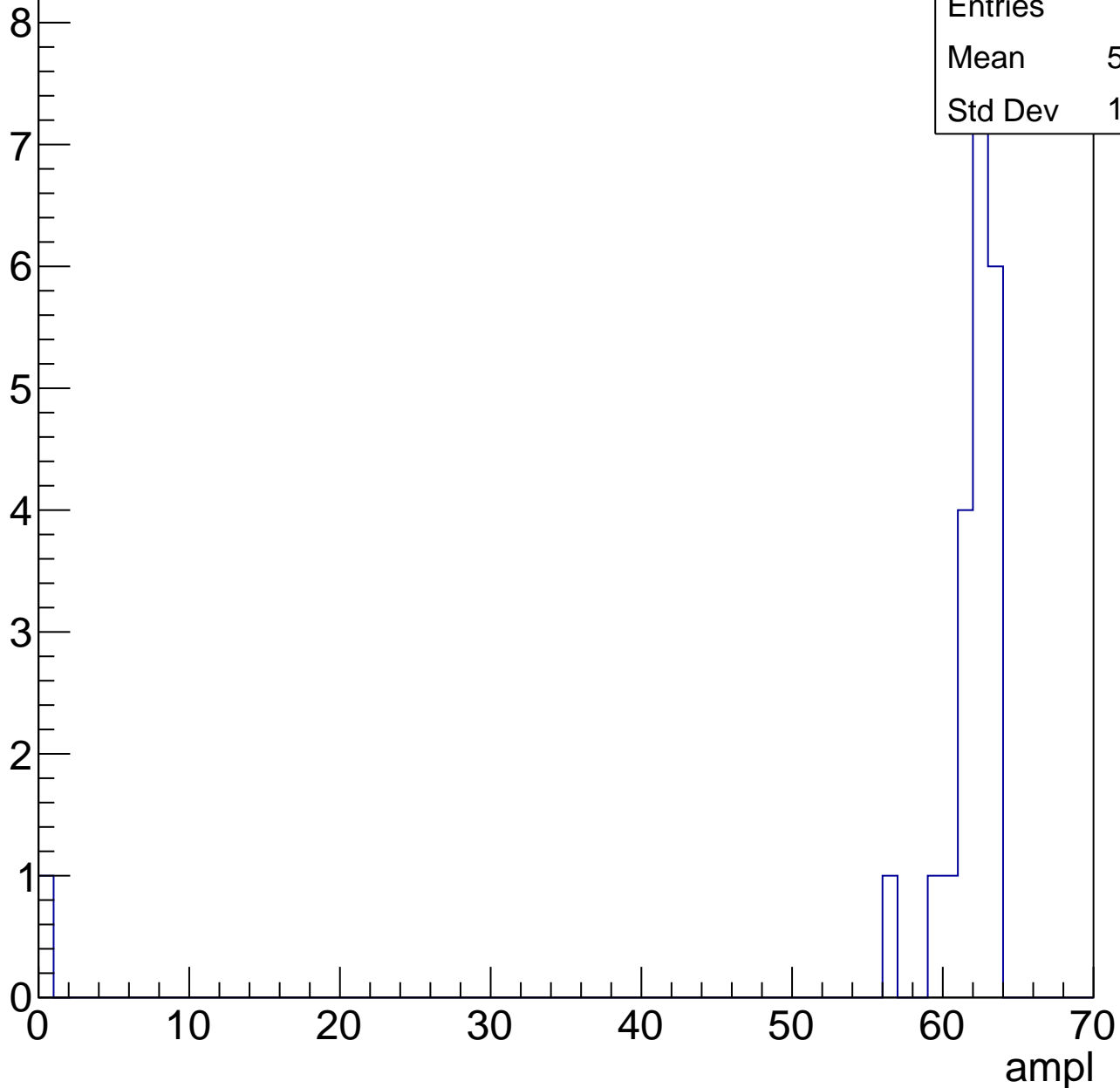


B1L001S, U16-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

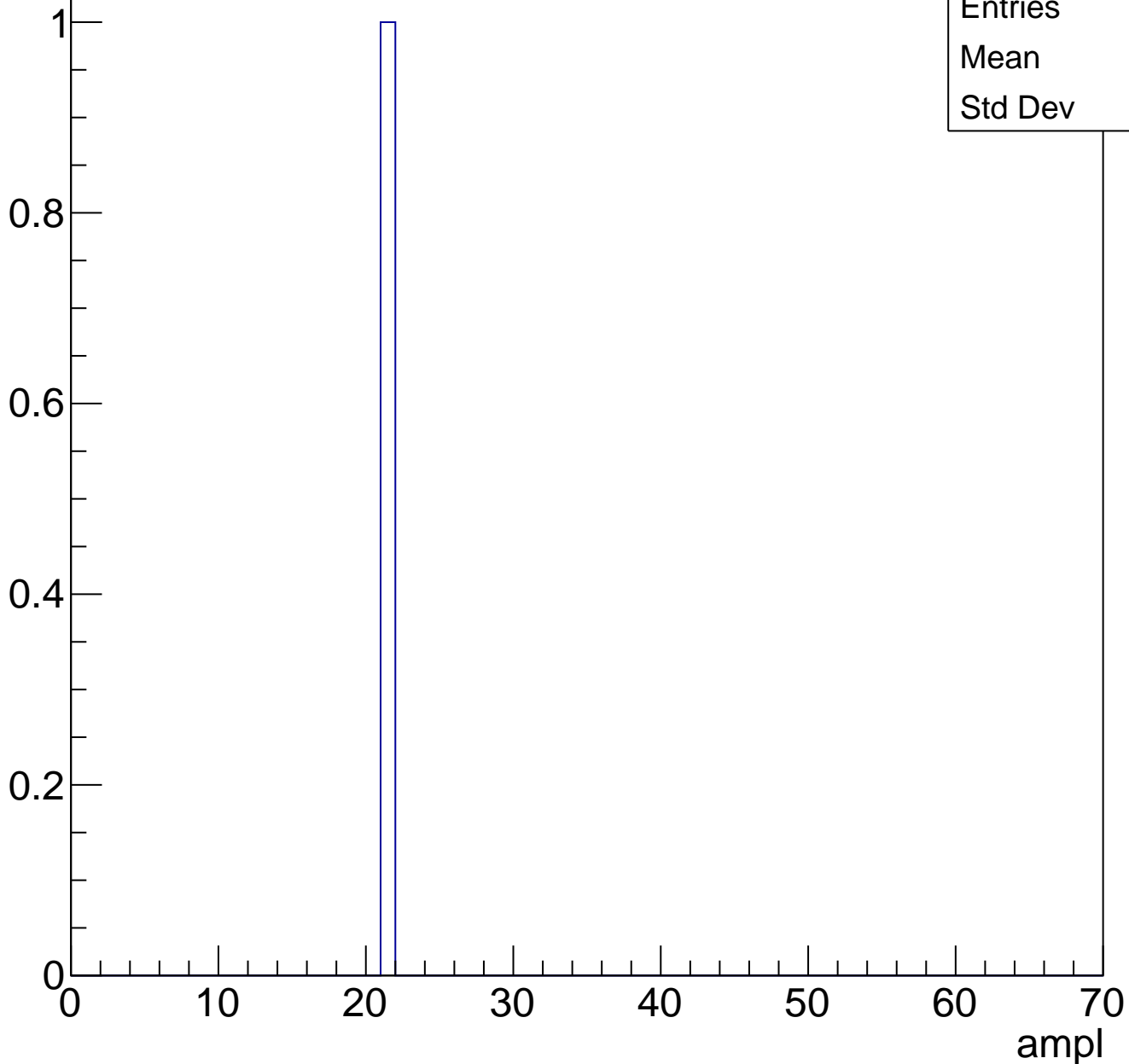
Entries	22
Mean	58.77
Std Dev	12.92



B1L001S, U16-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch45, adc0

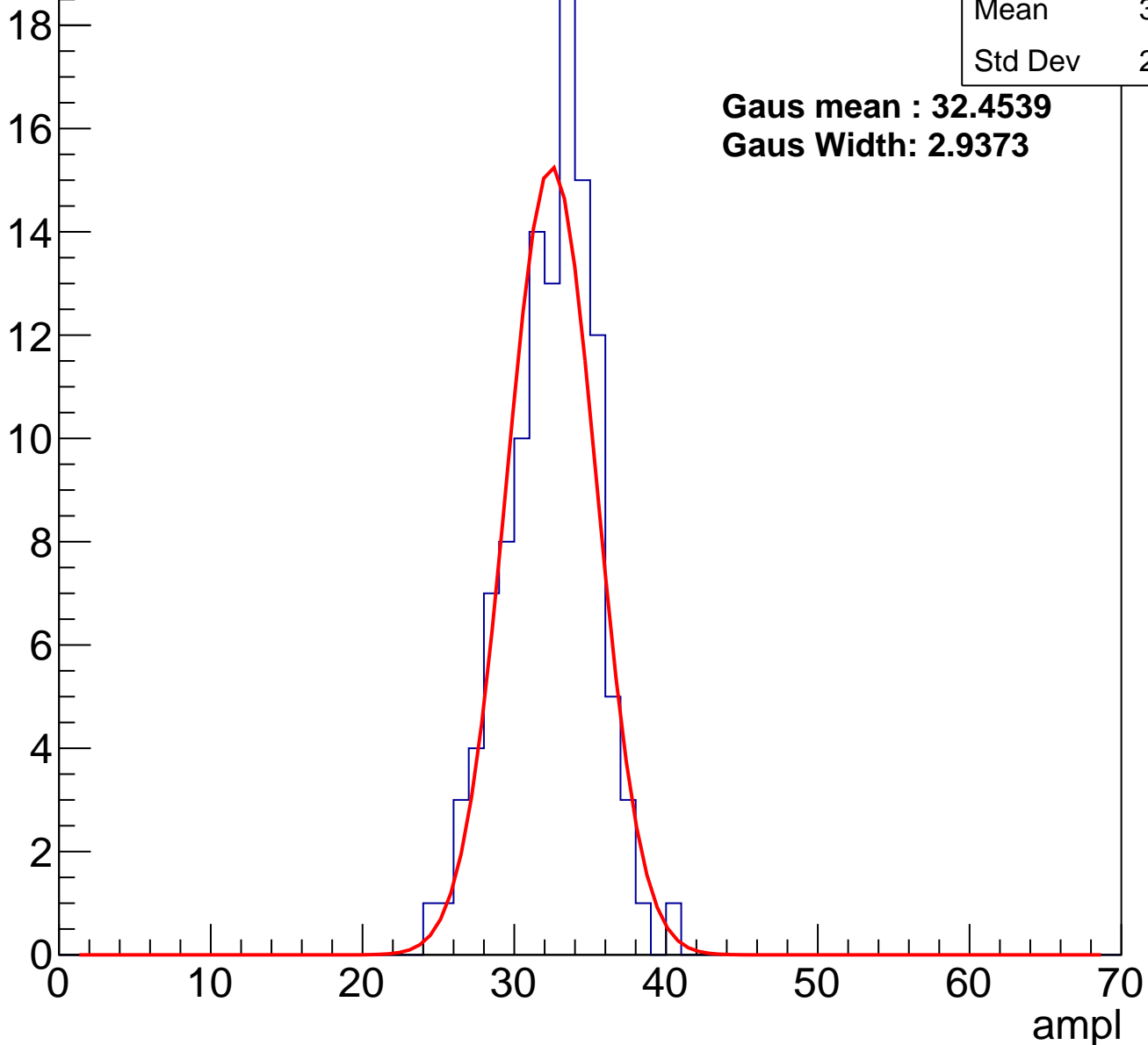
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	31.96
Std Dev	2.925

Gaus mean : 32.4539

Gaus Width: 2.9373

Entry

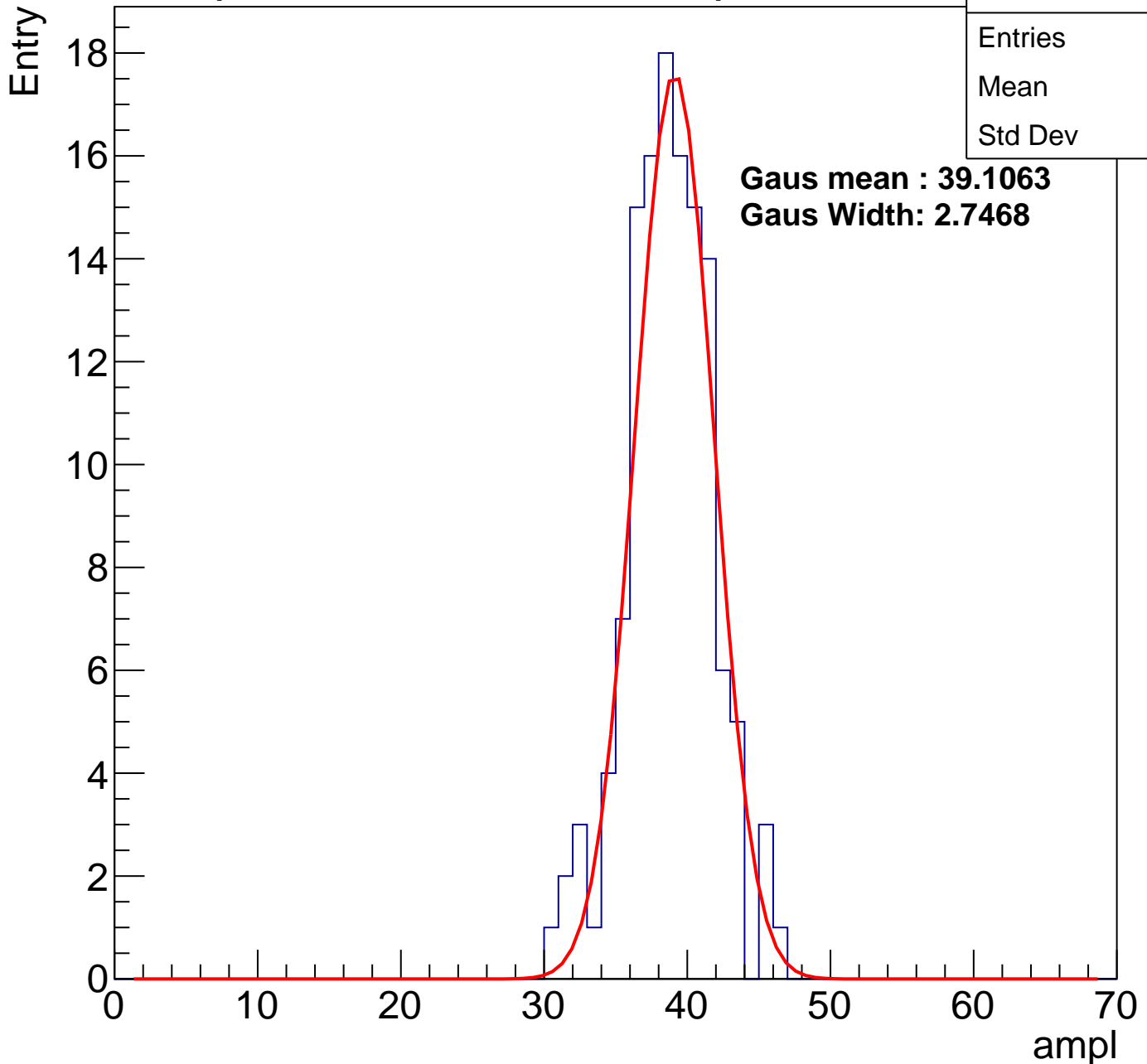


B1L001S, U16-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	38.3
Std Dev	2.97

Gaus mean : 39.1063
Gaus Width: 2.7468



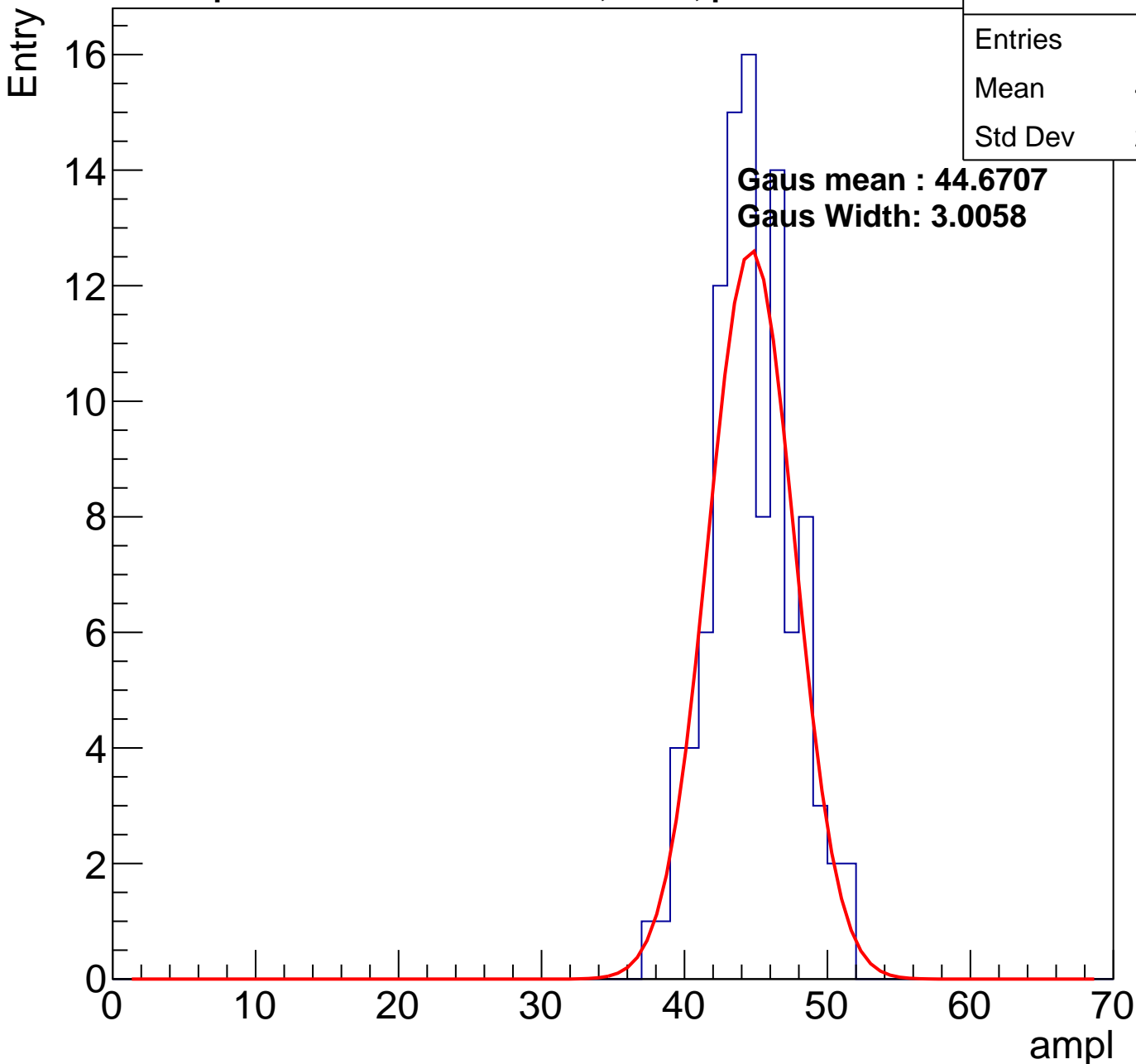
B1L001S, U16-ch45, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	44.21
Std Dev	2.891

Gaus mean : 44.6707

Gaus Width: 3.0058



B1L001S, U16-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

142

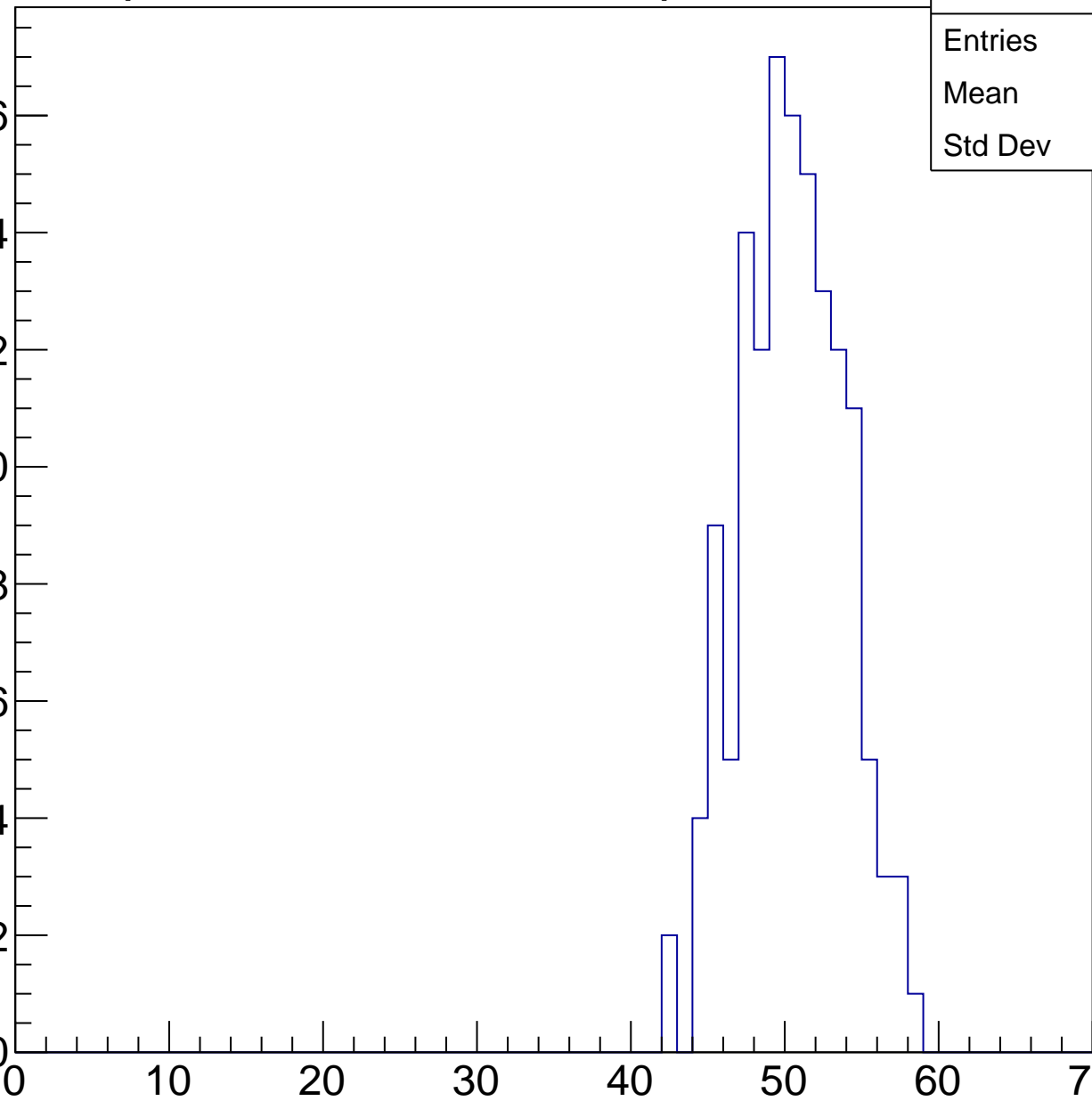
Mean

50.04

Std Dev

3.317

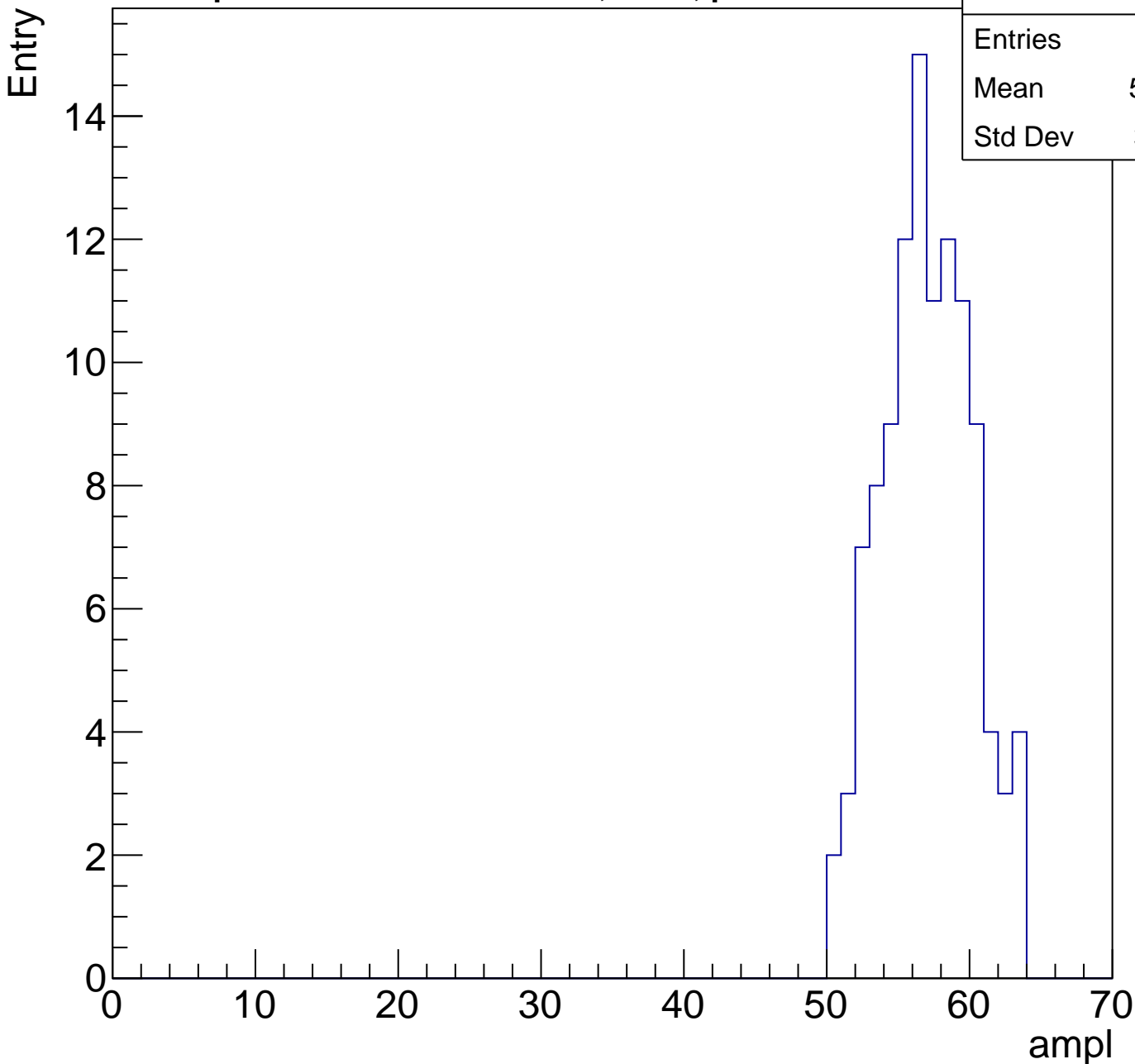
ampl



B1L001S, U16-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56.55
Std Dev	3.091



B1L001S, U16-ch45, adc5

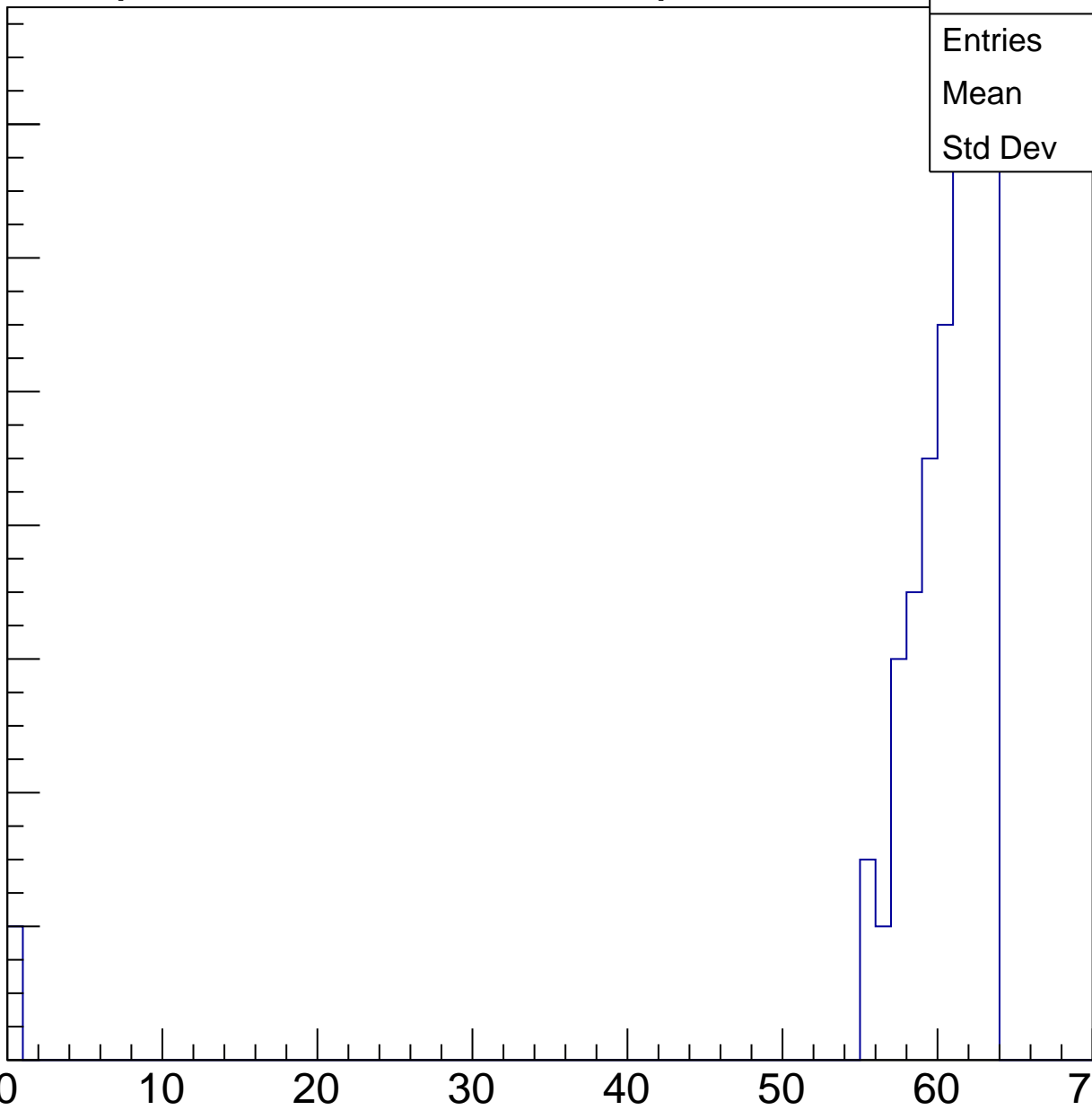
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	58.85
Std Dev	9.441

ampl



B1L001S, U16-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

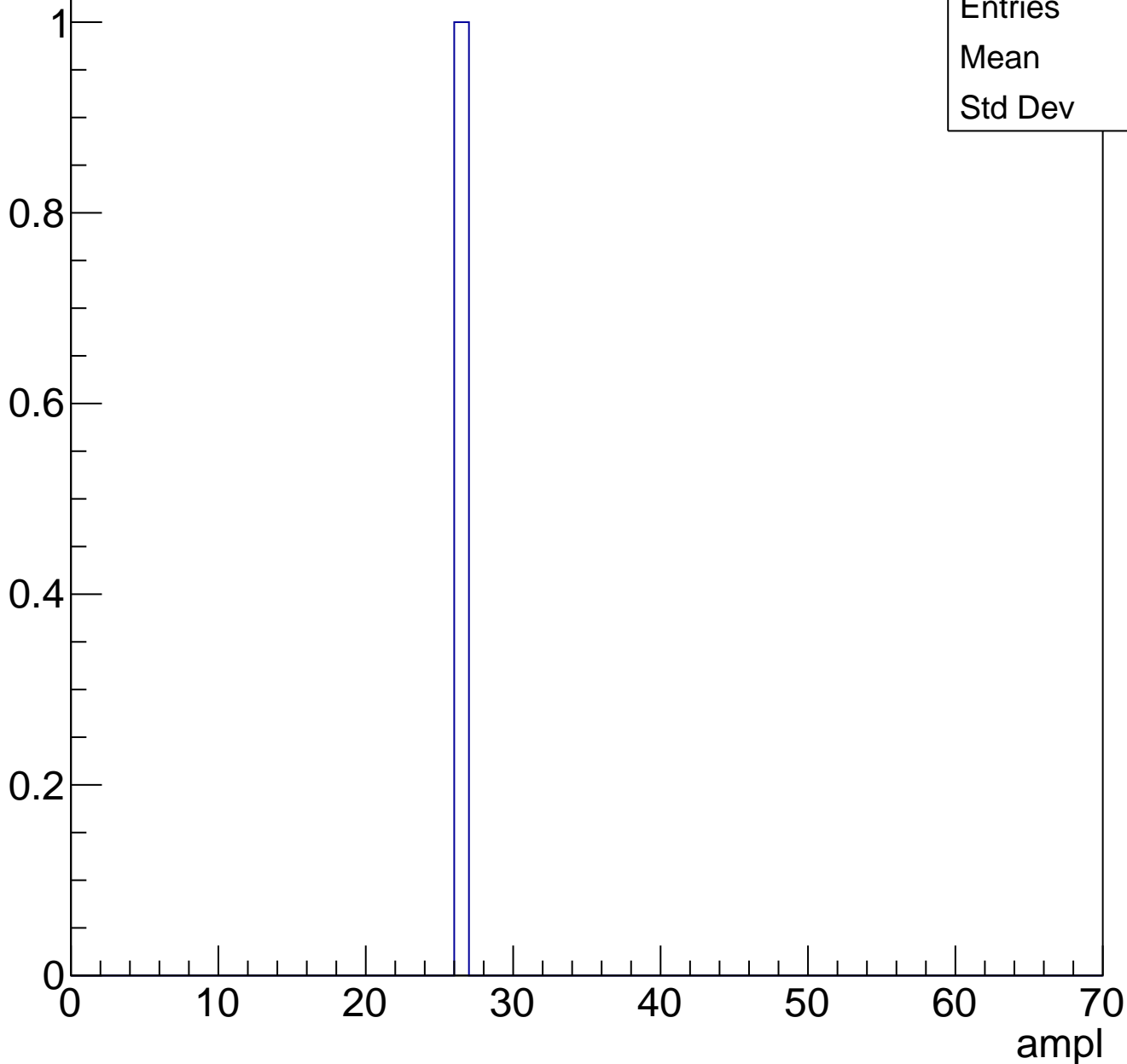
ampl

0 10 20 30 40 50 60 70

B1L001S, U16-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch46, adc0

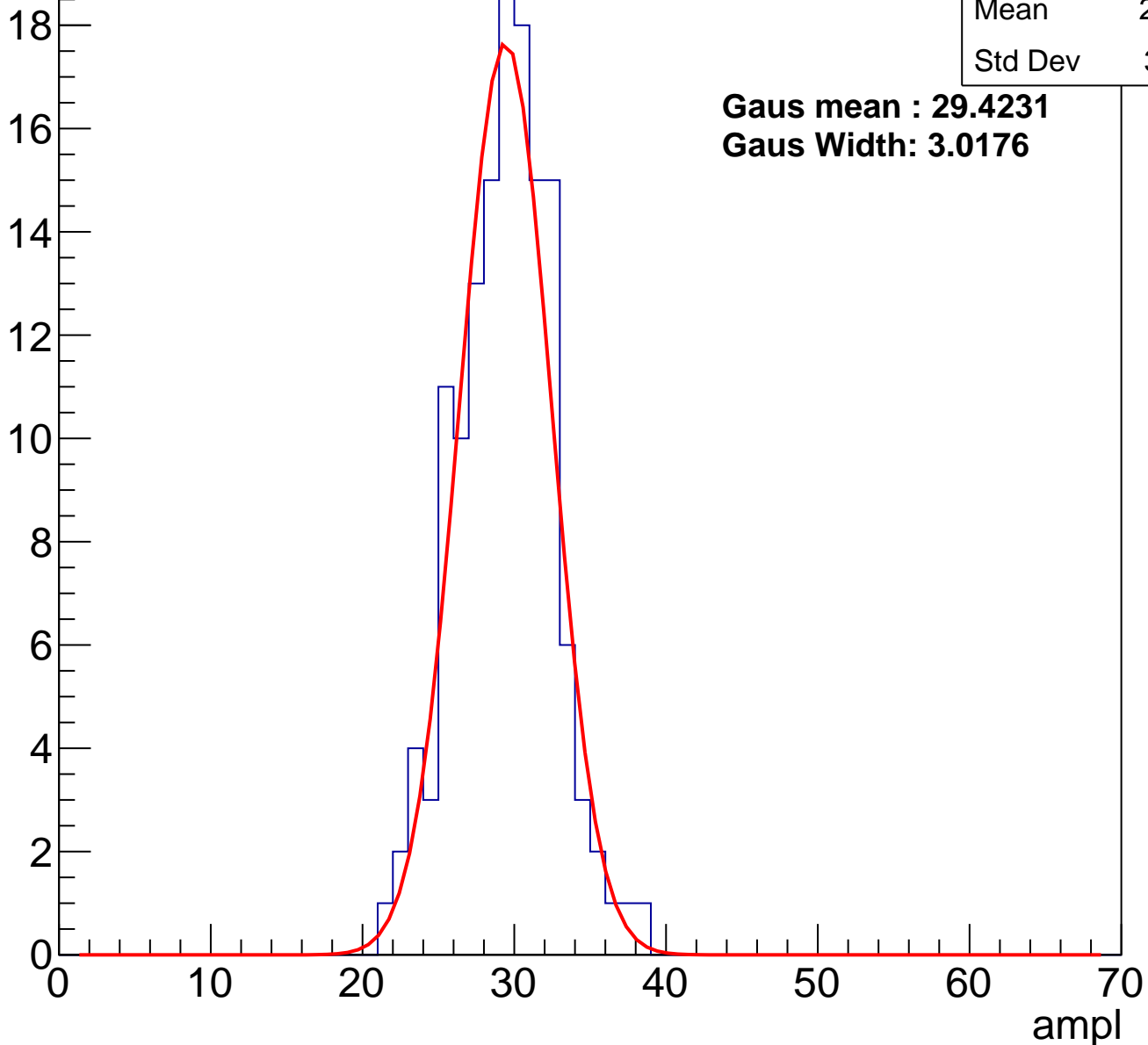
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.94
Std Dev	3.121

Gaus mean : 29.4231

Gaus Width: 3.0176

Entry



B1L001S, U16-ch46, adc1

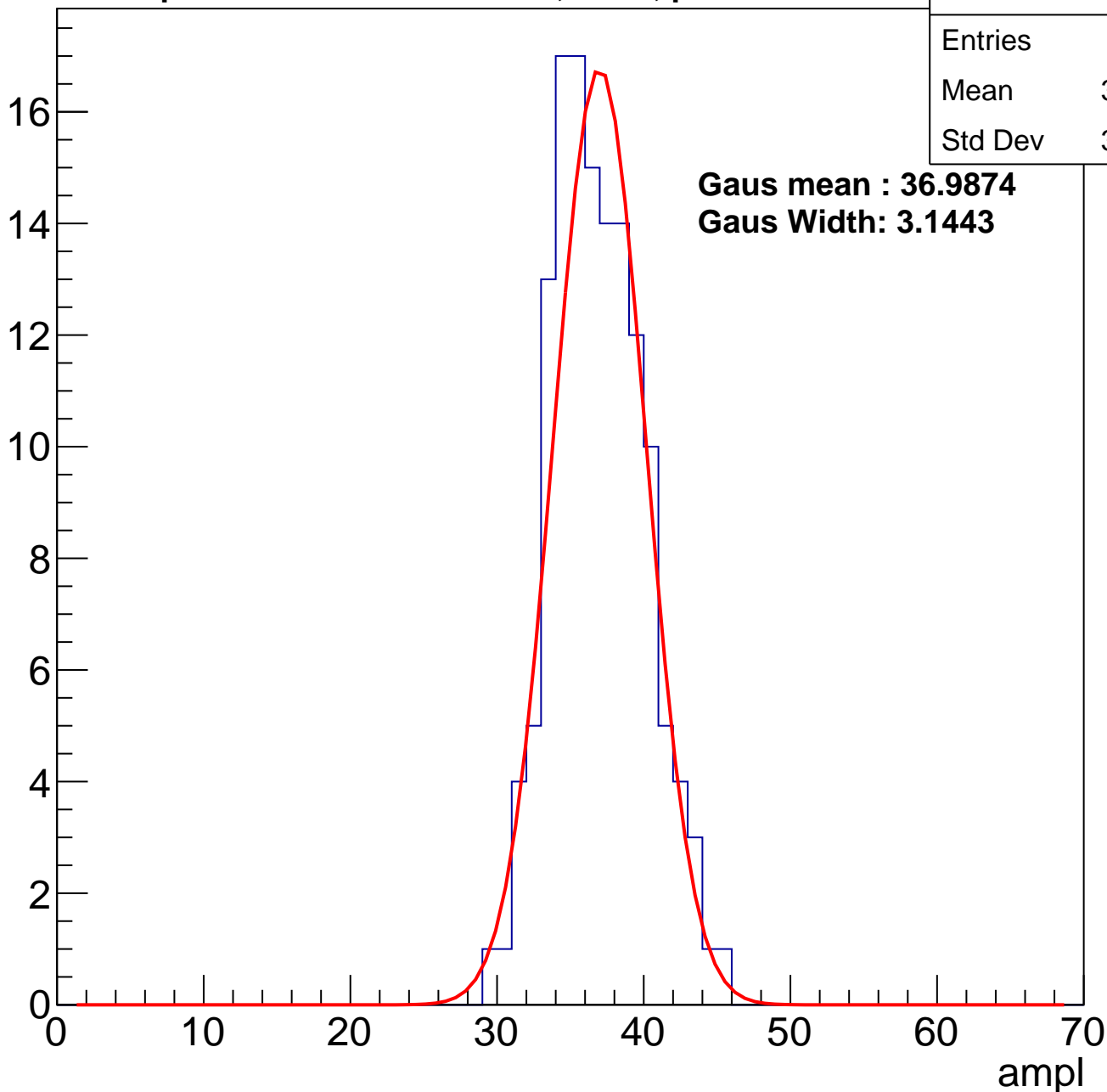
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	36.45
Std Dev	3.125

Gaus mean : 36.9874

Gaus Width: 3.1443

Entry



B1L001S, U16-ch46, adc2

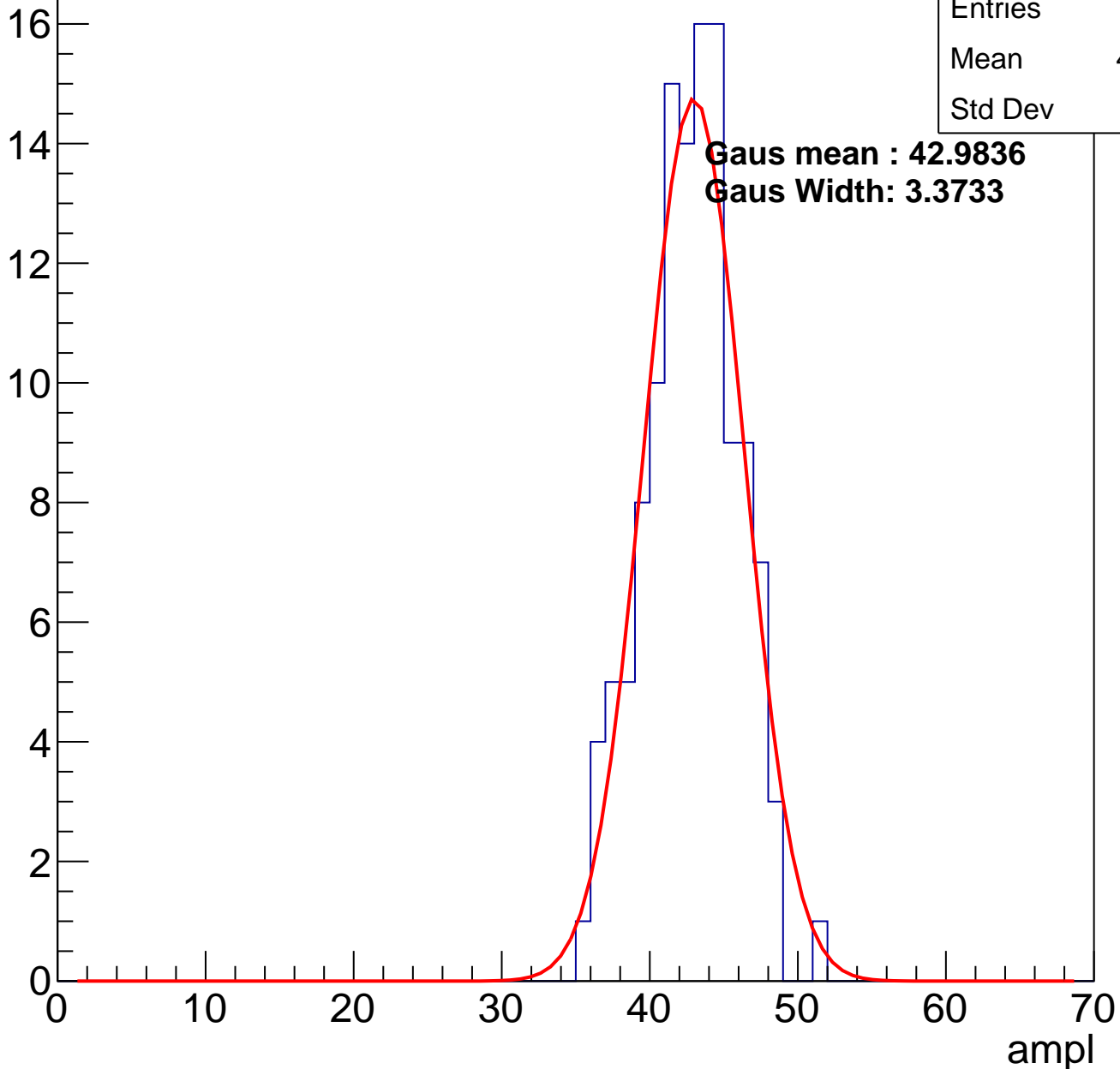
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	42.31
Std Dev	3.1

Gaus mean : 42.9836

Gaus Width: 3.3733

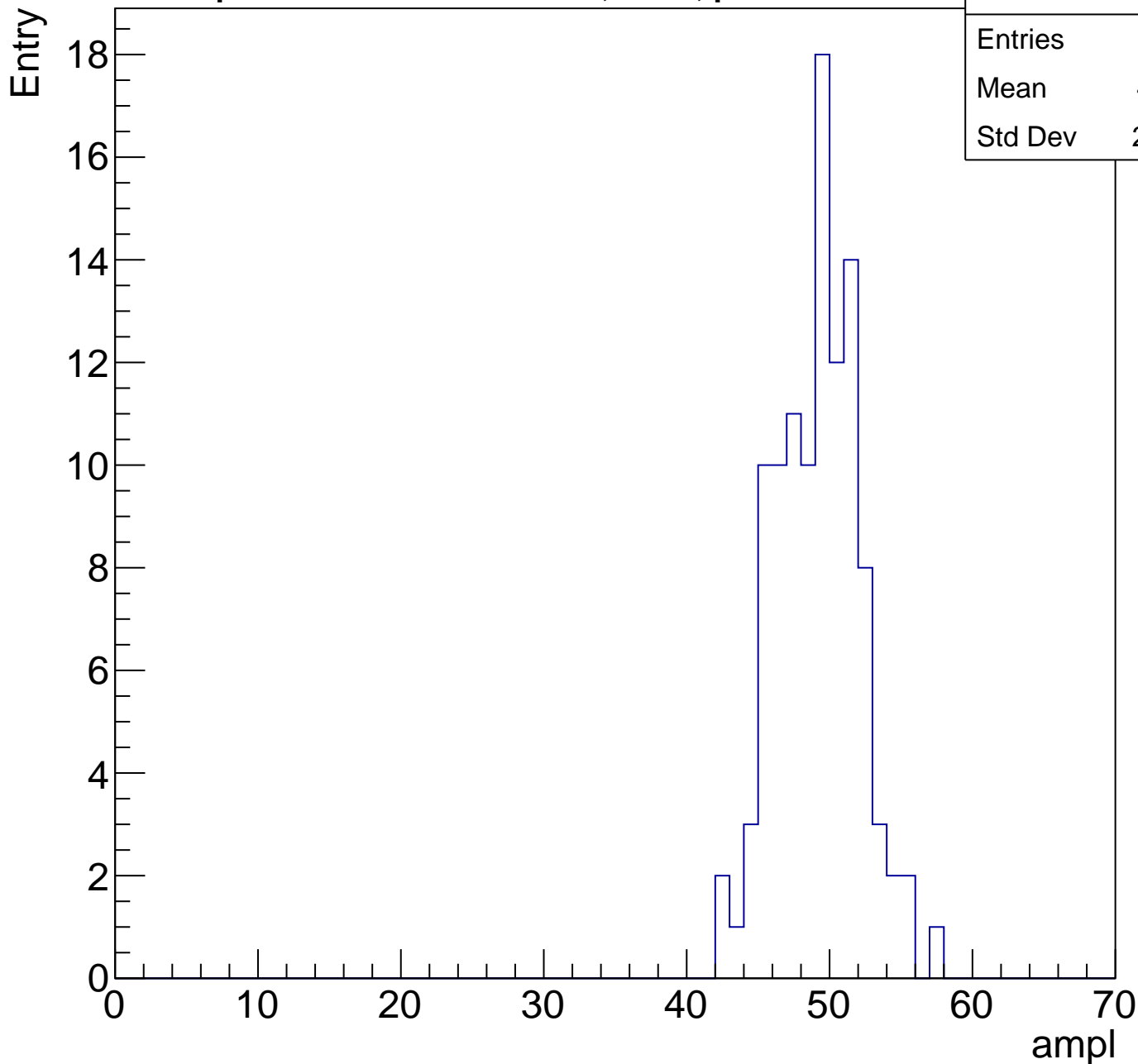
Entry



B1L001S, U16-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	48.71
Std Dev	2.865



B1L001S, U16-ch46, adc4

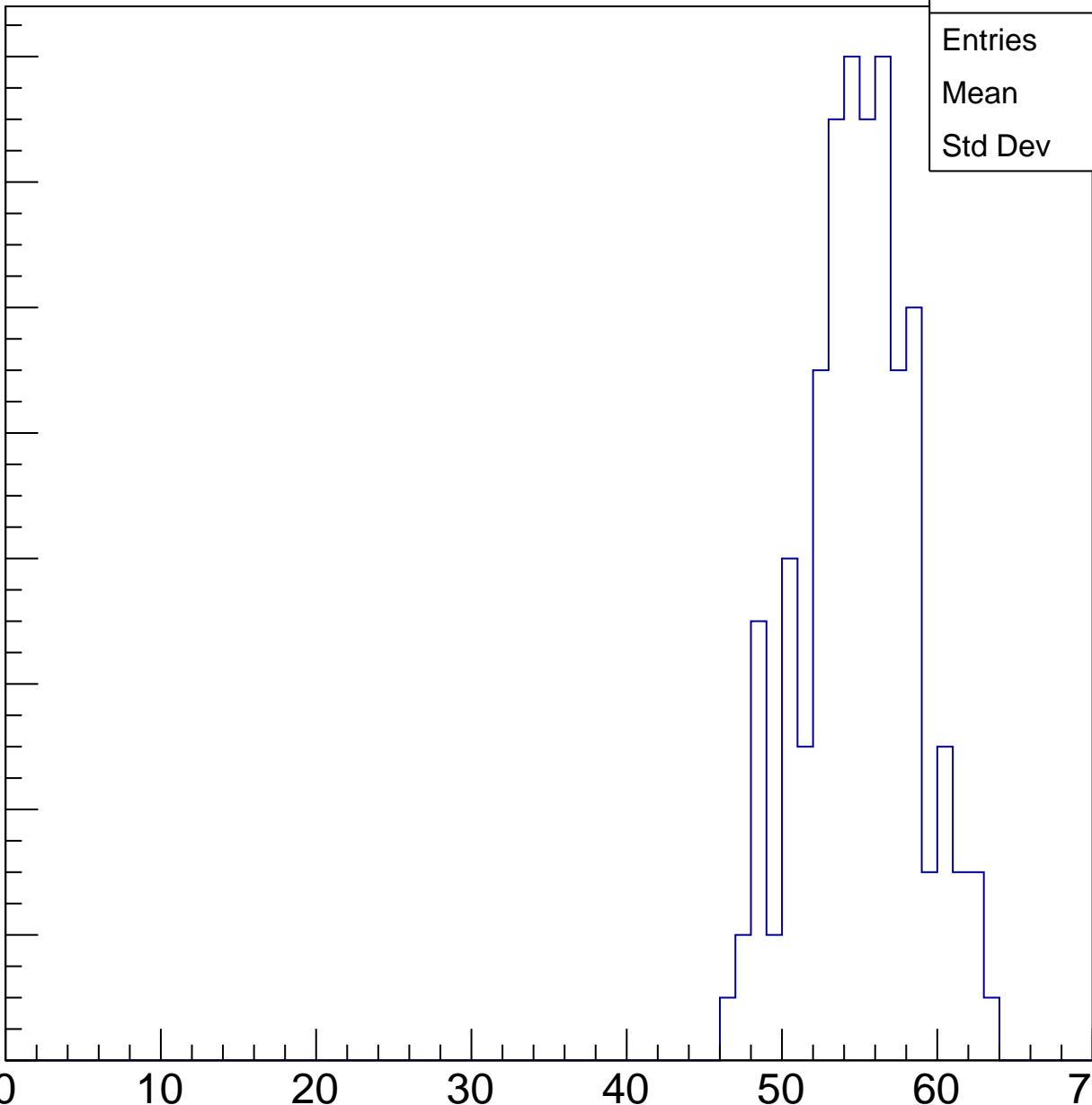
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	54.51
Std Dev	3.542

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch46, adc5

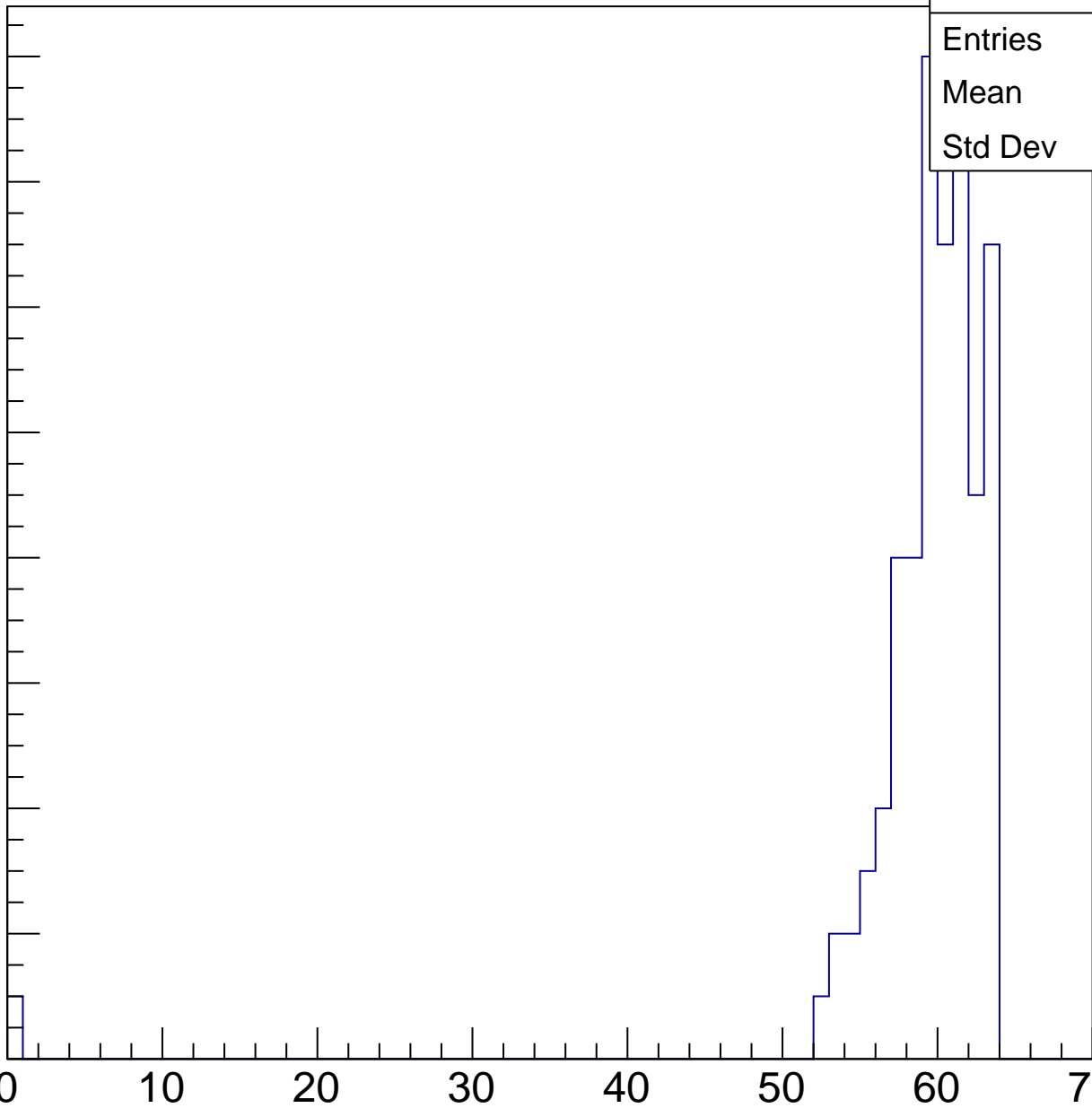
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.85
Std Dev	6.598

ampl

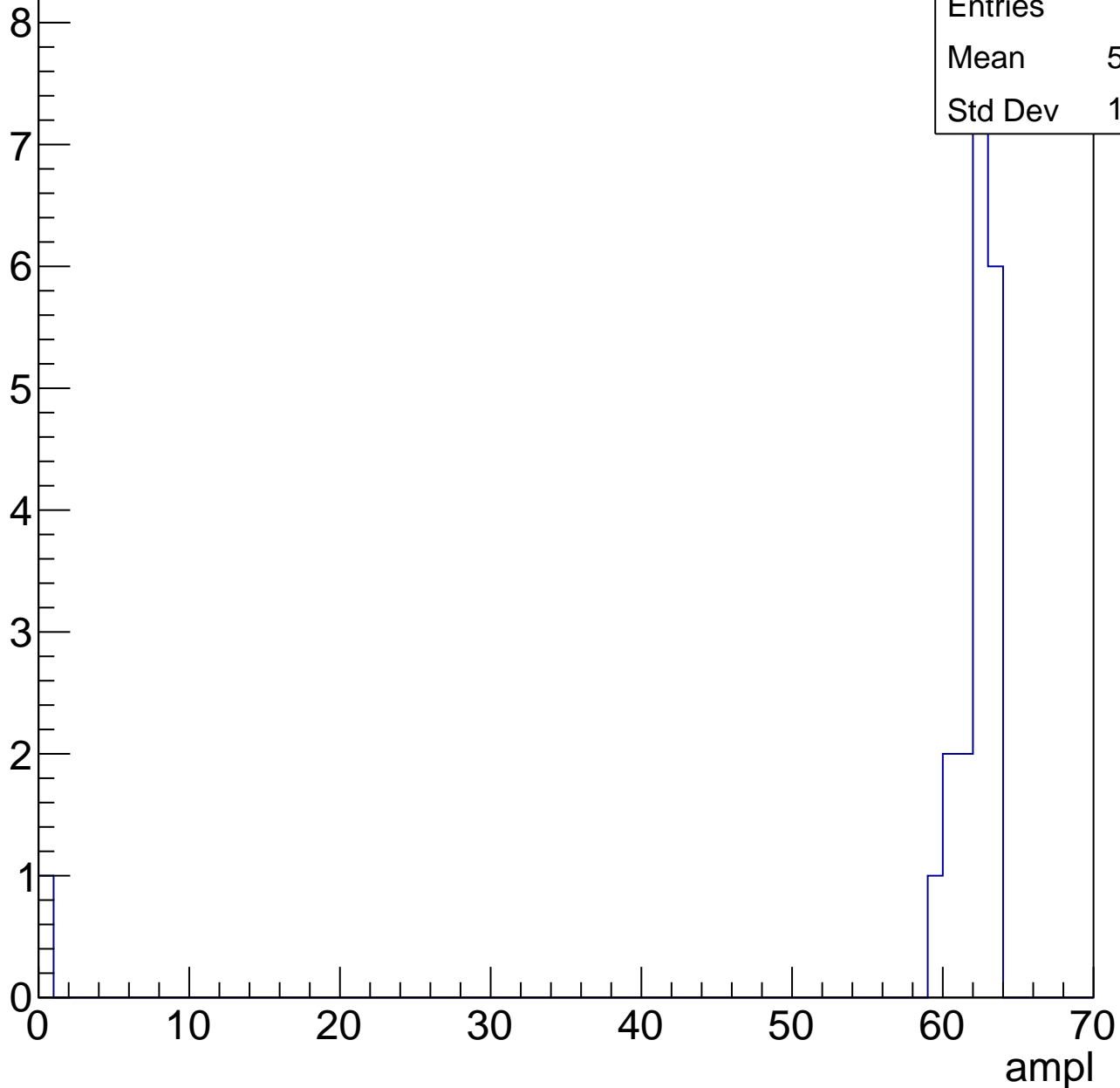


B1L001S, U16-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	58.75
Std Dev	13.52



B1L001S, U16-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

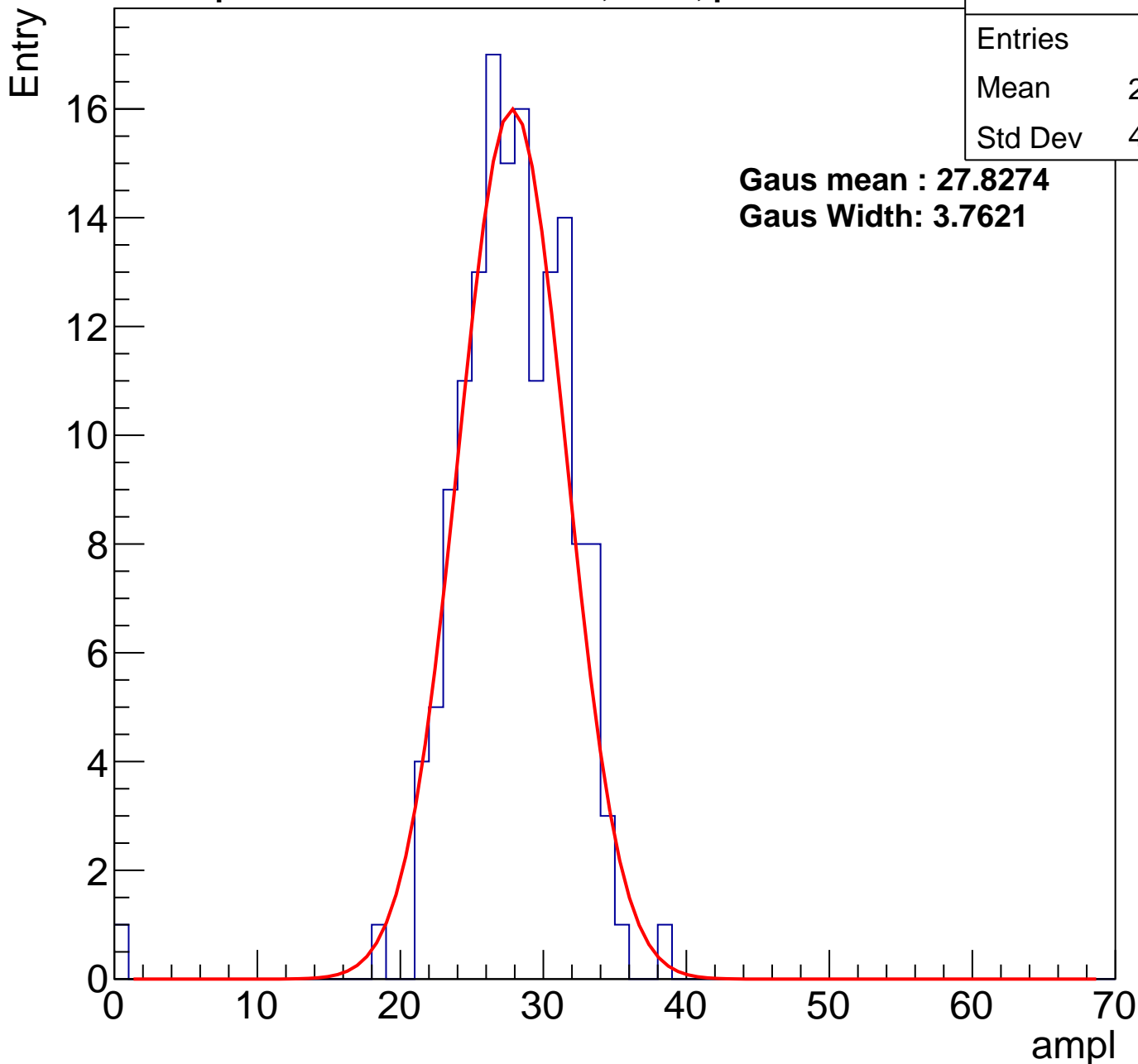
B1L001S, U16-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	27.42
Std Dev	4.137

Gaus mean : 27.8274

Gaus Width: 3.7621



B1L001S, U16-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	34.25
Std Dev	3.444

Gaus mean : 34.9110

Gaus Width: 3.7246

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

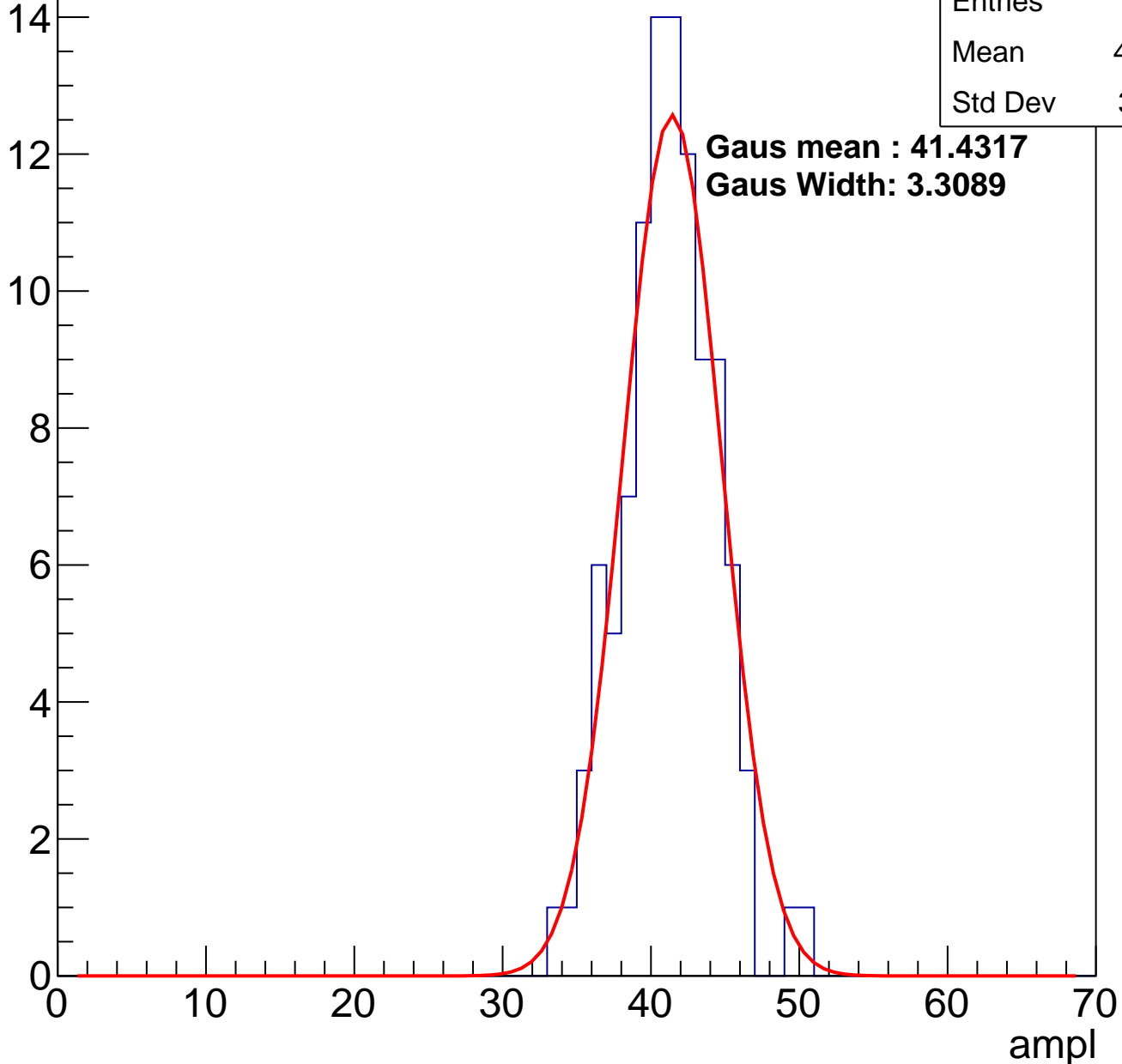
60

70

B1L001S, U16-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch47, adc3

calib_packv5_042523_0143.root, FC#2, port C2

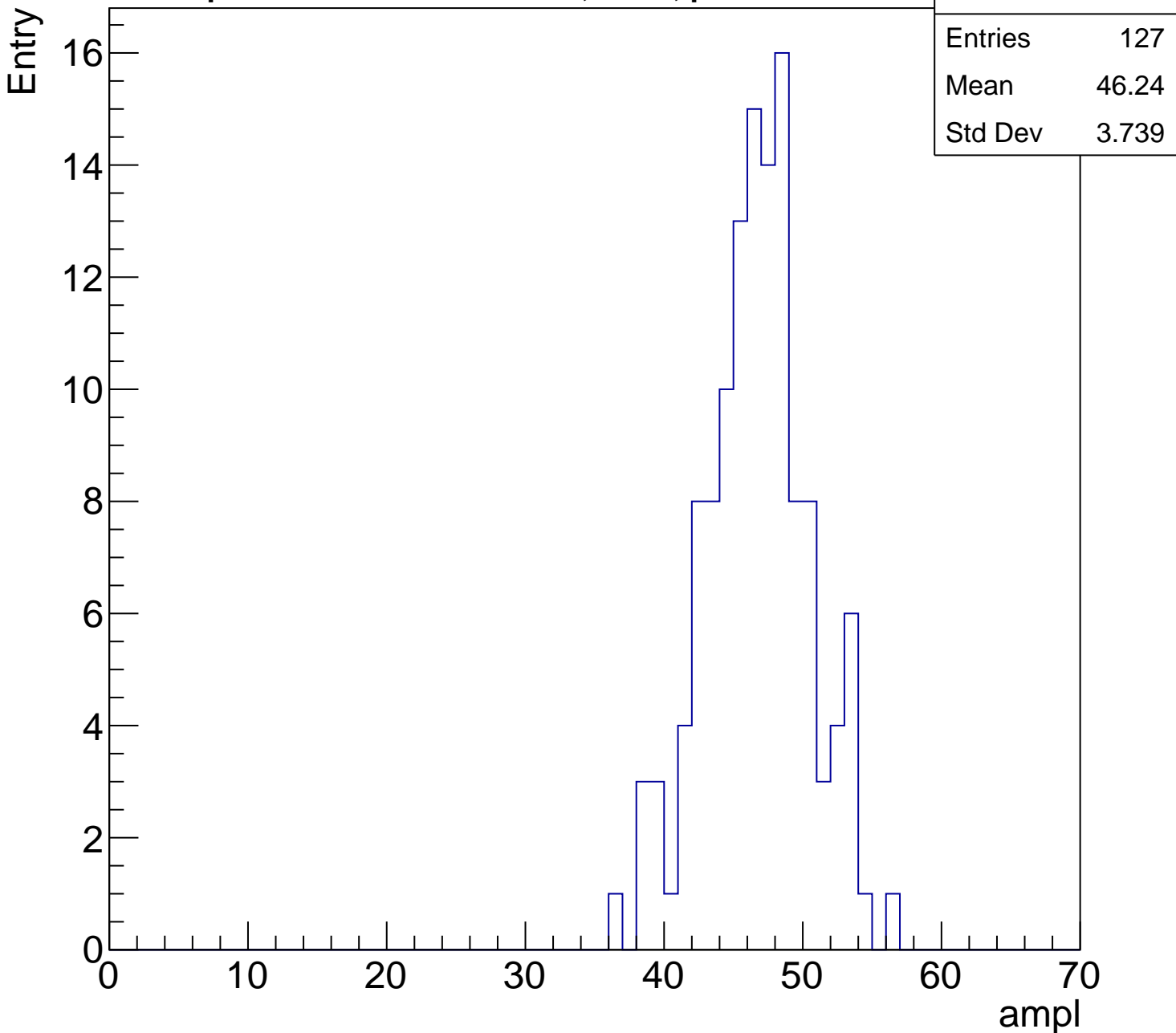
Entries	127
Mean	46.24
Std Dev	3.739

Entry

16
14
12
10
8
6
4
2
0

ampl

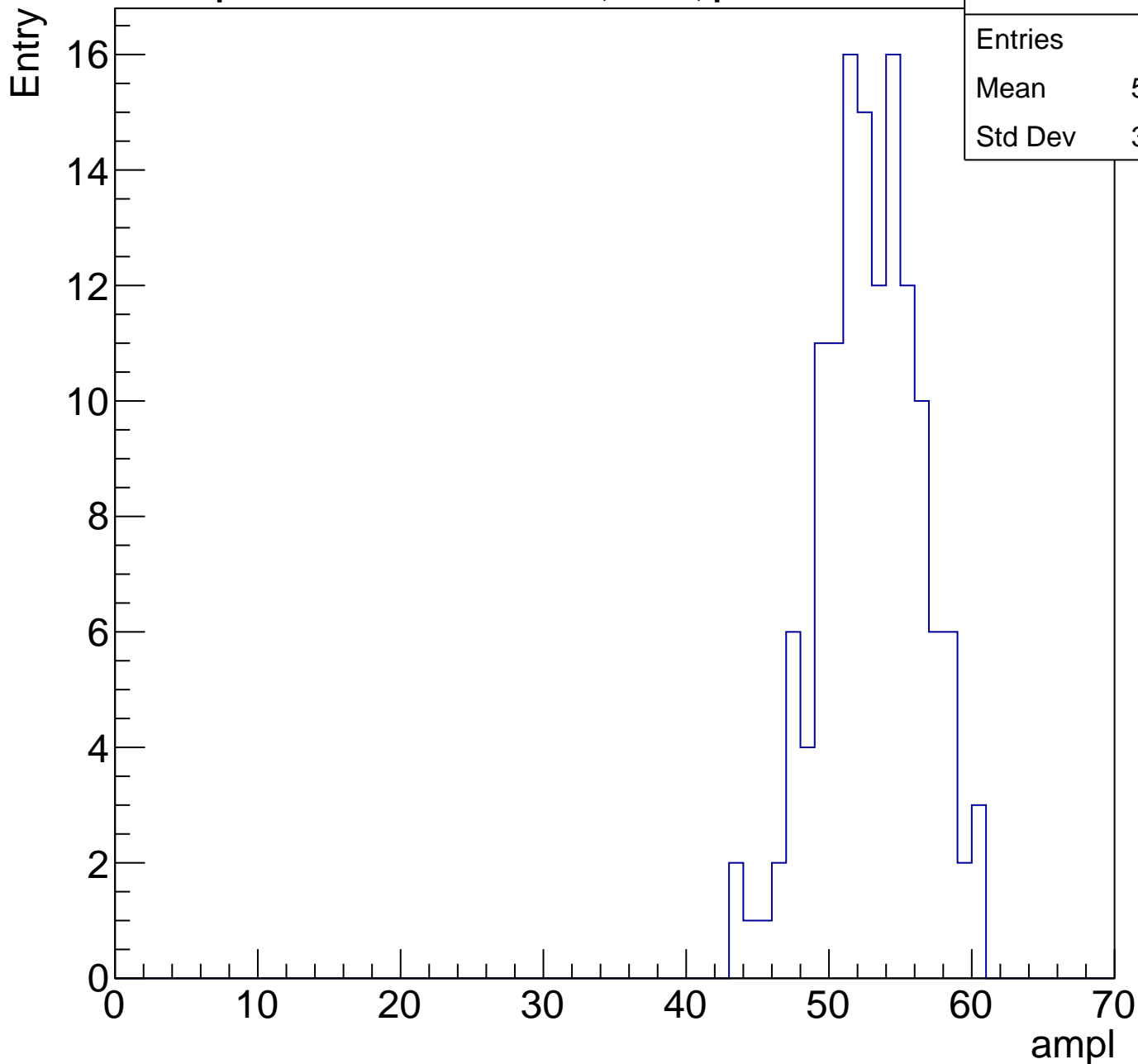
0 10 20 30 40 50 60 70



B1L001S, U16-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	52.46
Std Dev	3.527



B1L001S, U16-ch47, adc5

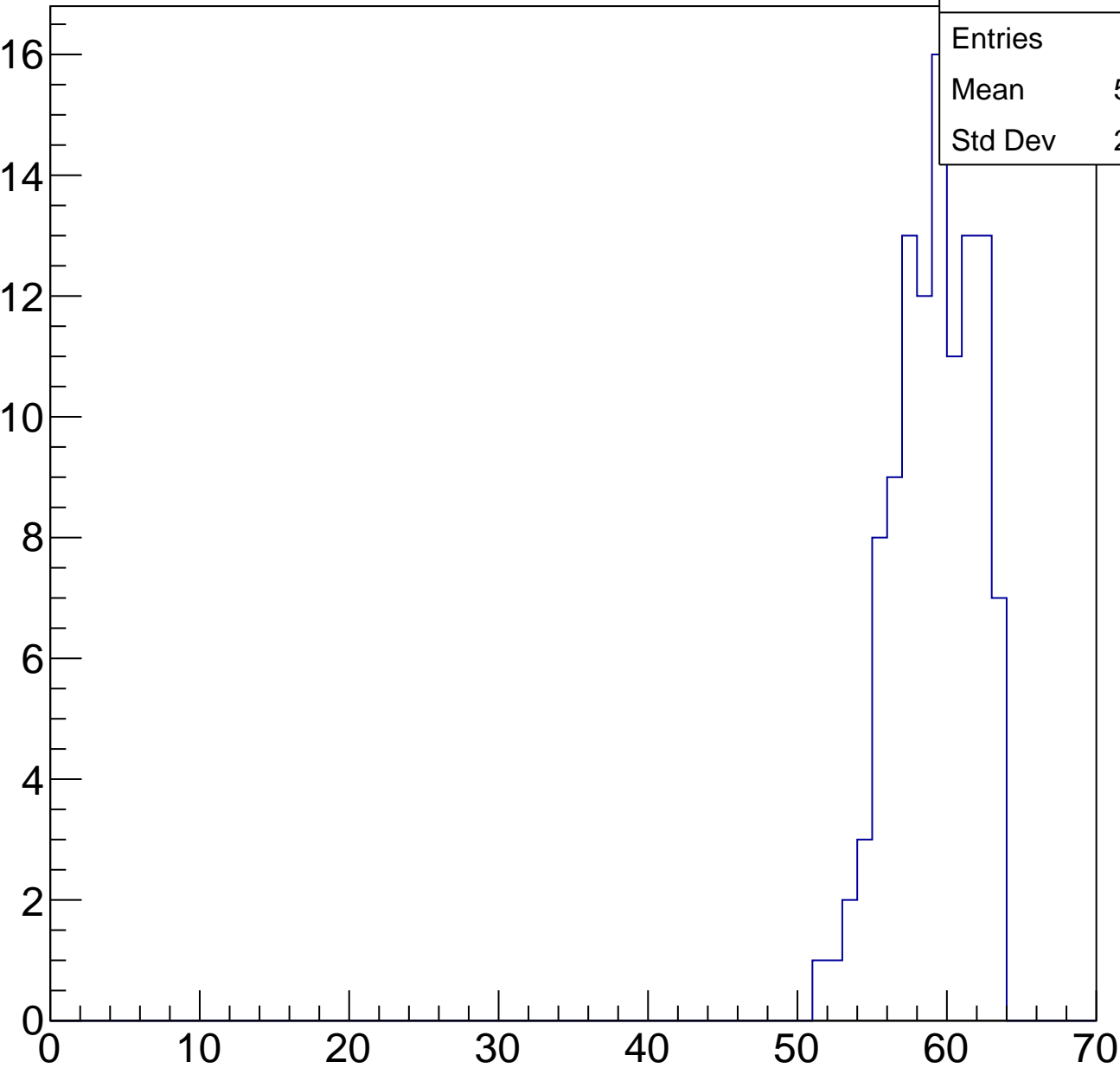
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.68
Std Dev	2.732

ampl



B1L001S, U16-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

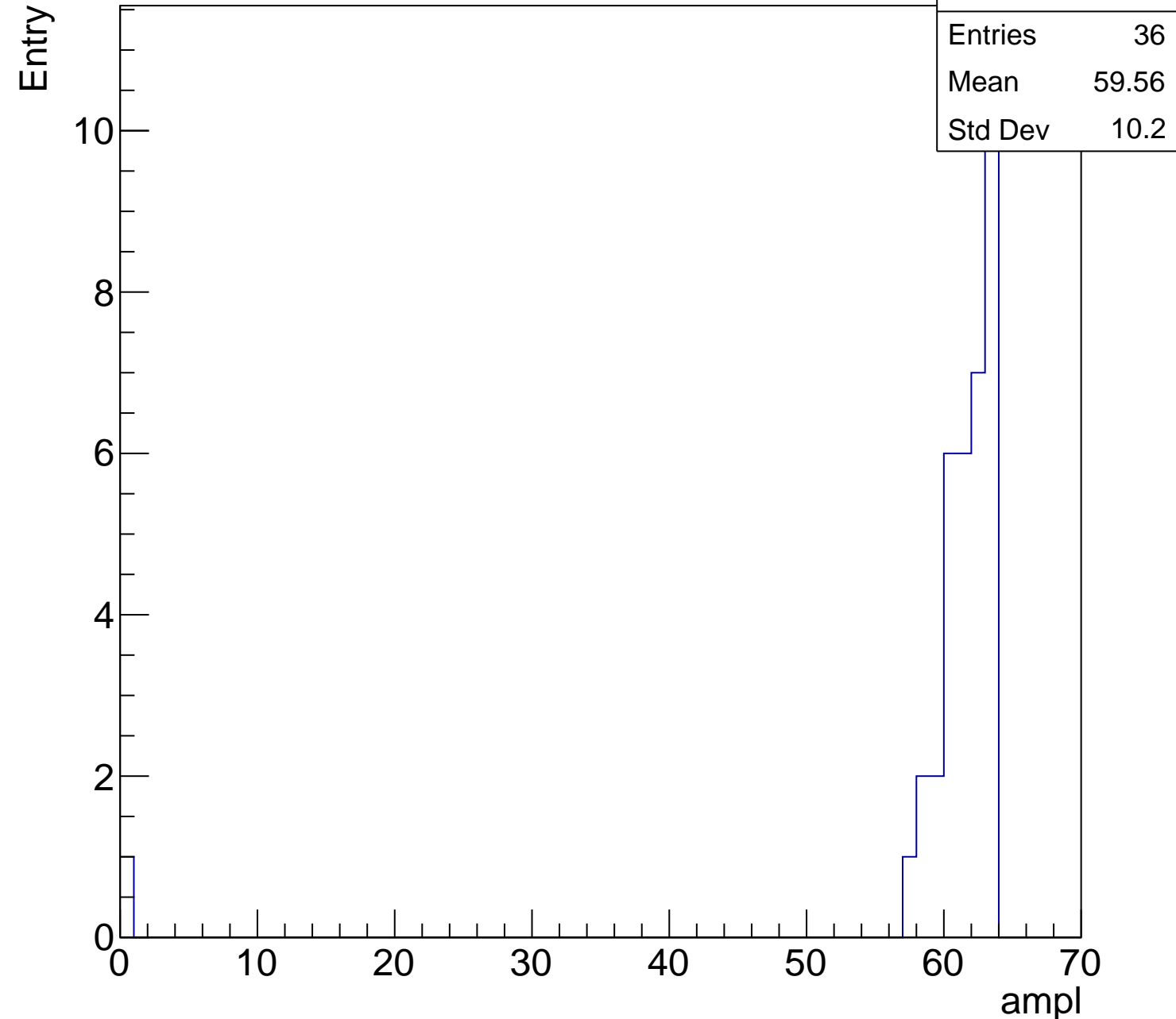
40

50

60

ampl

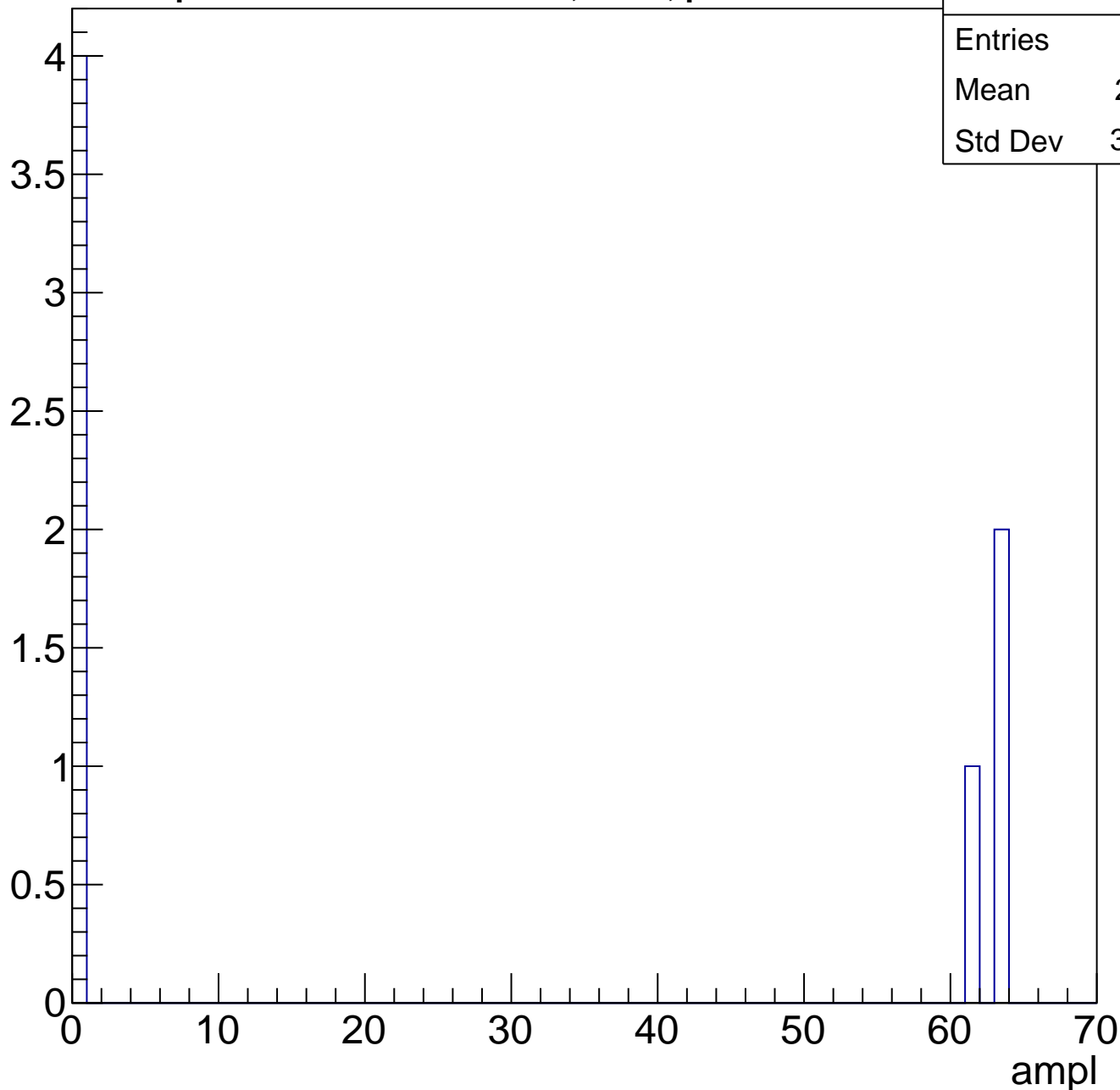
Entries	36
Mean	59.56
Std Dev	10.2



B1L001S, U16-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	7
Mean	26.71
Std Dev	30.85

B1L001S, U16-ch48, adc0

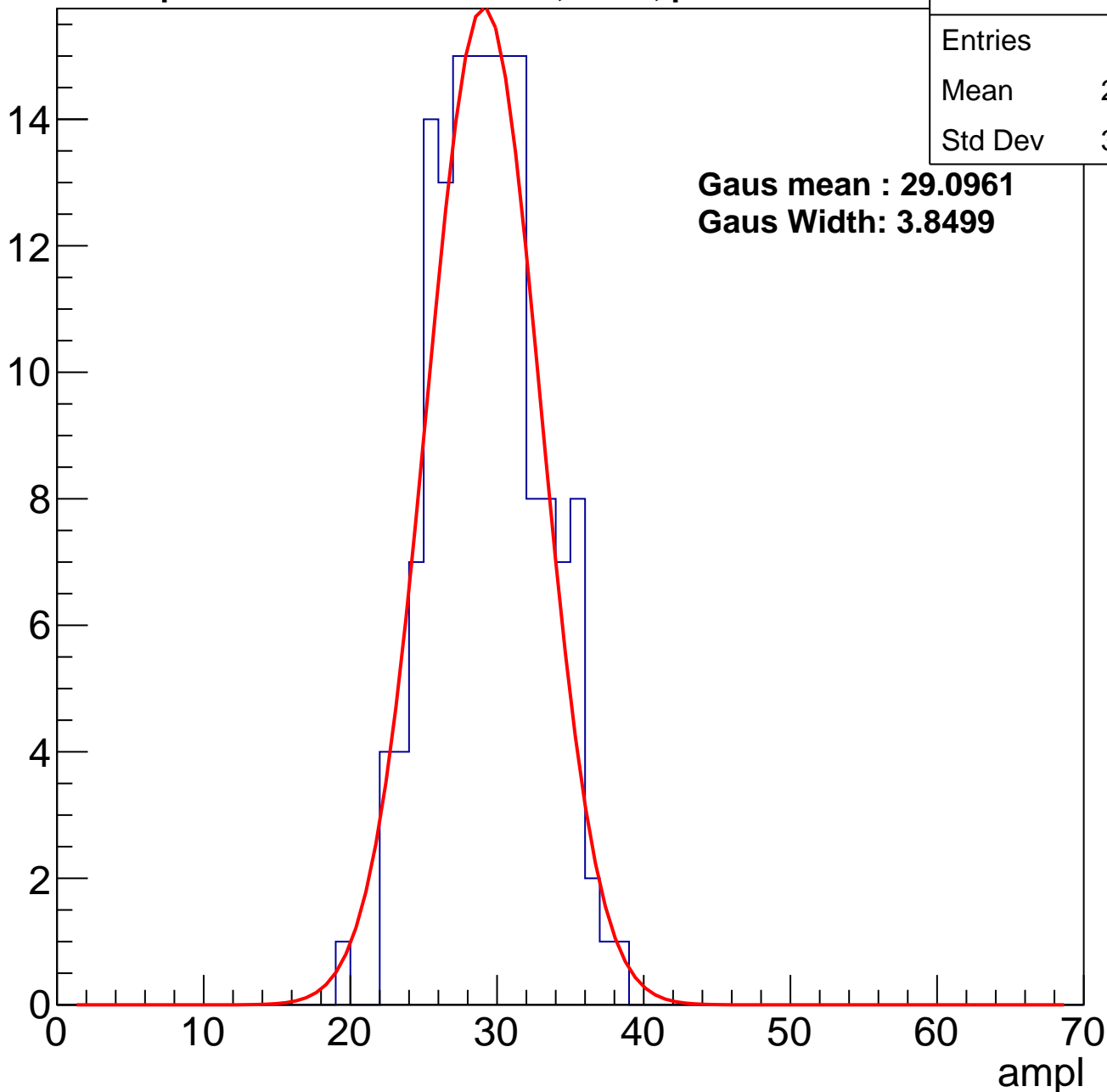
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	28.86
Std Dev	3.623

Gaus mean : 29.0961

Gaus Width: 3.8499

Entry



B1L001S, U16-ch48, adc1

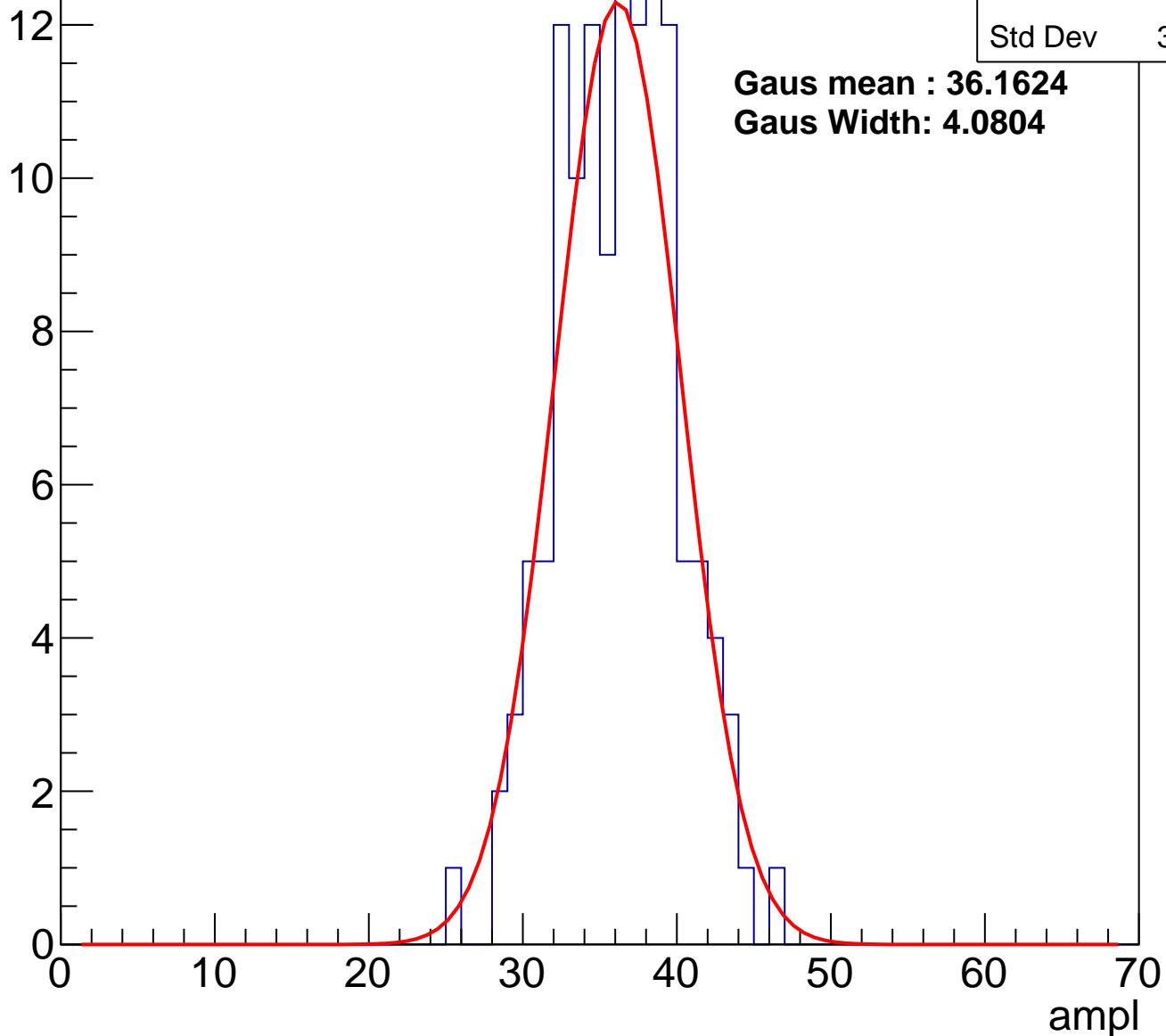
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	35.75
Std Dev	3.816

Gaus mean : 36.1624

Gaus Width: 4.0804

Entry



B1L001S, U16-ch48, adc2

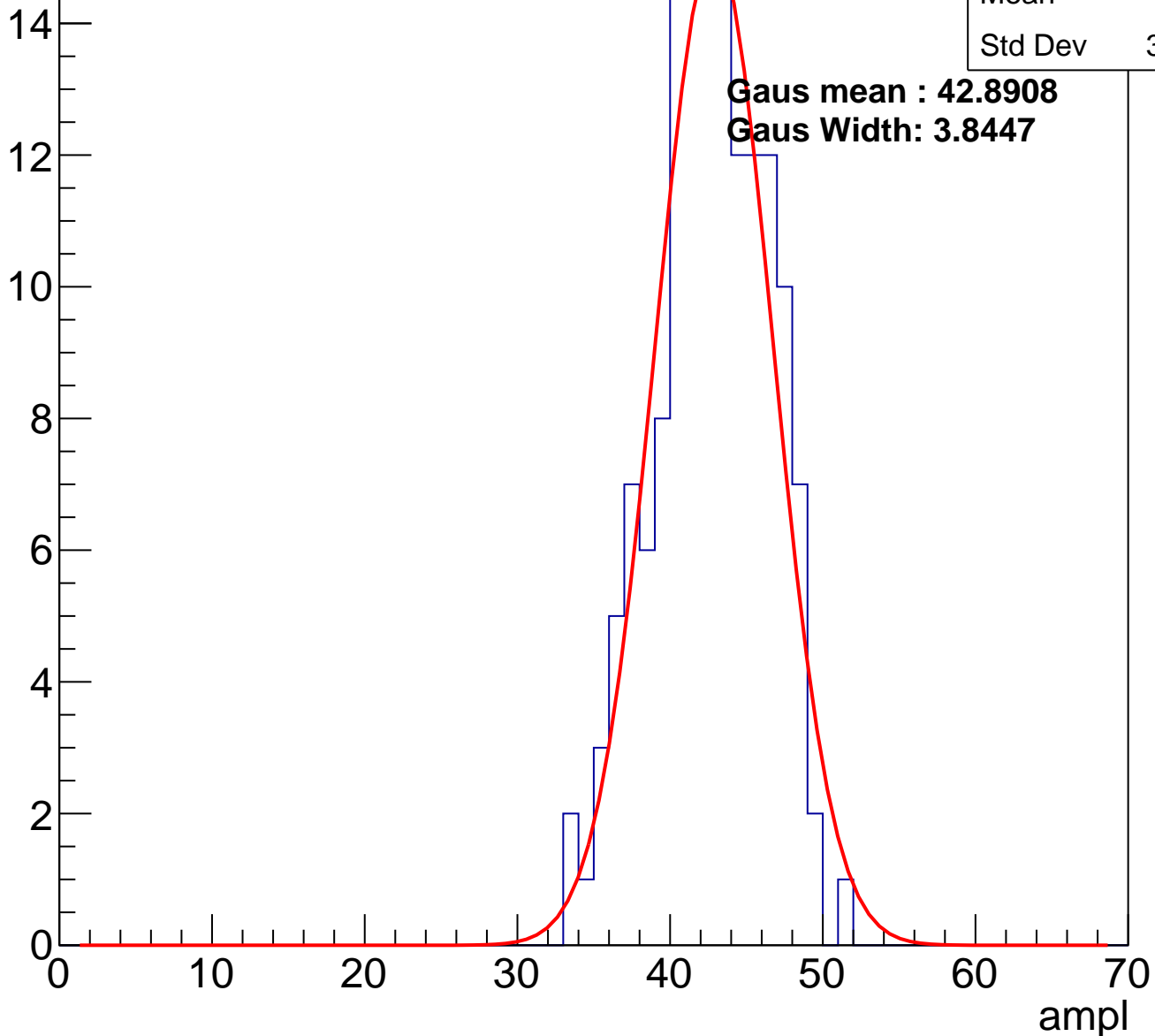
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	42.22
Std Dev	3.683

Gaus mean : 42.8908

Gaus Width: 3.8447

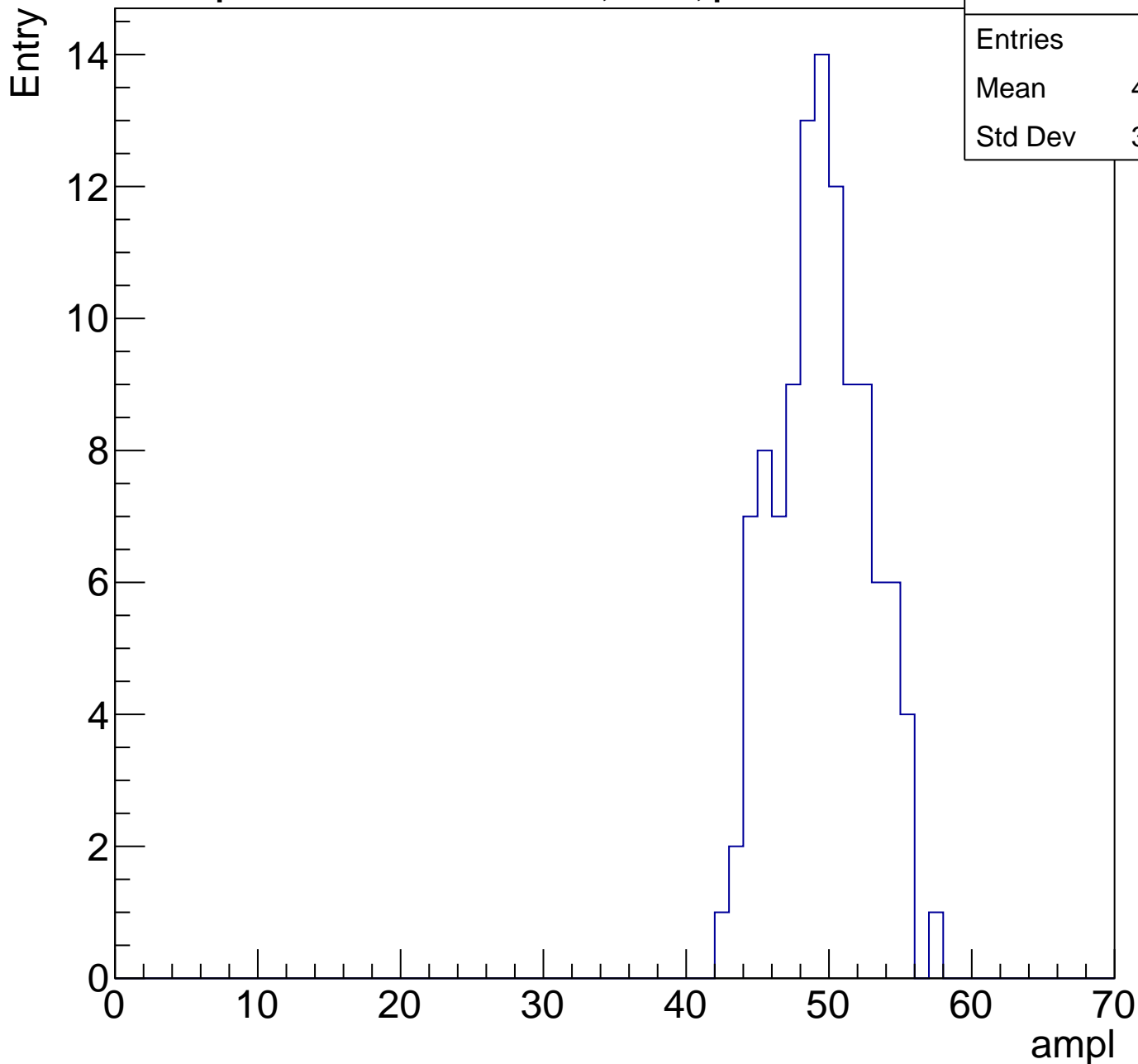
Entry



B1L001S, U16-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

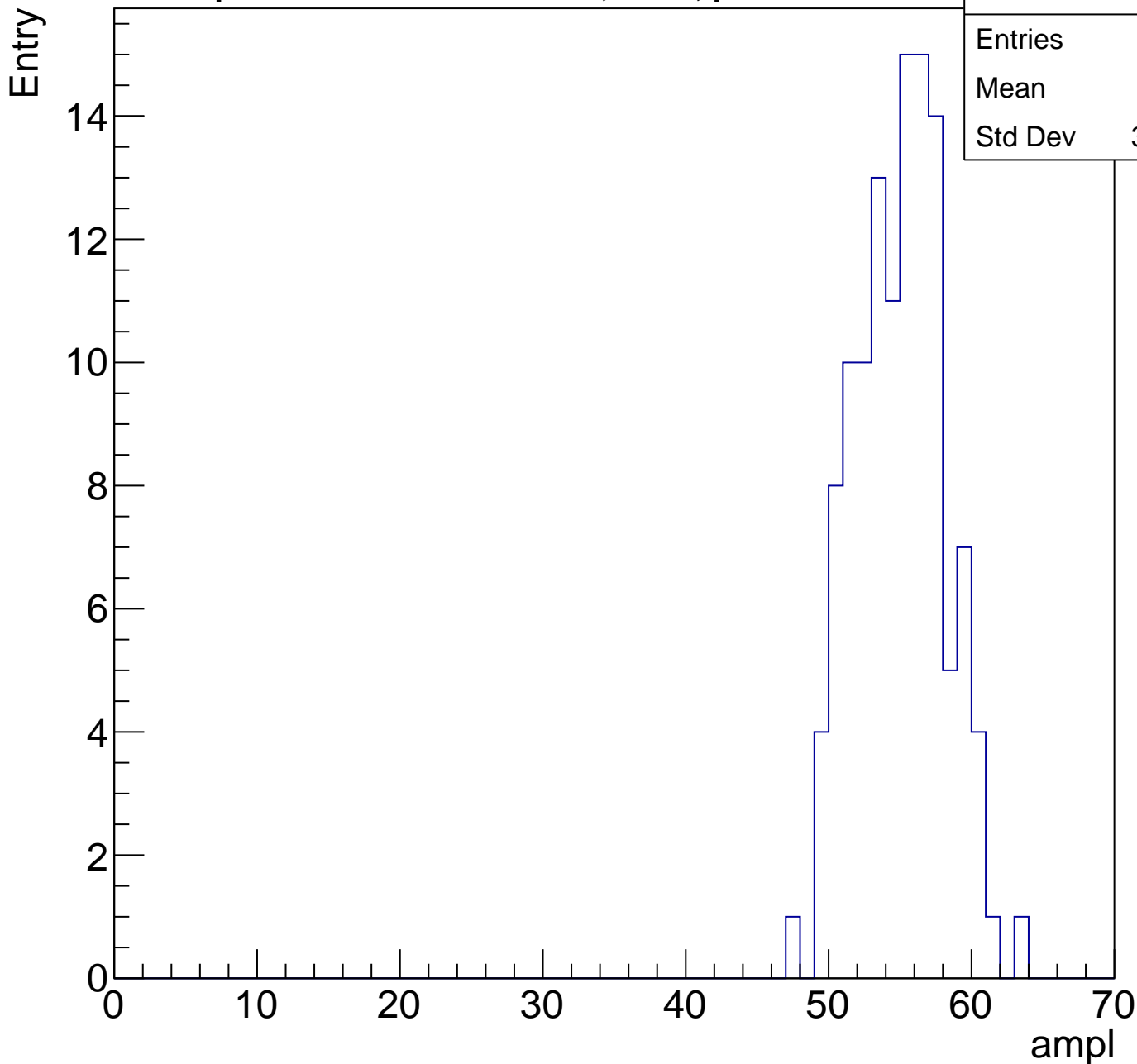
Entries	108
Mean	49.05
Std Dev	3.219



B1L001S, U16-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.5
Std Dev	3.068



B1L001S, U16-ch48, adc5

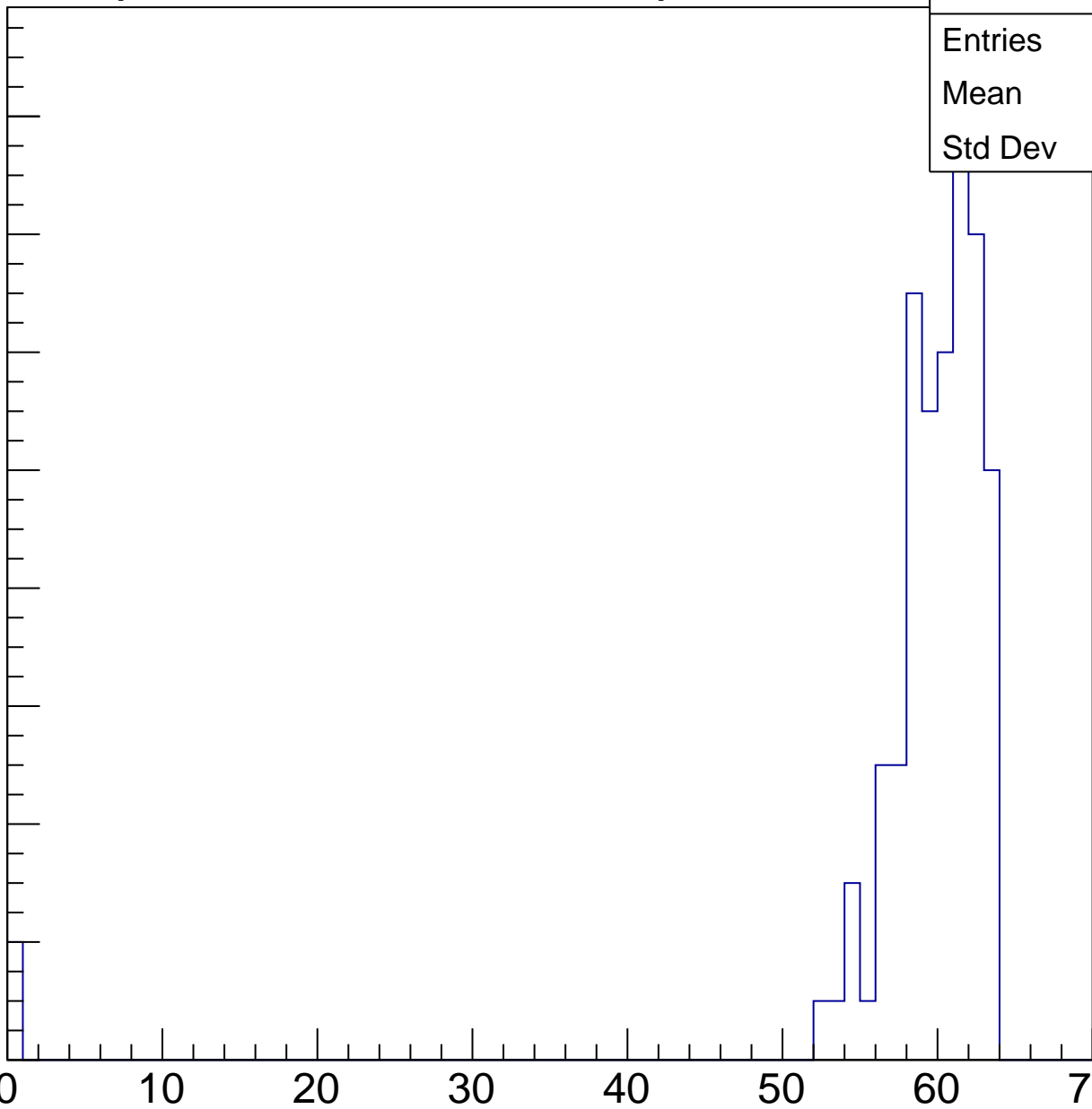
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.37
Std Dev	8.911

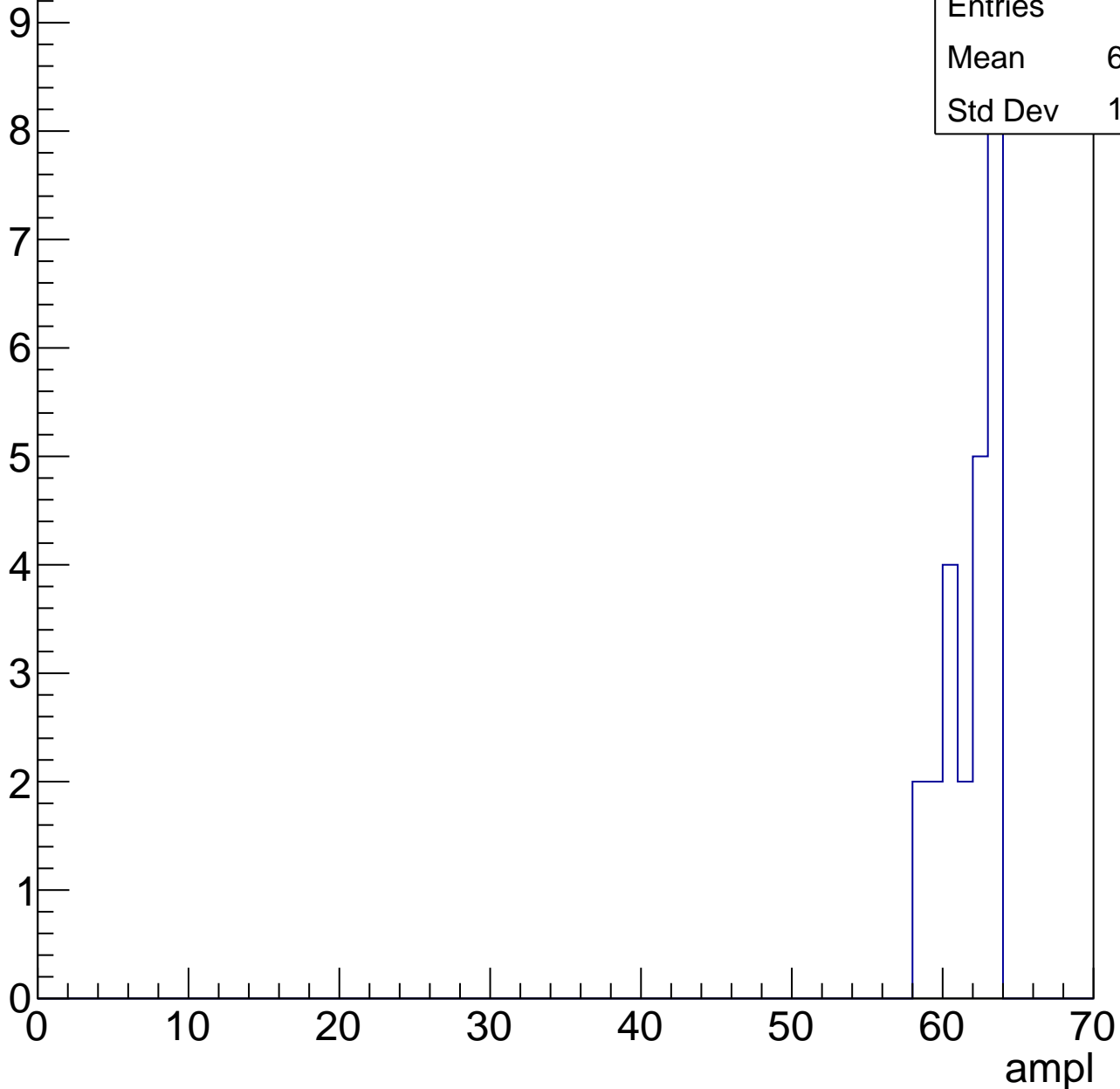
ampl



B1L001S, U16-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

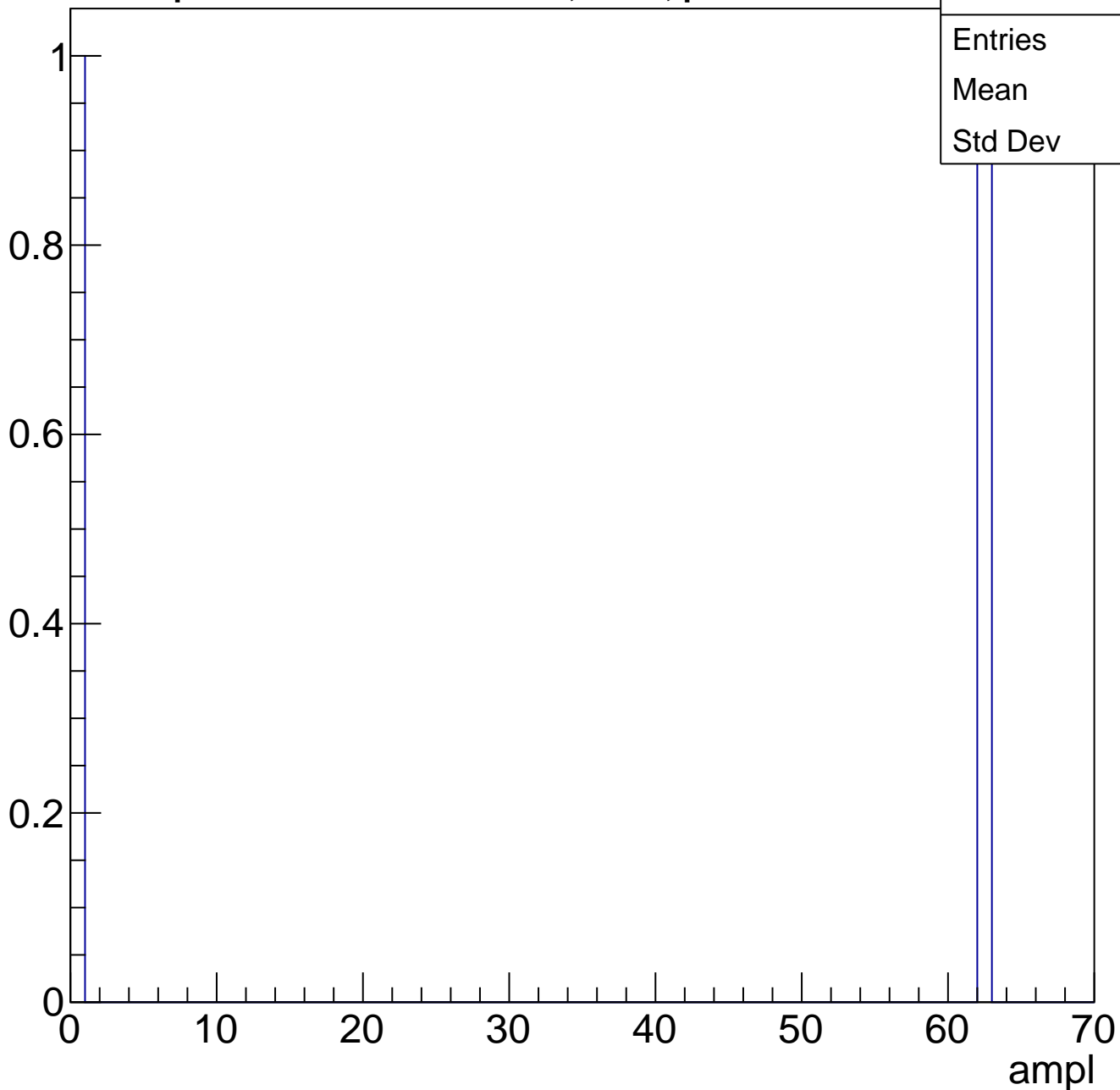
Entry



B1L001S, U16-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch49, adc0

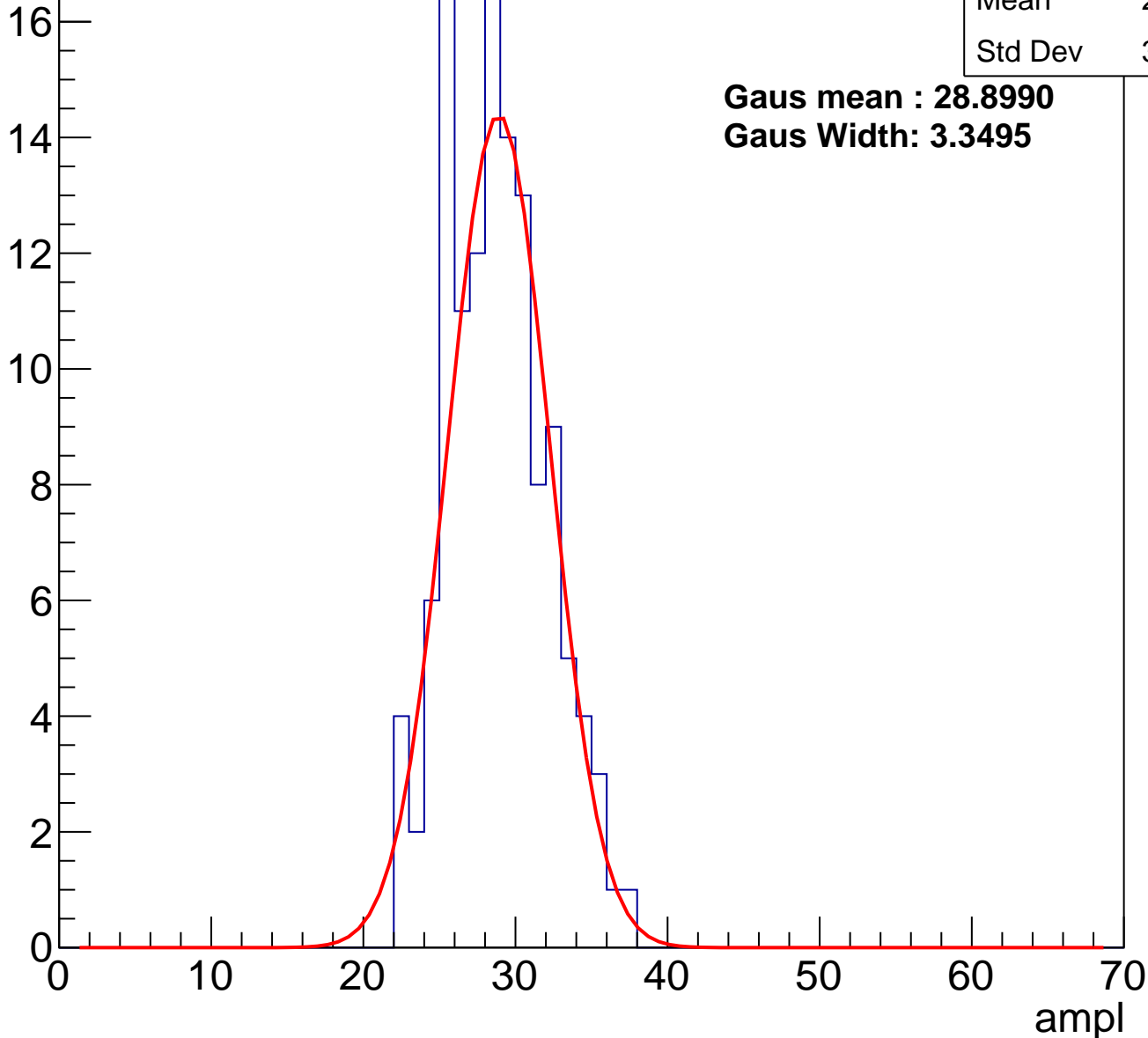
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	127
Mean	28.35
Std Dev	3.232

Gaus mean : 28.8990

Gaus Width: 3.3495



B1L001S, U16-ch49, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	35.36
Std Dev	3.867

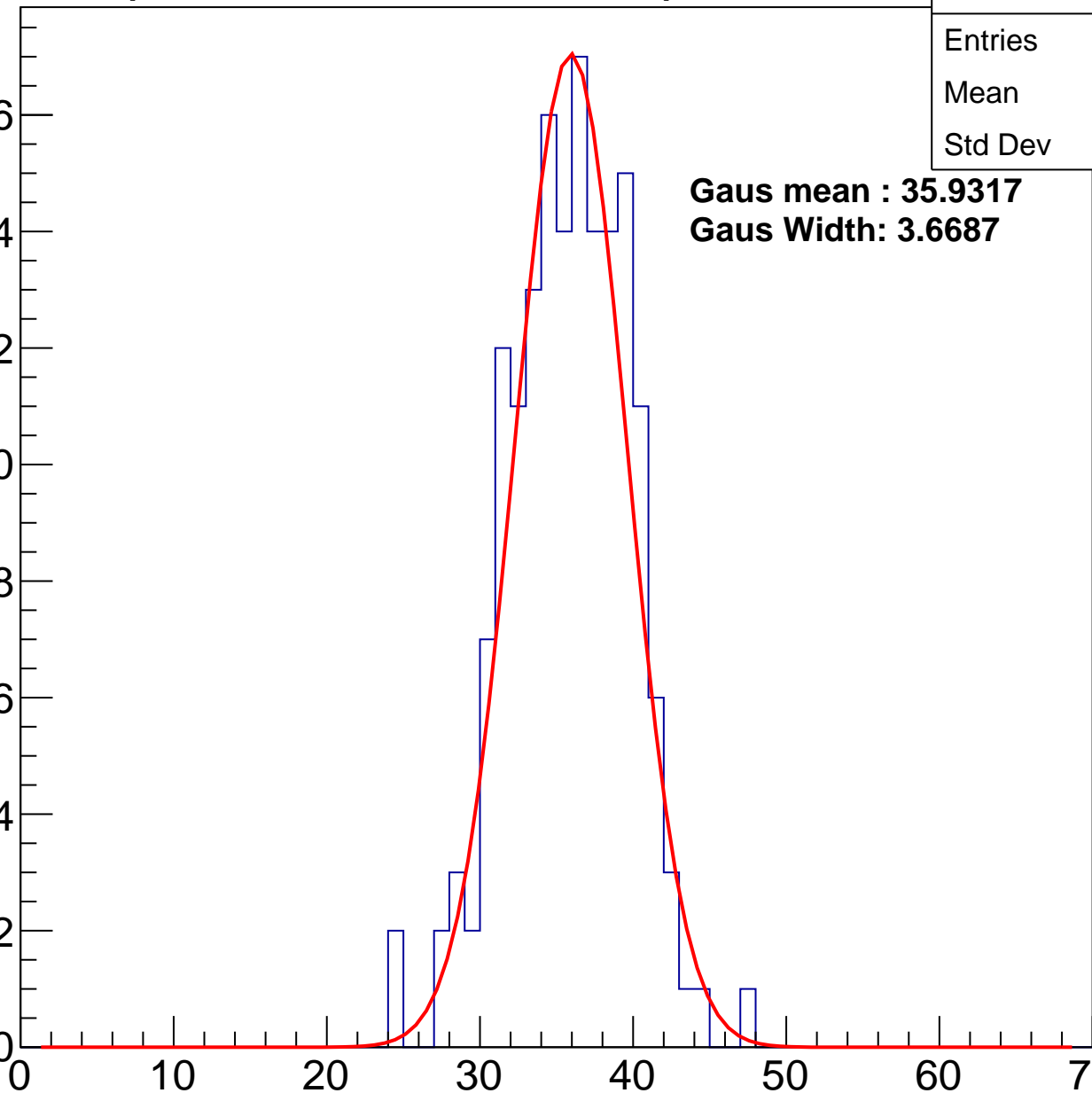
Gaus mean : 35.9317

Gaus Width: 3.6687

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch49, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

114

Mean

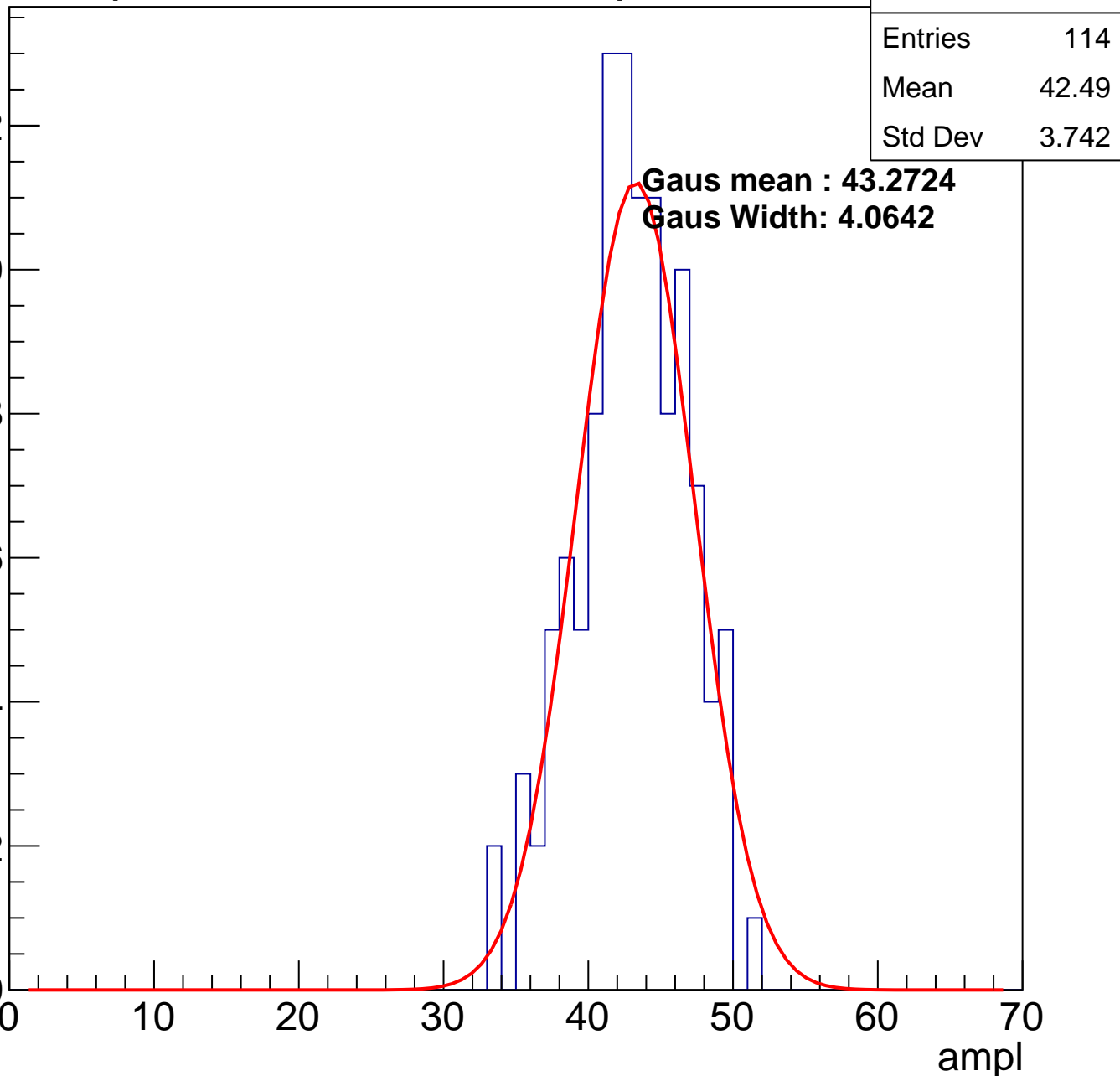
42.49

Std Dev

3.742

Gaus mean : 43.2724

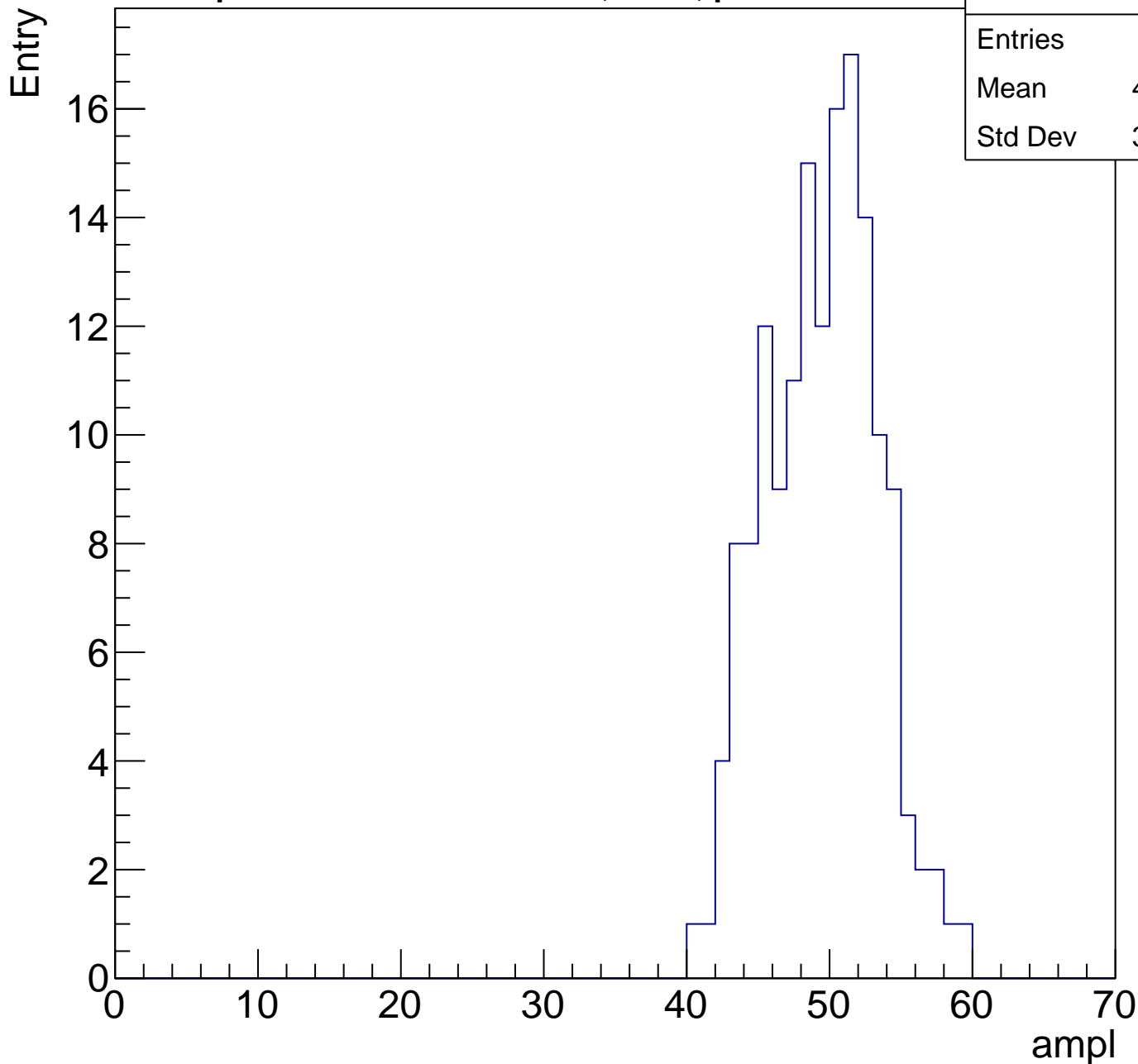
Gaus Width: 4.0642



B1L001S, U16-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	48.99
Std Dev	3.815



B1L001S, U16-ch49, adc4

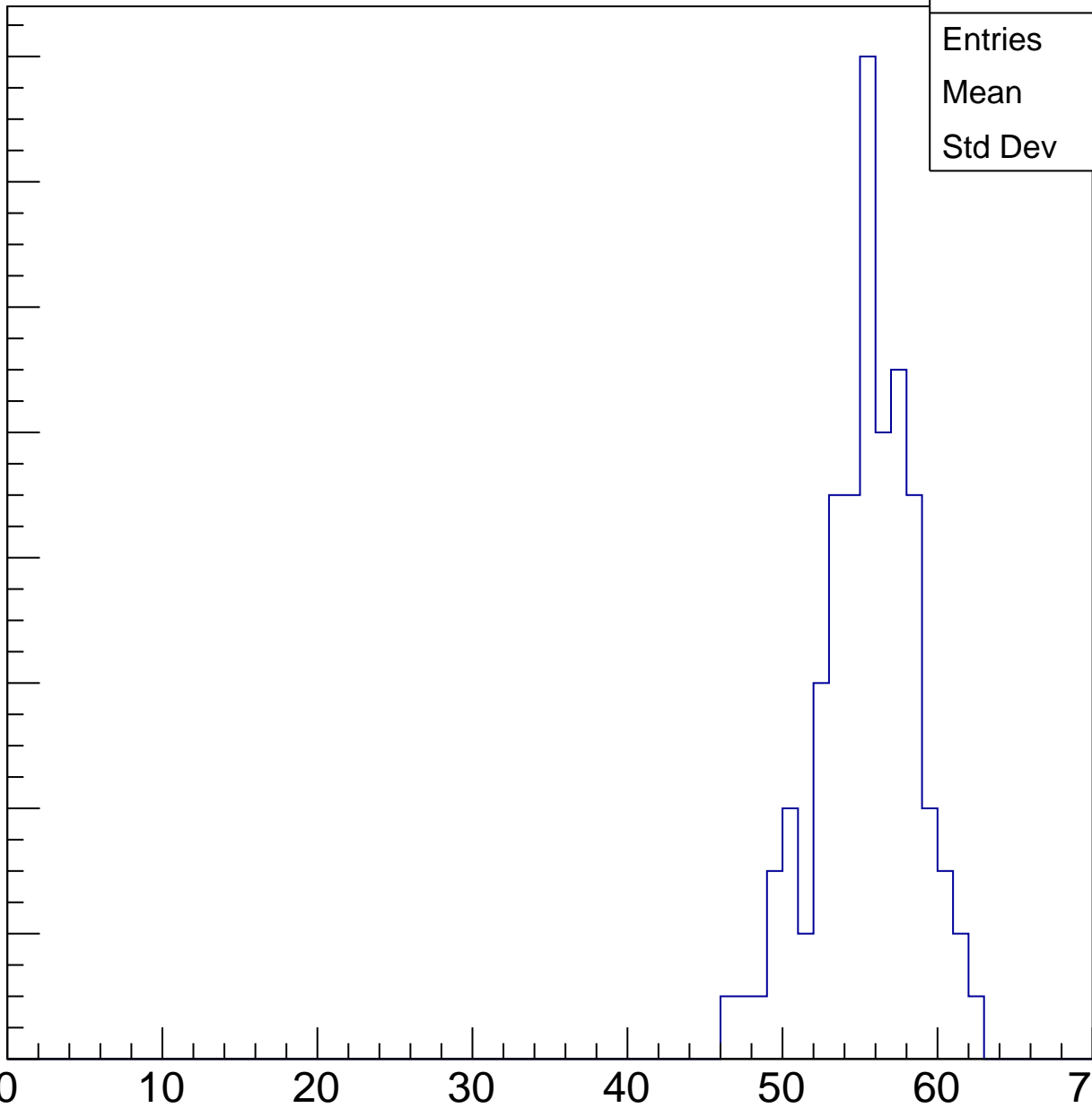
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	54.93
Std Dev	3.175

ampl



B1L001S, U16-ch49, adc5

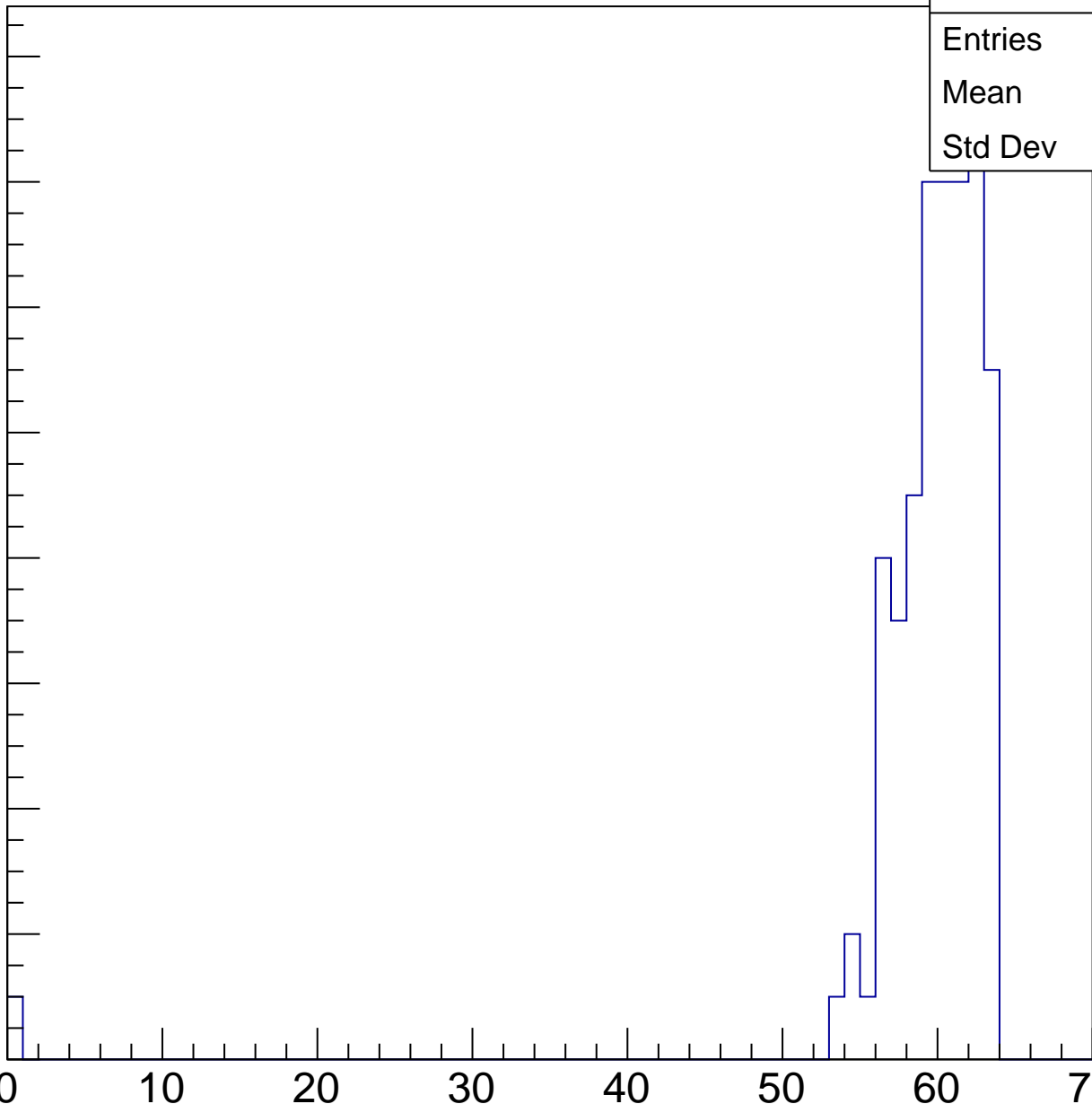
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.08
Std Dev	6.453

ampl

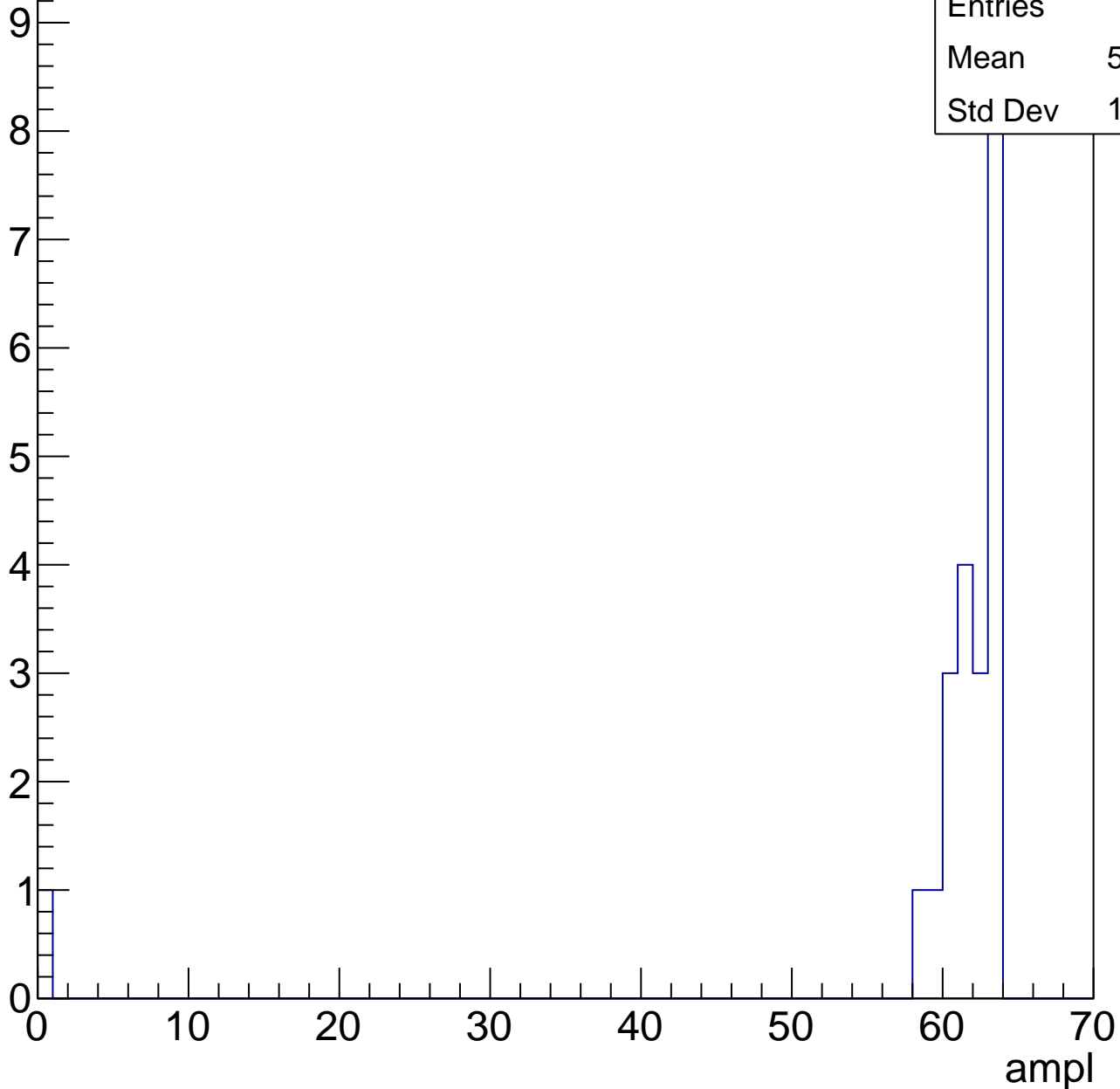


B1L001S, U16-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

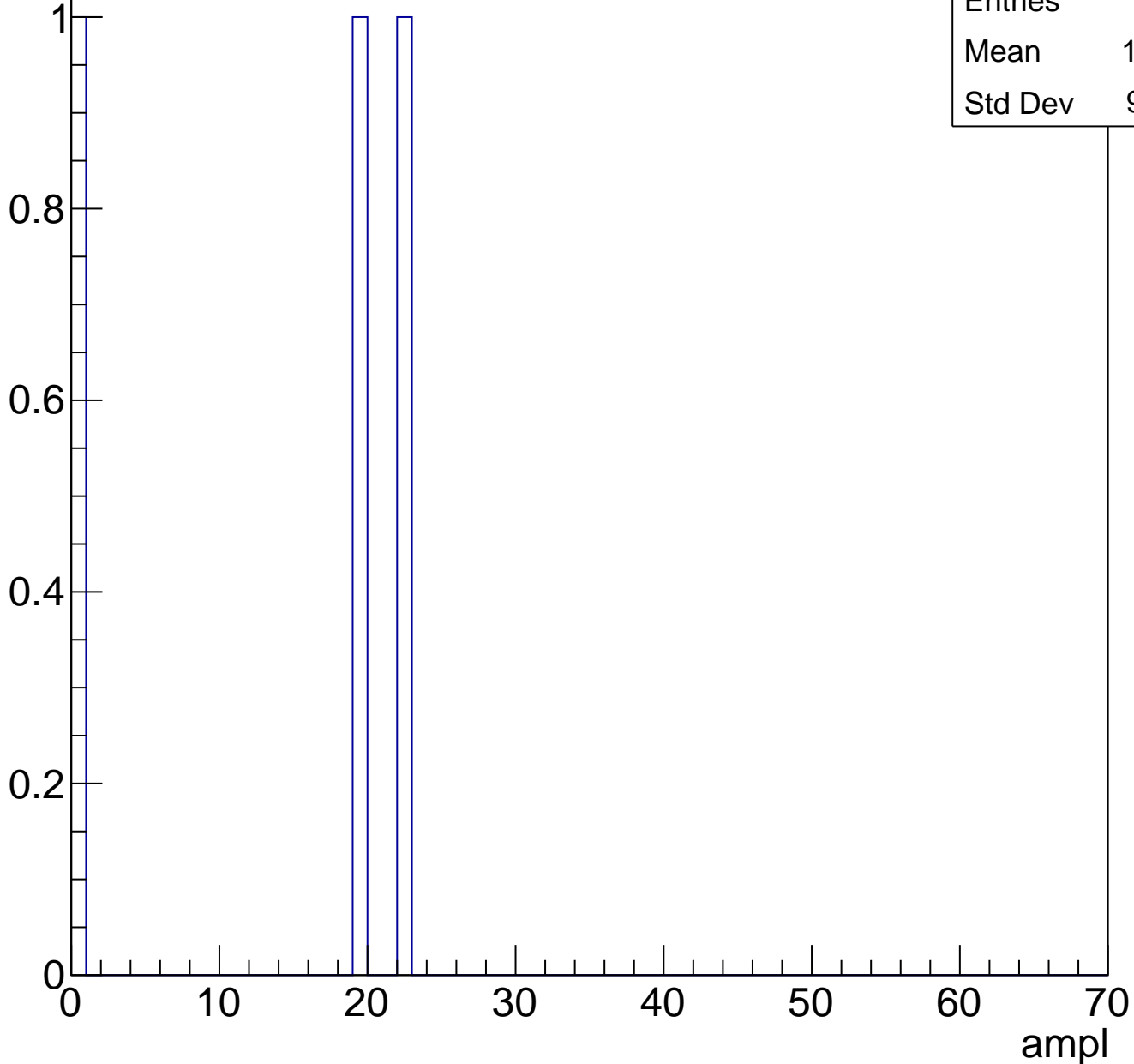
Entries	22
Mean	58.82
Std Dev	12.92



B1L001S, U16-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	13.67
Std Dev	9.741

B1L001S, U16-ch50, adc0

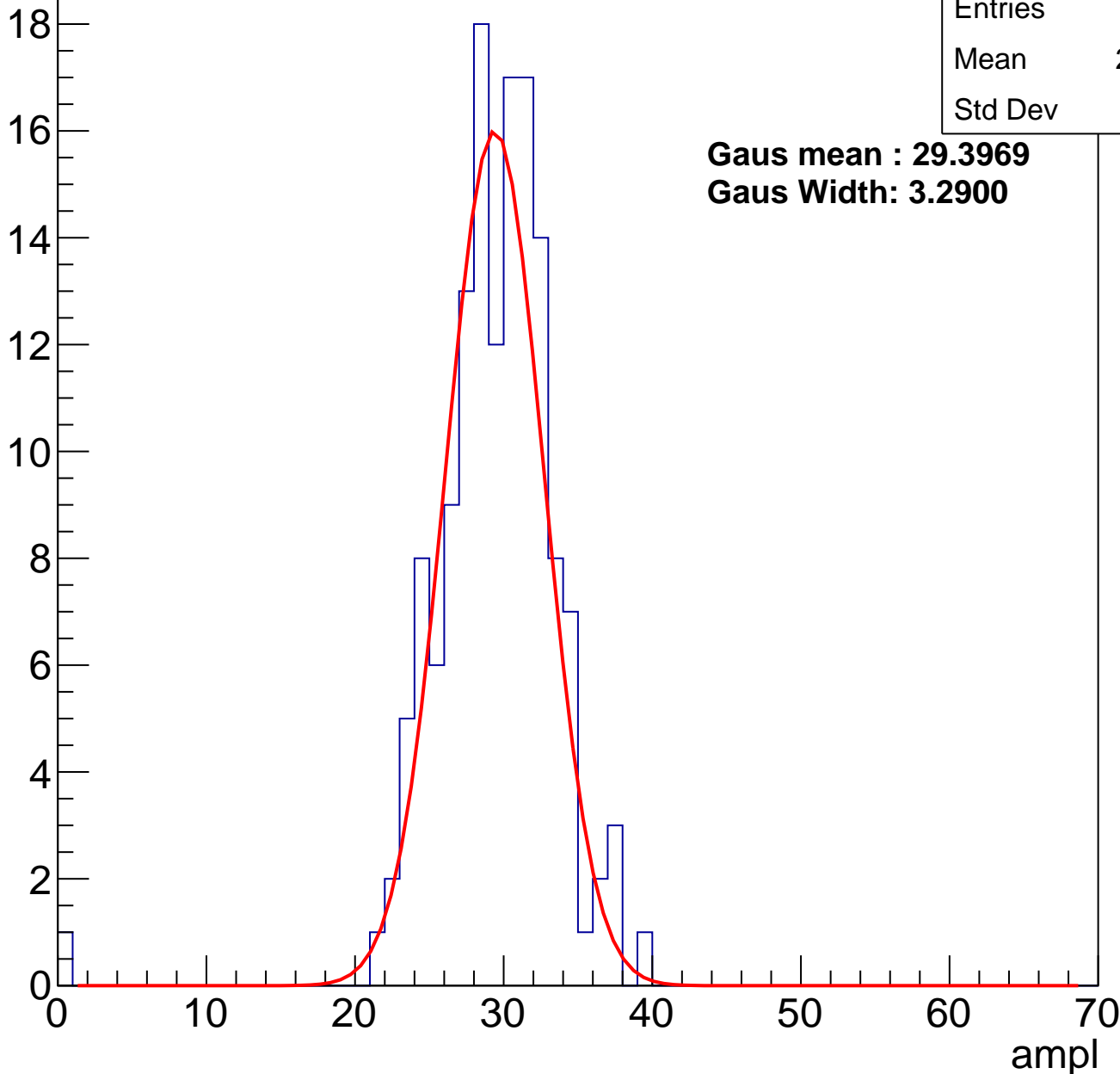
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.99
Std Dev	4.21

Gaus mean : 29.3969

Gaus Width: 3.2900

Entry



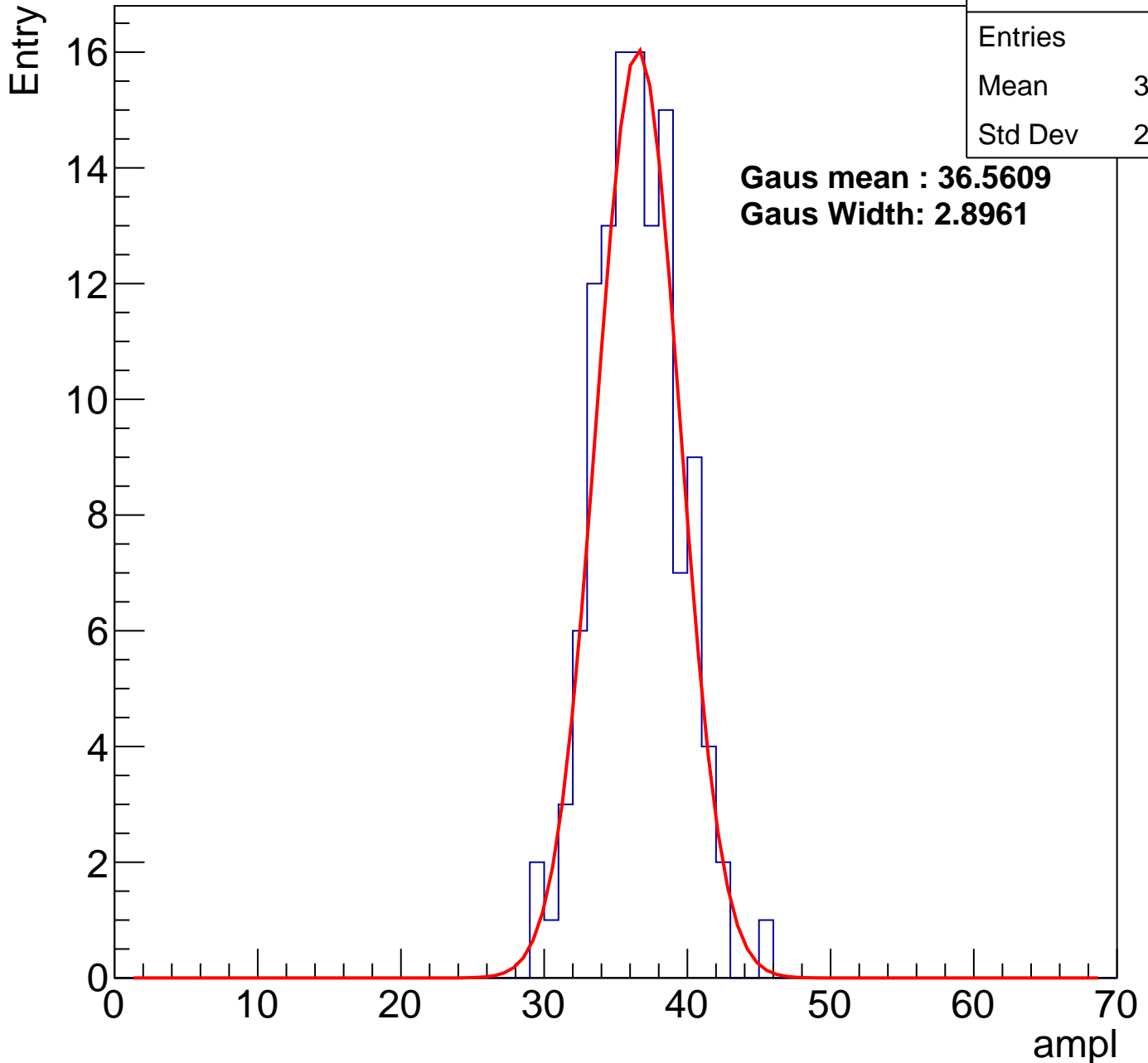
B1L001S, U16-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	36.03
Std Dev	2.907

Gaus mean : 36.5609

Gaus Width: 2.8961



B1L001S, U16-ch50, adc2

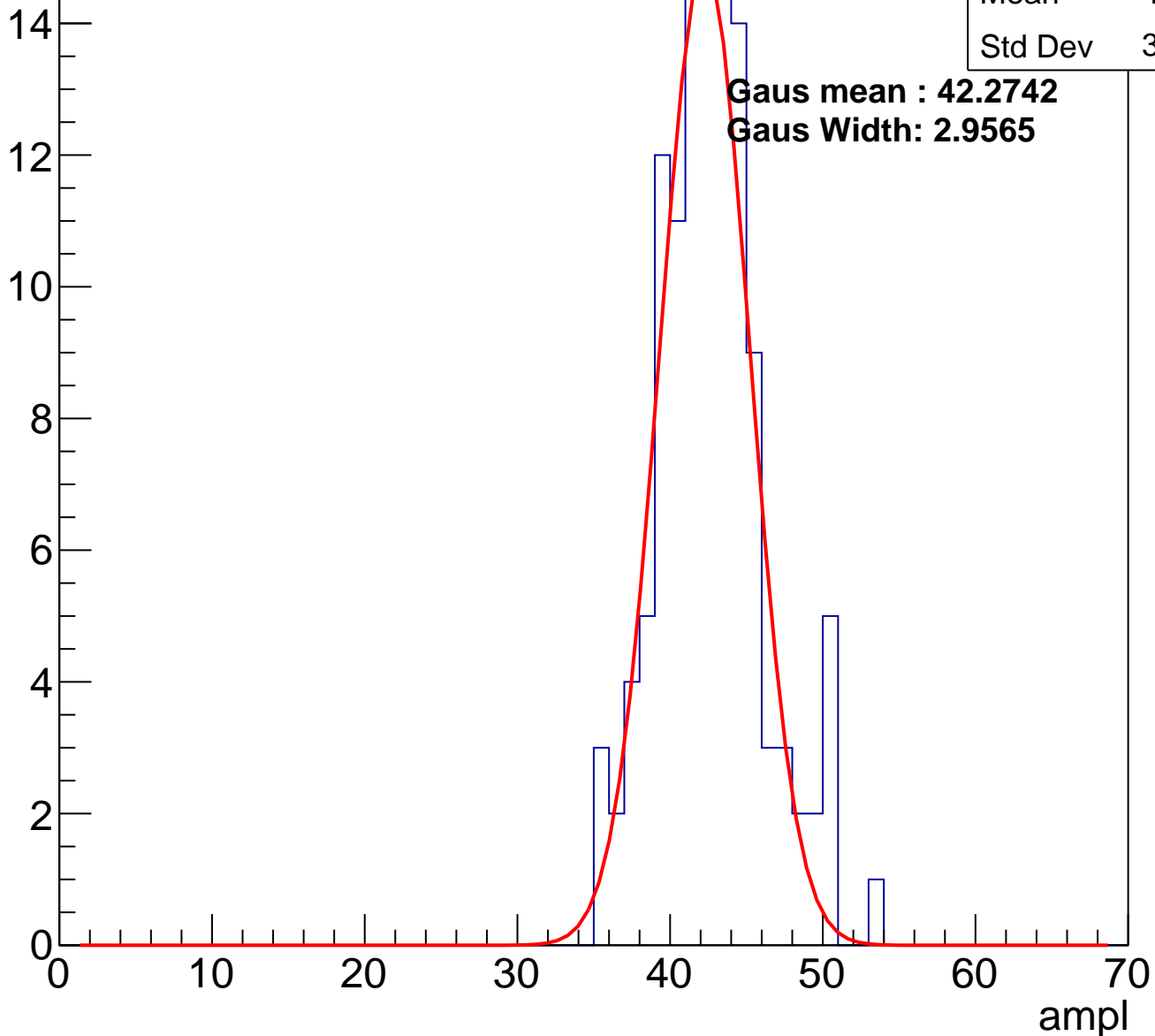
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.23
Std Dev	3.456

Gaus mean : 42.2742

Gaus Width: 2.9565

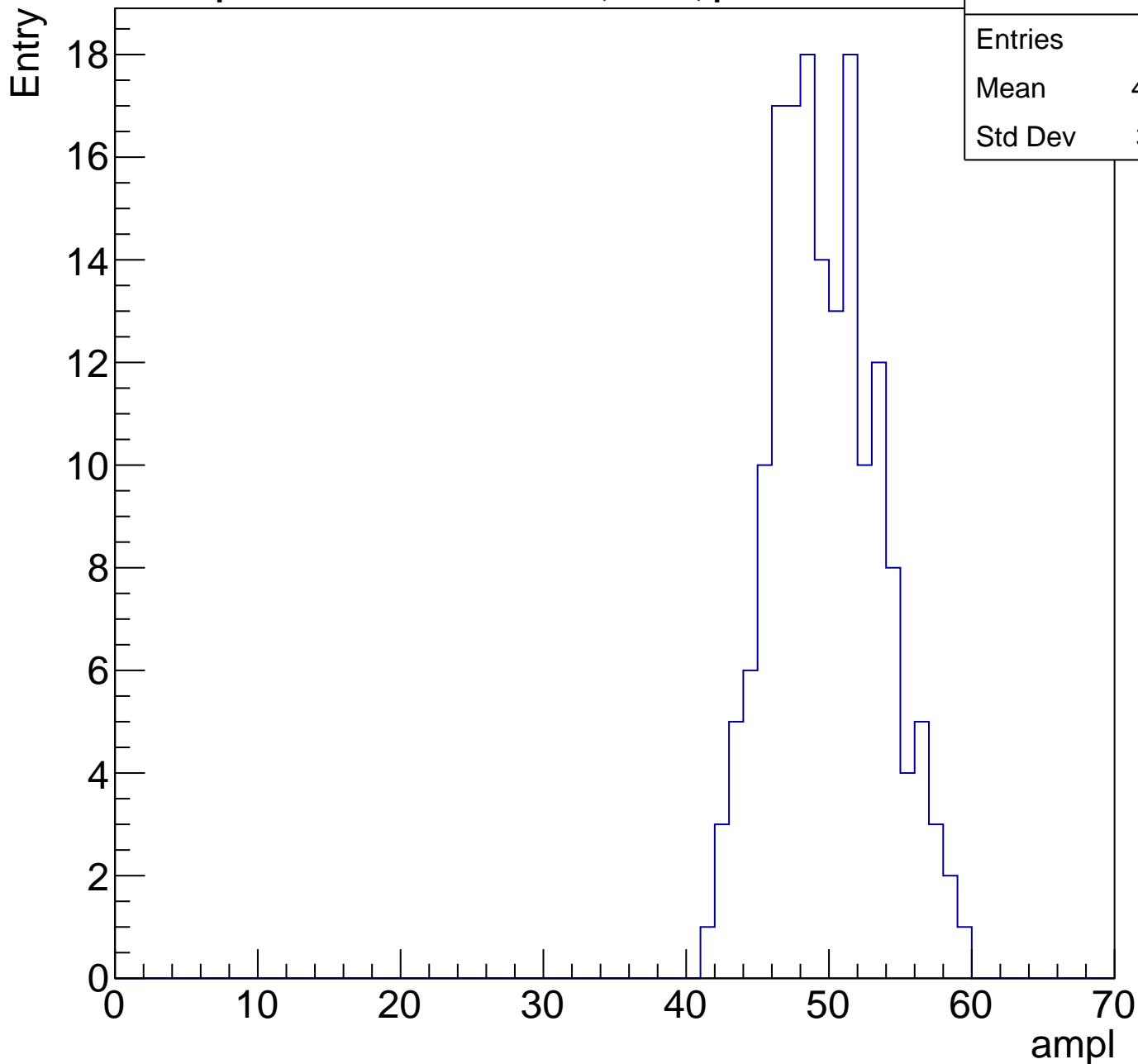
Entry



B1L001S, U16-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	49.28
Std Dev	3.741



B1L001S, U16-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	89
Mean	55.44
Std Dev	3.144

Entry

12

10

8

6

4

2

0

0

10

20

30

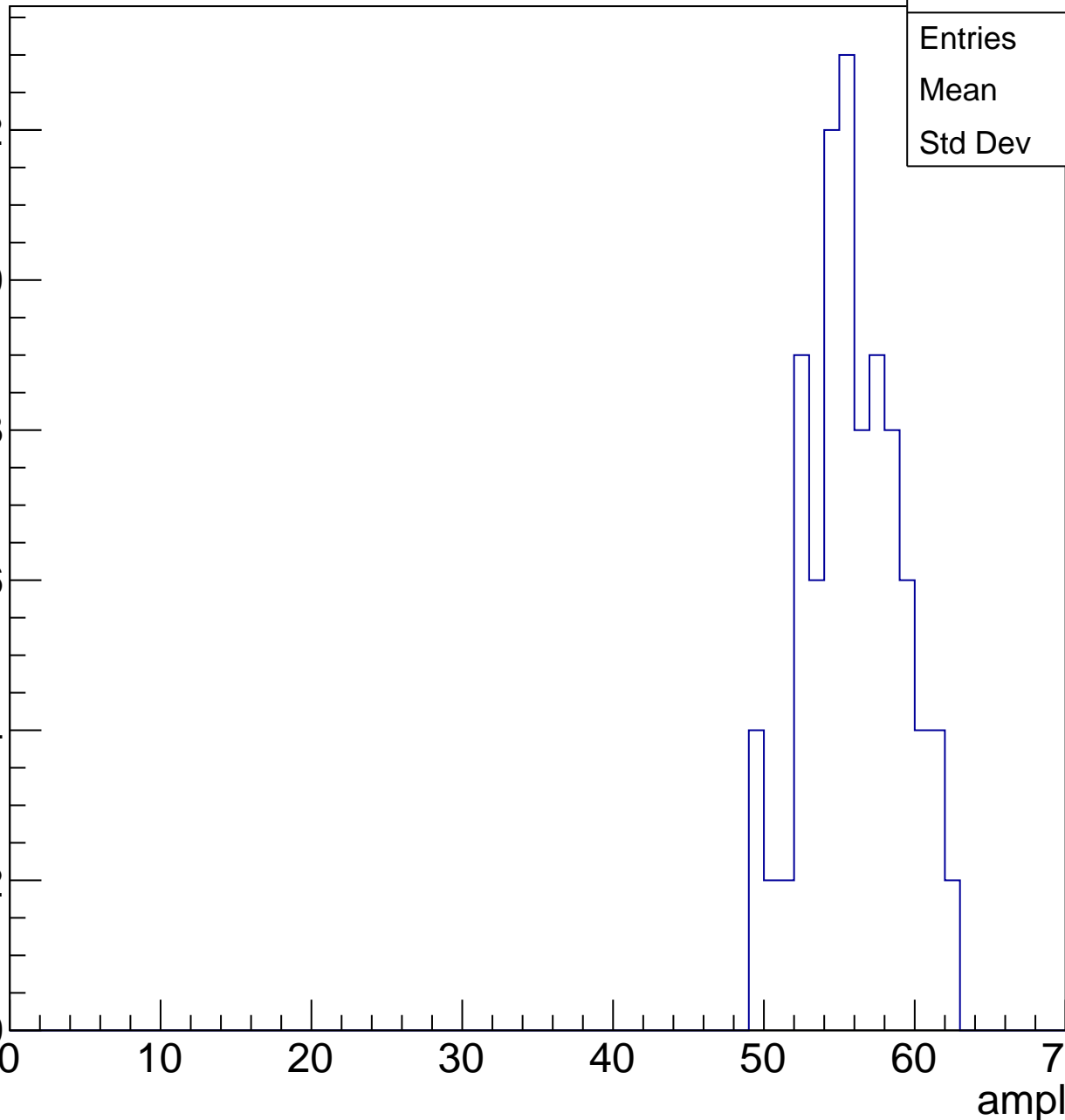
40

50

60

ampl

70



B1L001S, U16-ch50, adc5

calib_packv5_042523_0143.root, FC#2, port C2

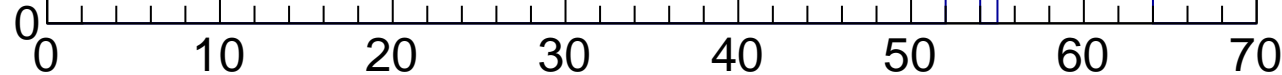
Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	59.6
Std Dev	2.473

ampl

0 10 20 30 40 50 60 70

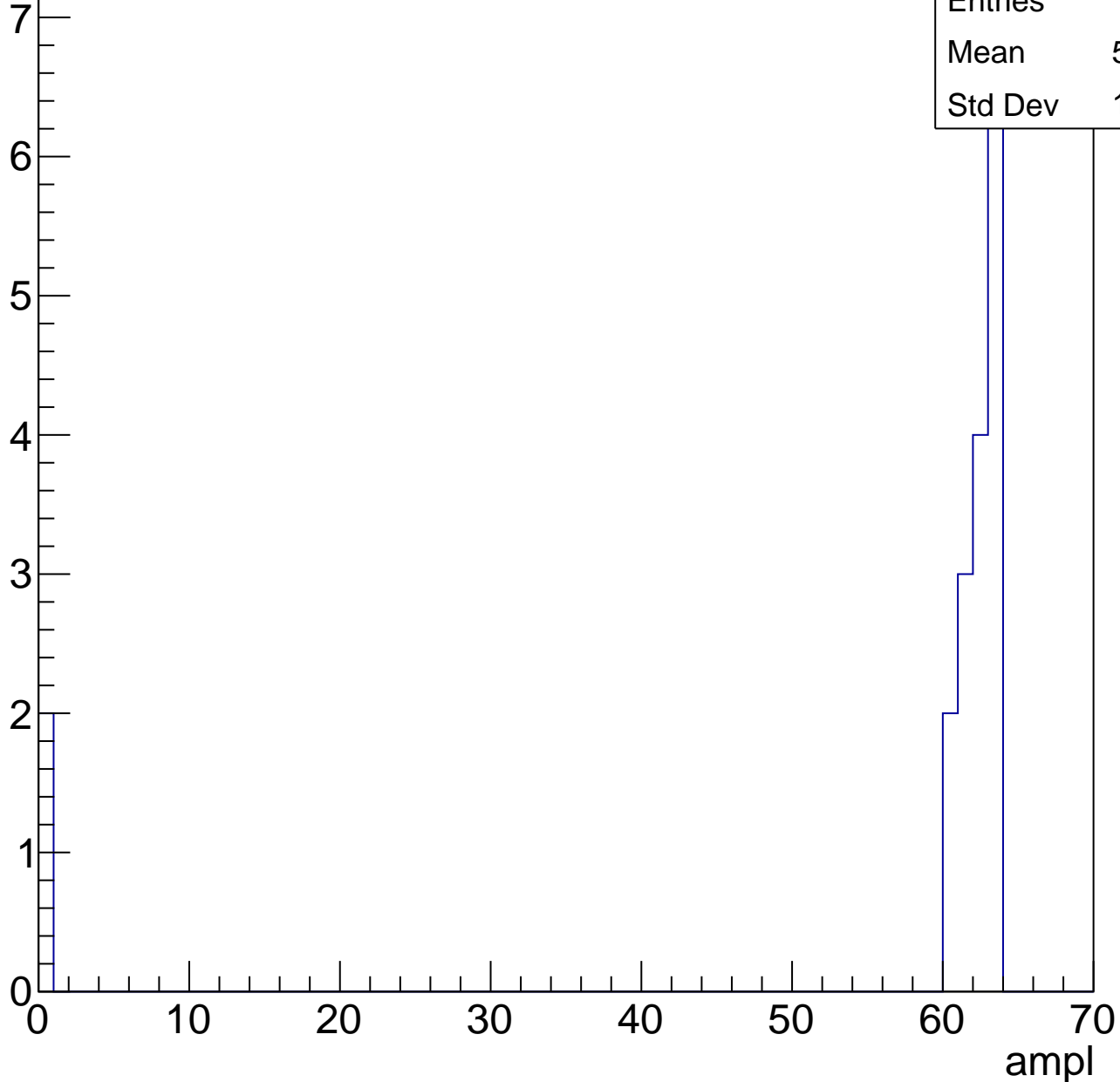


B1L001S, U16-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	55.11
Std Dev	19.51



B1L001S, U16-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch51, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	29.99
Std Dev	4.986

Gaus mean : 30.7374

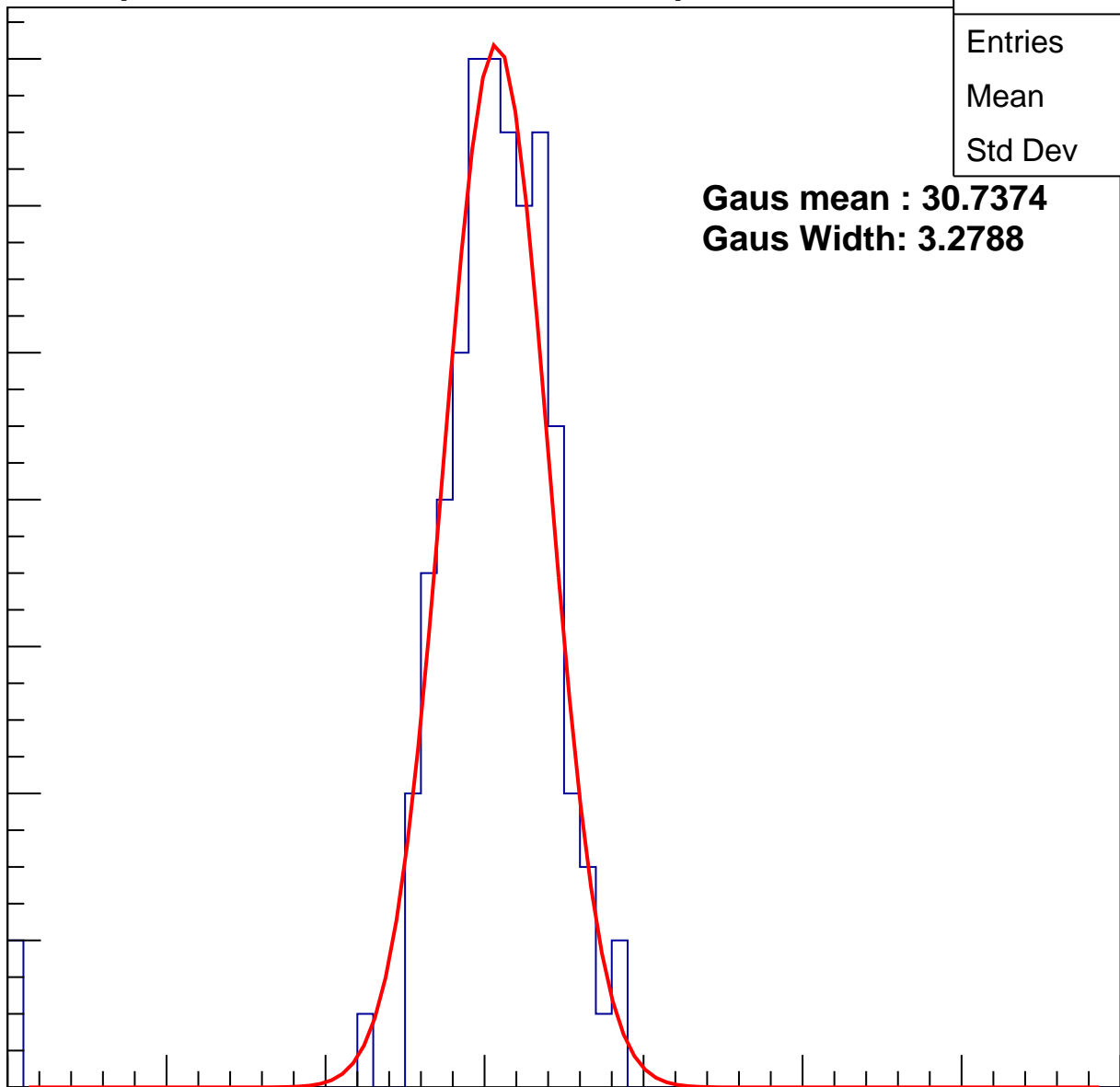
Gaus Width: 3.2788

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



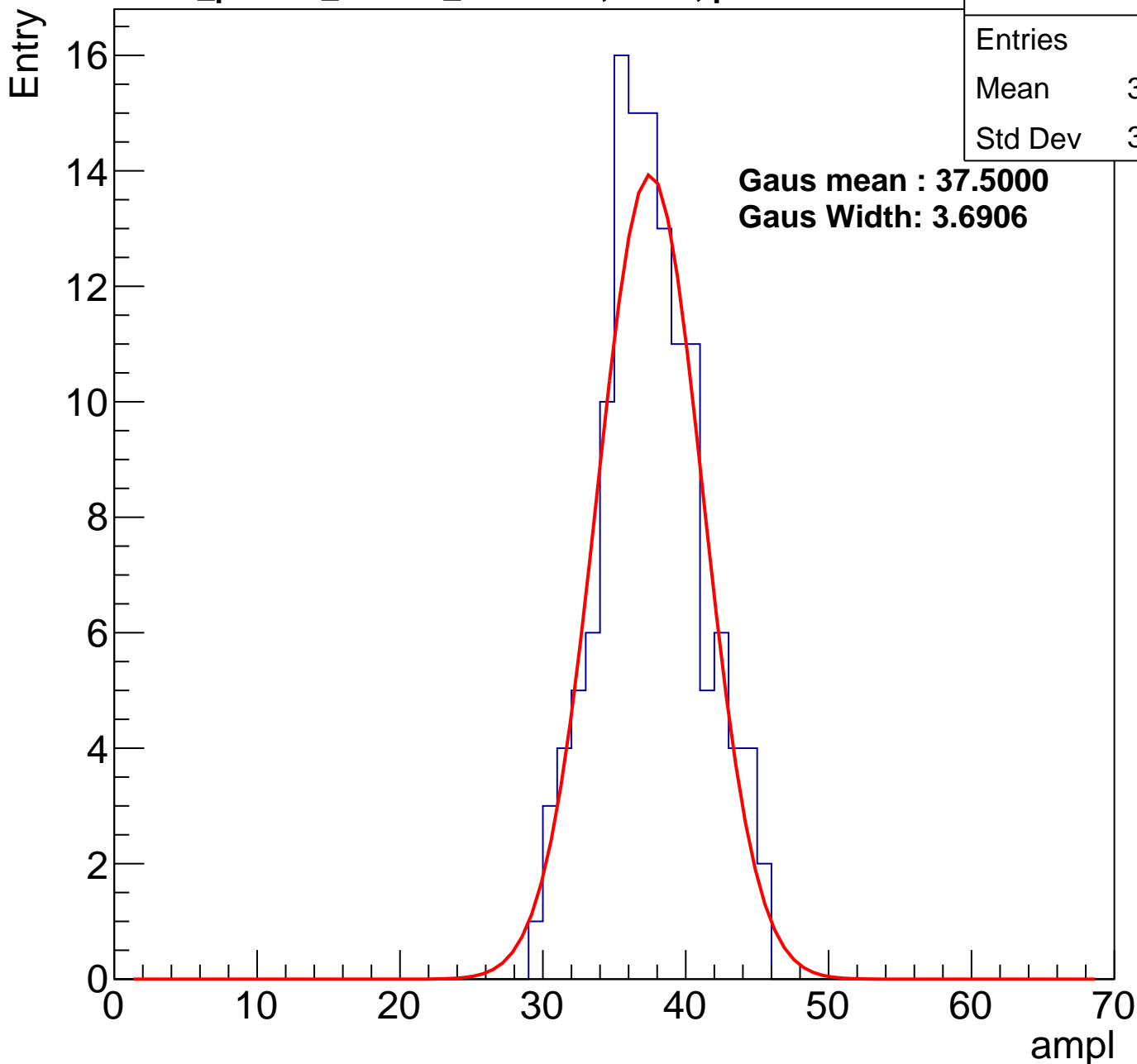
B1L001S, U16-ch51, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.05
Std Dev	3.493

Gaus mean : 37.5000

Gaus Width: 3.6906



B1L001S, U16-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

143

Mean

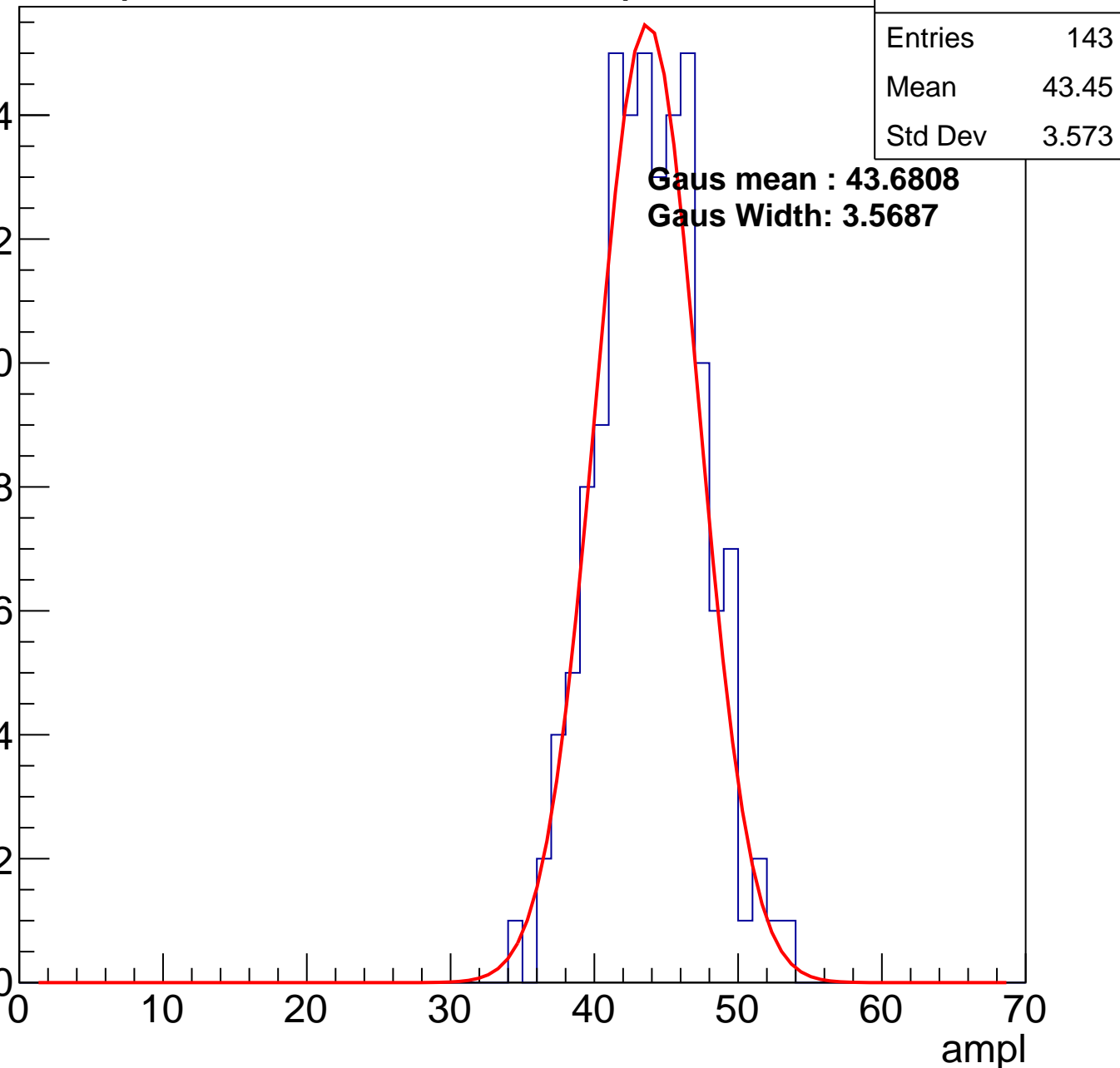
43.45

Std Dev

3.573

Gaus mean : 43.6808

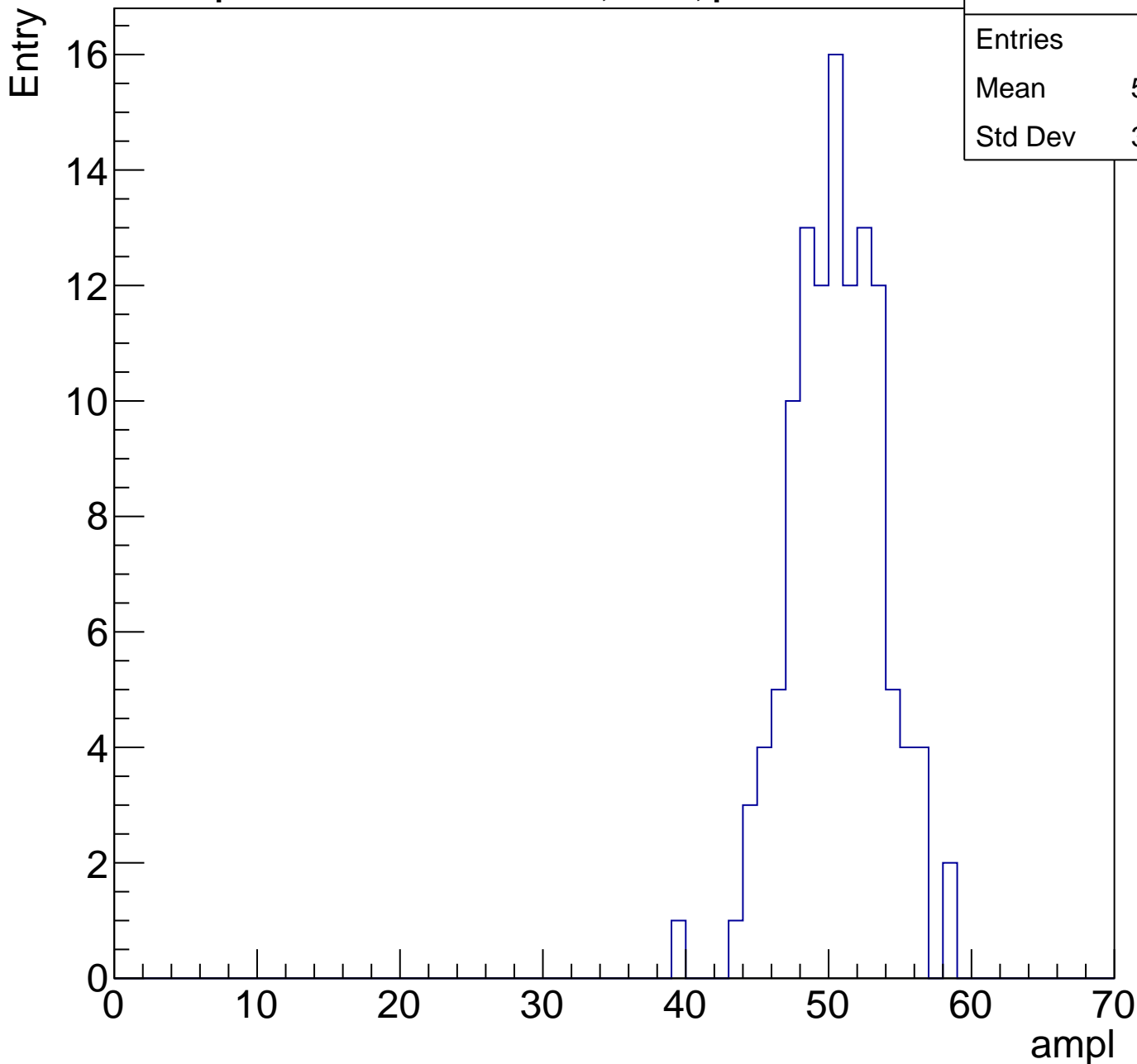
Gaus Width: 3.5687



B1L001S, U16-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

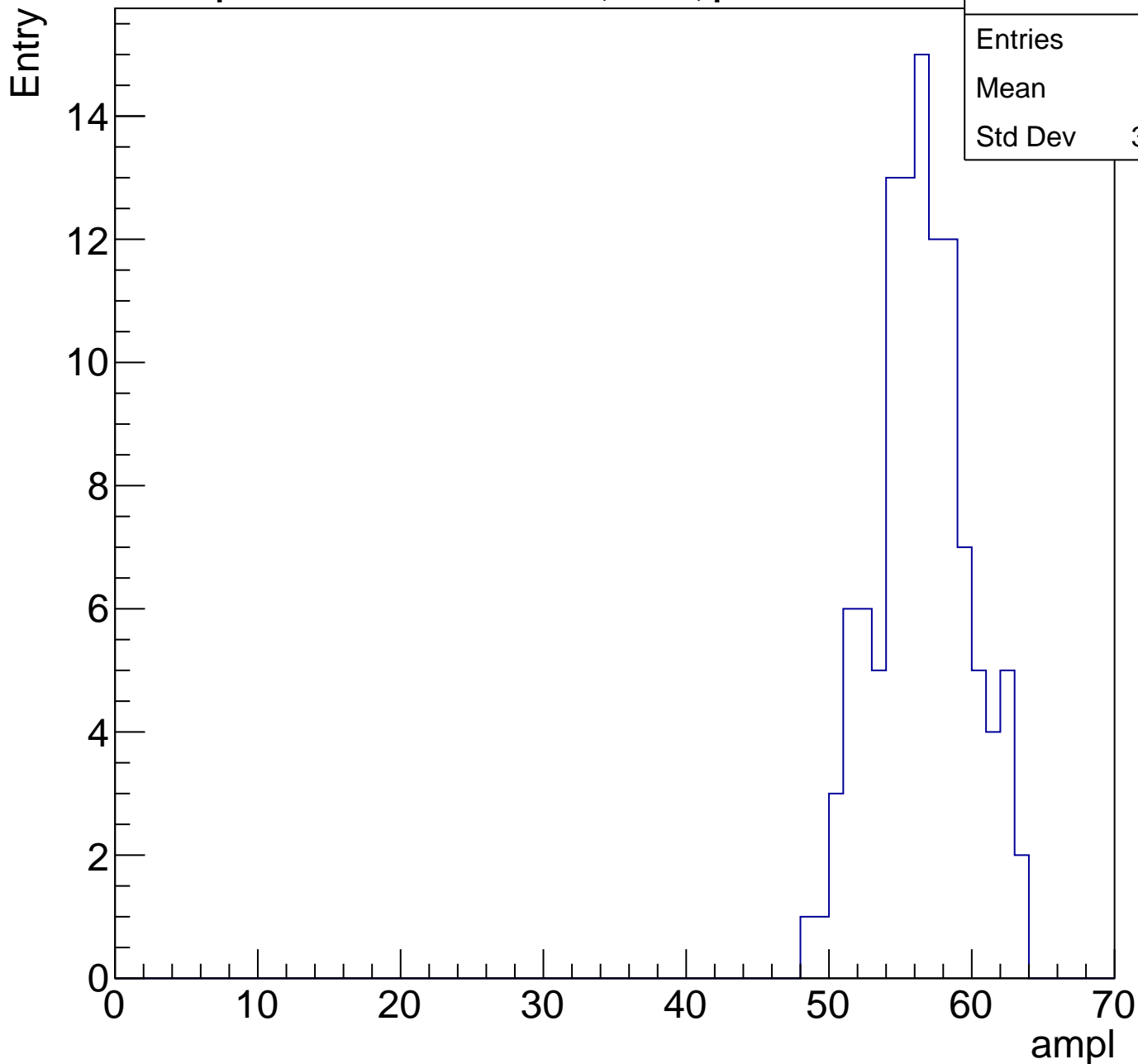
Entries	117
Mean	50.09
Std Dev	3.249



B1L001S, U16-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56
Std Dev	3.245



B1L001S, U16-ch51, adc5

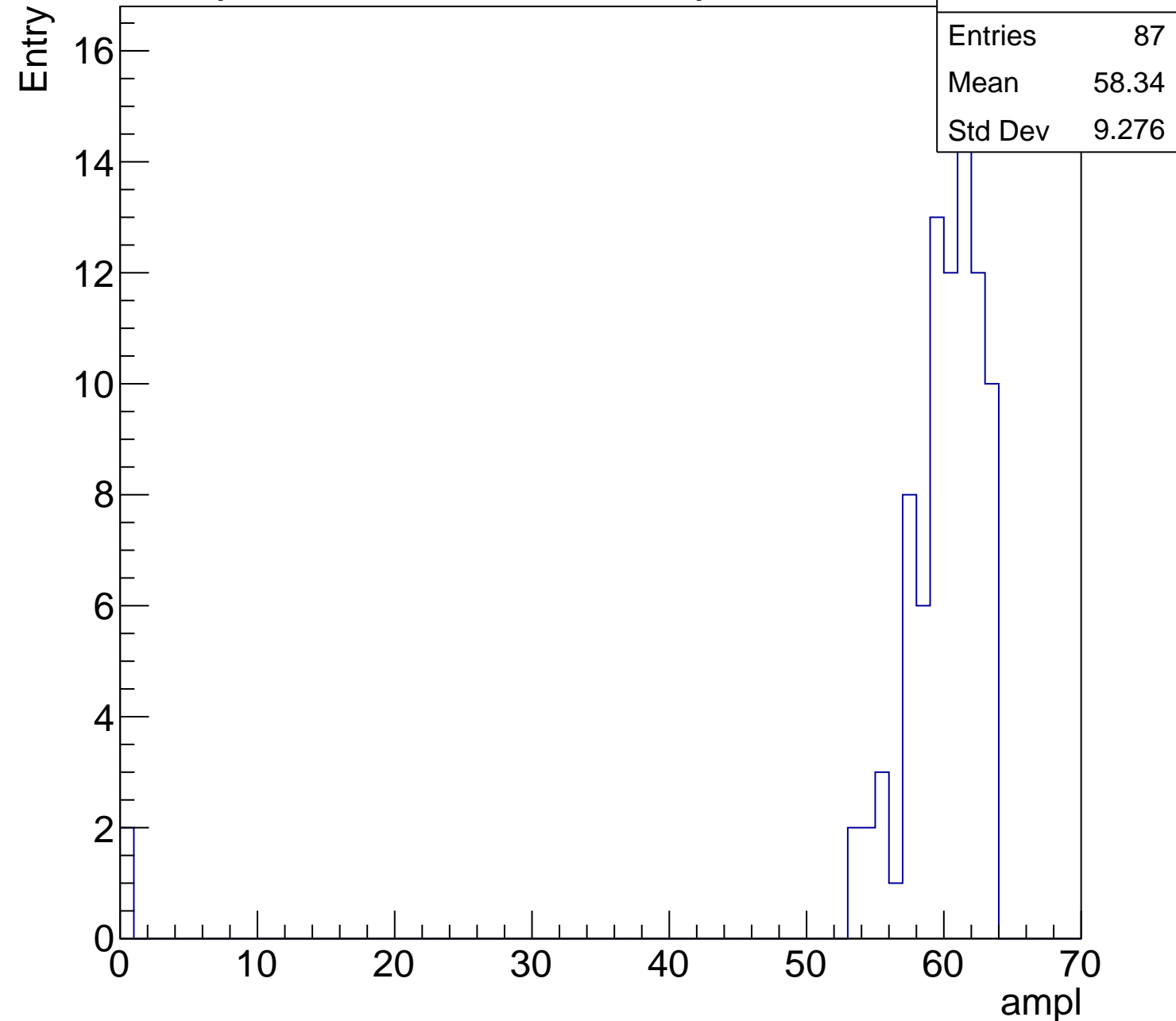
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.34
Std Dev	9.276

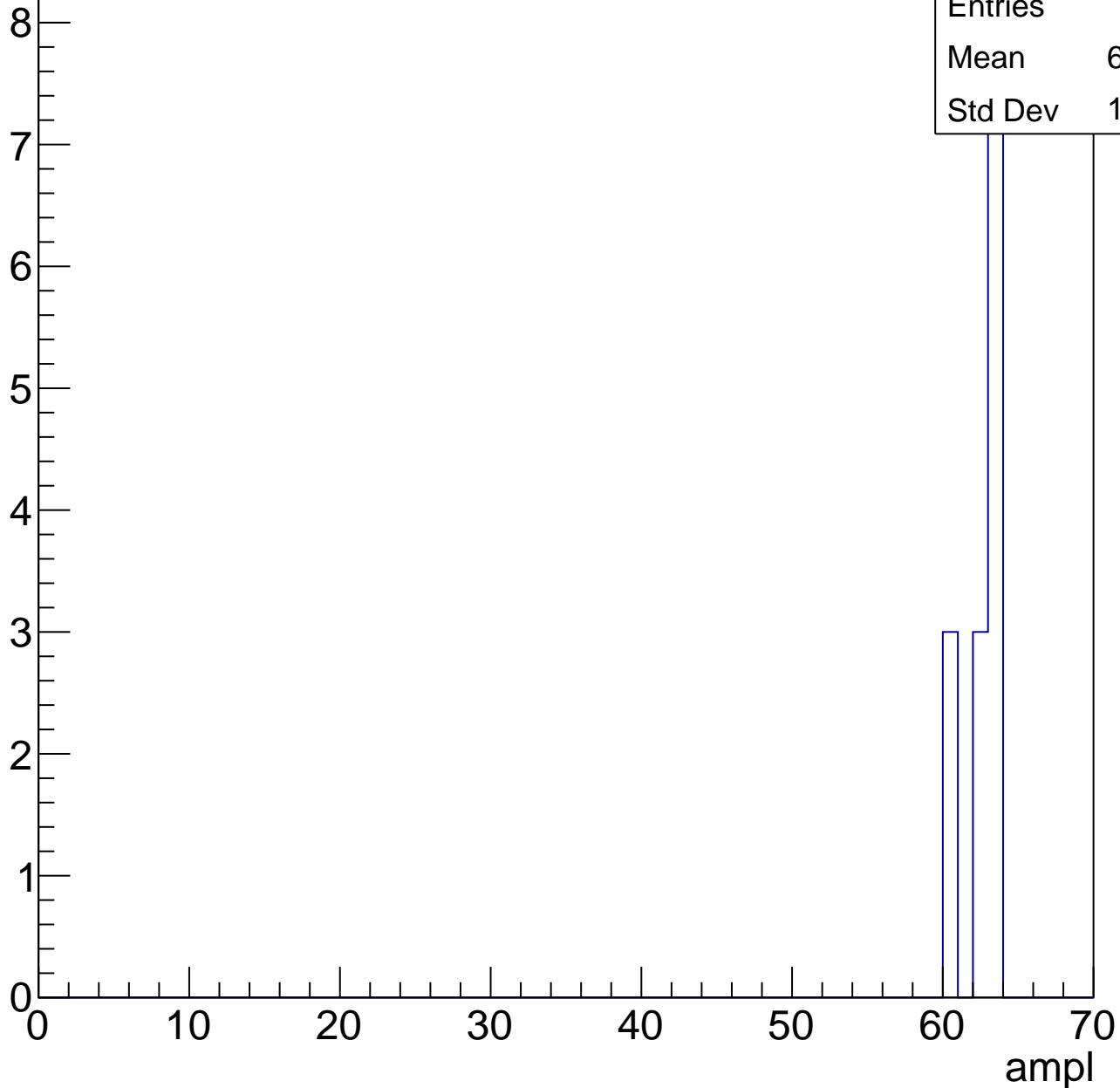
ampl



B1L001S, U16-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

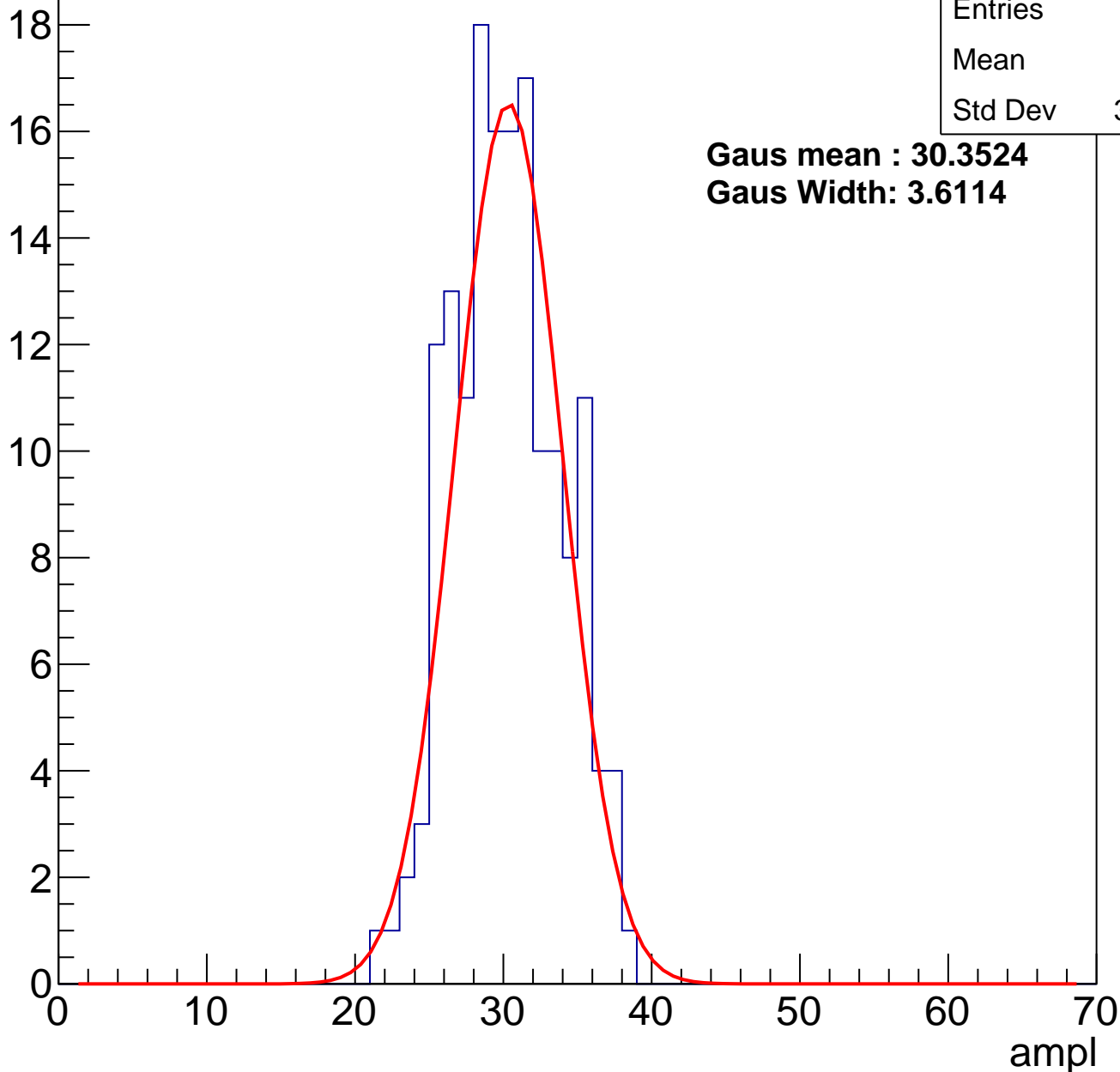
Entry



B1L001S, U16-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch52, adc1

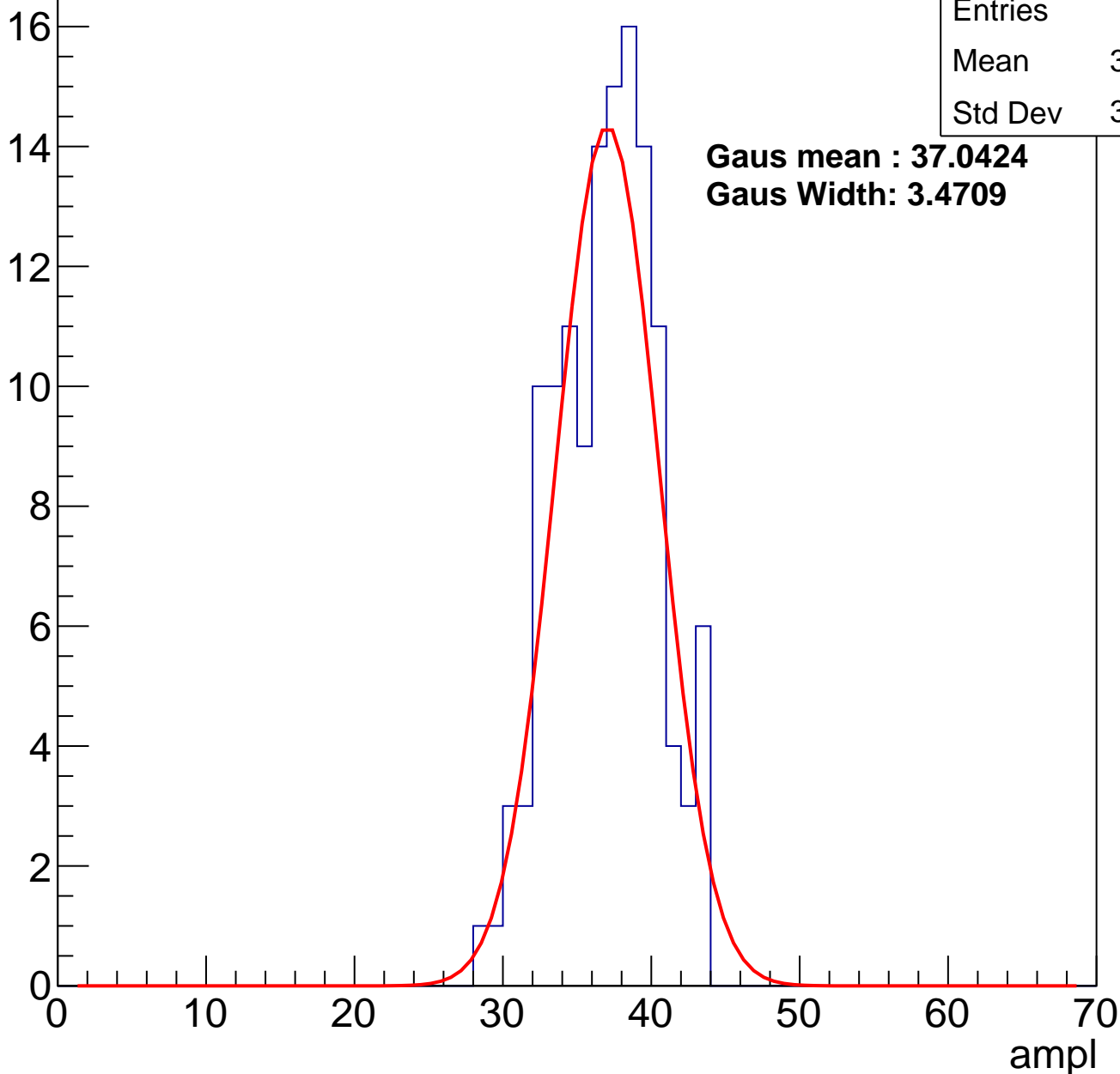
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.49
Std Dev	3.328

Gaus mean : 37.0424

Gaus Width: 3.4709

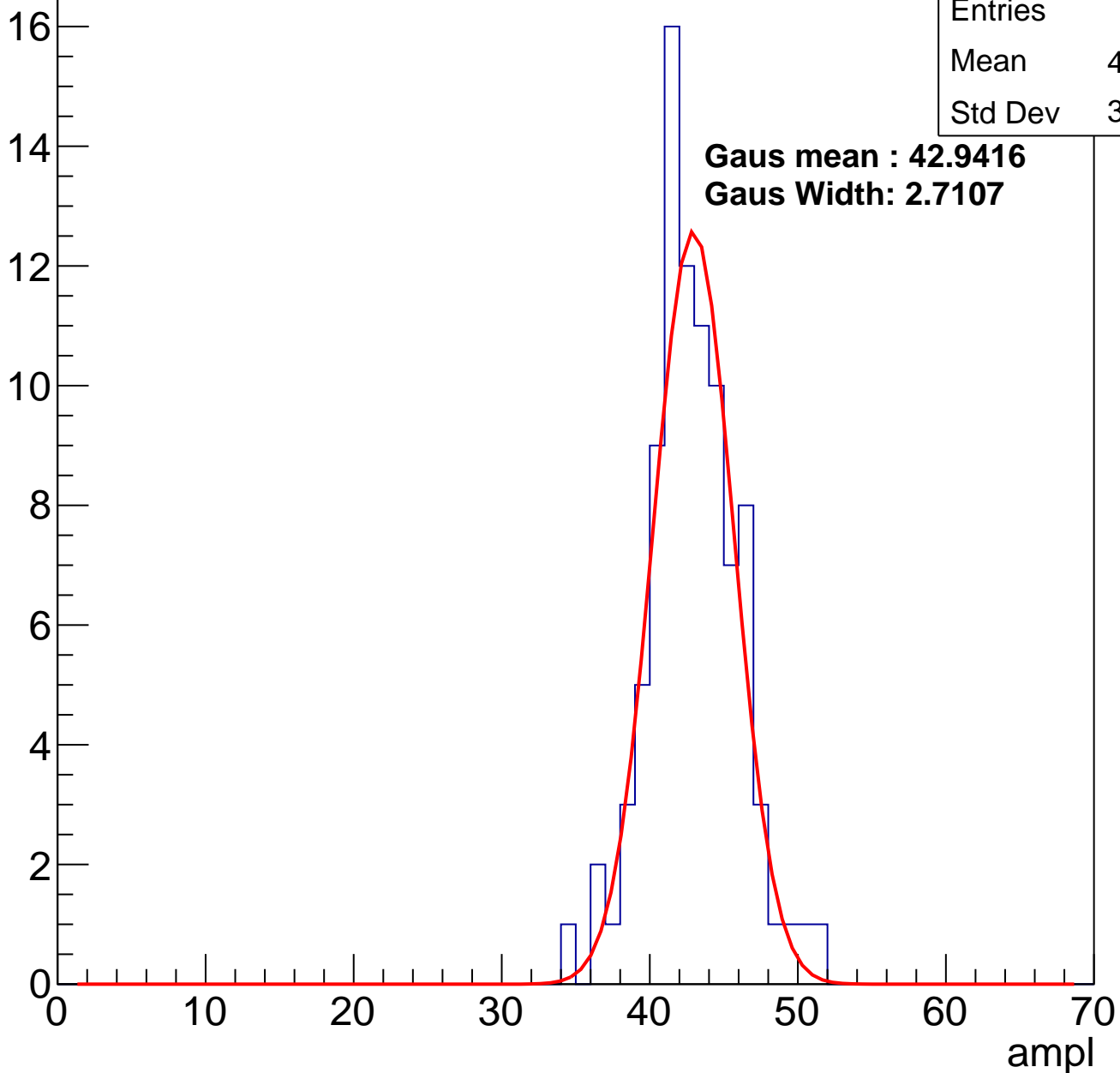
Entry



B1L001S, U16-ch52, adc2

calib_packv5_042523_0143.root, FC#2, port C2

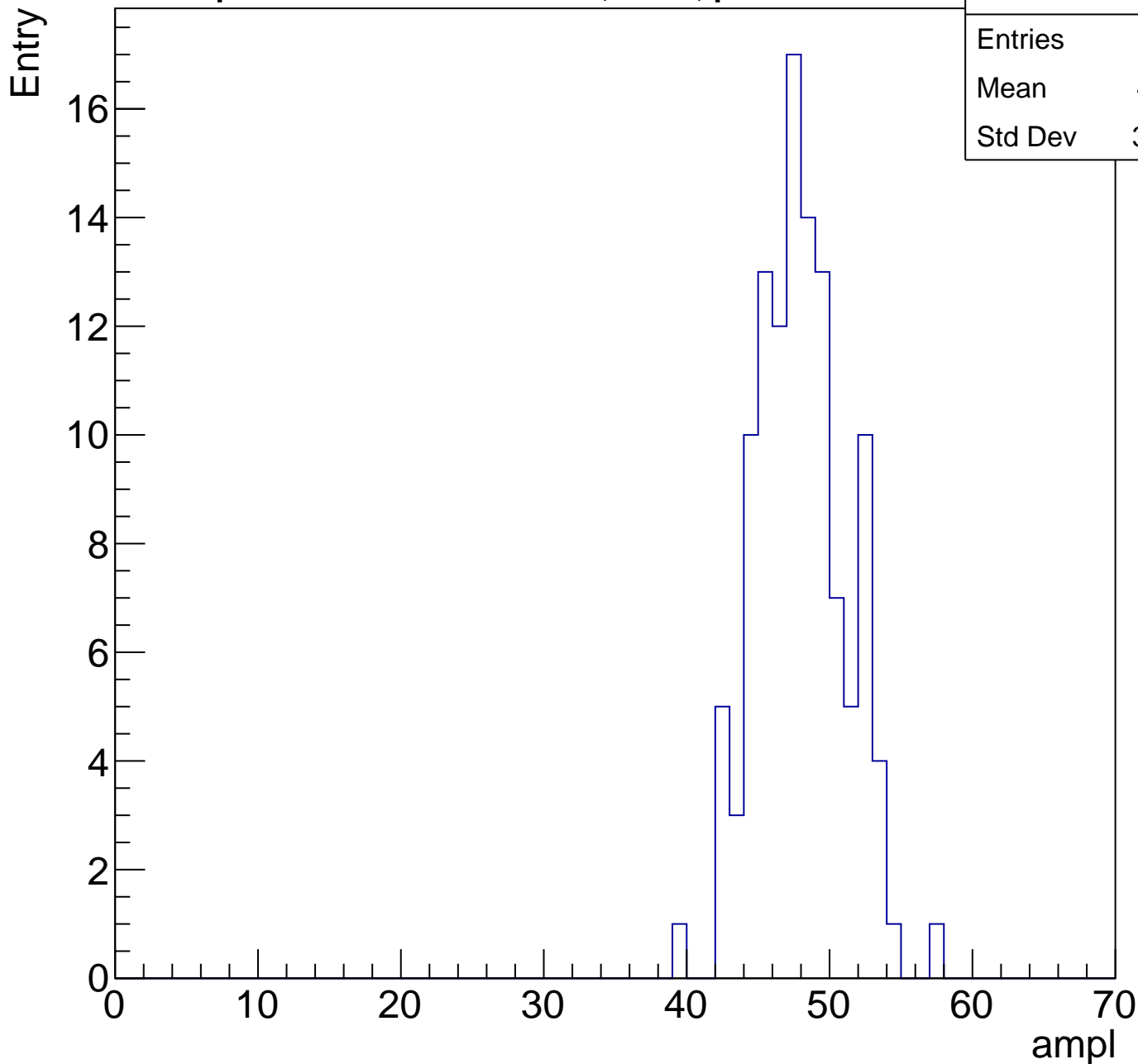
Entry



B1L001S, U16-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

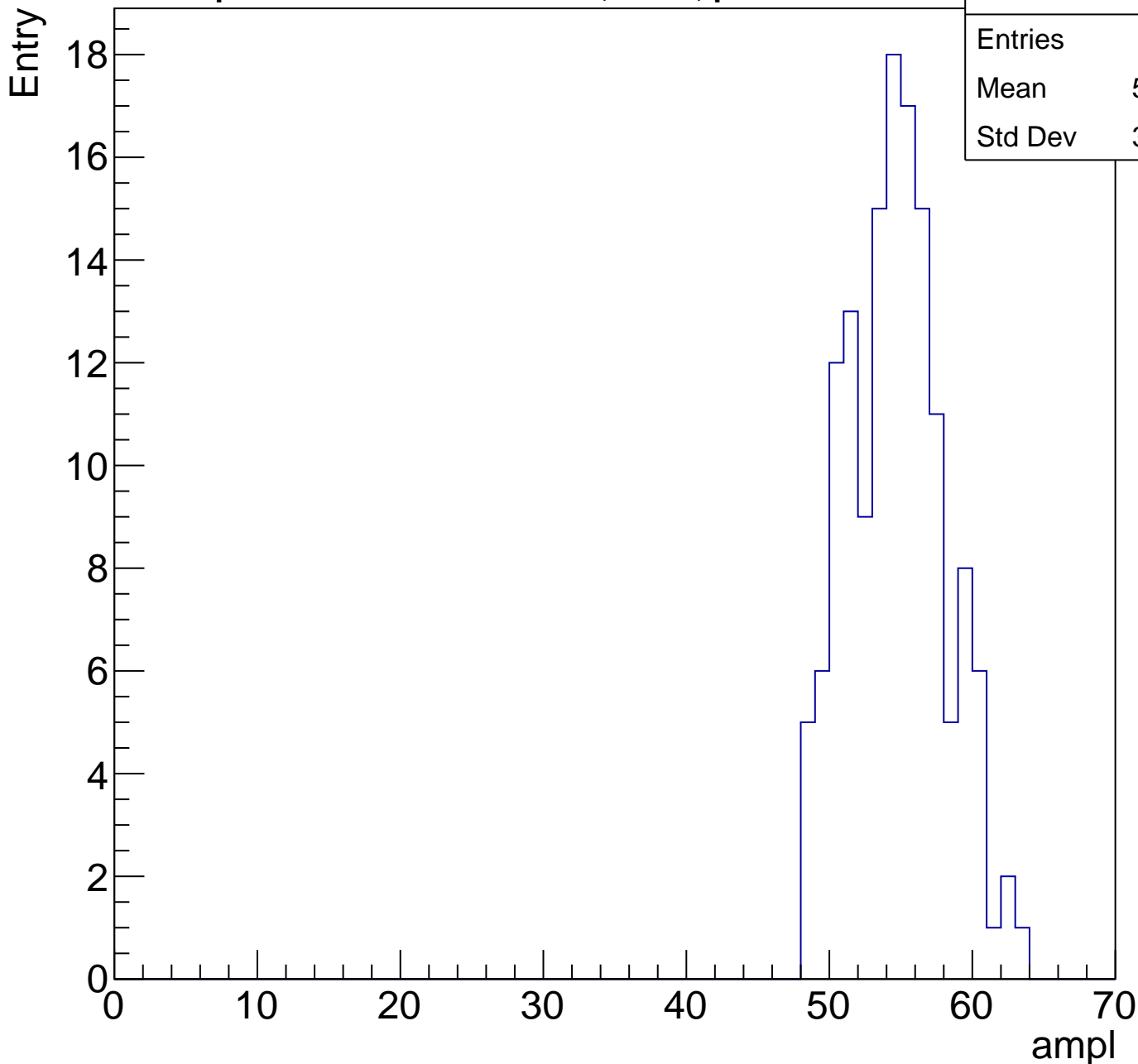
Entries	116
Mean	47.51
Std Dev	3.098



B1L001S, U16-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	54.19
Std Dev	3.348



B1L001S, U16-ch52, adc5

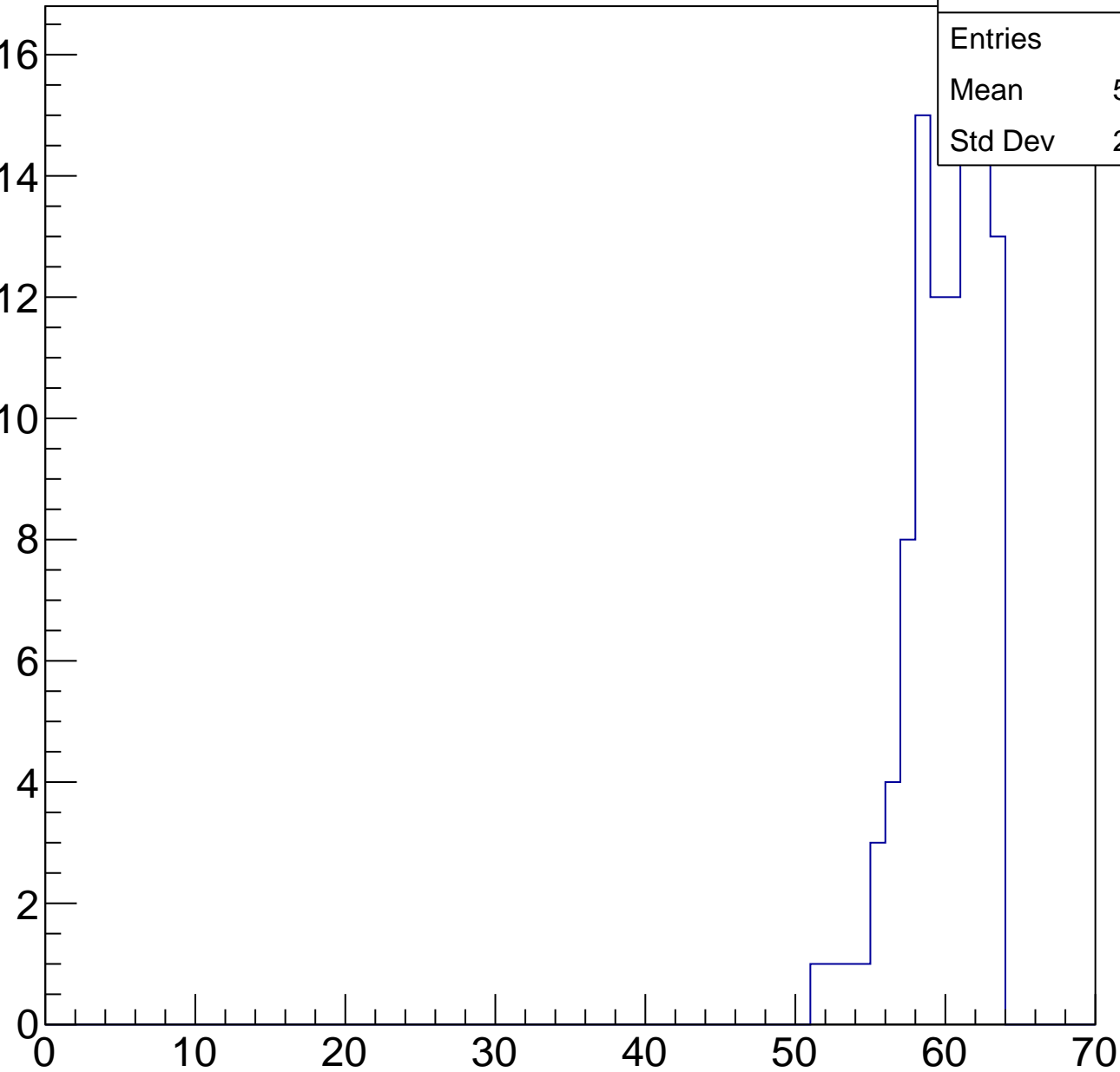
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.59
Std Dev	2.598

ampl

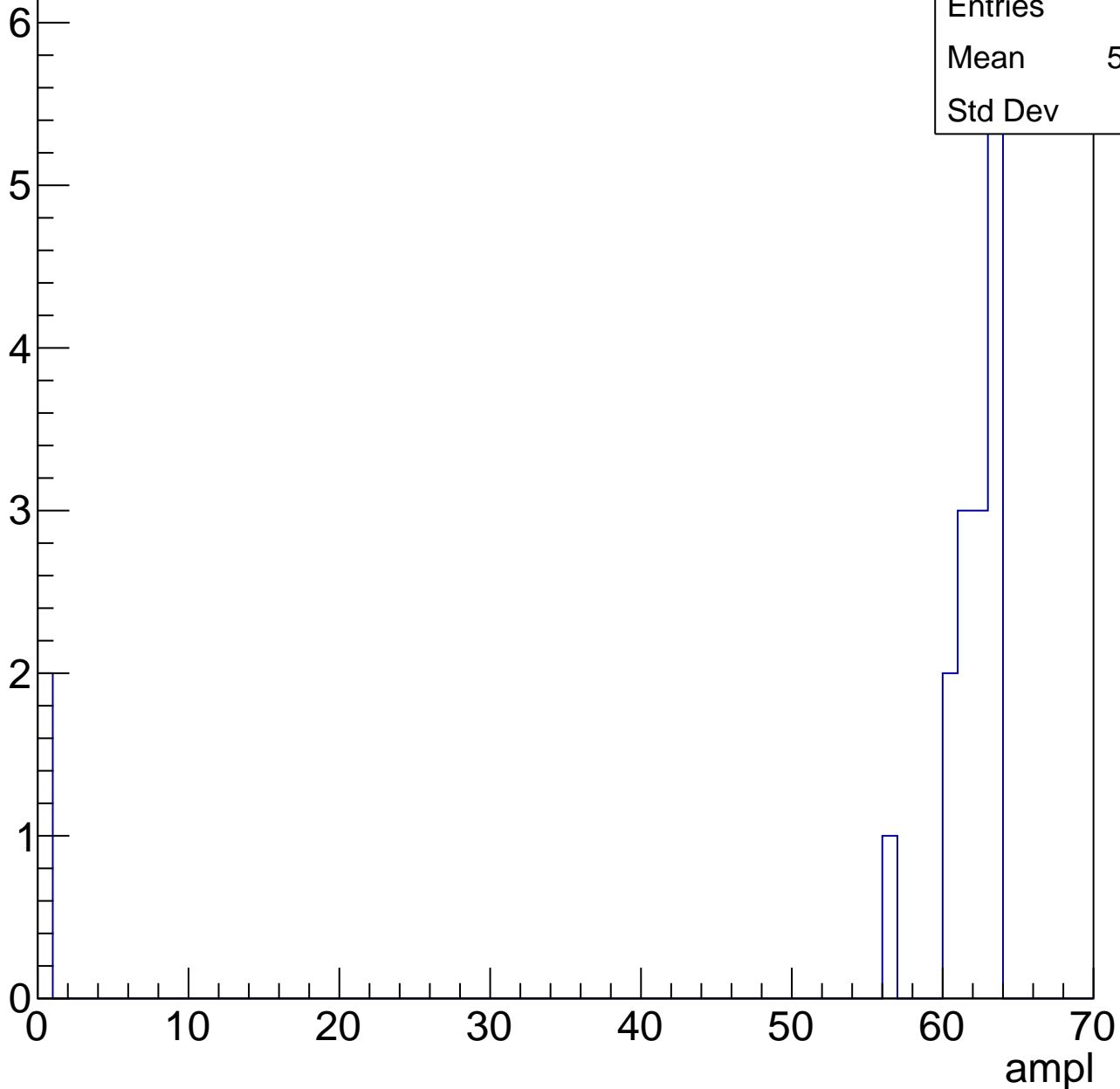


B1L001S, U16-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	54.29
Std Dev	19.9



B1L001S, U16-ch52, adc7

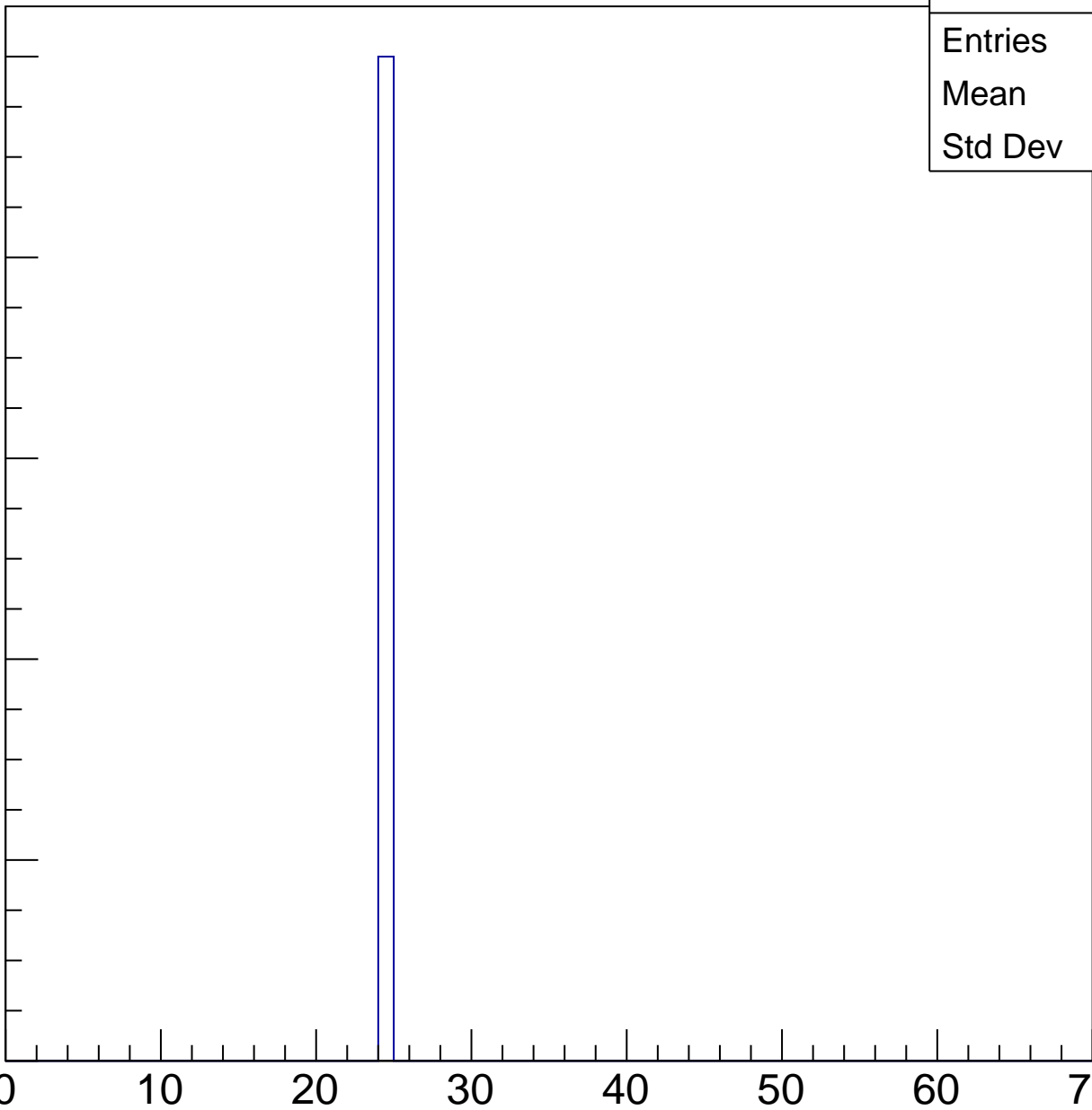
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U16-ch53, adc0

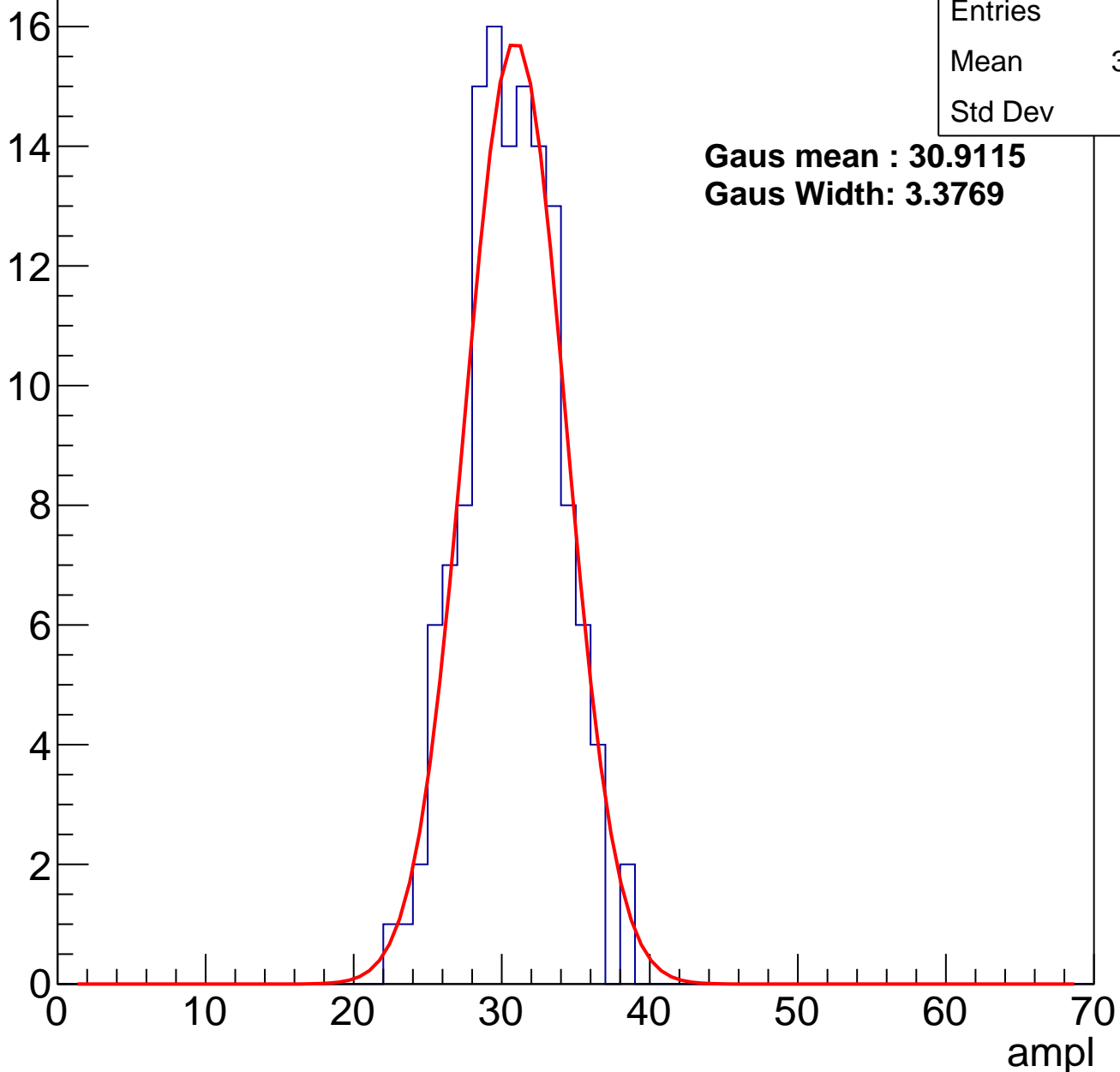
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	30.22
Std Dev	3.17

Gaus mean : 30.9115

Gaus Width: 3.3769

Entry



B1L001S, U16-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	37.01
Std Dev	3.656

Gaus mean : 37.4317

Gaus Width: 3.5531

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

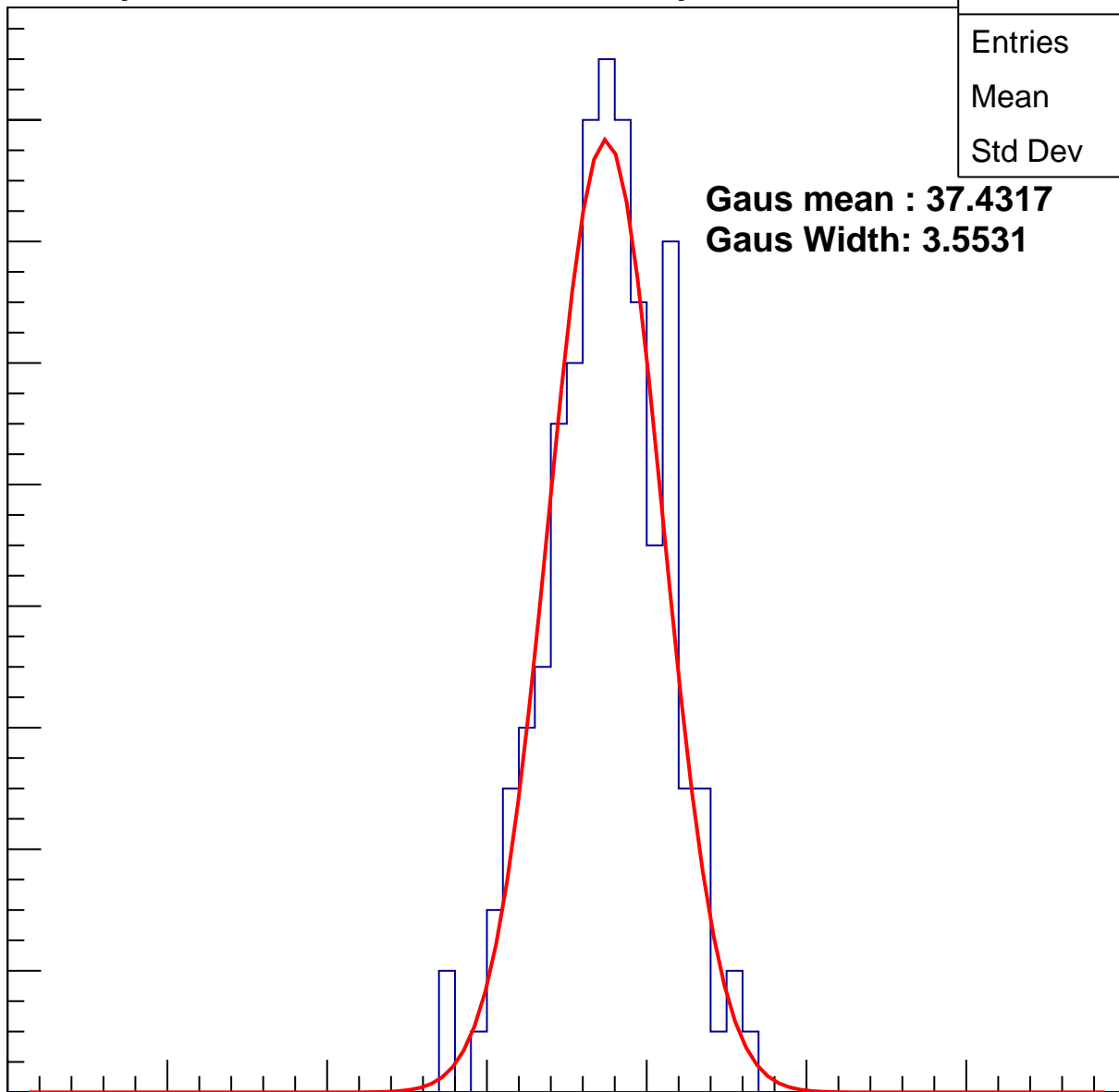
40

50

60

70

ampl



B1L001S, U16-ch53, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

8

6

4

2

0

Entries

109

Mean

43.27

Std Dev

3.626

Gaus mean : 43.7264

Gaus Width: 3.9315

ampl

70

60

50

40

30

20

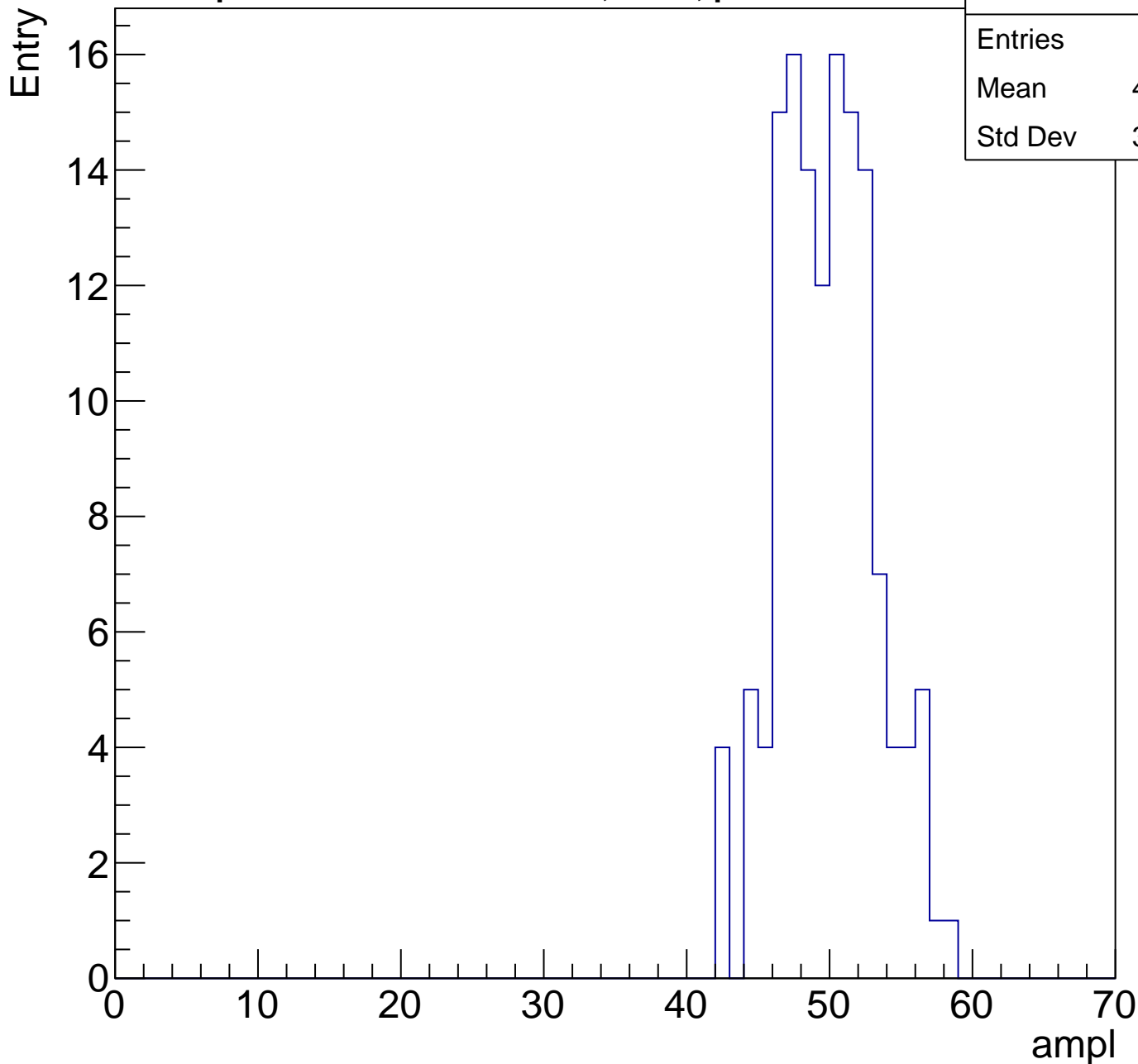
10

0

B1L001S, U16-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	49.38
Std Dev	3.338



B1L001S, U16-ch53, adc4

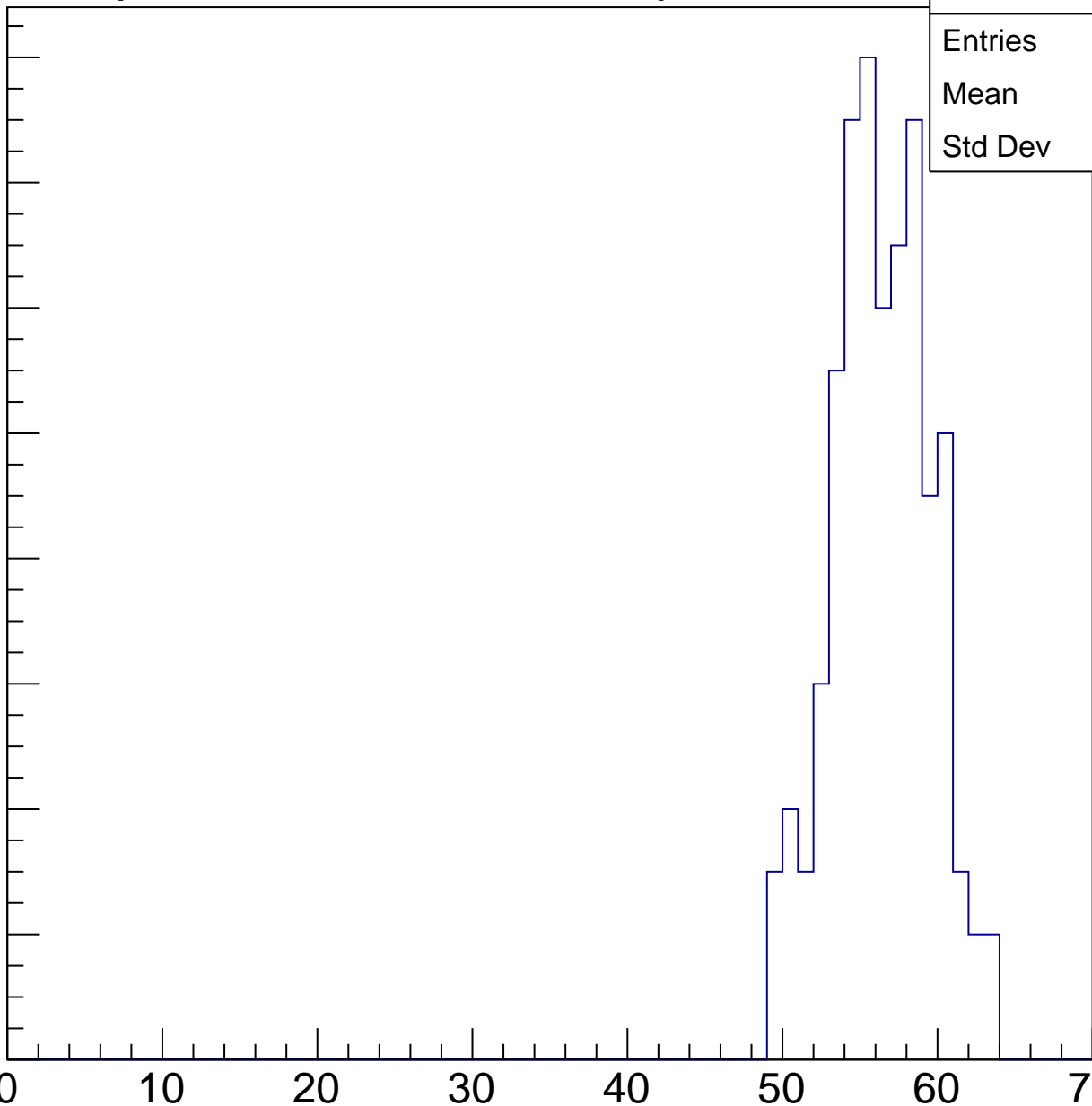
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	55.9
Std Dev	3.104

ampl



B1L001S, U16-ch53, adc5

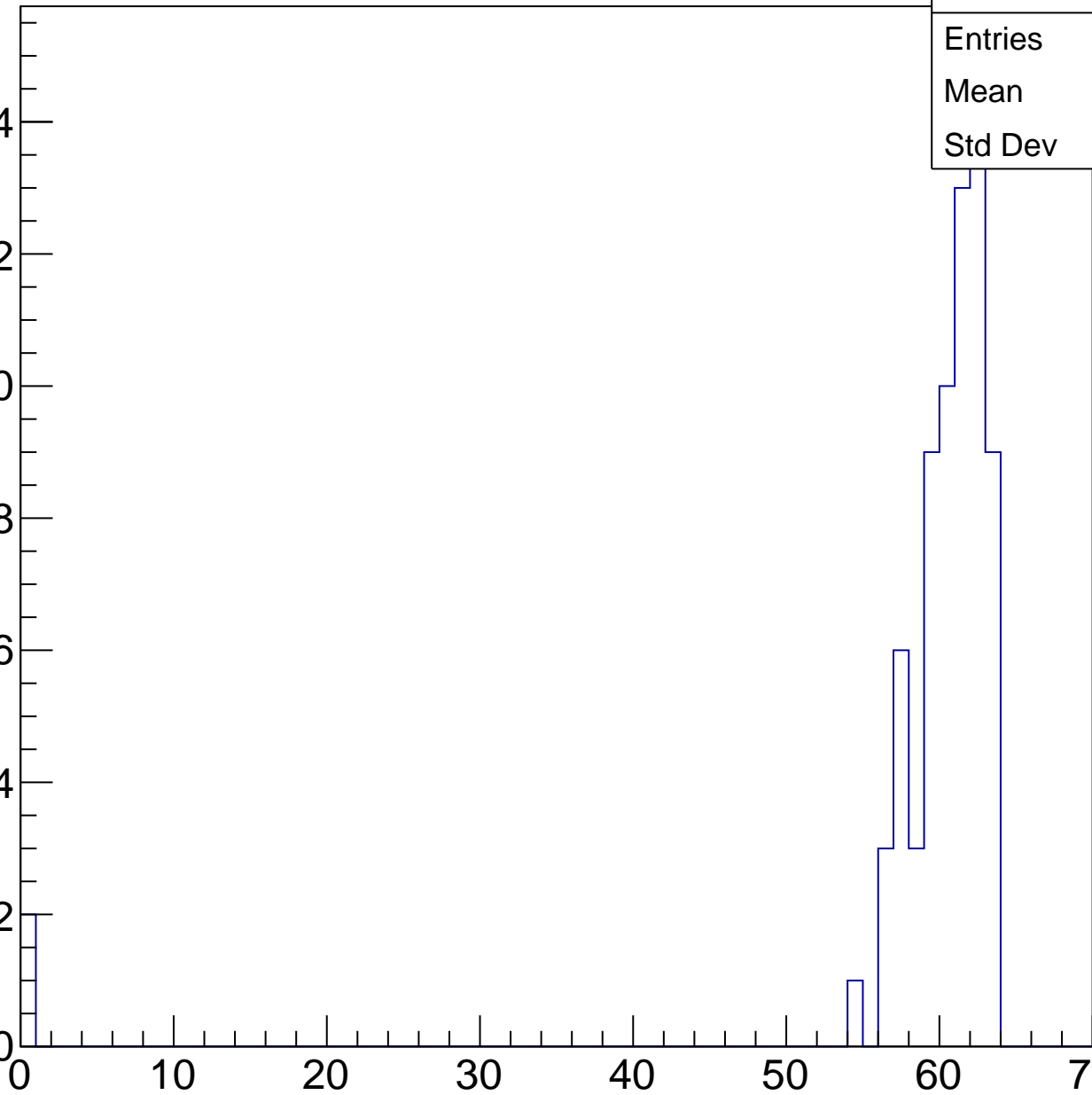
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.58
Std Dev	10.19

ampl

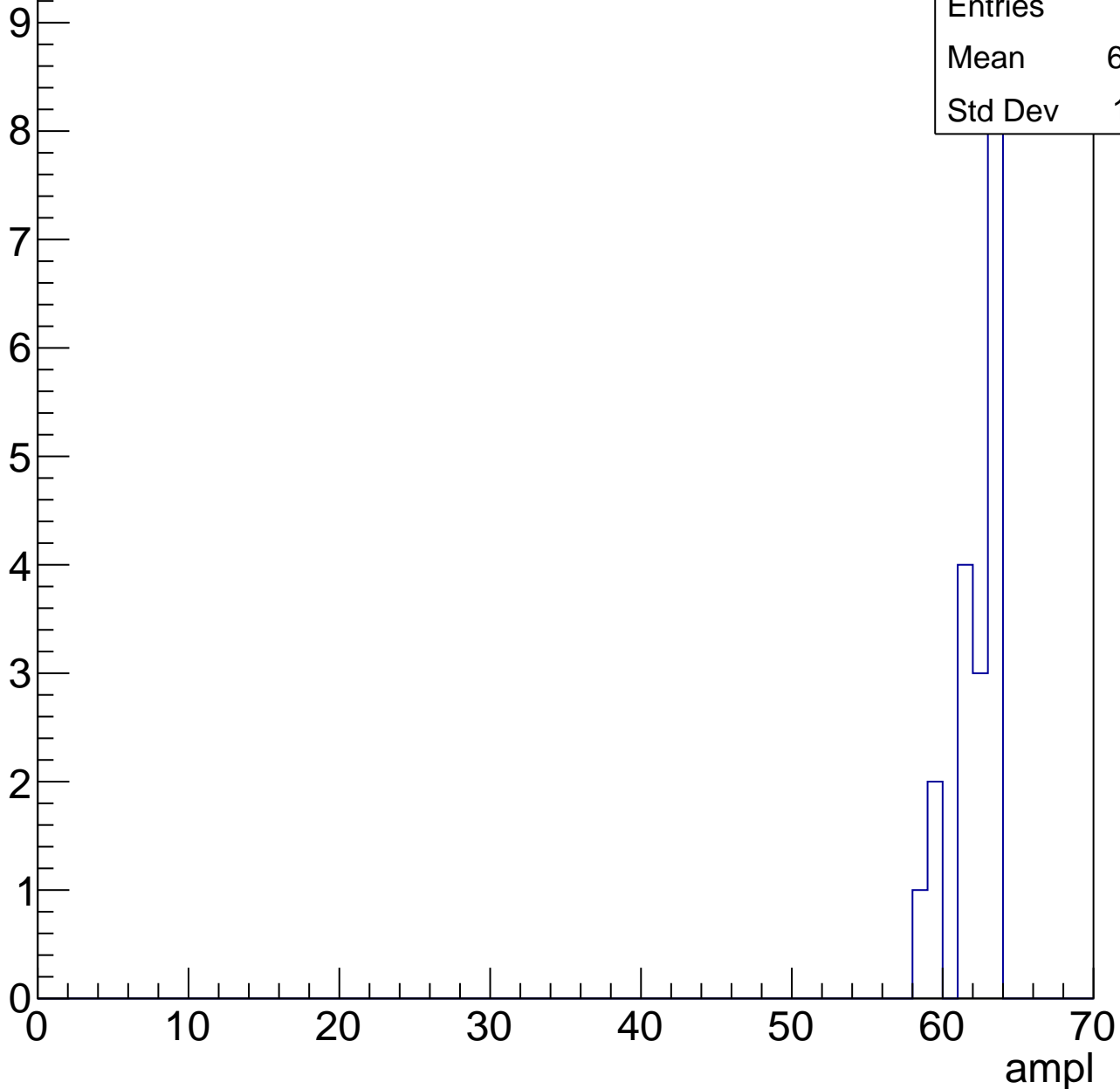


B1L001S, U16-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.74
Std Dev	1.551



B1L001S, U16-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

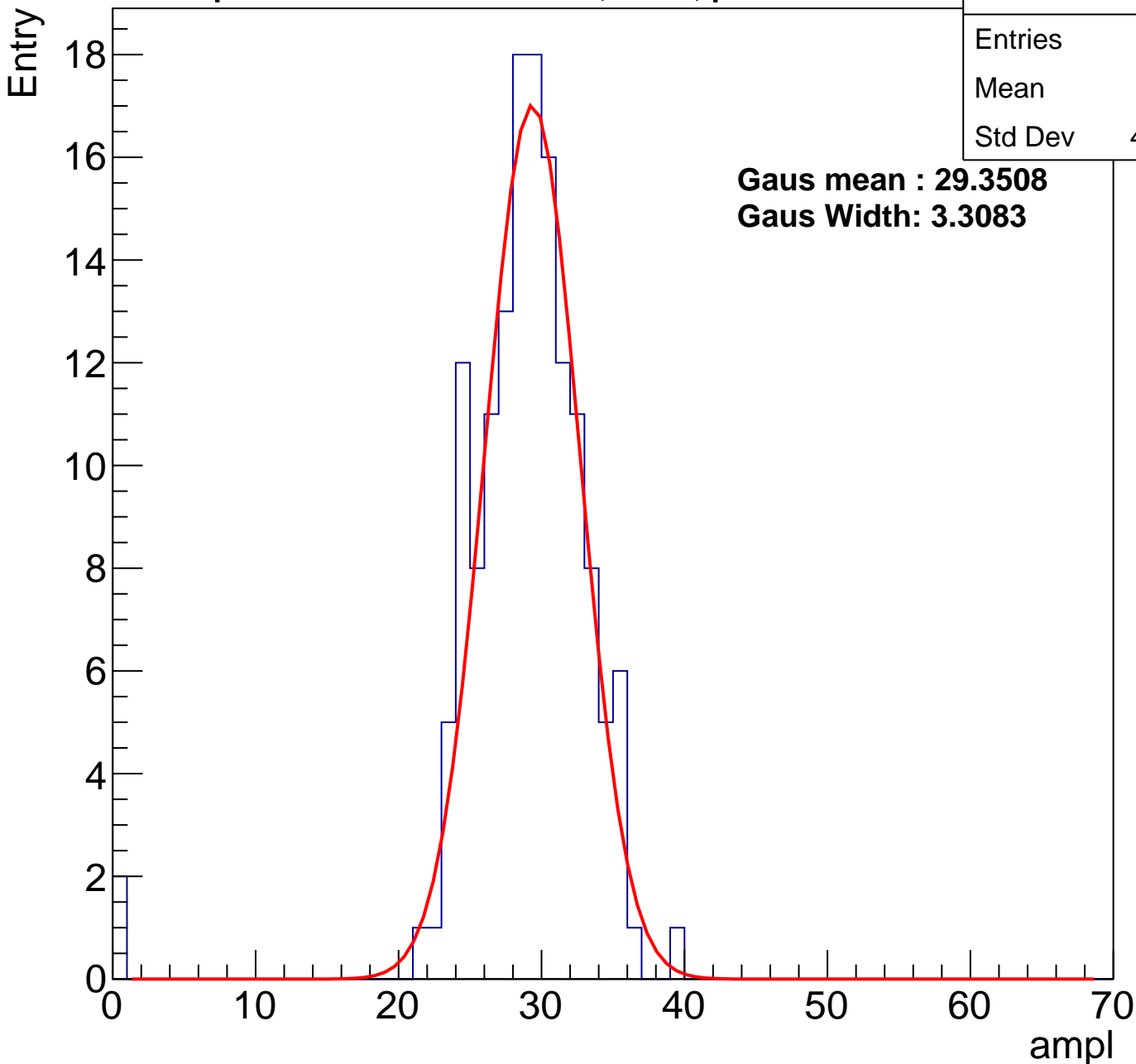
B1L001S, U16-ch54, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	28.4
Std Dev	4.706

Gaus mean : 29.3508

Gaus Width: 3.3083



B1L001S, U16-ch54, adc1

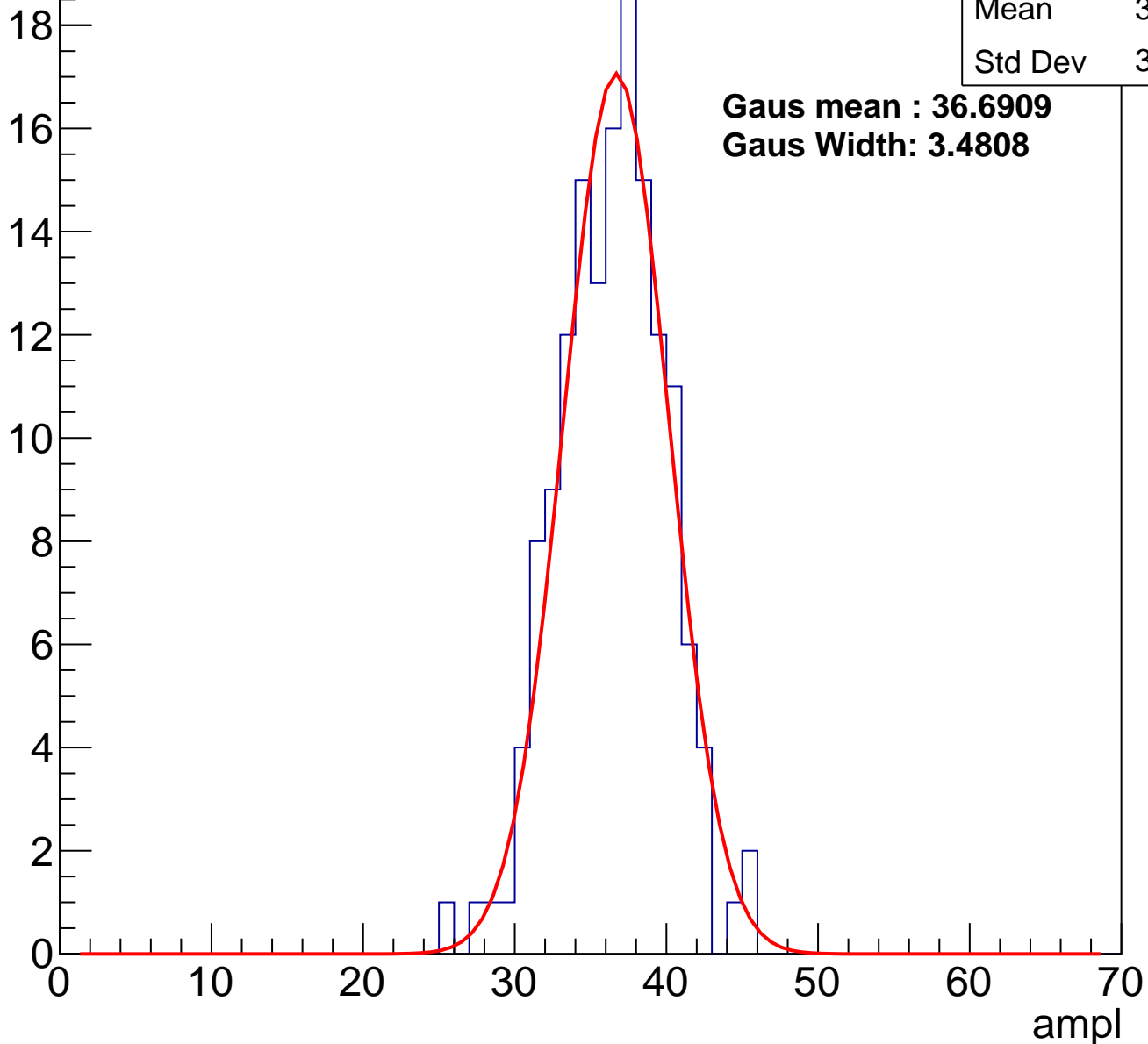
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.97
Std Dev	3.516

Gaus mean : 36.6909

Gaus Width: 3.4808

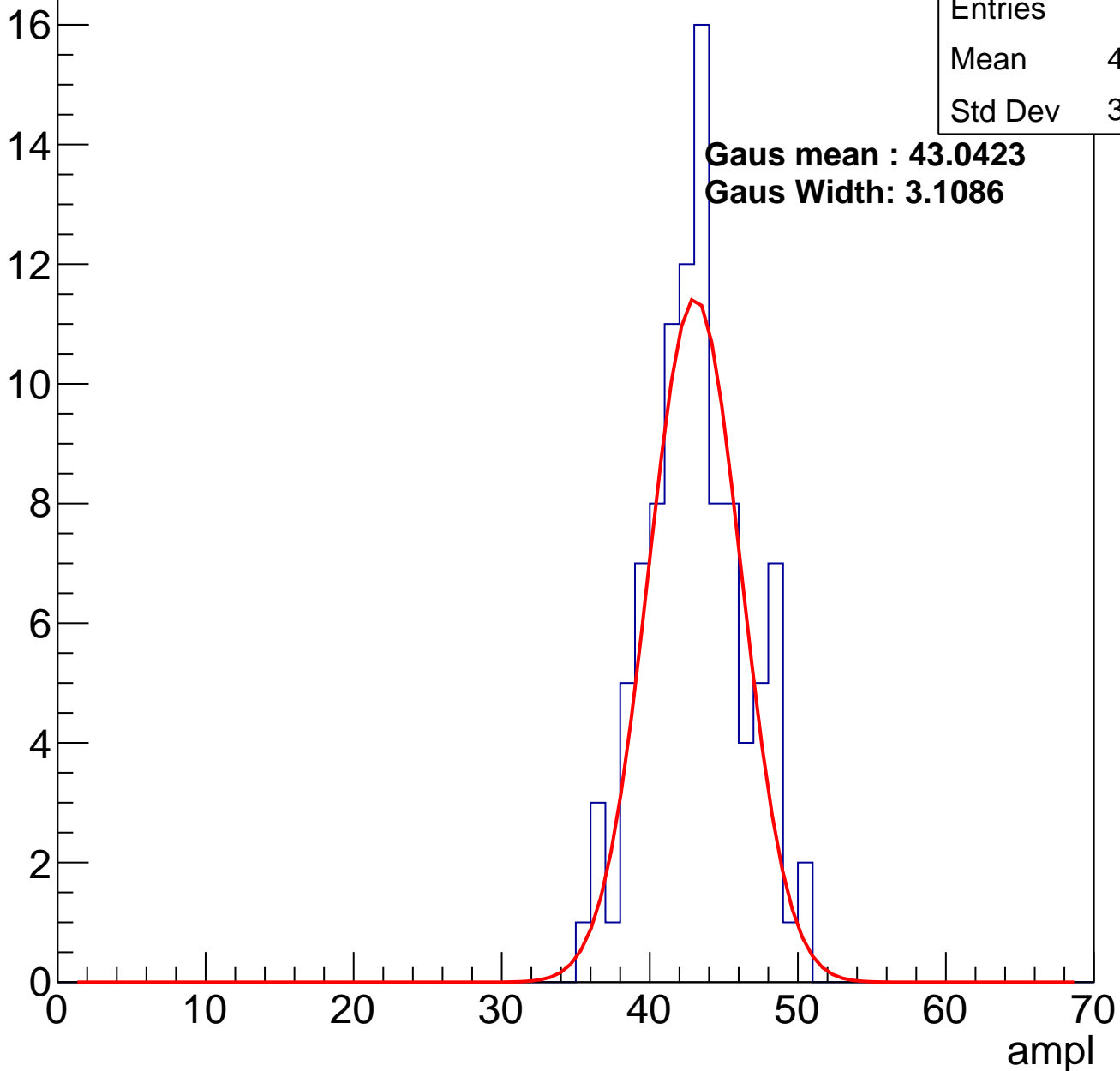
Entry



B1L001S, U16-ch54, adc2

calib_packv5_042523_0143.root, FC#2, port C2

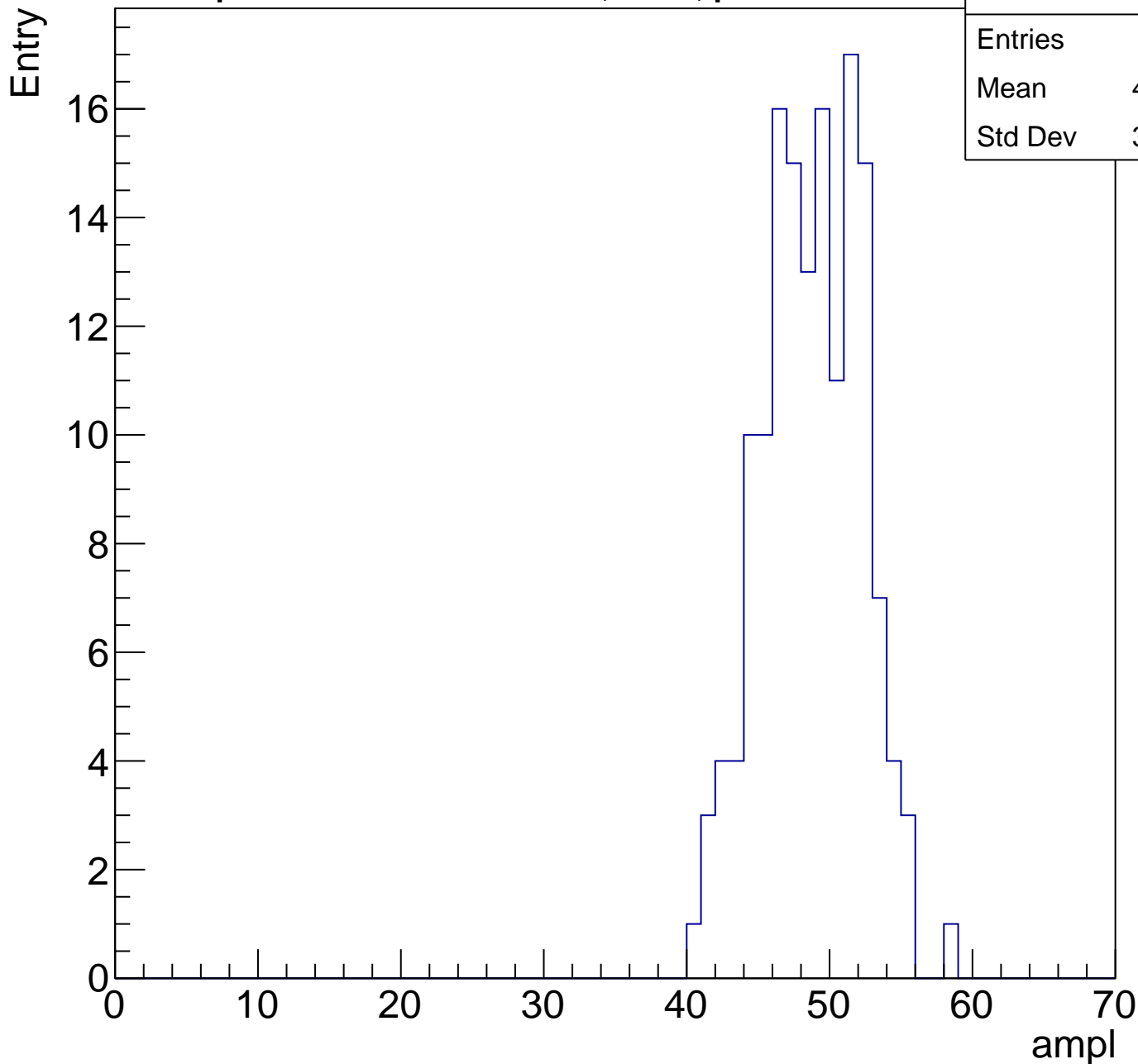
Entry



B1L001S, U16-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	48.33
Std Dev	3.452



B1L001S, U16-ch54, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	55.17
Std Dev	3.196

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

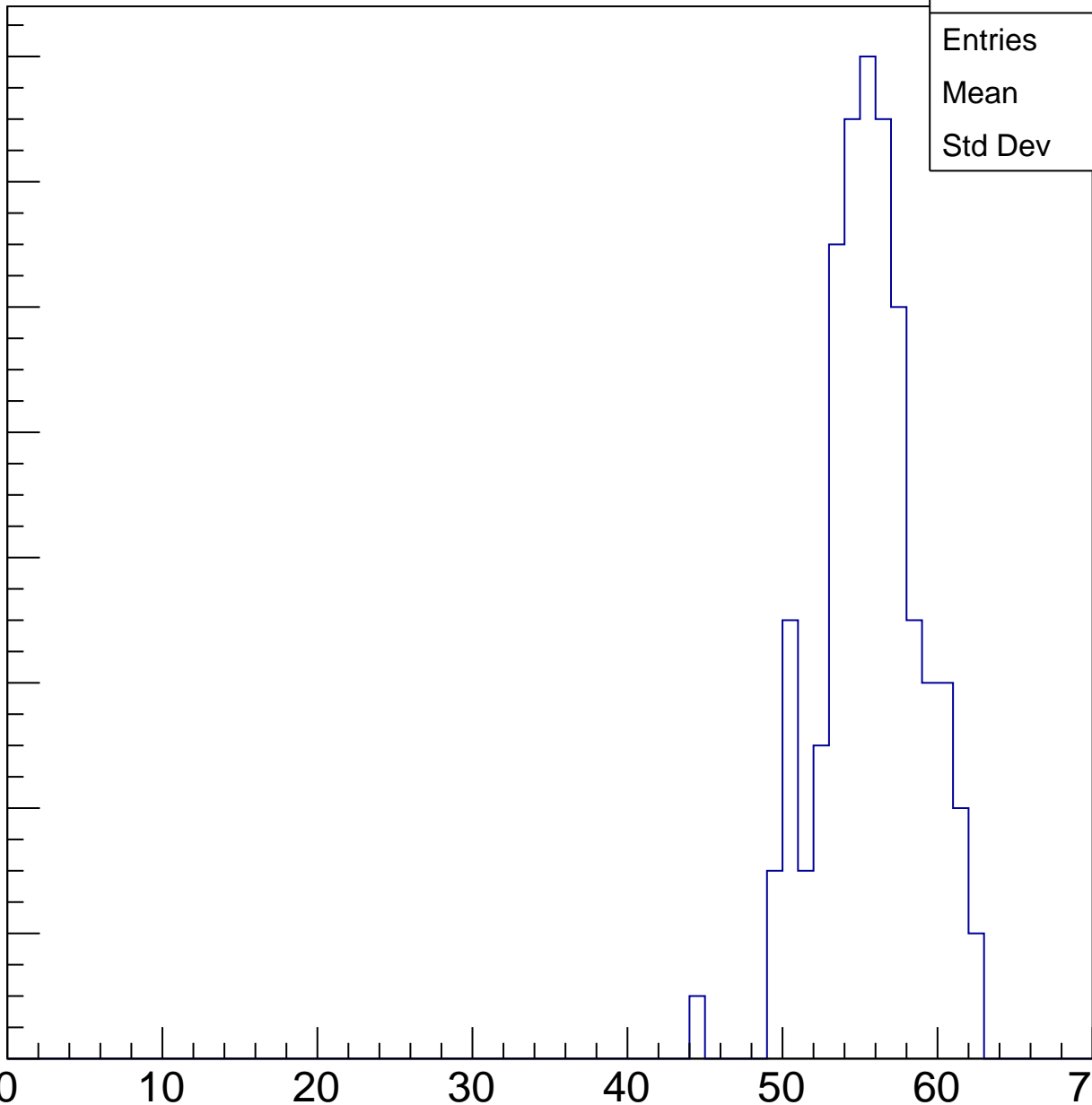
40

50

60

70

ampl



B1L001S, U16-ch54, adc5

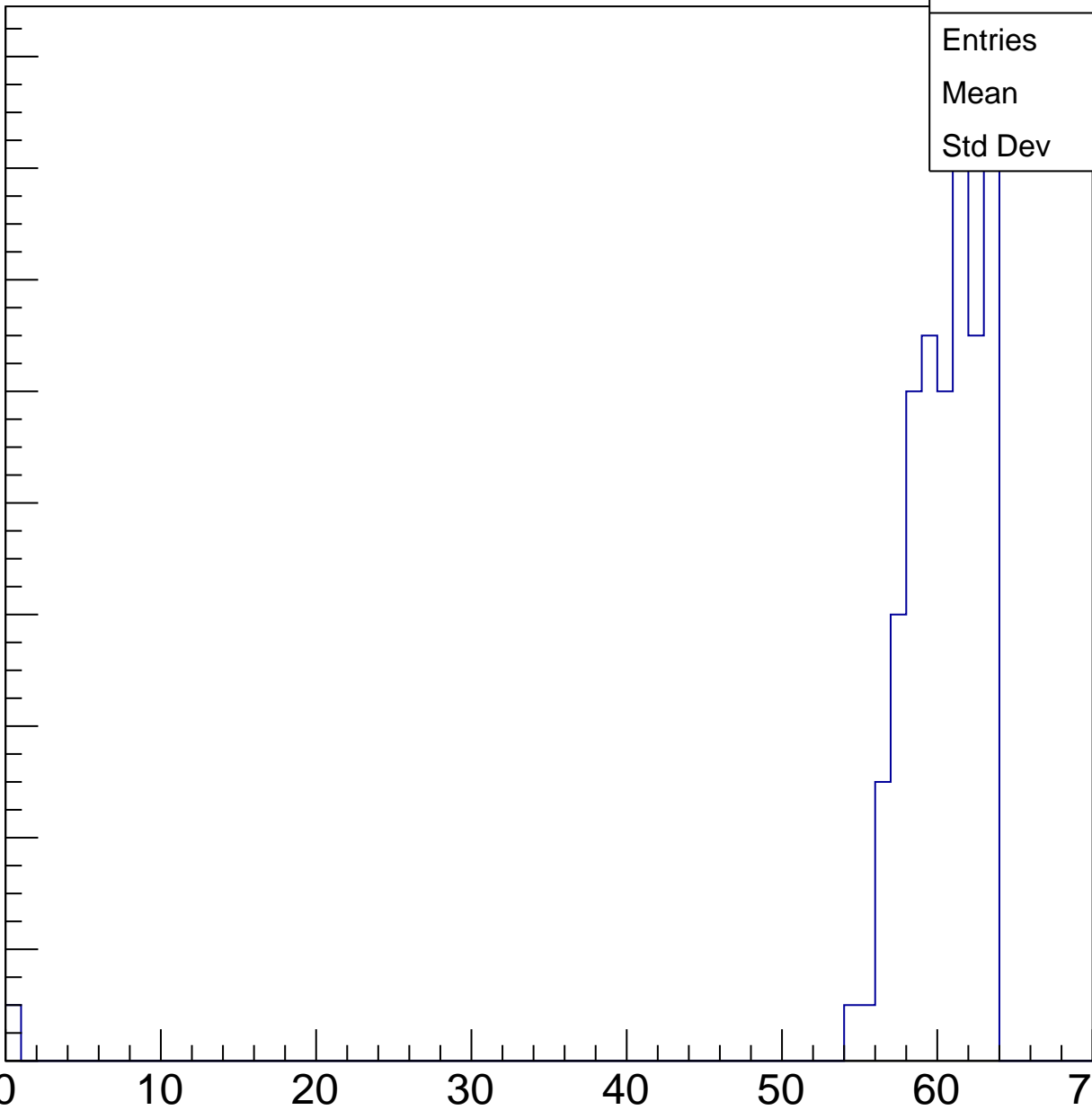
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.44
Std Dev	6.382

ampl

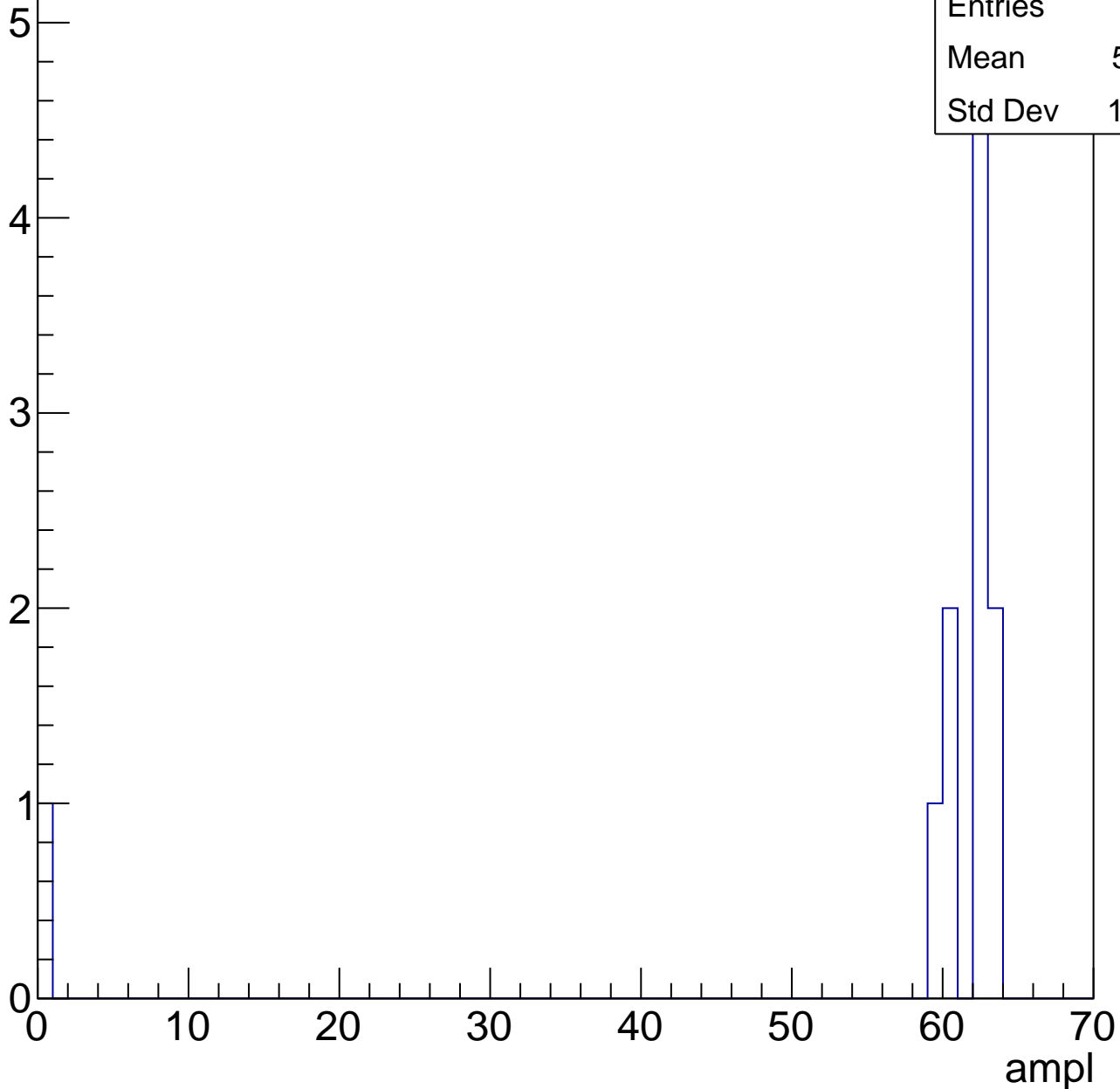


B1L001S, U16-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	55.91
Std Dev	17.72



B1L001S, U16-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch55, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	29.65
Std Dev	3.466

Gaus mean : 30.4281

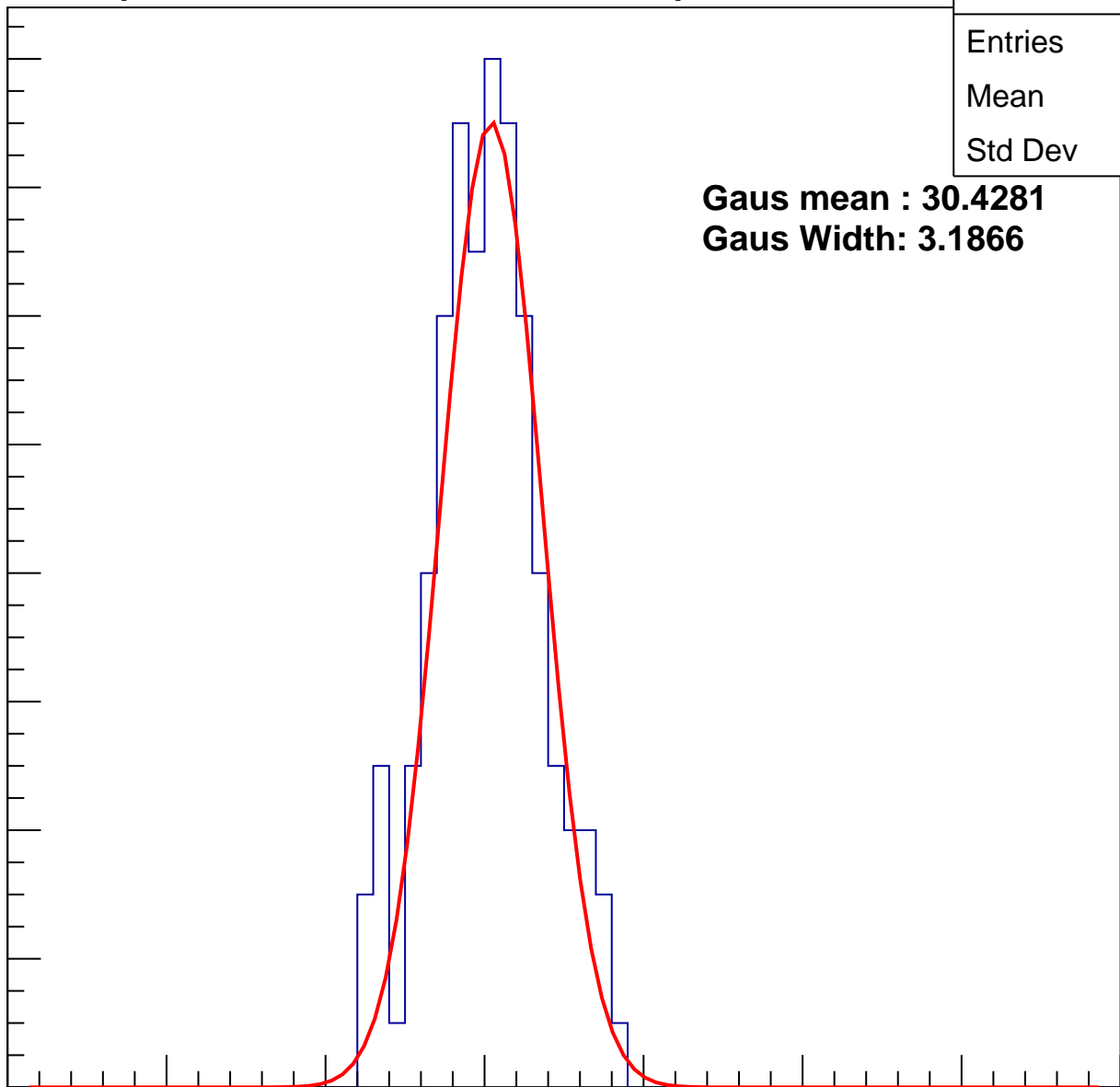
Gaus Width: 3.1866

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch55, adc1

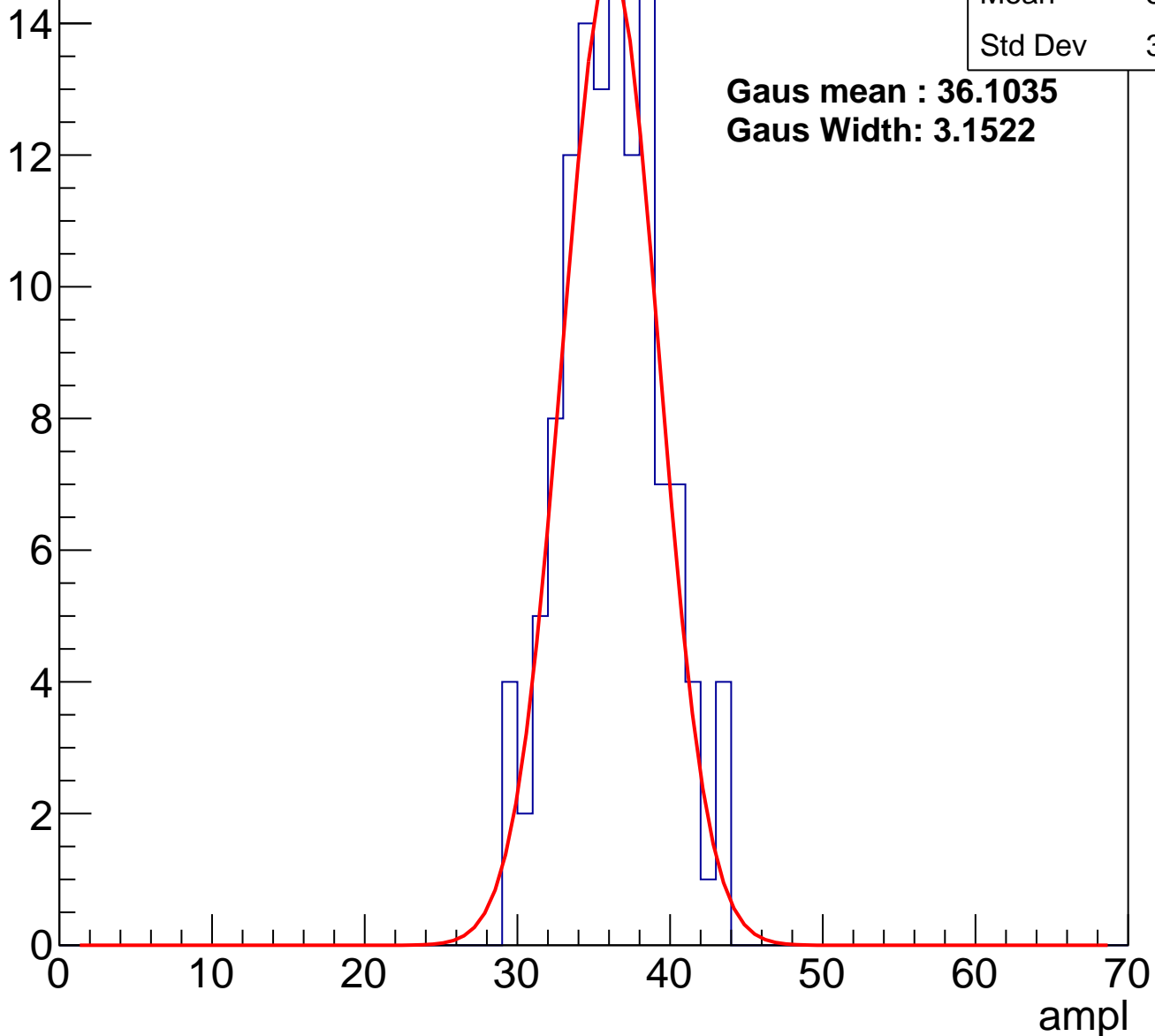
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.76
Std Dev	3.213

Gaus mean : 36.1035

Gaus Width: 3.1522

Entry



B1L001S, U16-ch55, adc2

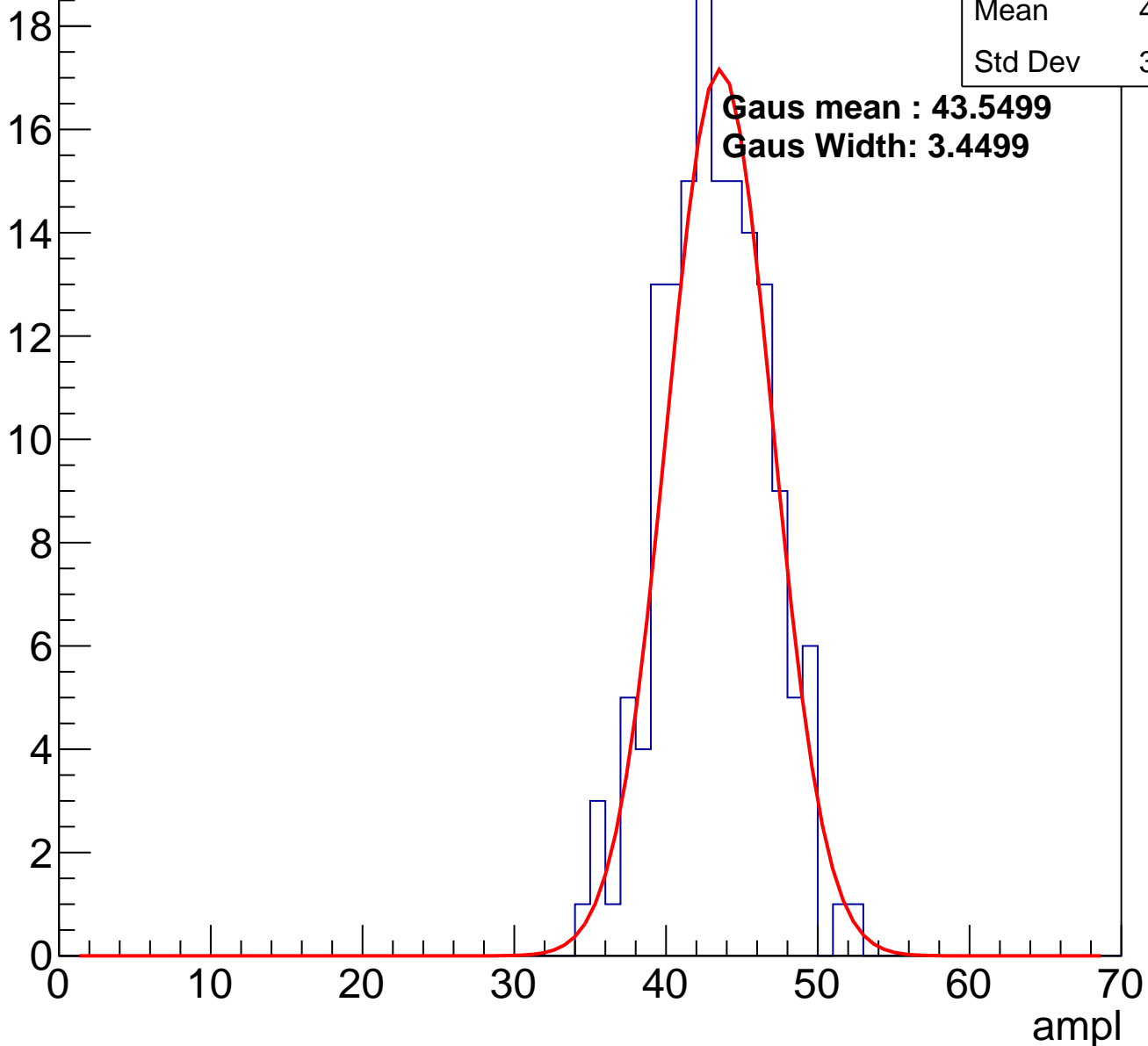
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	42.78
Std Dev	3.446

Gaus mean : 43.5499

Gaus Width: 3.4499

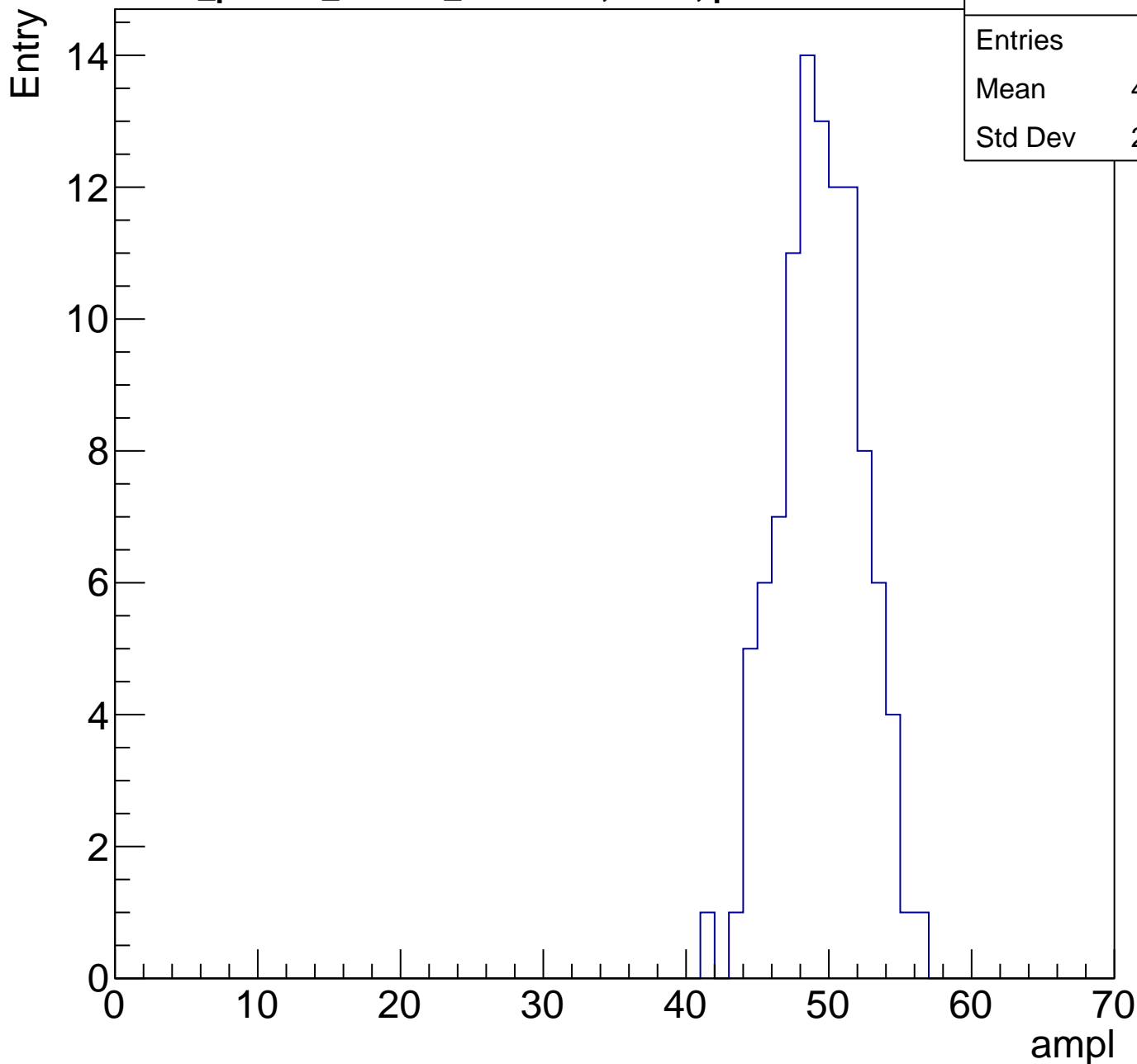
Entry



B1L001S, U16-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

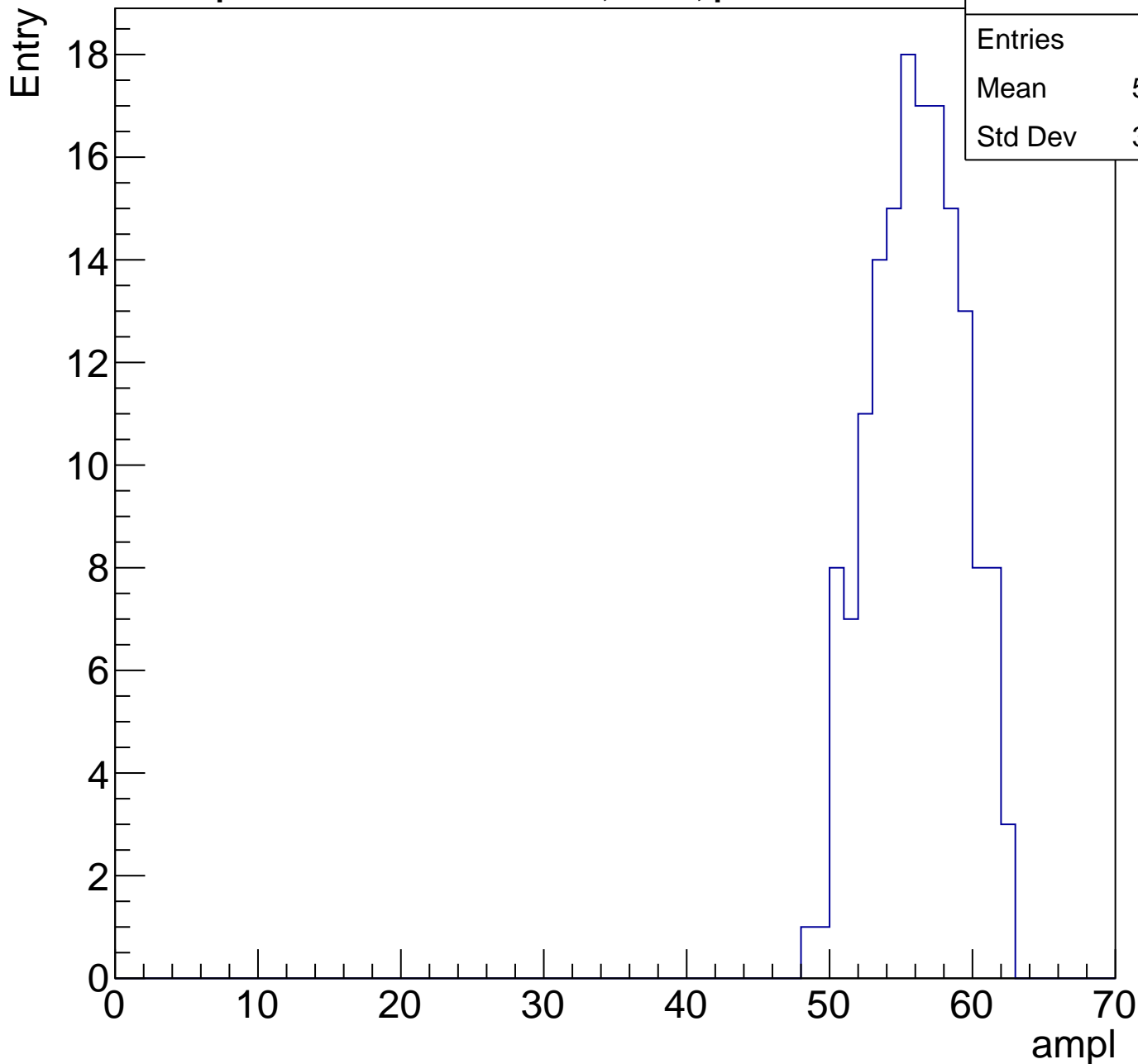
Entries	102
Mean	48.97
Std Dev	2.895



B1L001S, U16-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	55.64
Std Dev	3.168



B1L001S, U16-ch55, adc5

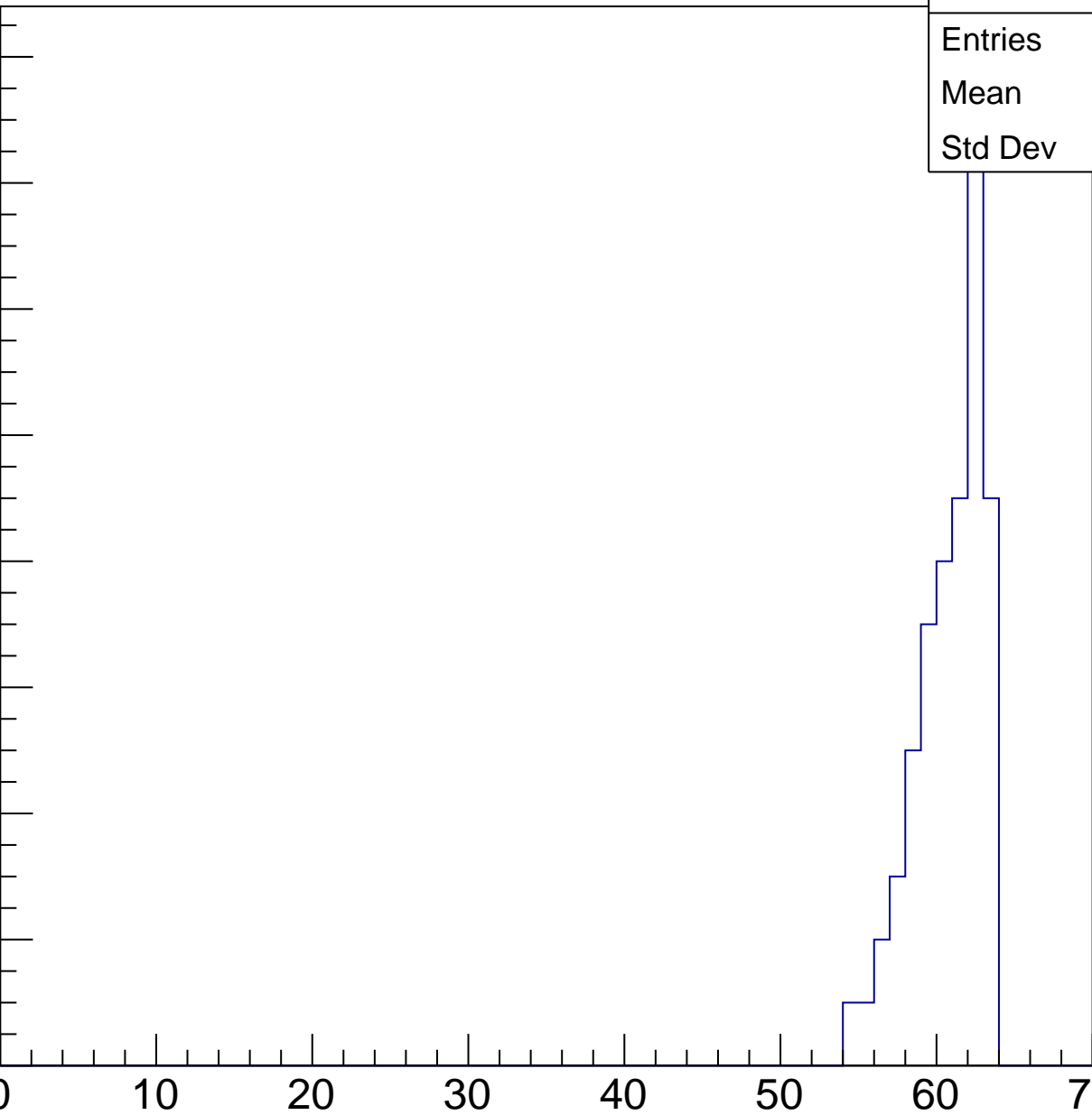
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	61
Mean	60.38
Std Dev	2.189

ampl



B1L001S, U16-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	21
Mean	56.1
Std Dev	18.24

B1L001S, U16-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U16-ch56, adc0

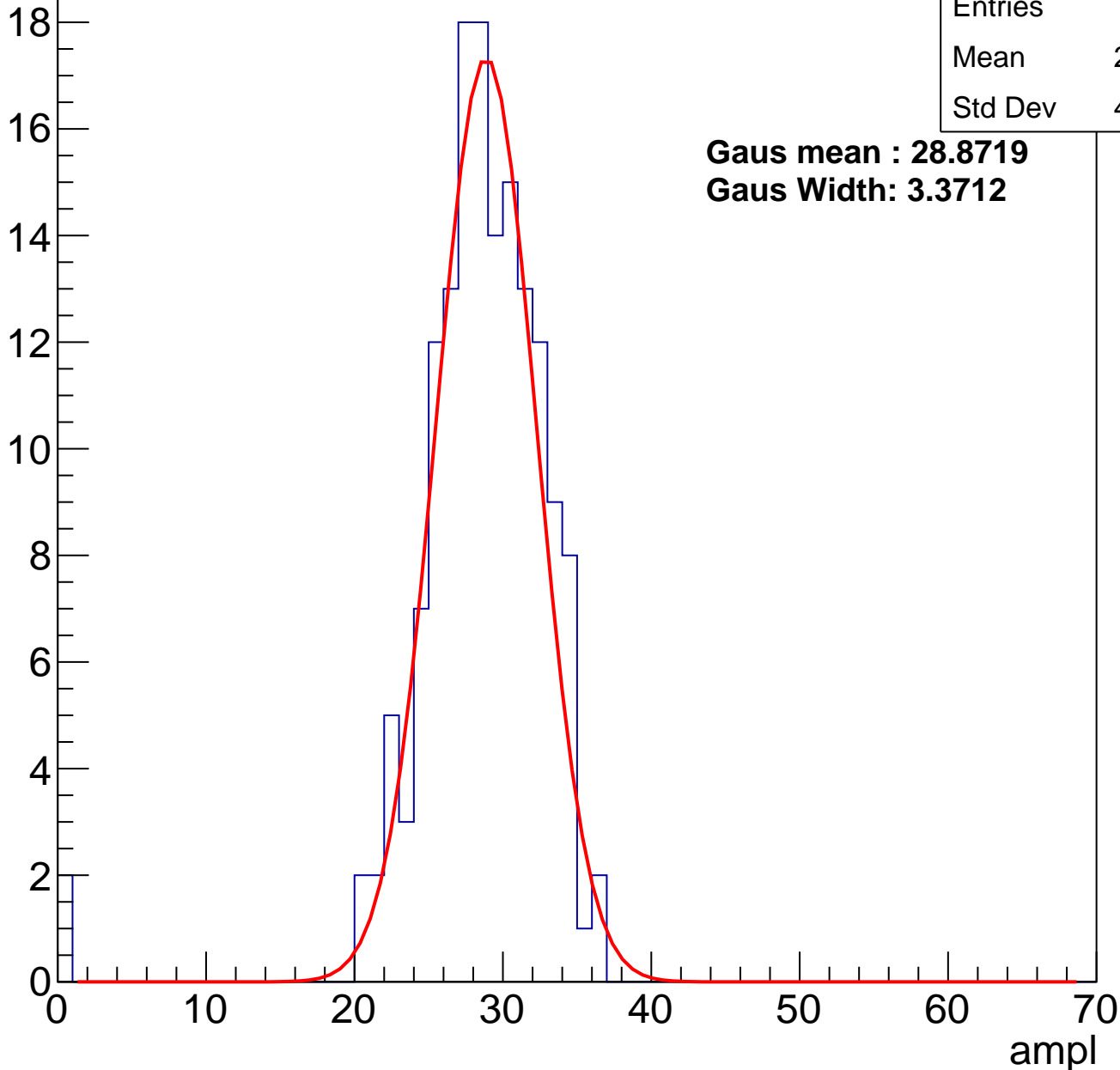
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	28.05
Std Dev	4.676

Gaus mean : 28.8719

Gaus Width: 3.3712

Entry



B1L001S, U16-ch56, adc1

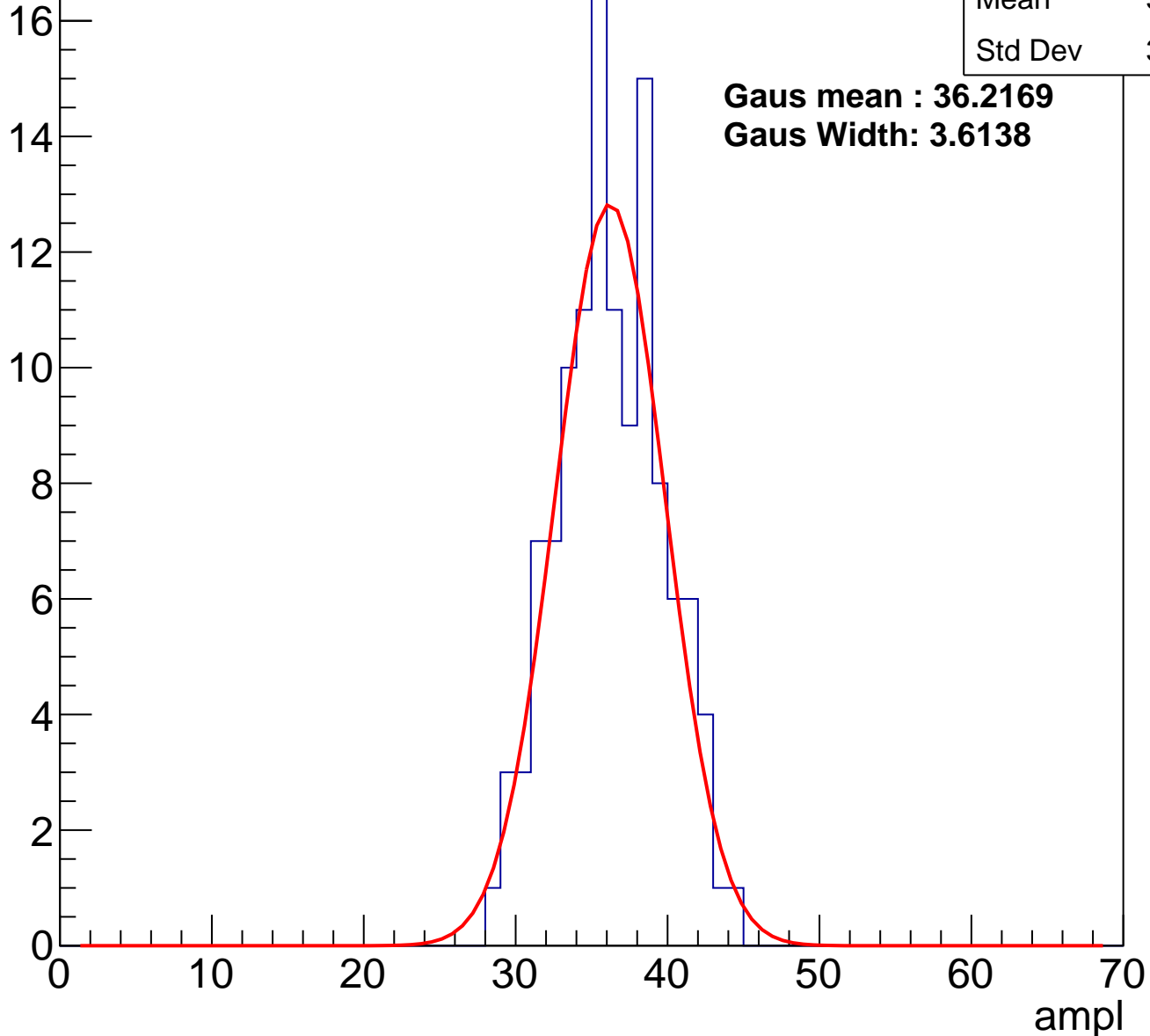
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	35.81
Std Dev	3.411

Gaus mean : 36.2169

Gaus Width: 3.6138

Entry



B1L001S, U16-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

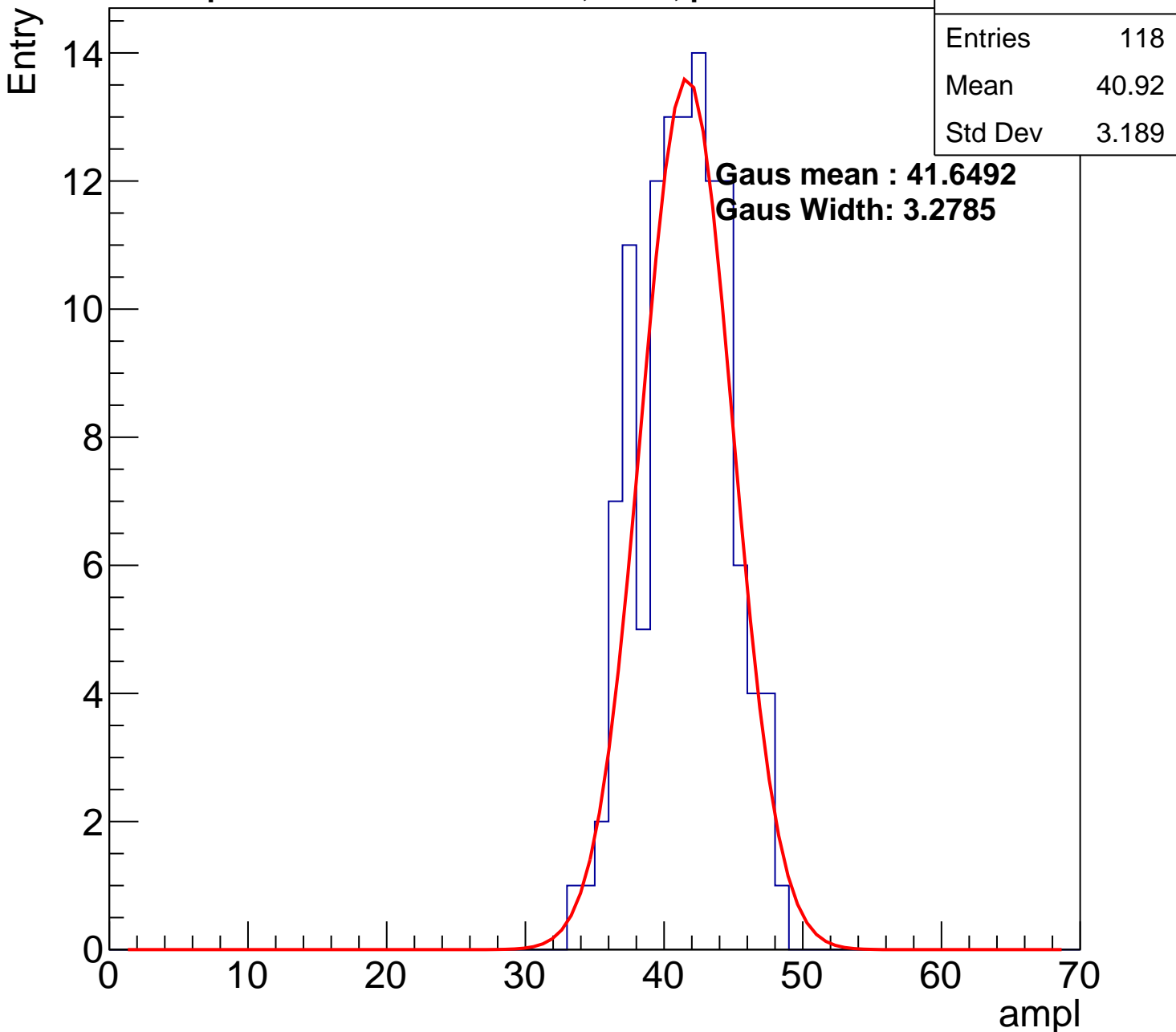
Entries	118
Mean	40.92
Std Dev	3.189

Gaus mean : 41.6492

Gaus Width: 3.2785

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

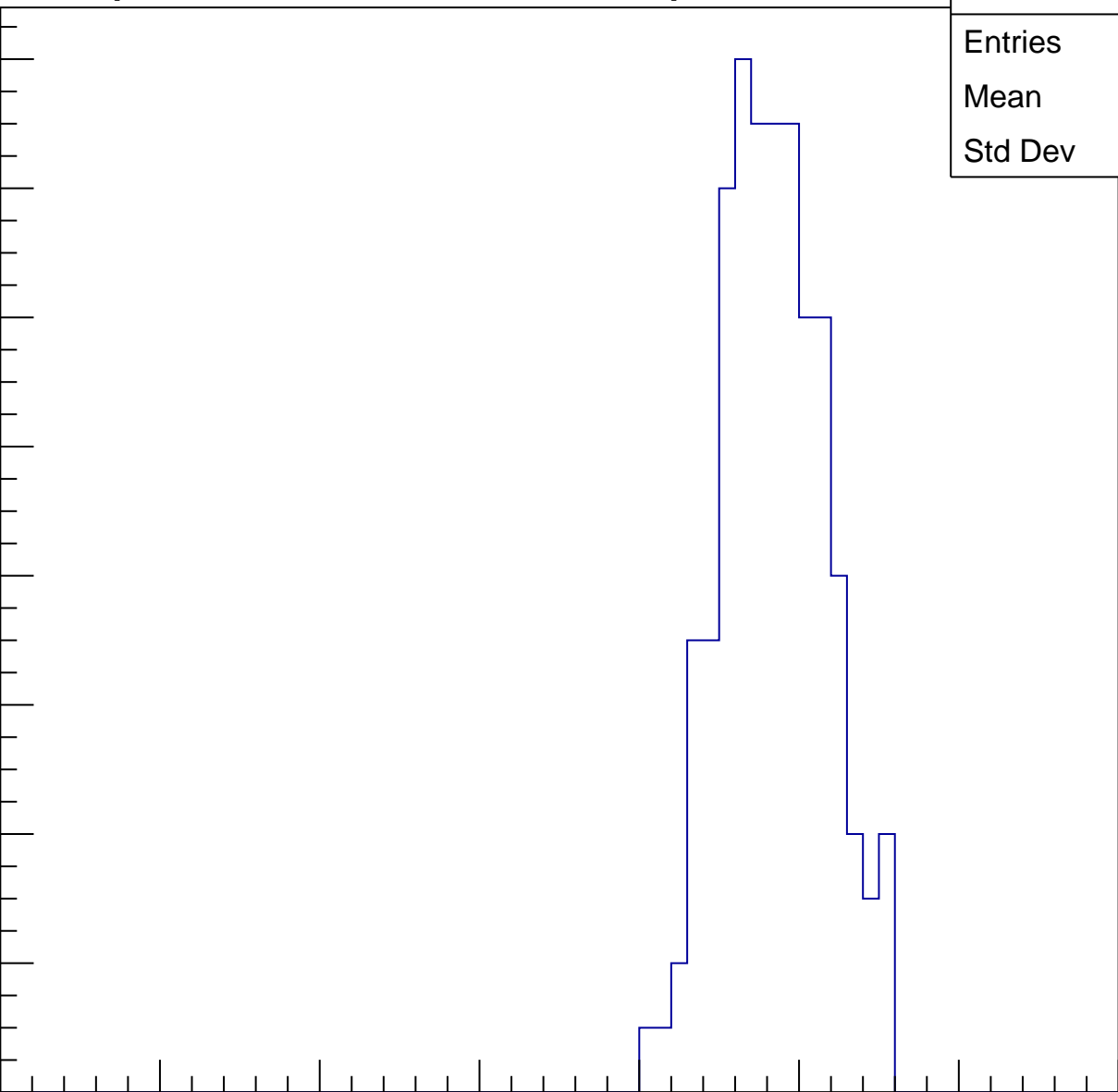
Entries	136
Mean	47.96
Std Dev	3.185

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

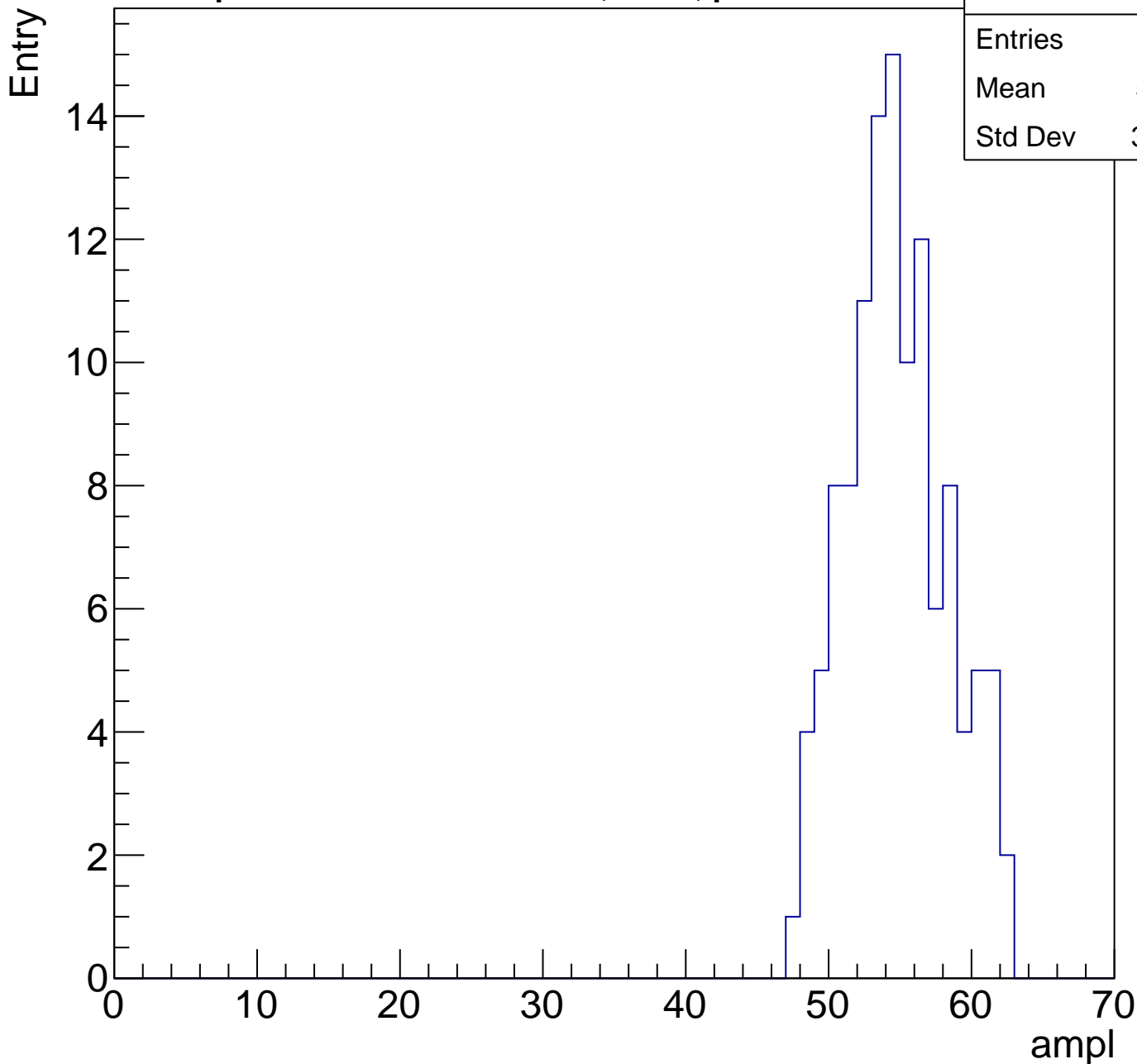
ampl



B1L001S, U16-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	54.31
Std Dev	3.514



B1L001S, U16-ch56, adc5

calib_packv5_042523_0143.root, FC#2, port C2

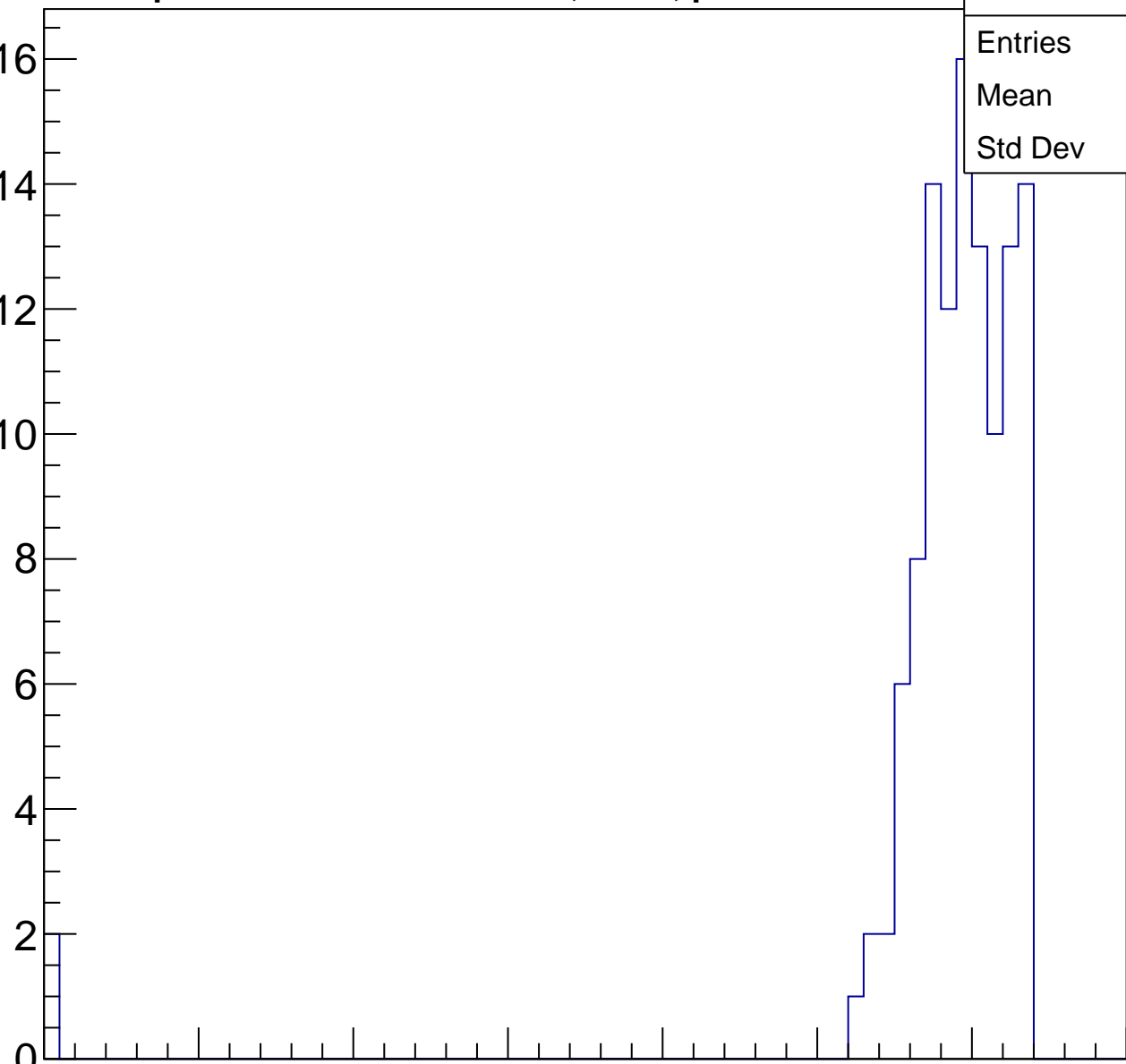
Entries	113
Mean	58.05
Std Dev	8.234

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

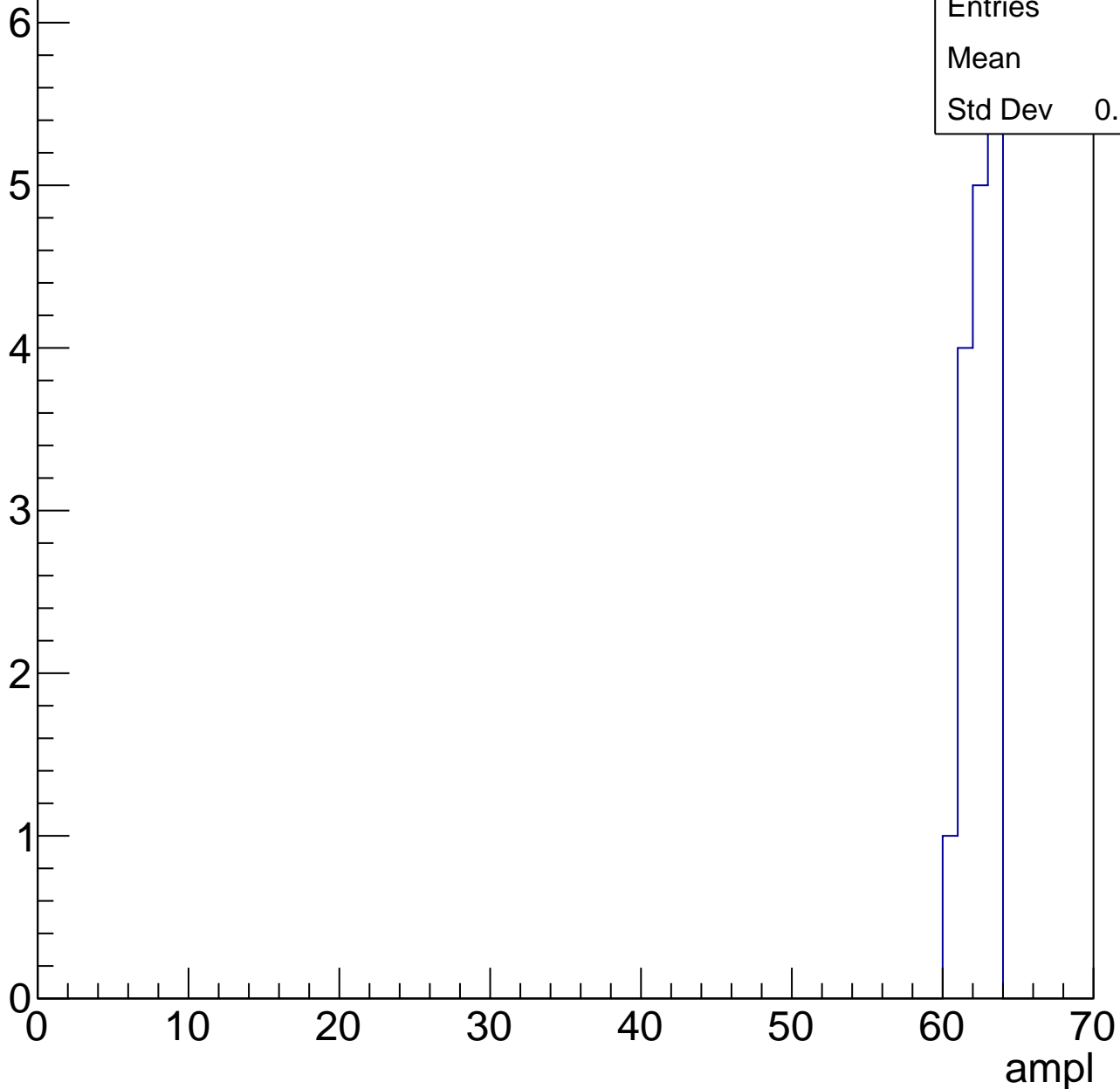


B1L001S, U16-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

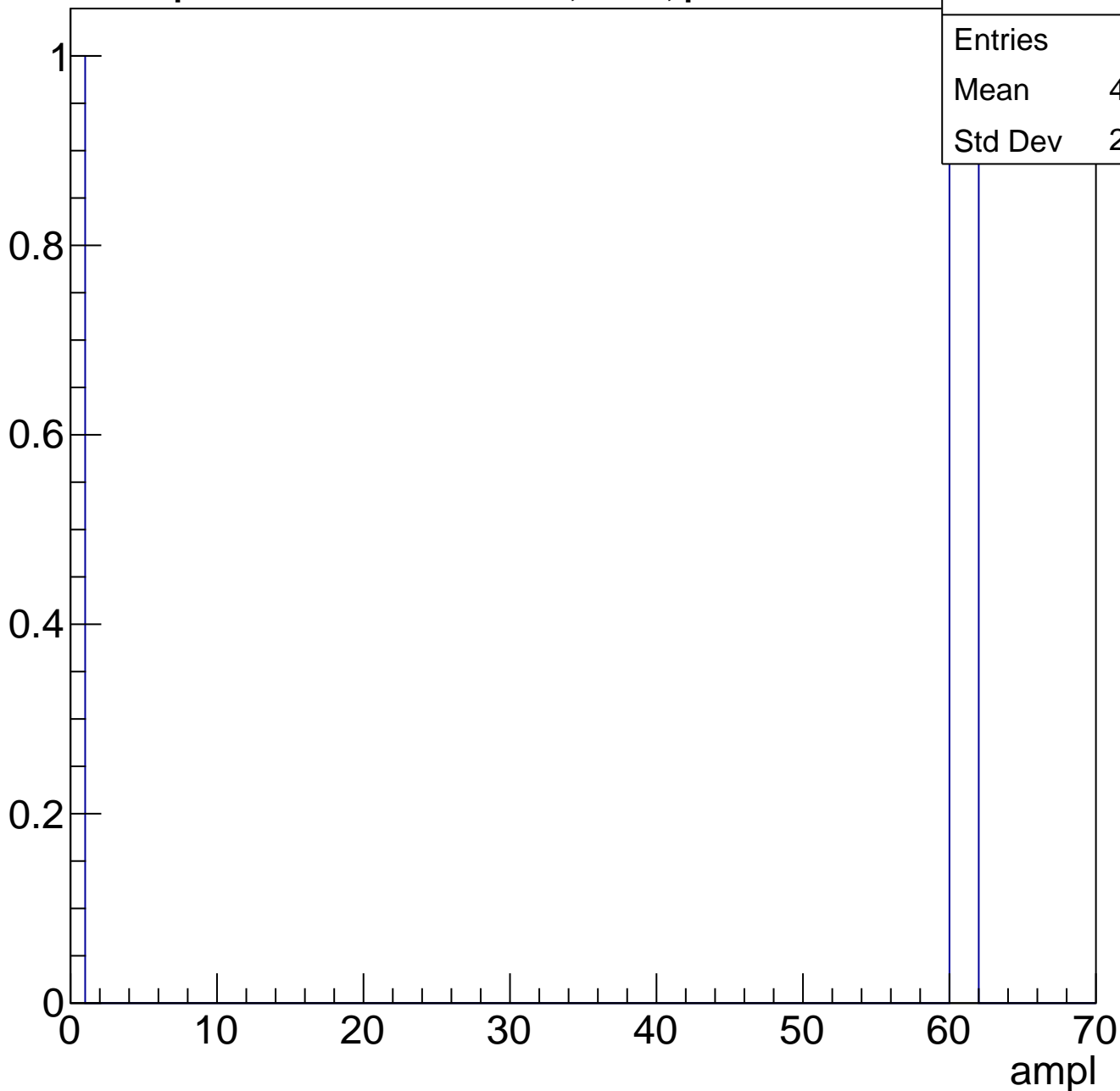
Entries	16
Mean	62
Std Dev	0.9354



B1L001S, U16-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch57, adc0

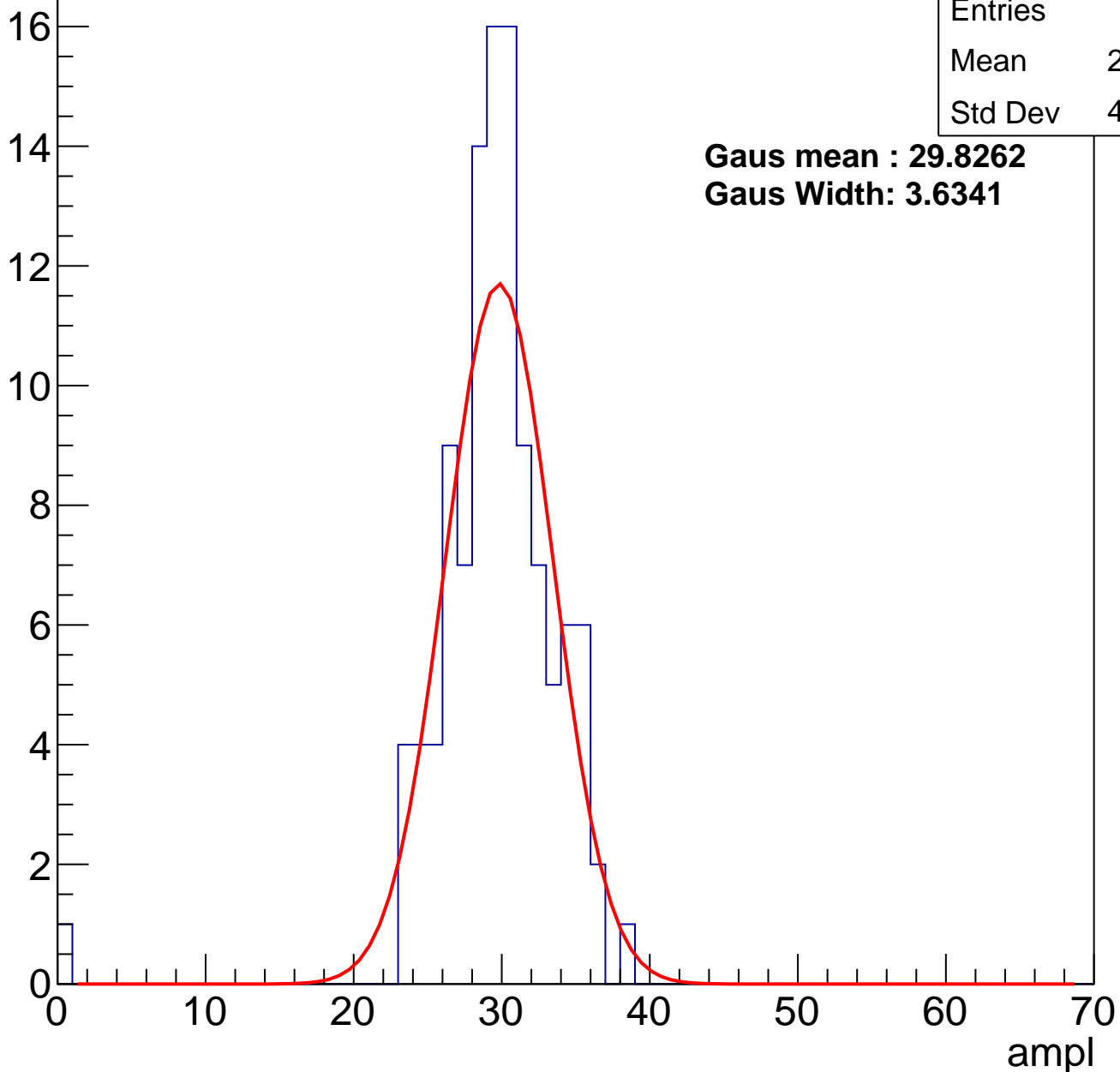
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	29.18
Std Dev	4.254

Gaus mean : 29.8262

Gaus Width: 3.6341

Entry



B1L001S, U16-ch57, adc1

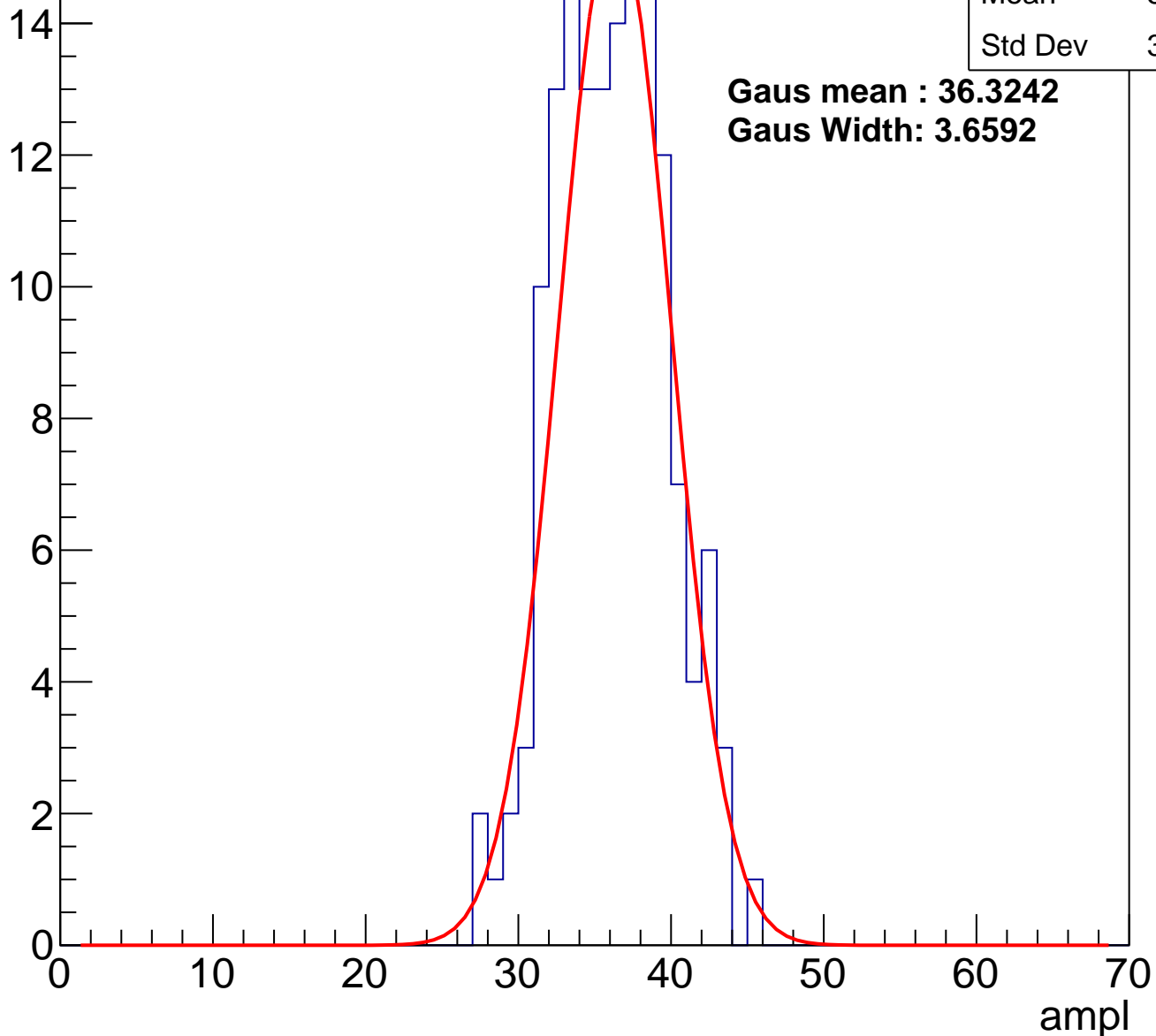
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	35.67
Std Dev	3.583

Gaus mean : 36.3242

Gaus Width: 3.6592

Entry



B1L001S, U16-ch57, adc2

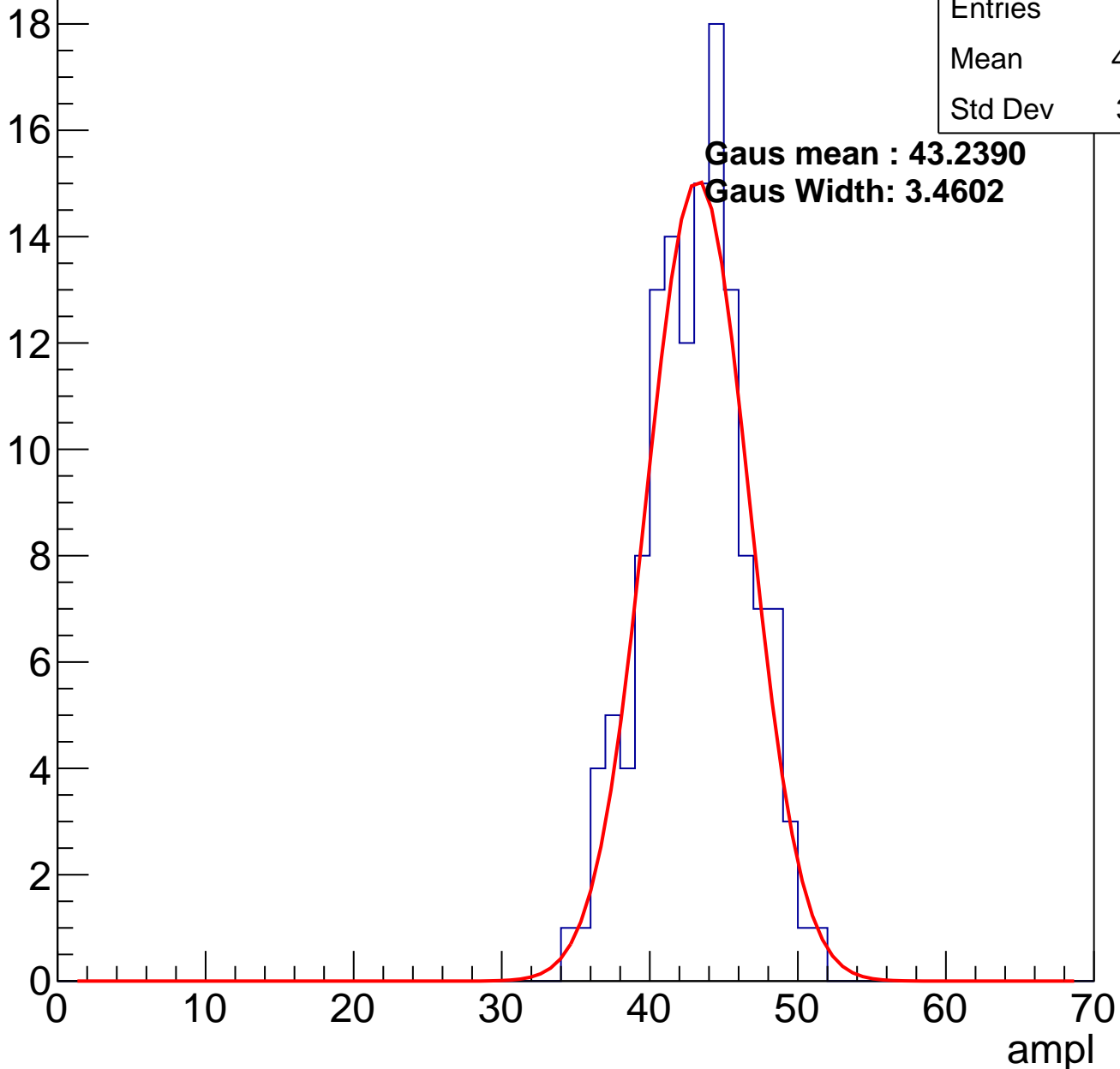
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	42.69
Std Dev	3.411

Gaus mean : 43.2390

Gaus Width: 3.4602

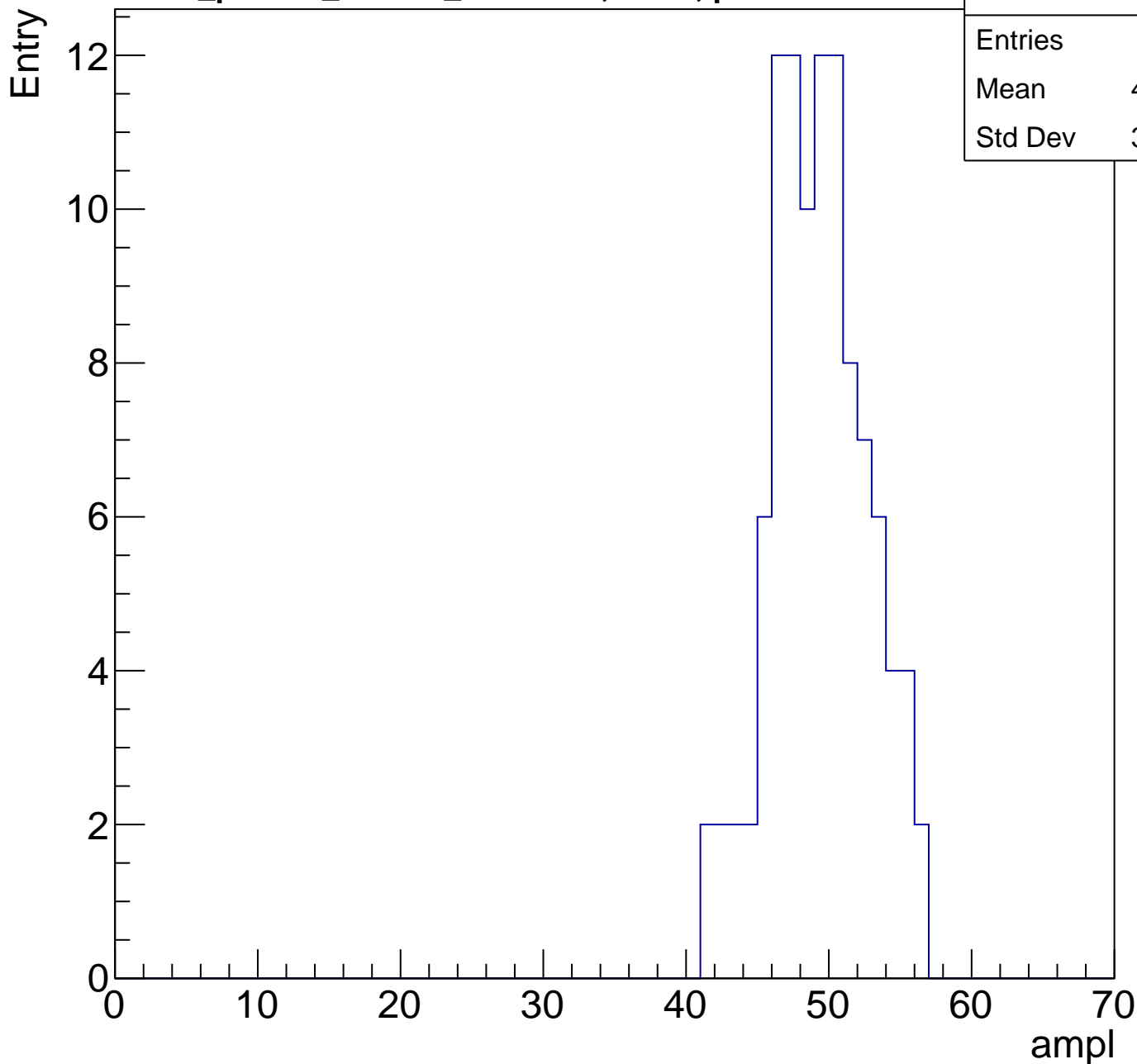
Entry



B1L001S, U16-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	48.85
Std Dev	3.346



B1L001S, U16-ch57, adc4

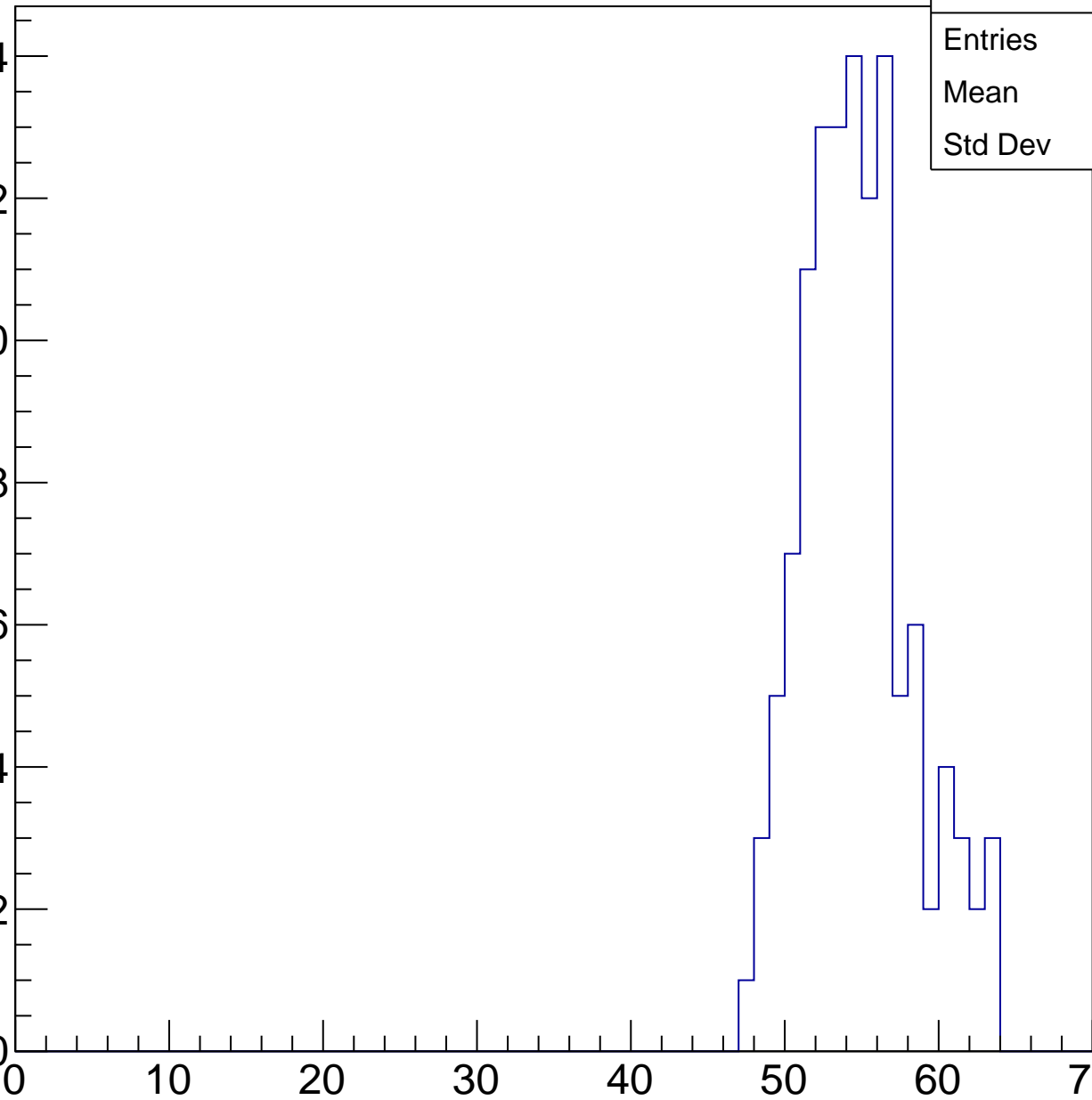
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	54.23
Std Dev	3.533

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch57, adc5

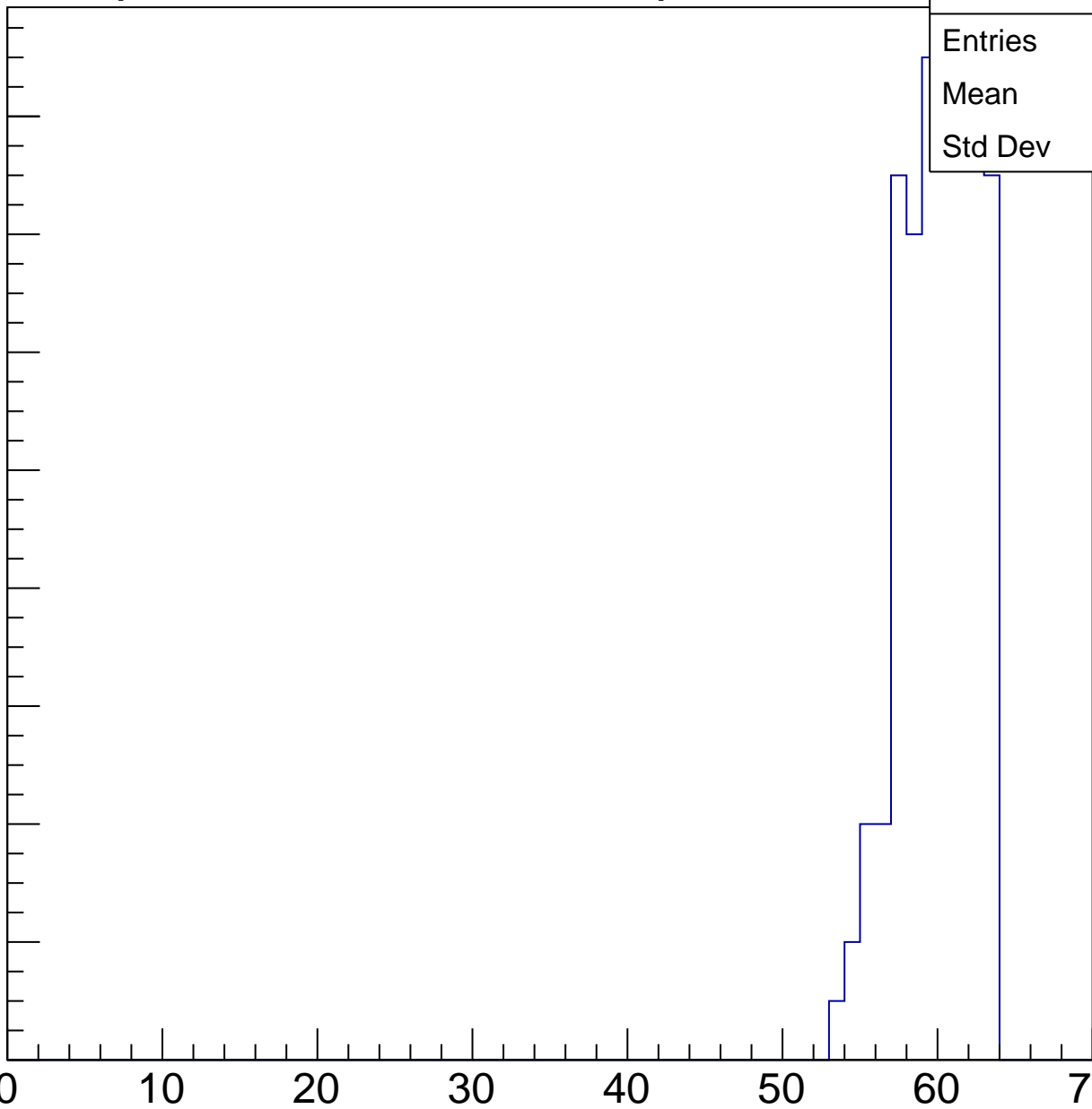
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	59.57
Std Dev	2.39

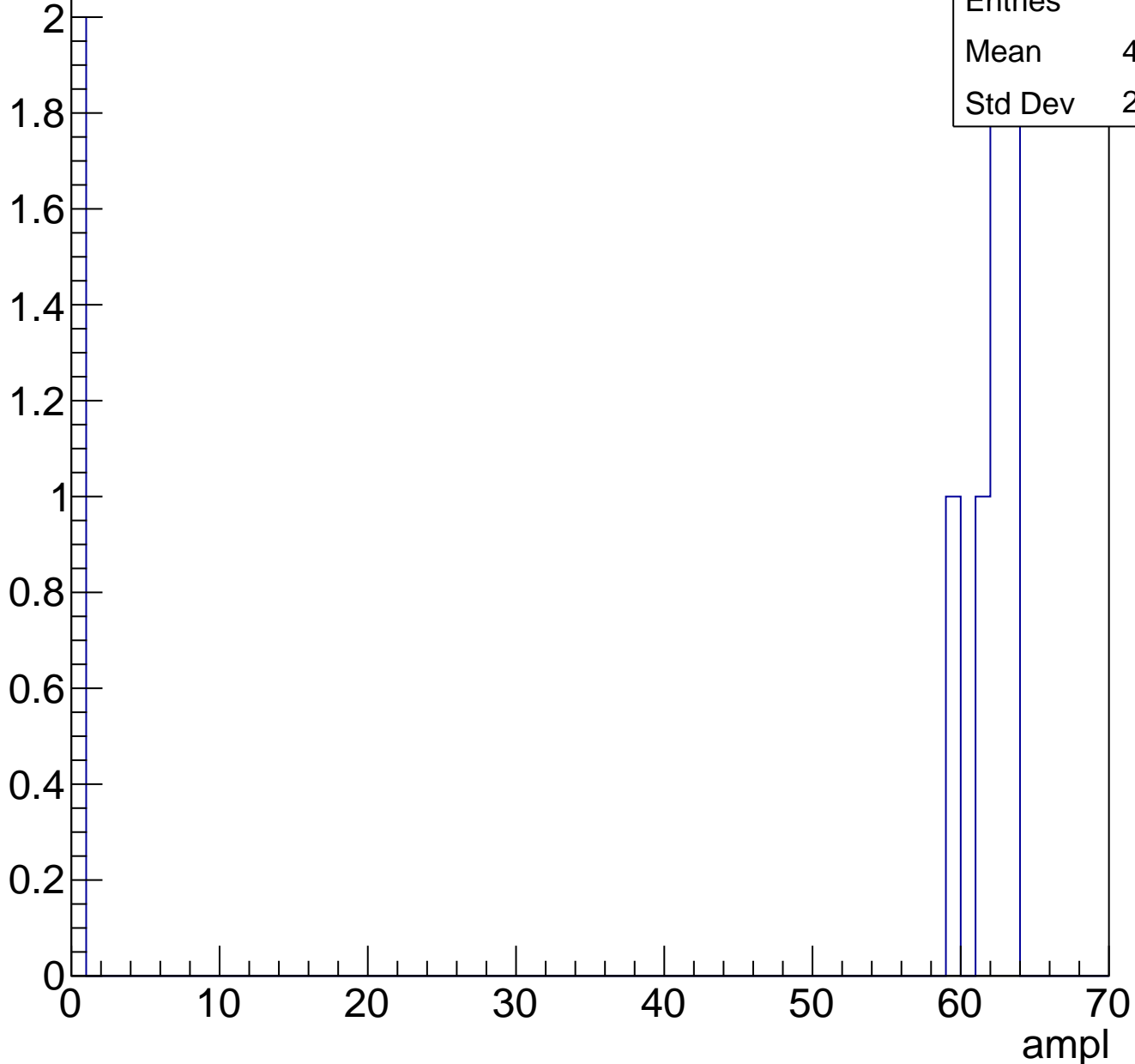
ampl



B1L001S, U16-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

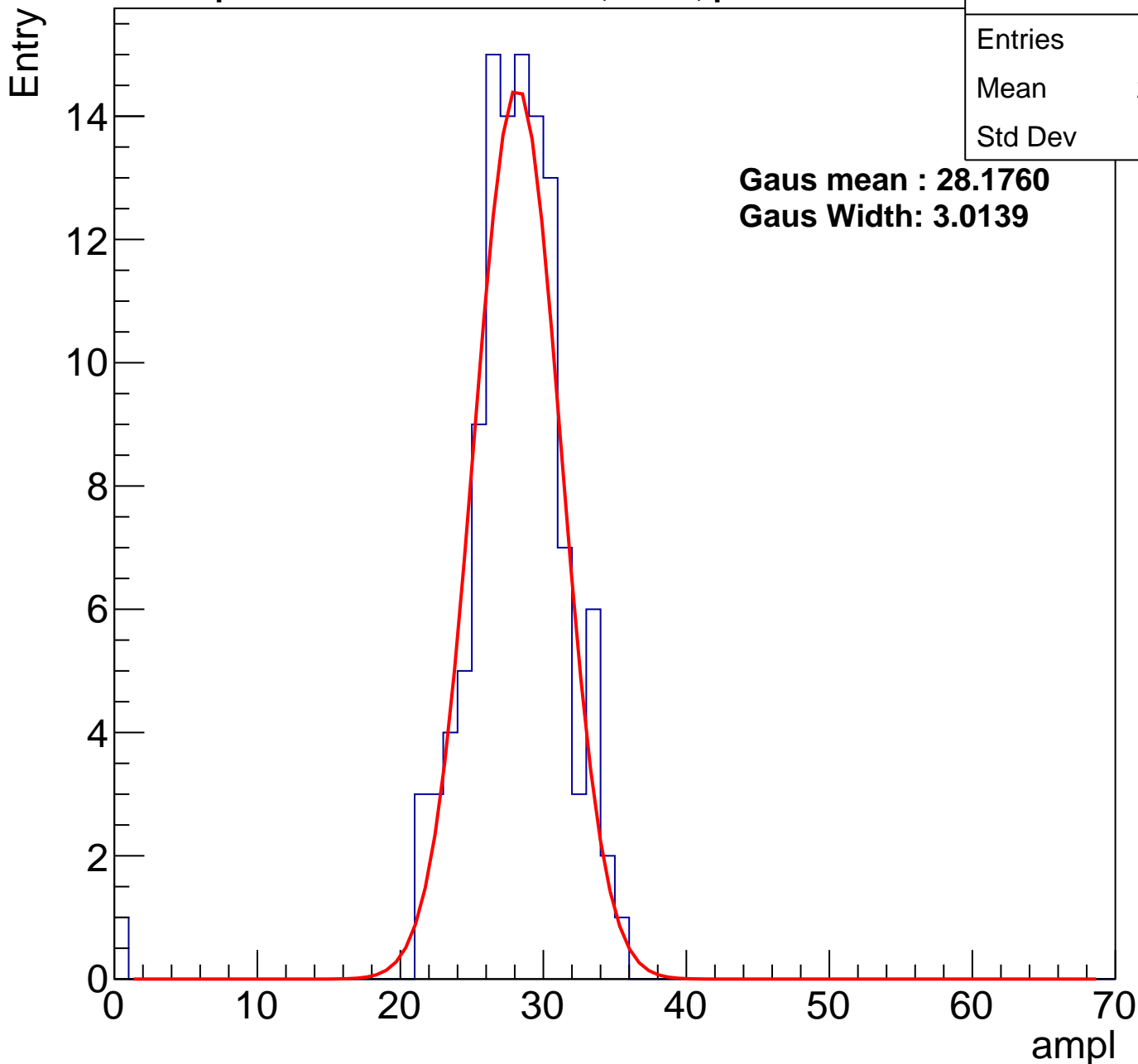
B1L001S, U16-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	27.51
Std Dev	3.95

Gaus mean : 28.1760

Gaus Width: 3.0139



B1L001S, U16-ch58, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	34.73
Std Dev	3.484

Gaus mean : 35.0809

Gaus Width: 3.6552

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

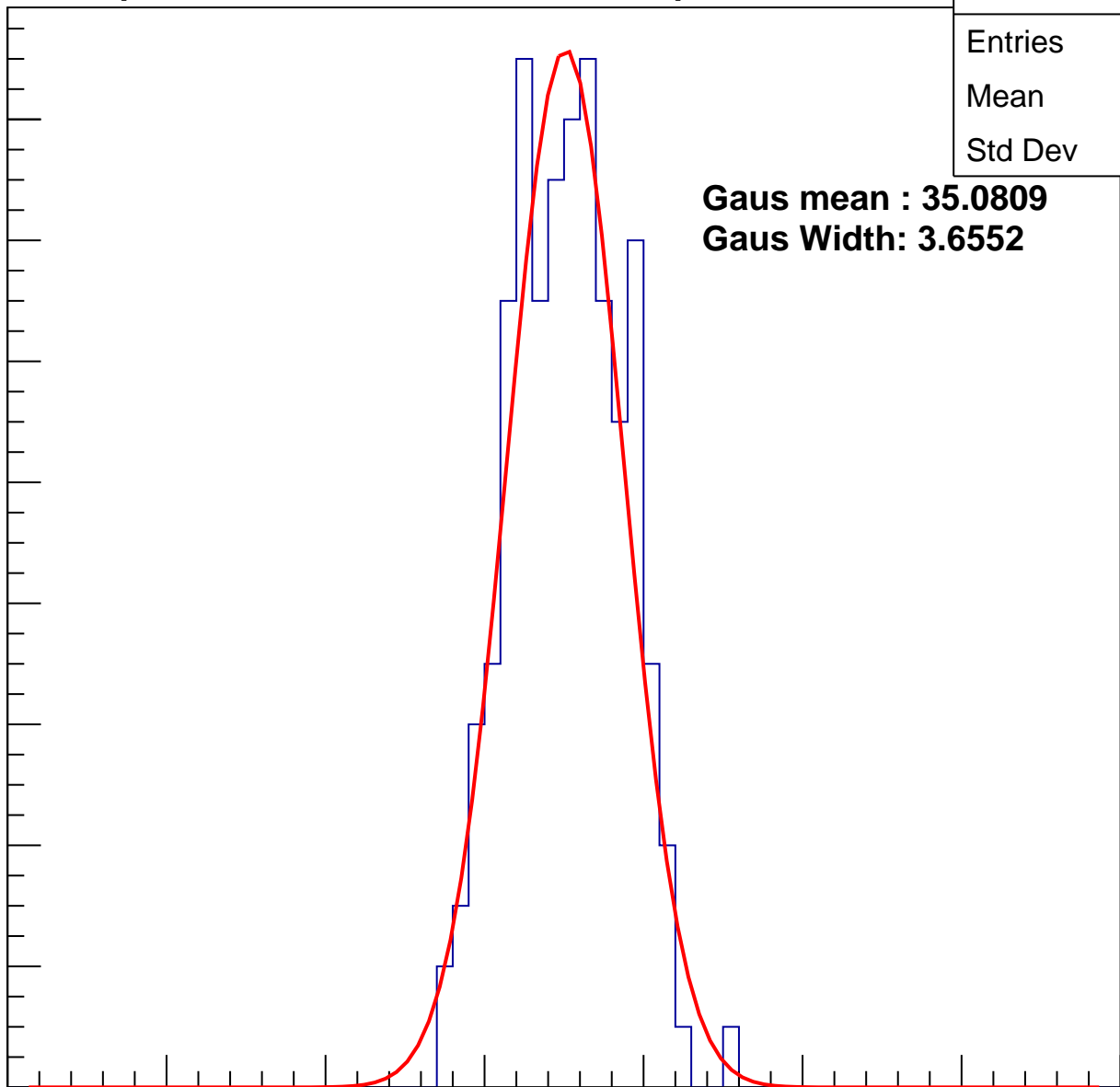
40

50

60

70

ampl



B1L001S, U16-ch58, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	97
Mean	40.79
Std Dev	3.299

Gaus mean : 41.4144

Gaus Width: 3.5235

0

2

4

6

8

10

12

B1L001S, U16-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

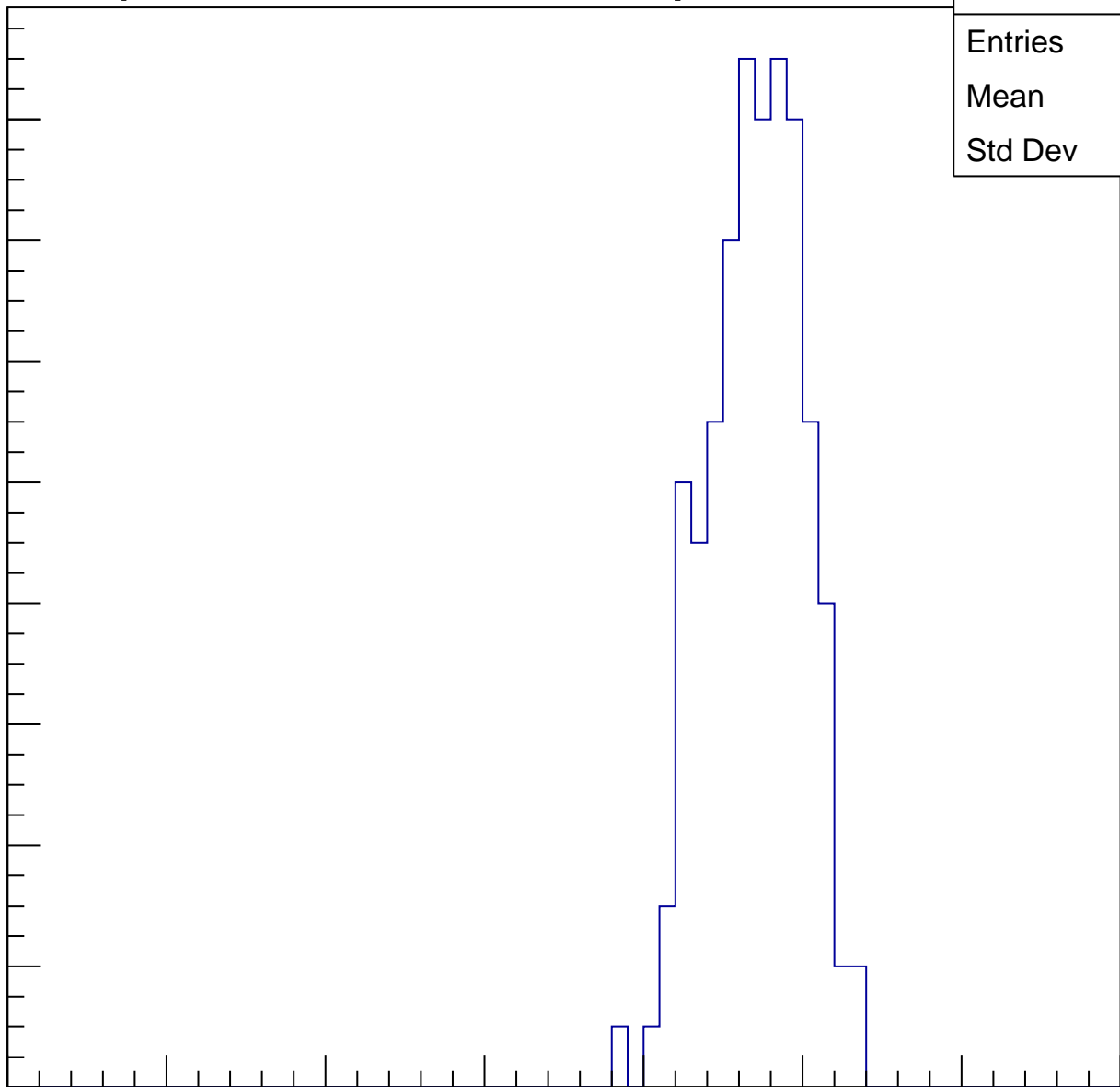
138

Mean

46.55

Std Dev

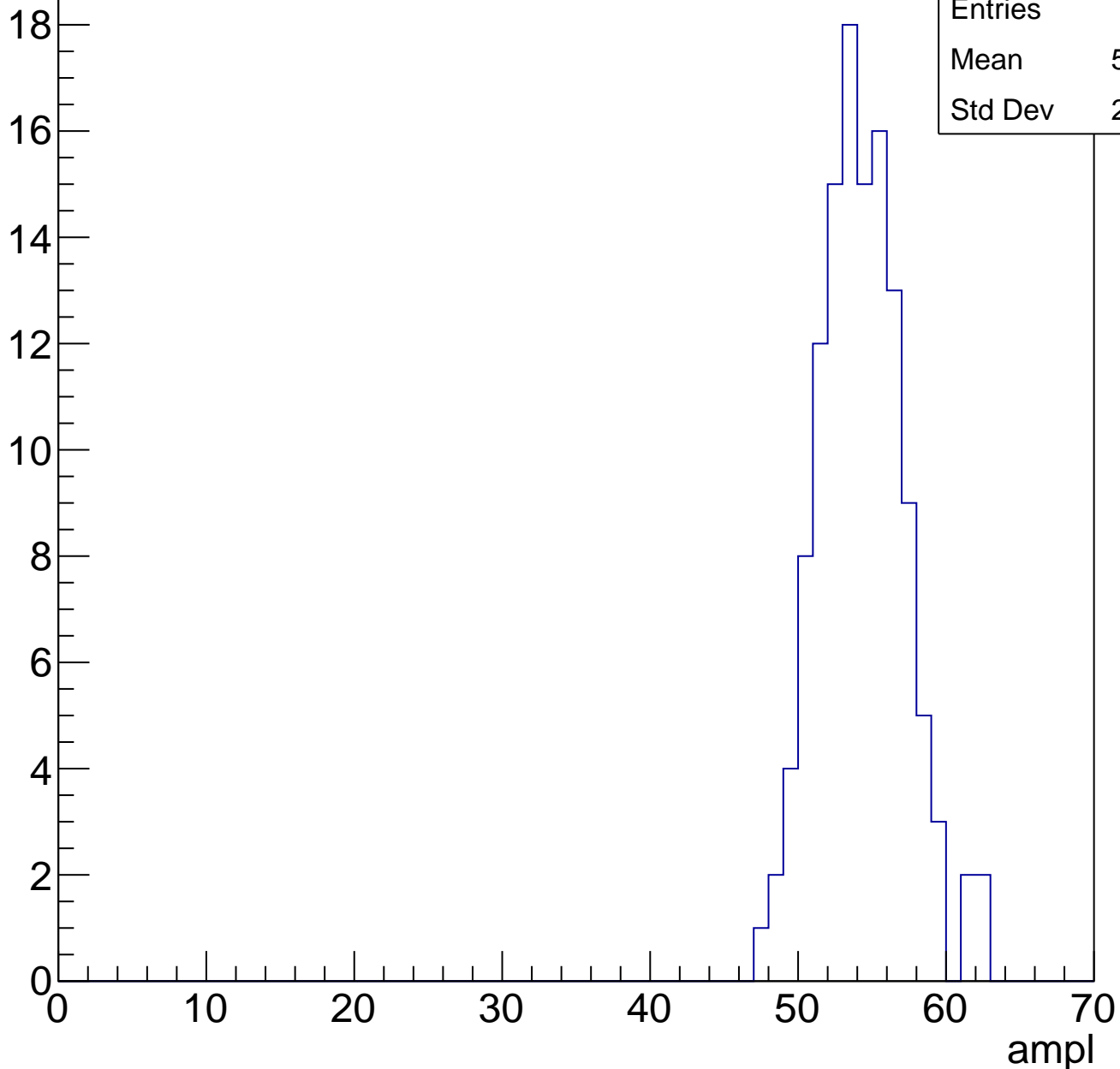
2.956



B1L001S, U16-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	125
Mean	53.83
Std Dev	2.914

B1L001S, U16-ch58, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	58.28
Std Dev	6.226

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

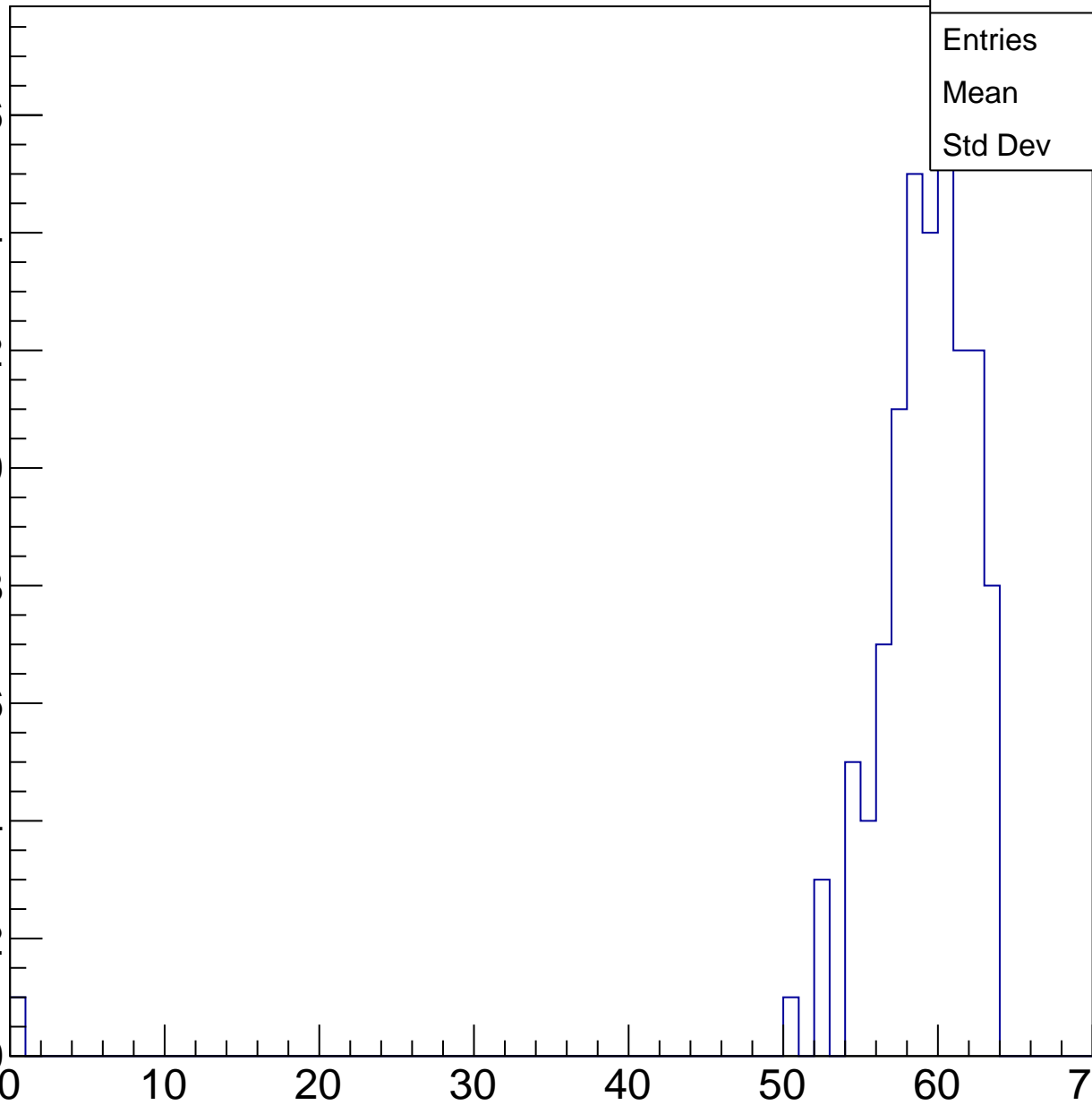
40

50

60

70

ampl



B1L001S, U16-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

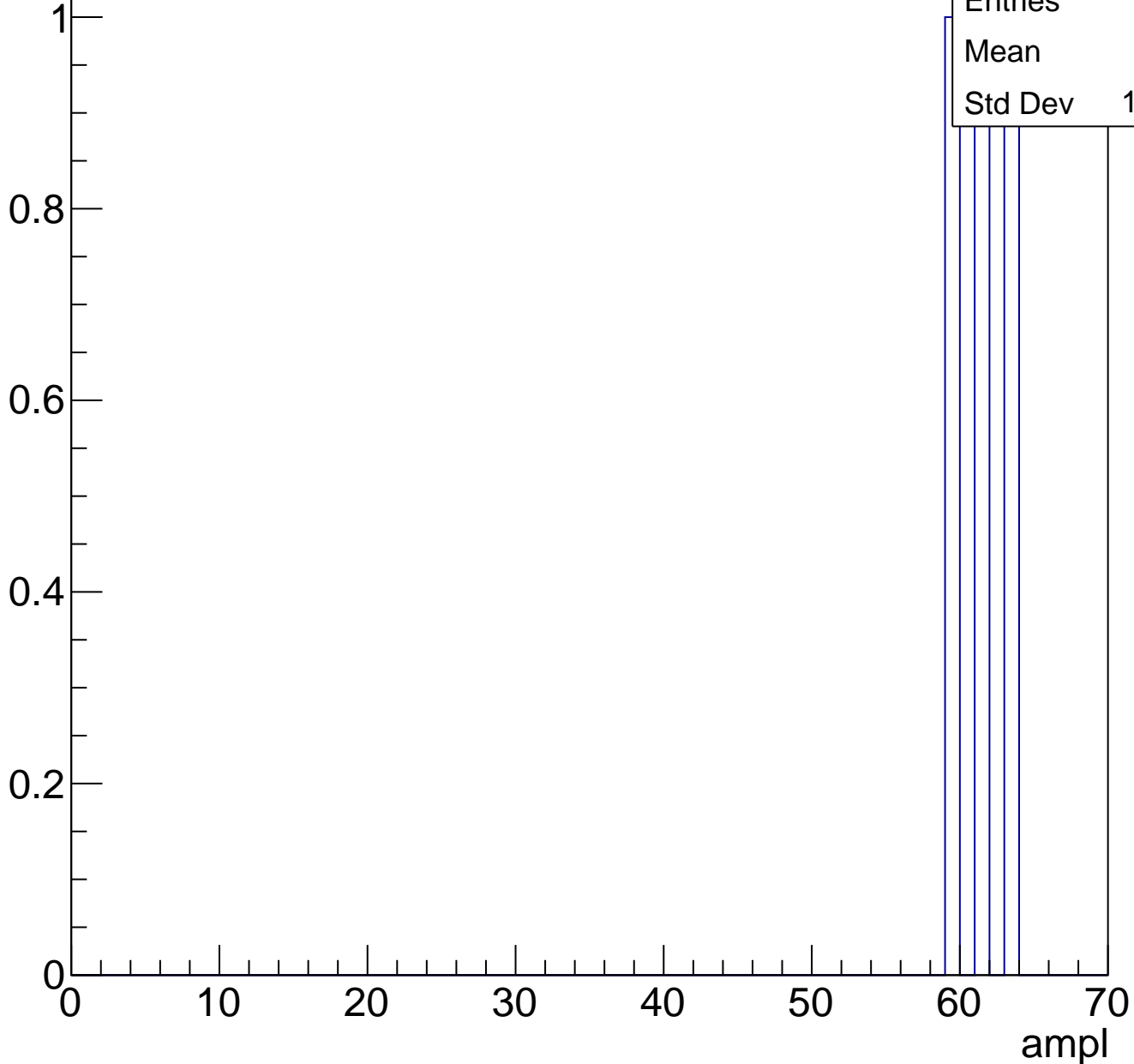
ampl

Entries	28
Mean	59.57
Std Dev	11.53

B1L001S, U16-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch59, adc0

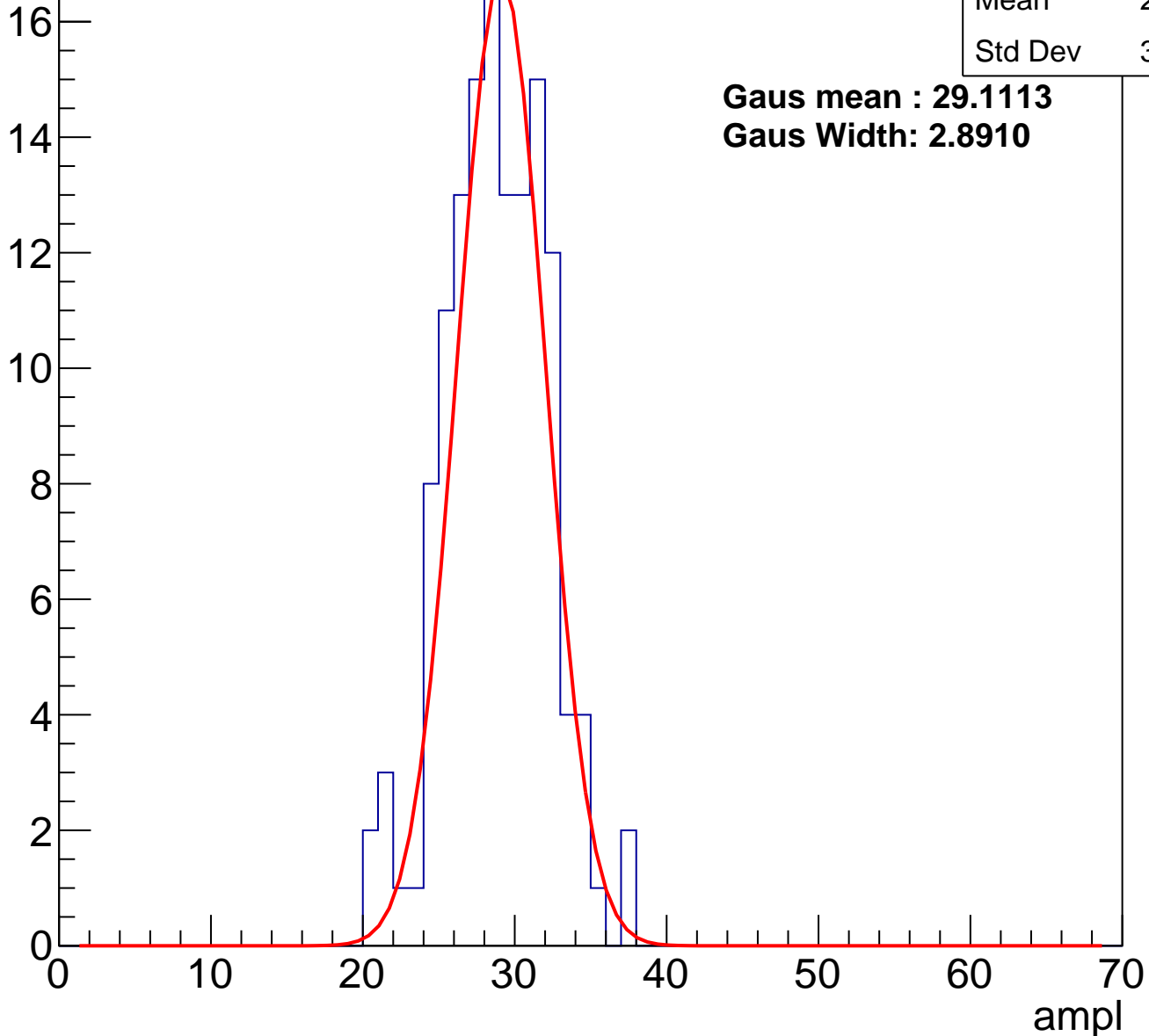
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	135
Mean	28.35
Std Dev	3.289

Gaus mean : 29.1113

Gaus Width: 2.8910

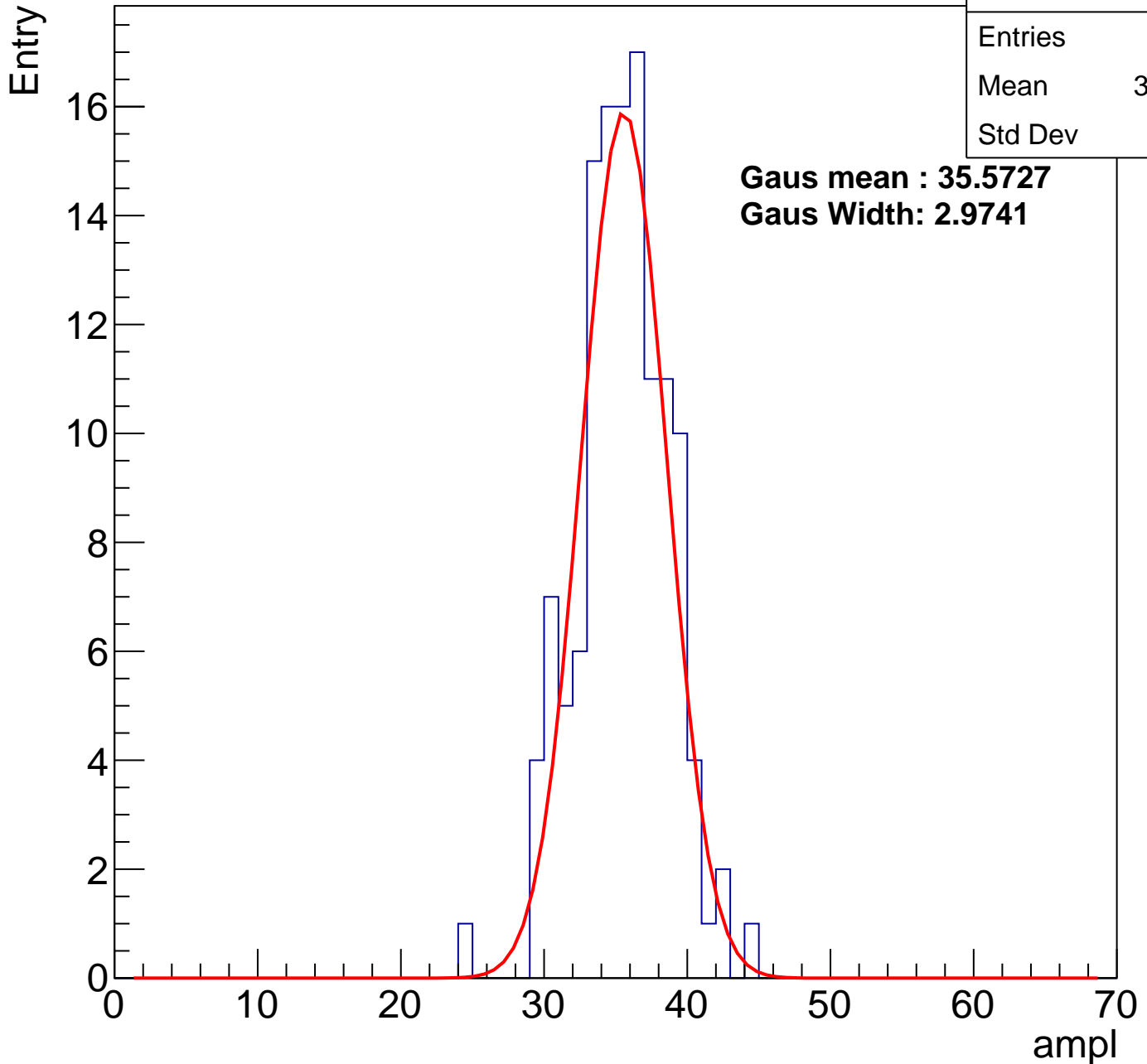


B1L001S, U16-ch59, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	35.06
Std Dev	3.18

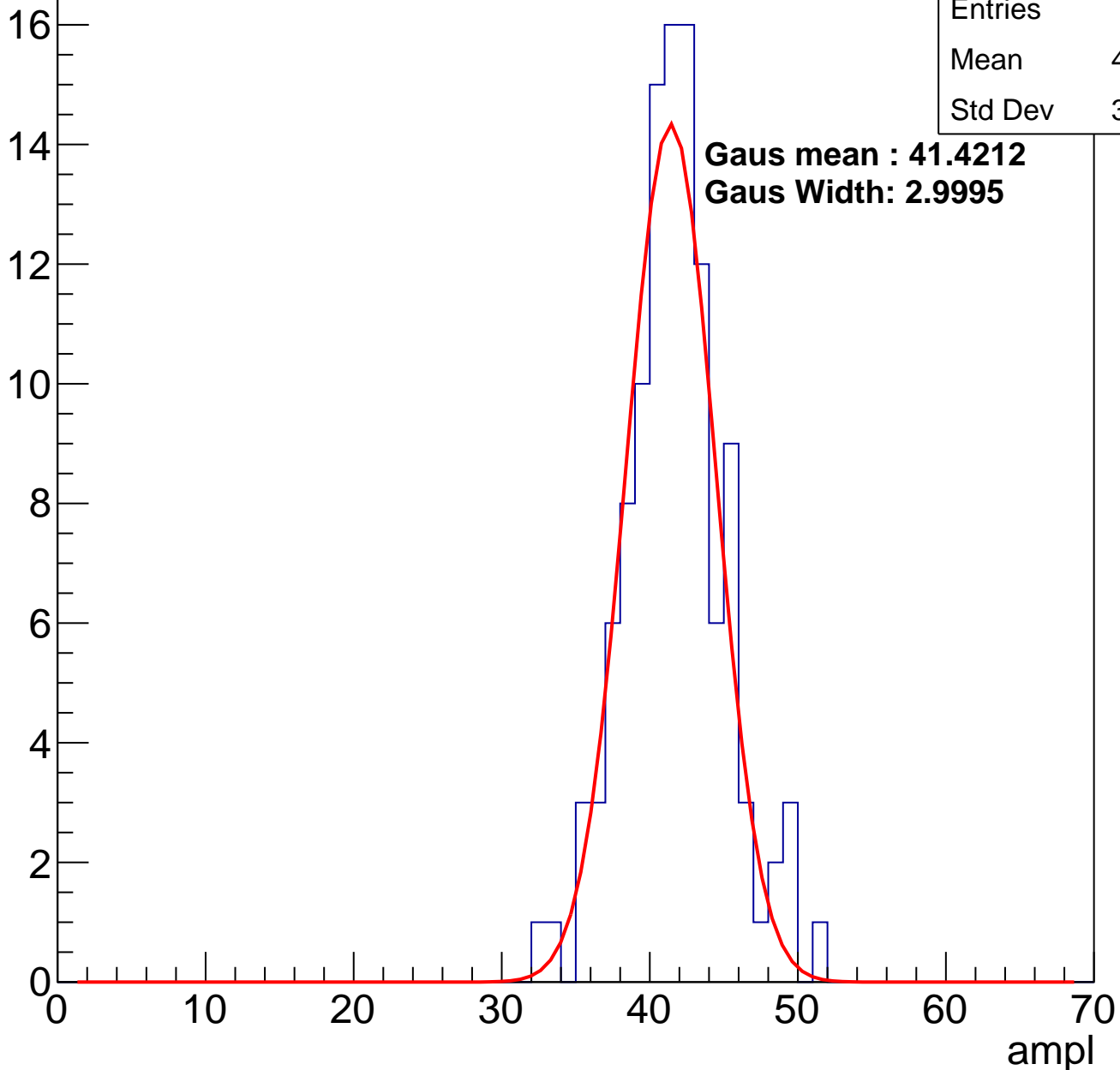
Gaus mean : 35.5727
Gaus Width: 2.9741



B1L001S, U16-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

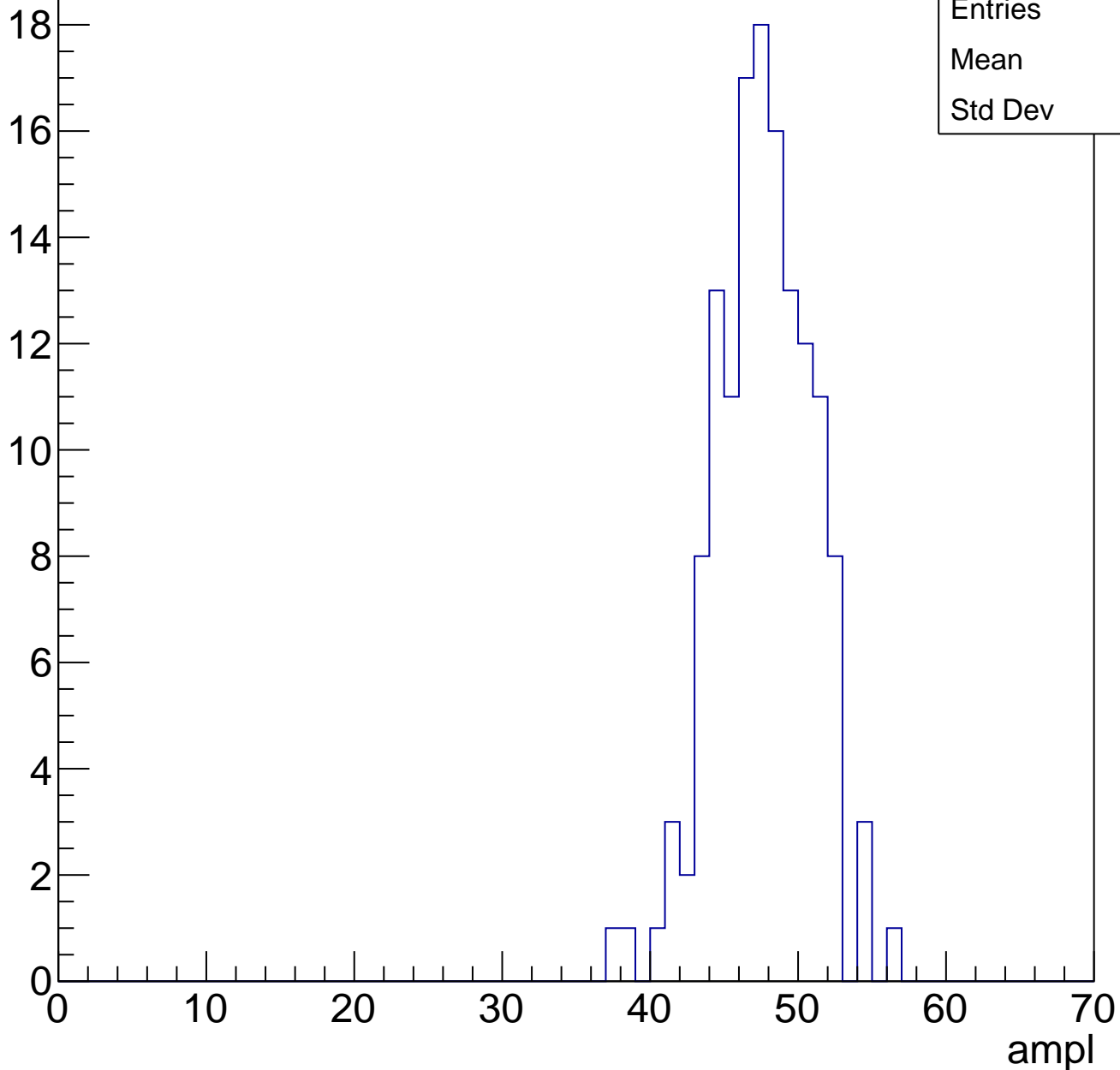


B1L001S, U16-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	47.2
Std Dev	3.25

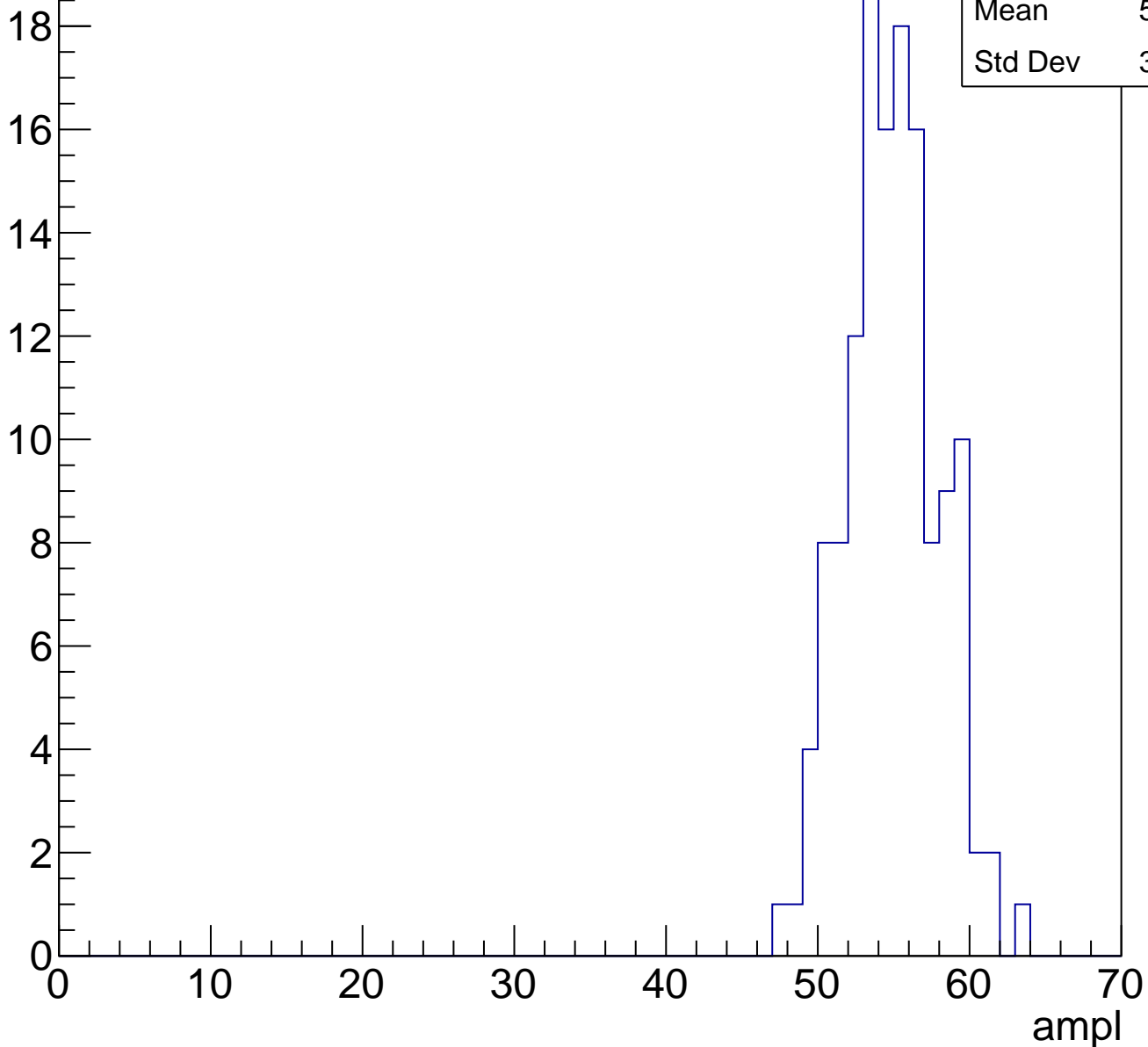
Entry



B1L001S, U16-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch59, adc5

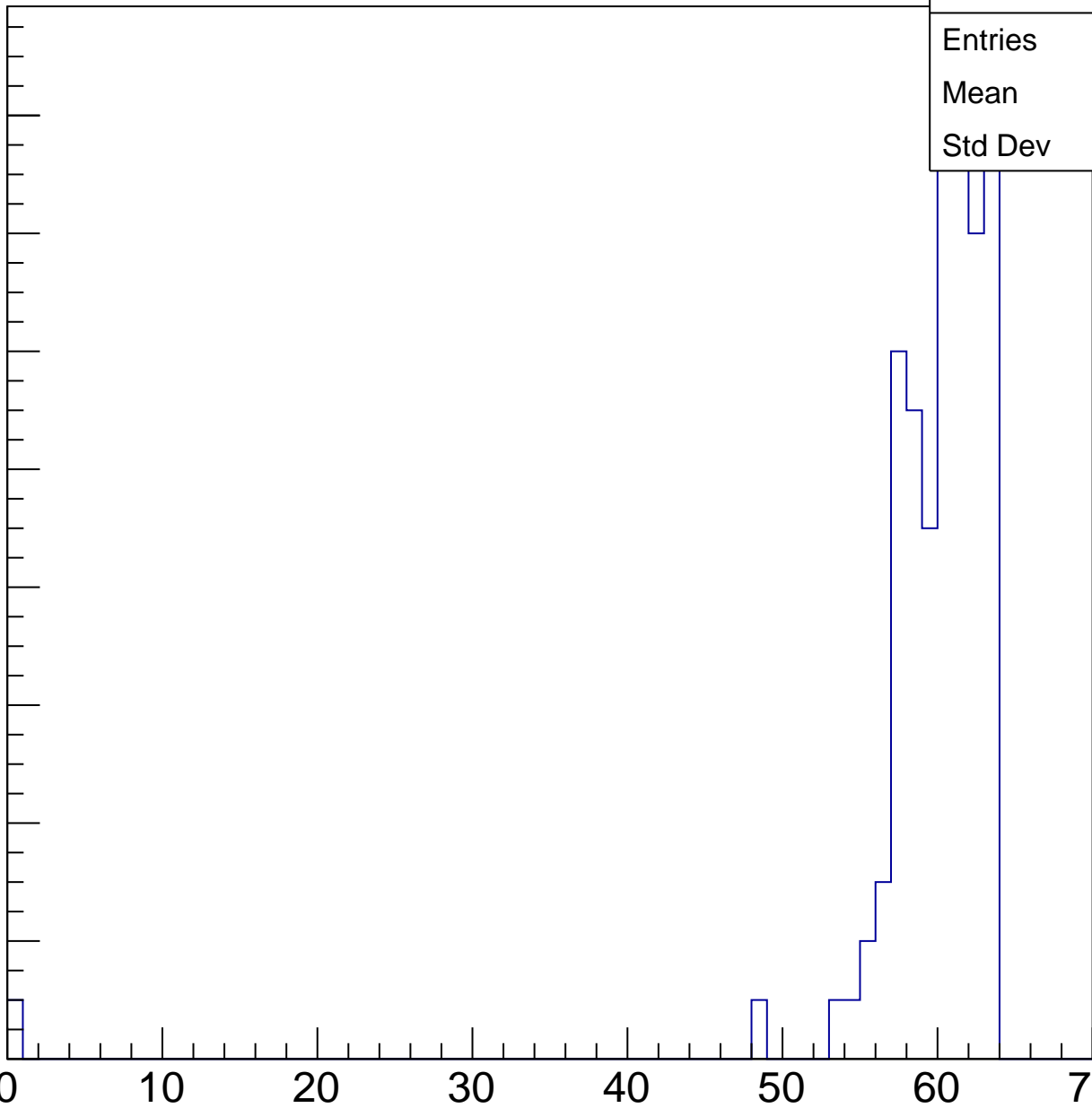
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	59.26
Std Dev	6.337

ampl

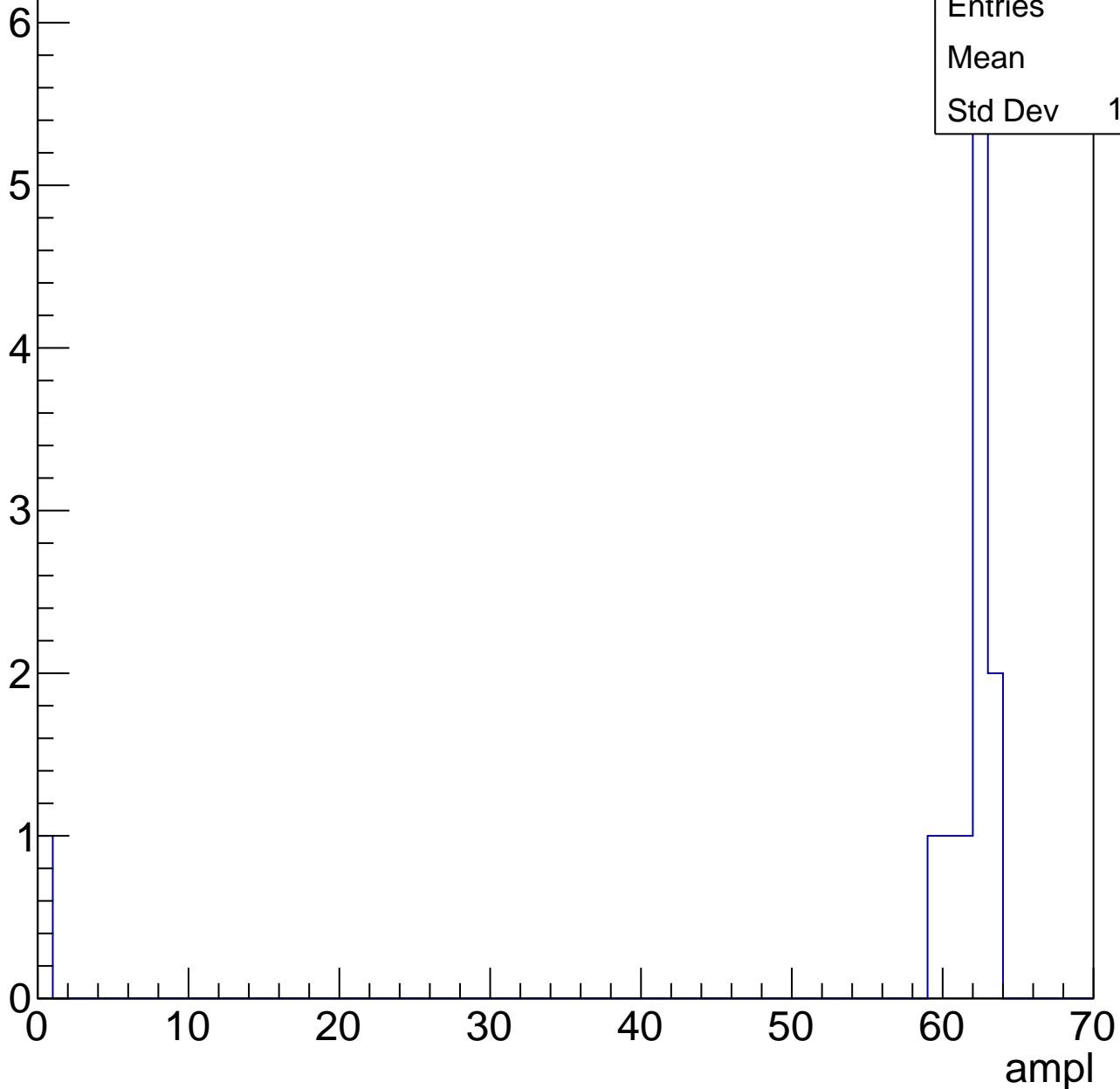


B1L001S, U16-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	56.5
Std Dev	17.07



B1L001S, U16-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U16-ch60, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.44
Std Dev	3.275

Gaus mean : 30.8141

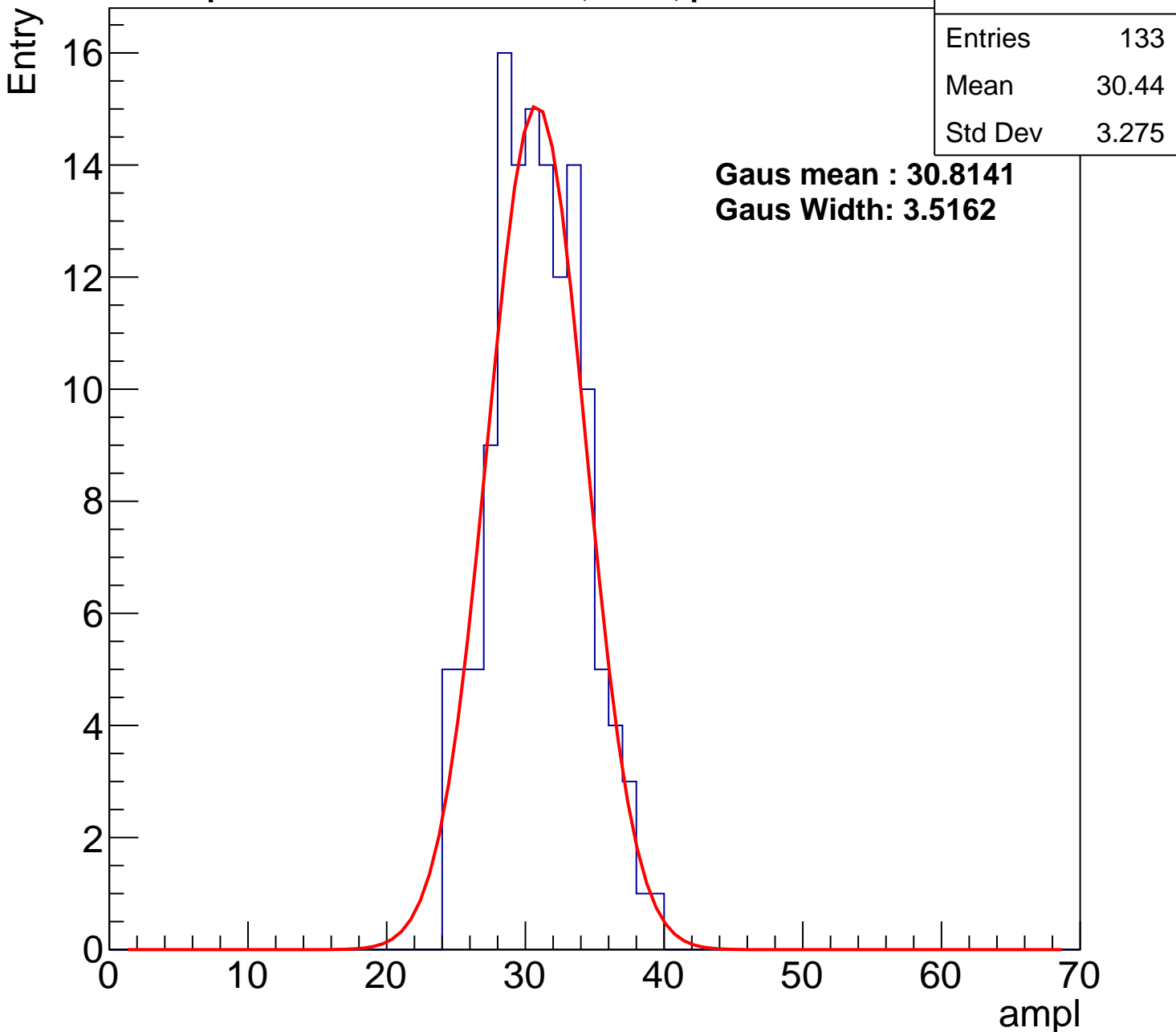
Gaus Width: 3.5162

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch60, adc1

calib_packv5_042523_0143.root, FC#2, port C2

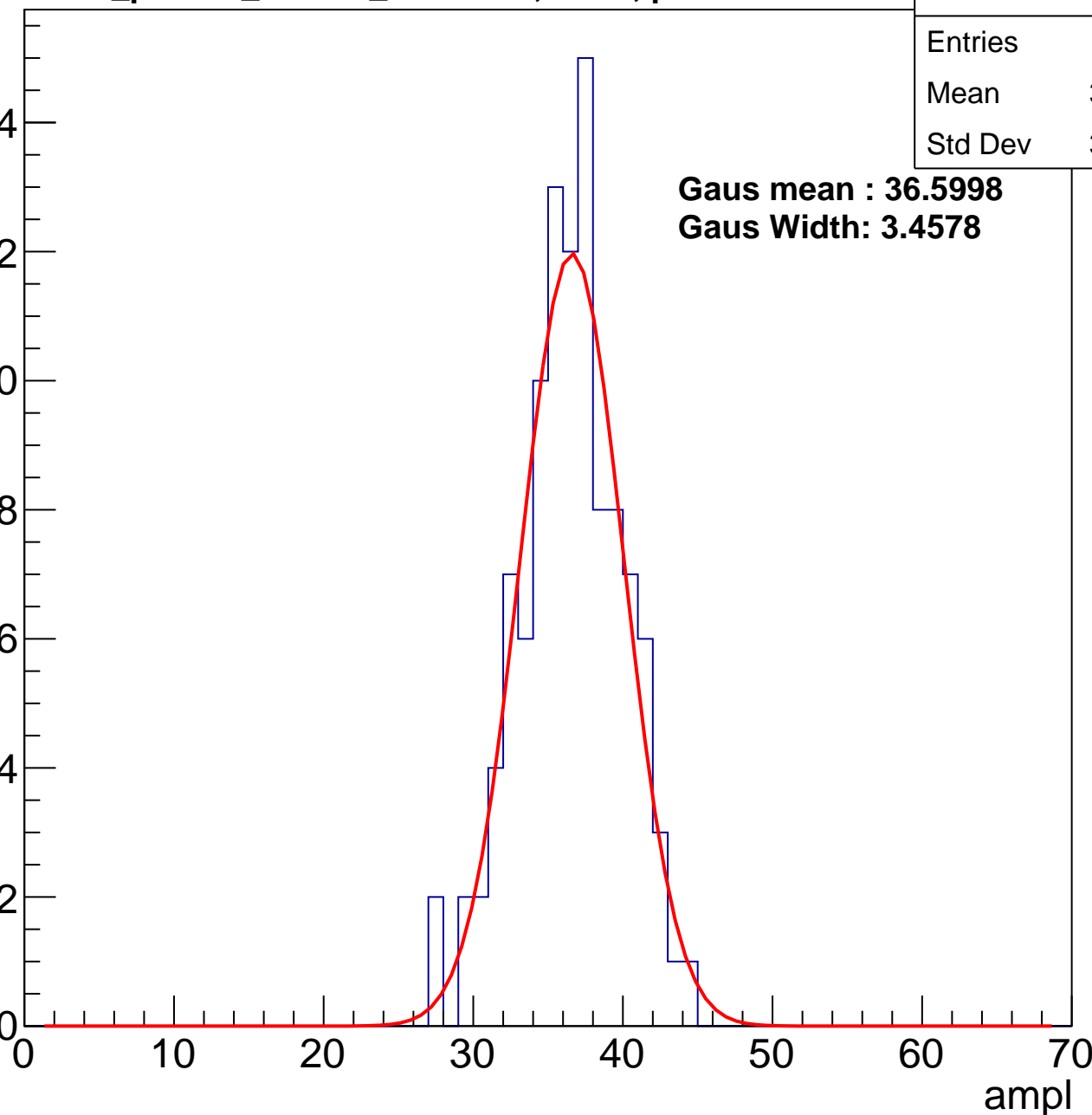
Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	36.03
Std Dev	3.422

Gaus mean : 36.5998

Gaus Width: 3.4578



B1L001S, U16-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	42.36
Std Dev	3.528

Gaus mean : 42.8164

Gaus Width: 3.2552

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

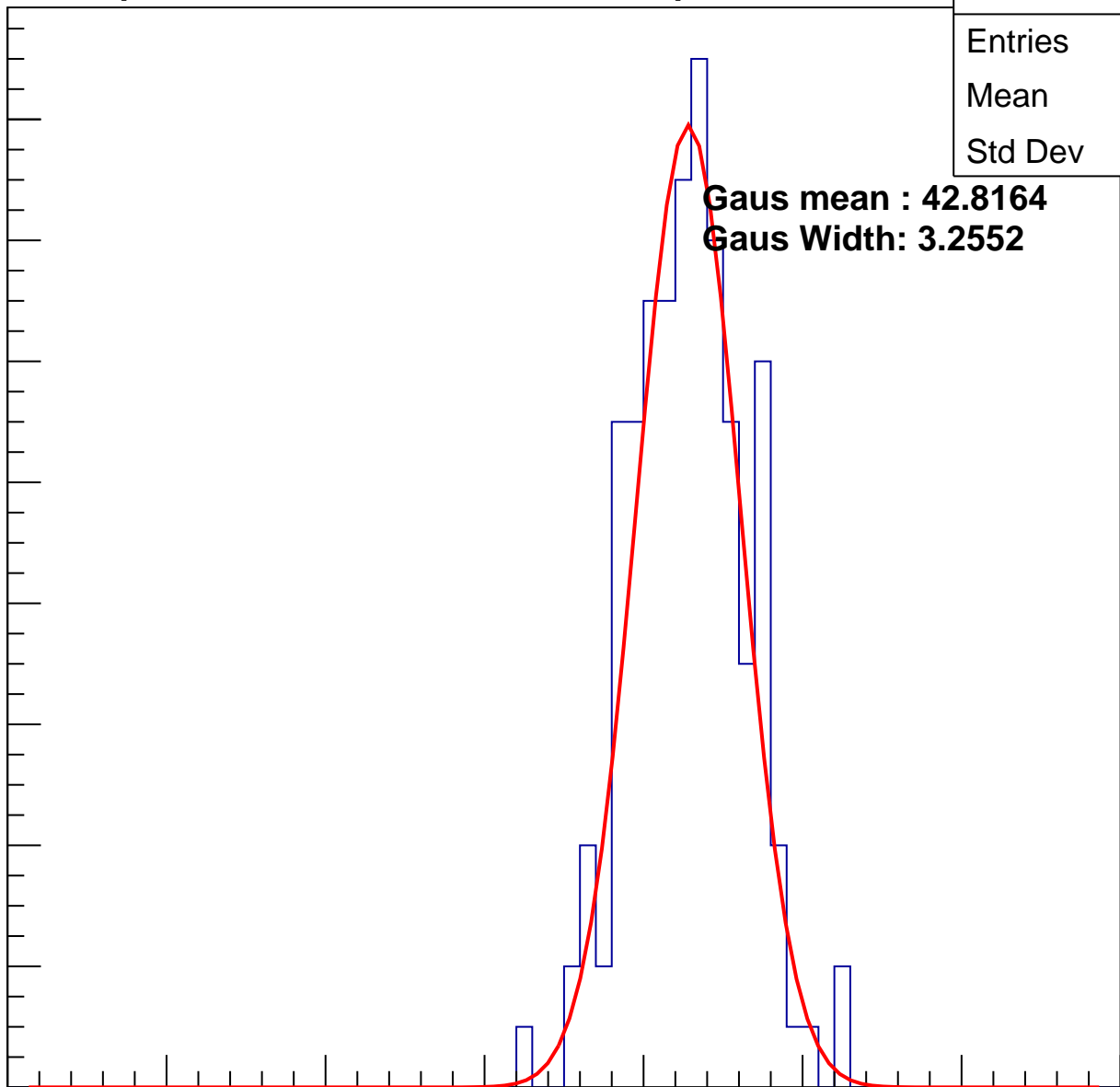
40

50

60

70

ampl



B1L001S, U16-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

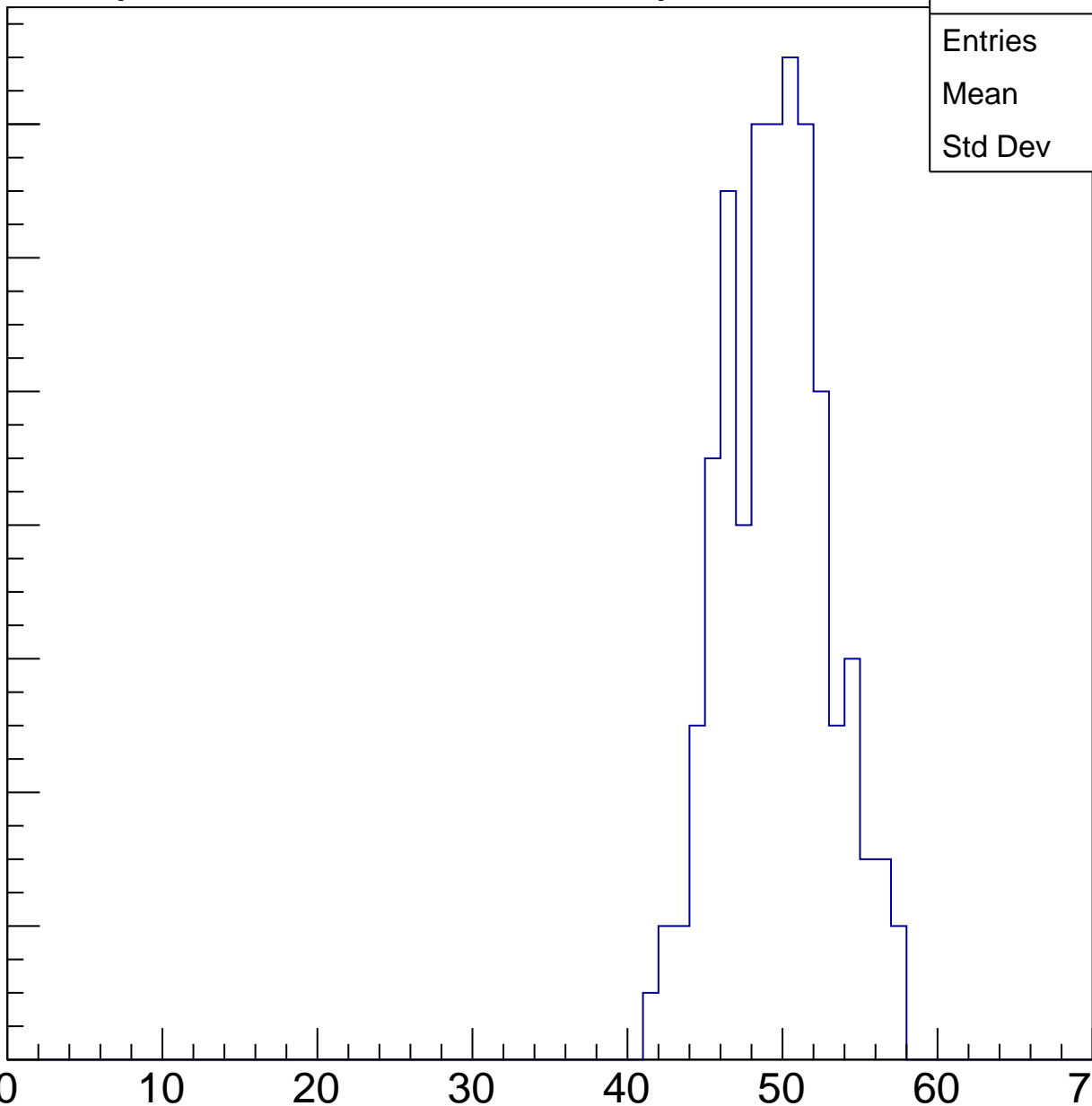
Entries	126
Mean	49.11
Std Dev	3.379

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch60, adc4

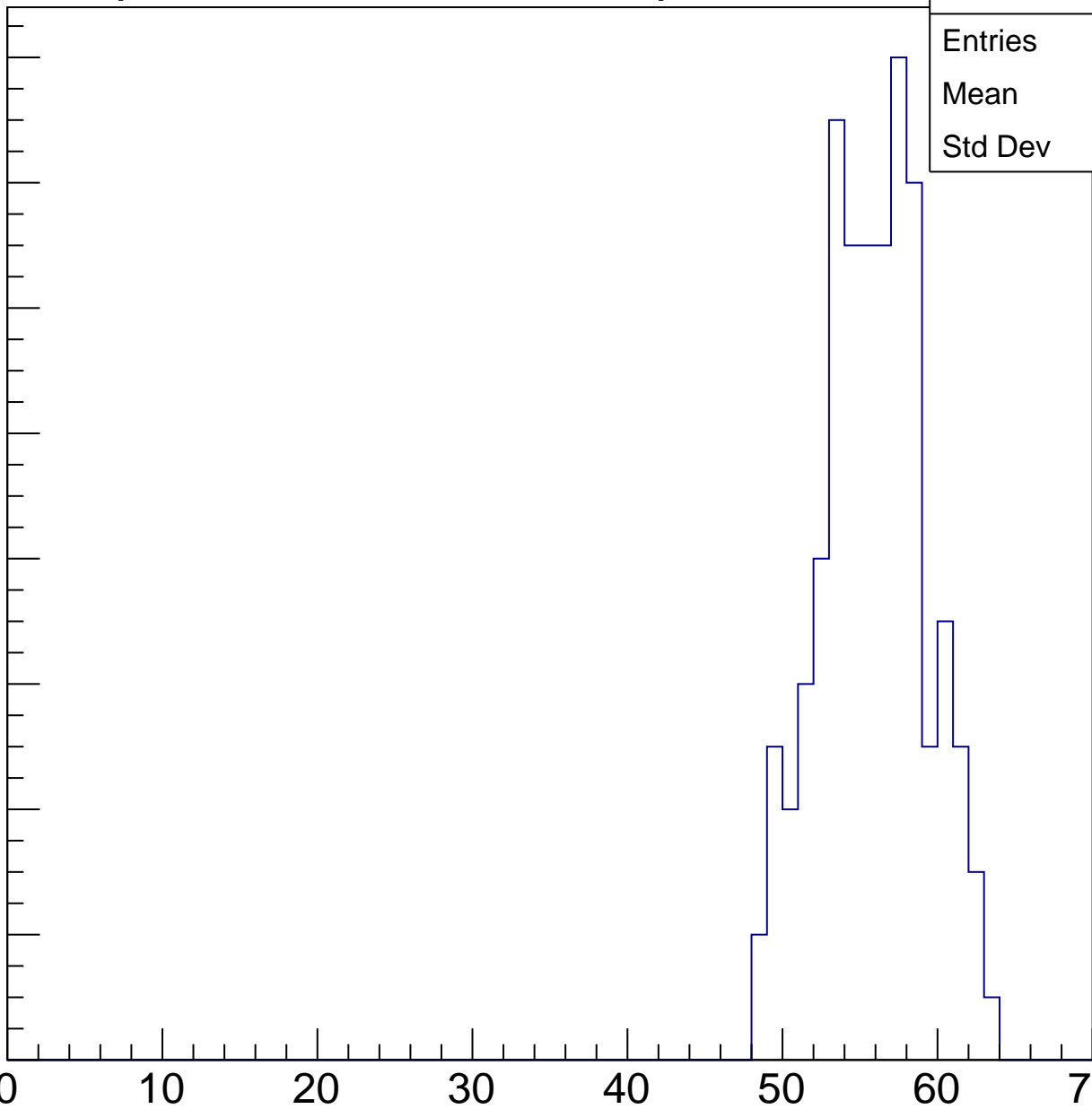
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	130
Mean	55.35
Std Dev	3.342

ampl



B1L001S, U16-ch60, adc5

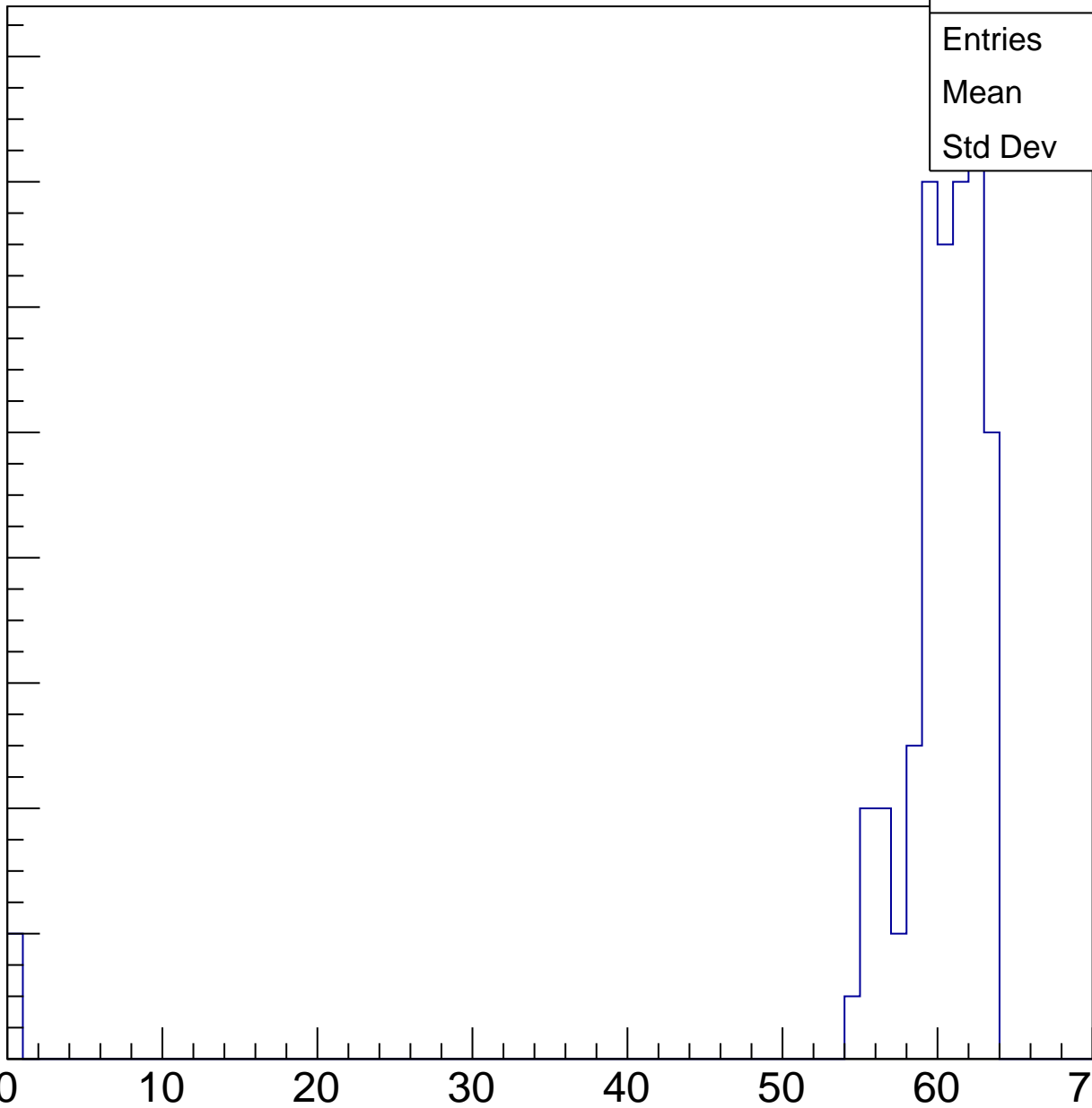
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.64
Std Dev	9.369

ampl

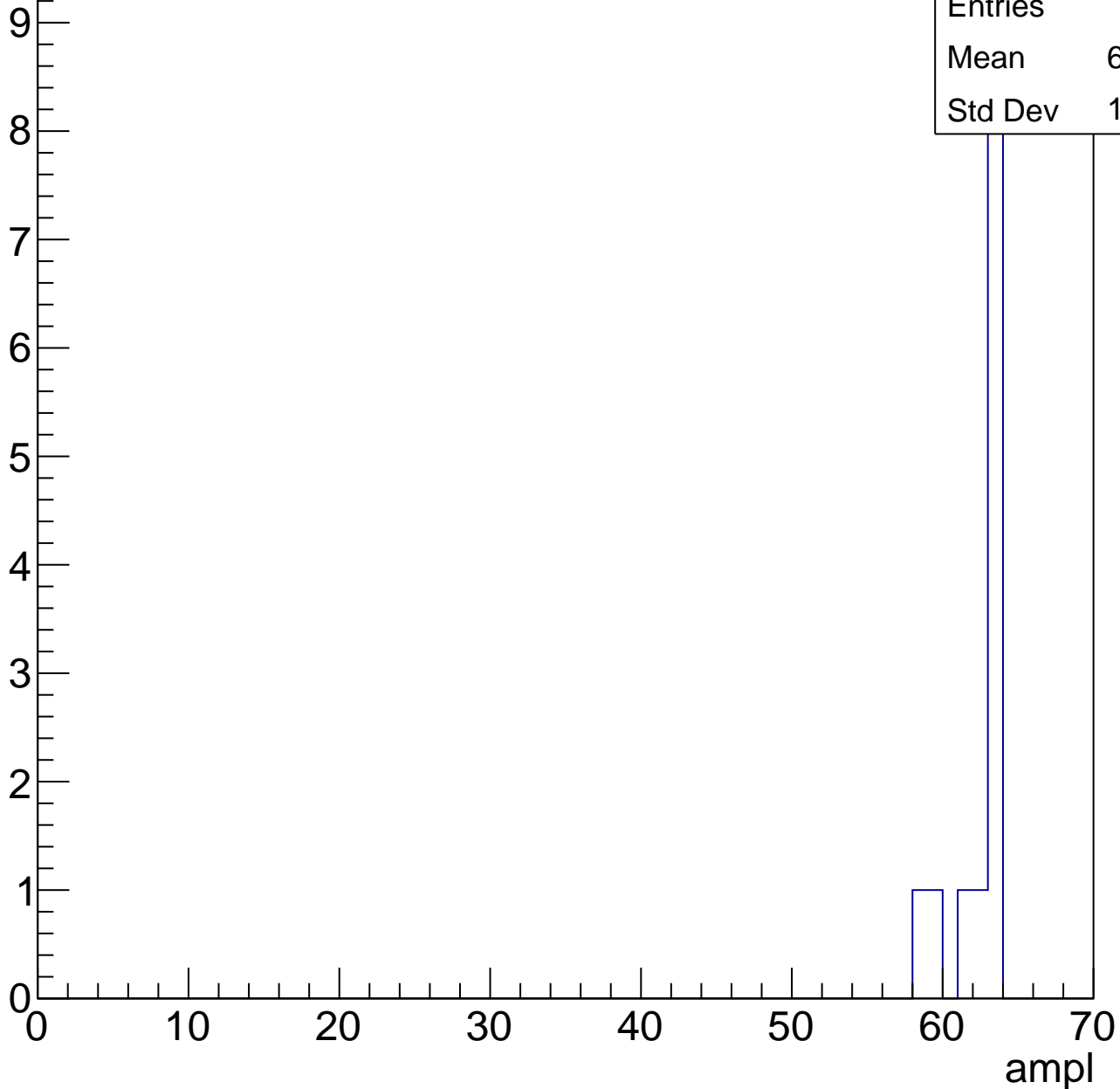


B1L001S, U16-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62.08
Std Dev	1.639



B1L001S, U16-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

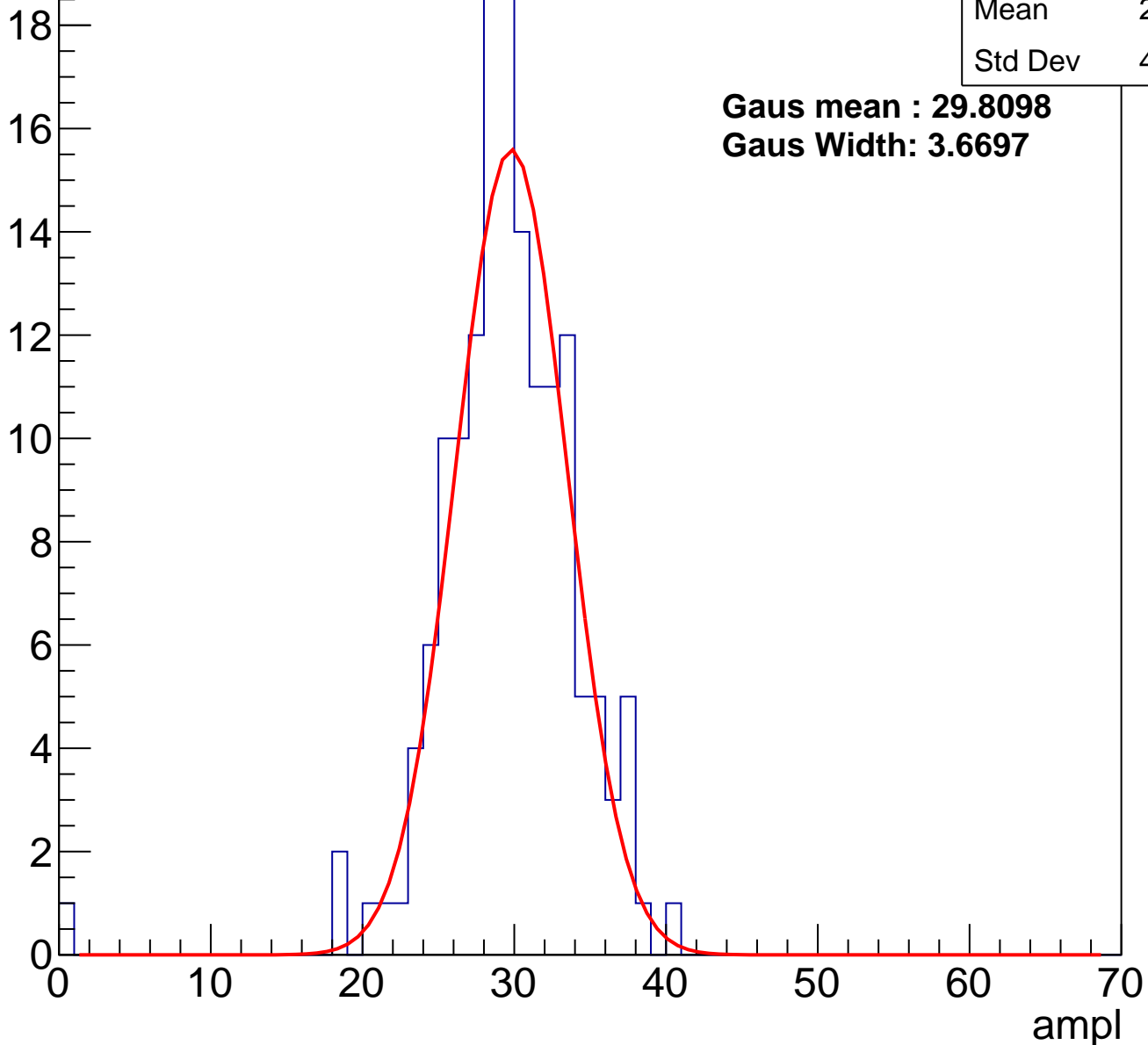


Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch61, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	154
Mean	29.07
Std Dev	4.566

Gaus mean : 29.8098

Gaus Width: 3.6697

B1L001S, U16-ch61, adc1

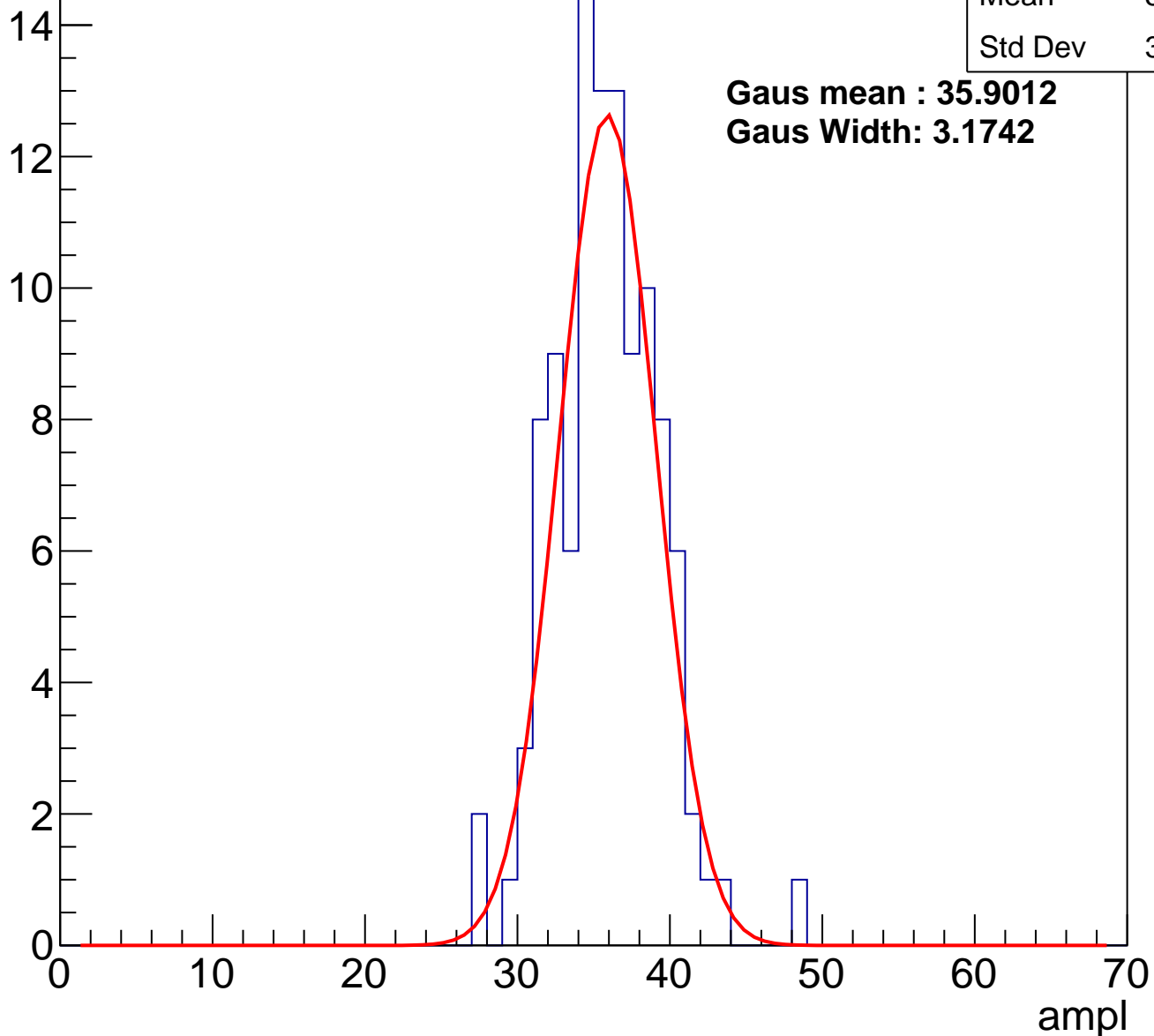
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	35.37
Std Dev	3.393

Gaus mean : 35.9012

Gaus Width: 3.1742

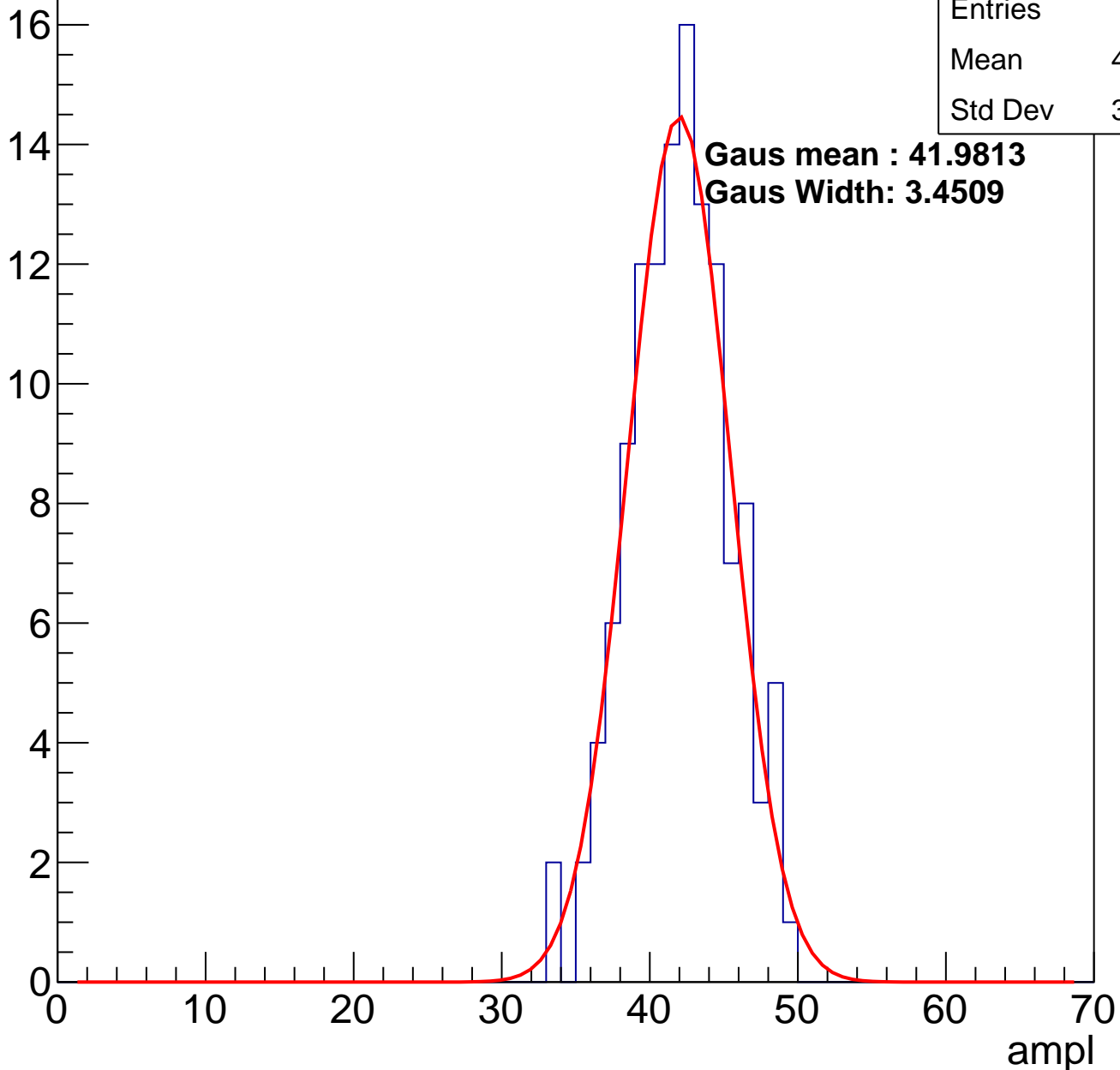
Entry



B1L001S, U16-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

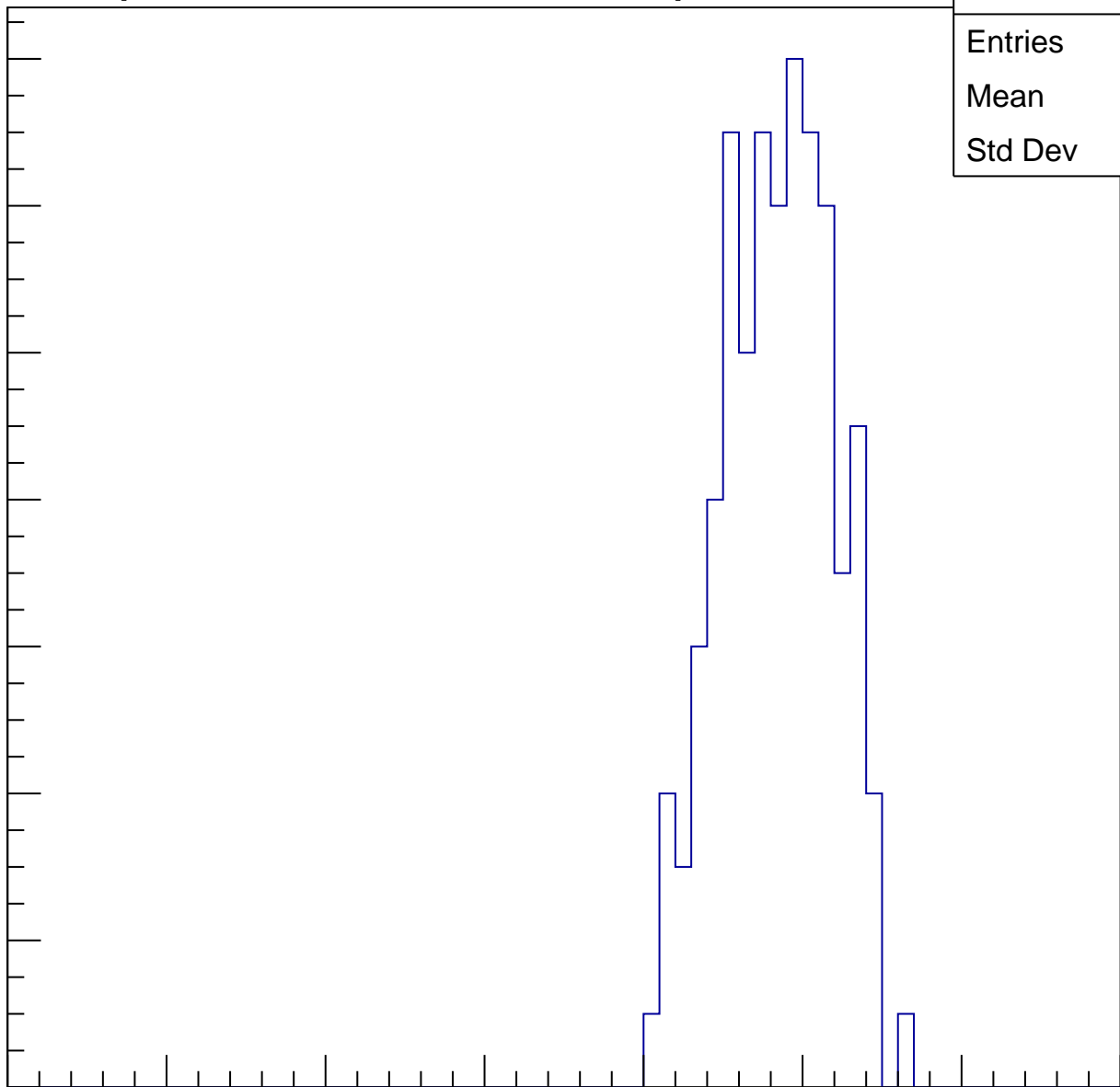
Entries	130
Mean	47.95
Std Dev	3.436

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

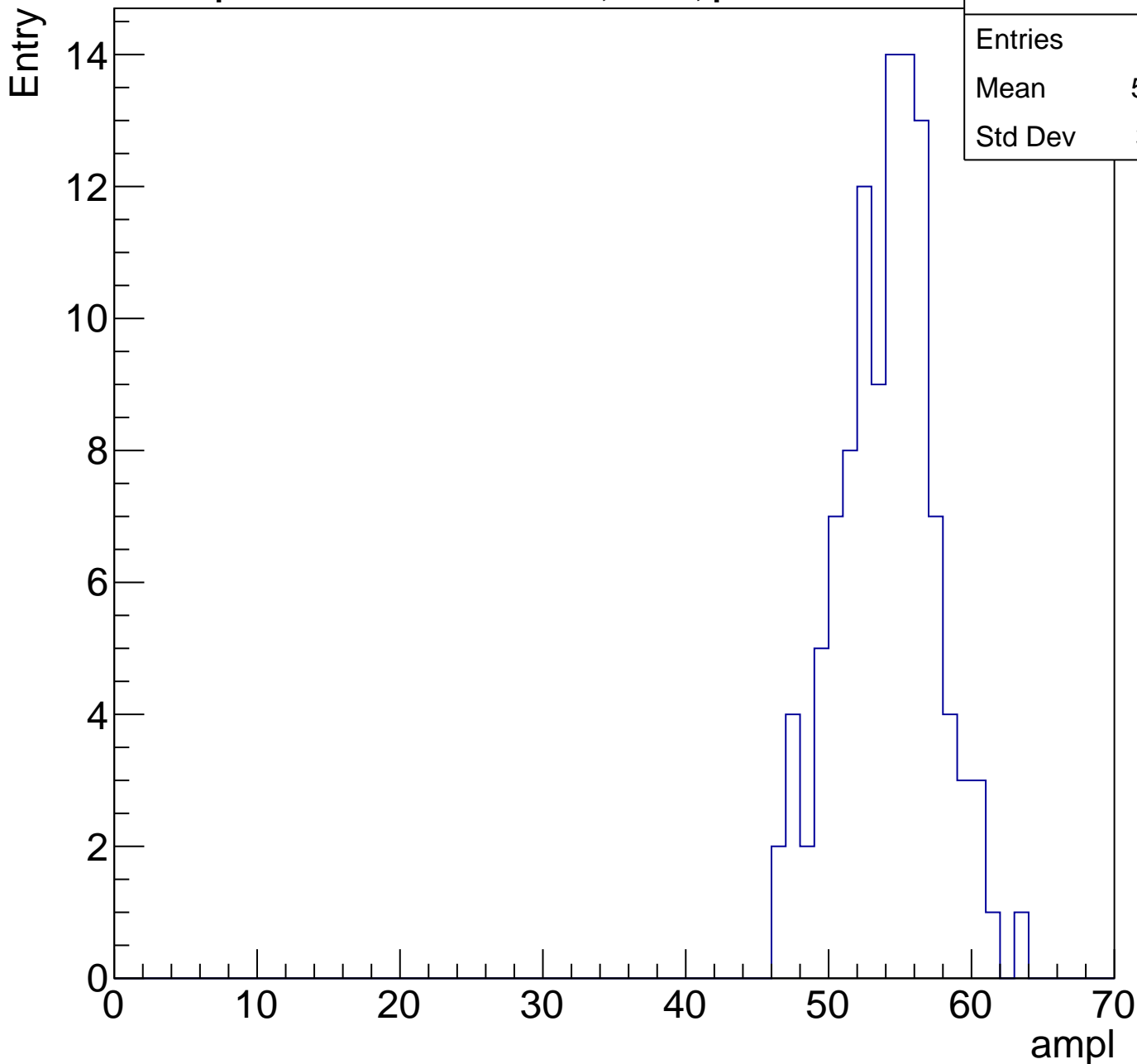
ampl



B1L001S, U16-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	53.63
Std Dev	3.391



B1L001S, U16-ch61, adc5

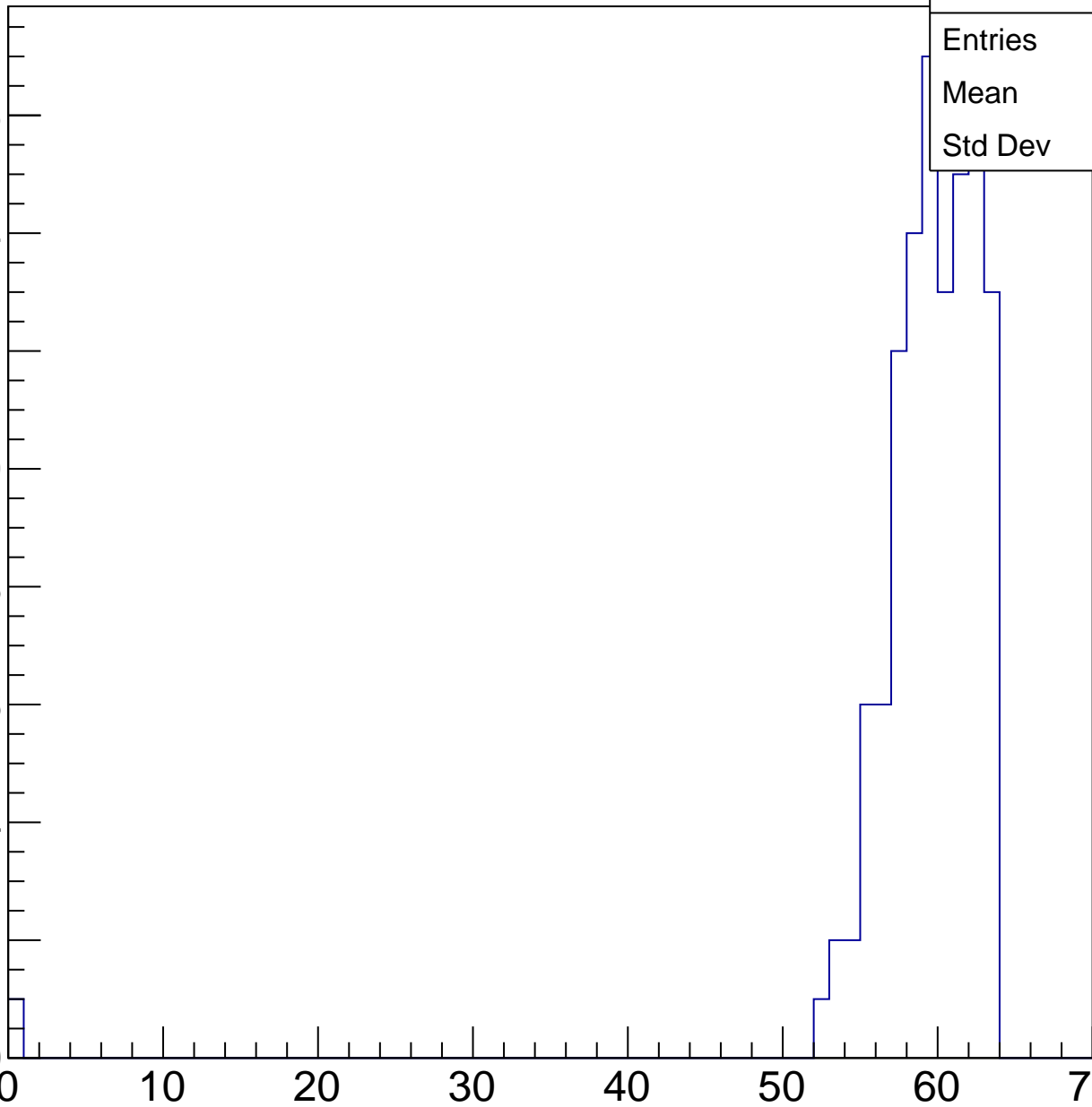
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	58.79
Std Dev	6.025

ampl

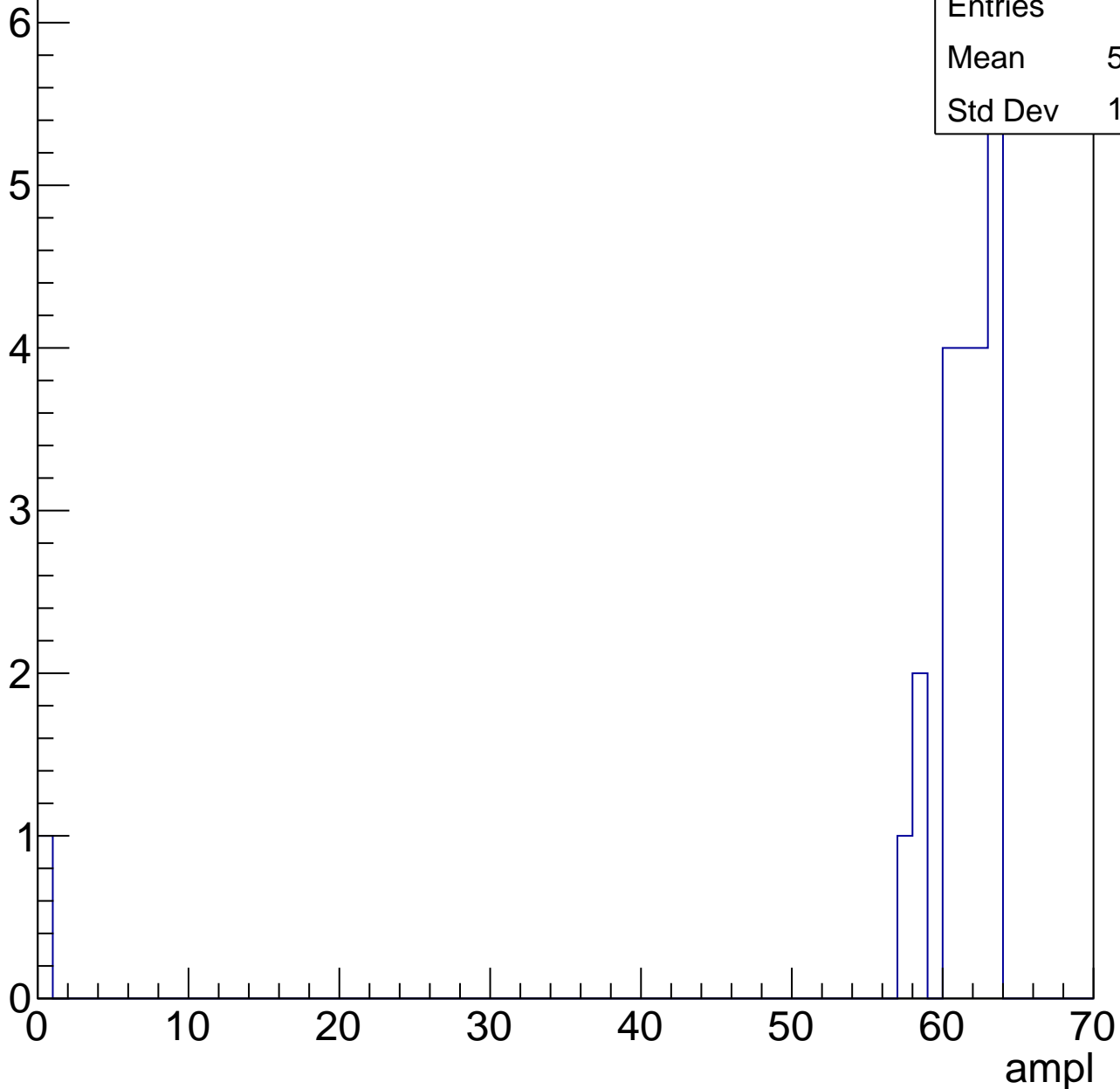


B1L001S, U16-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	58.32
Std Dev	12.84



B1L001S, U16-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch62, adc0

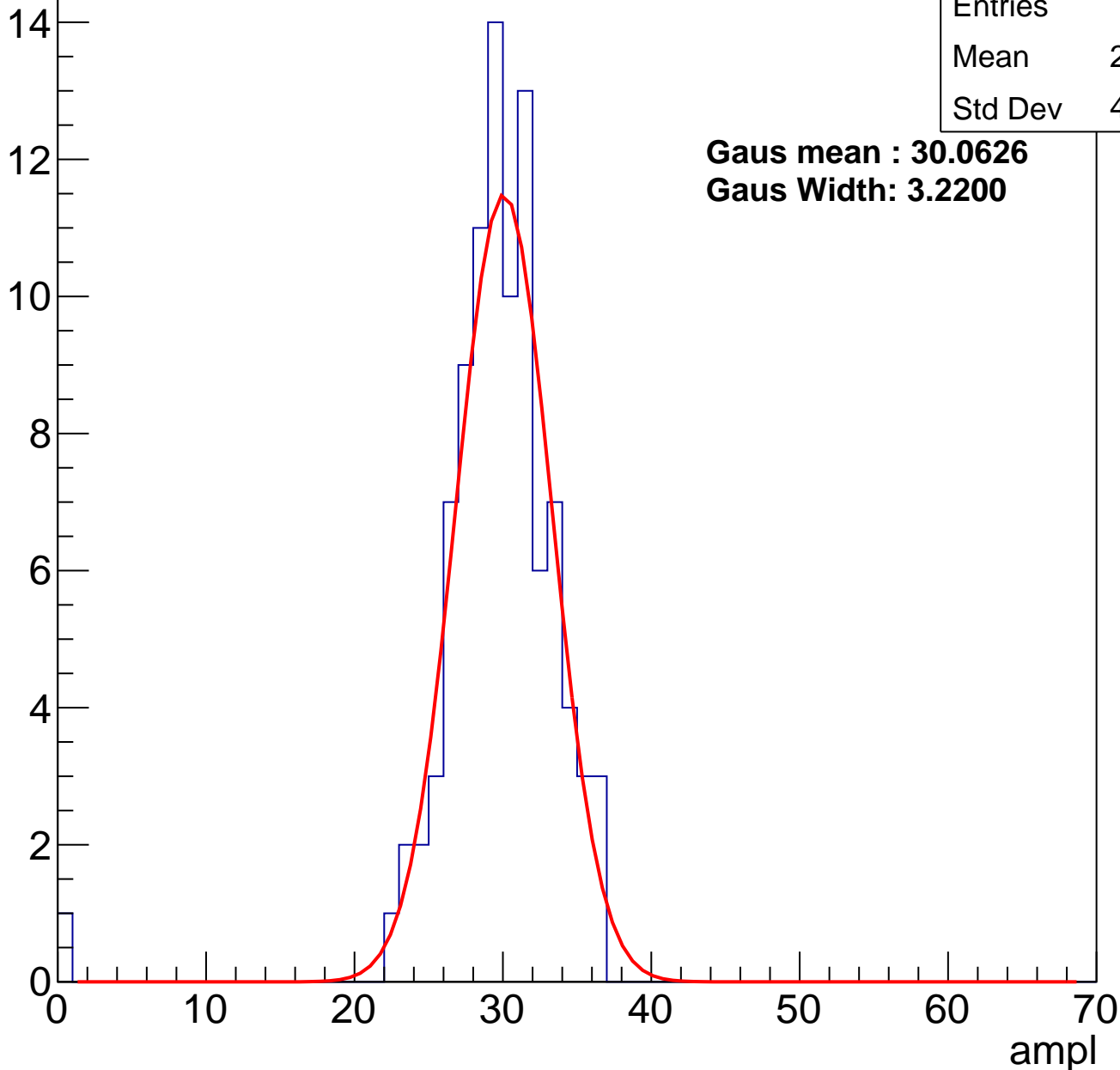
calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	29.22
Std Dev	4.268

Gaus mean : 30.0626

Gaus Width: 3.2200

Entry



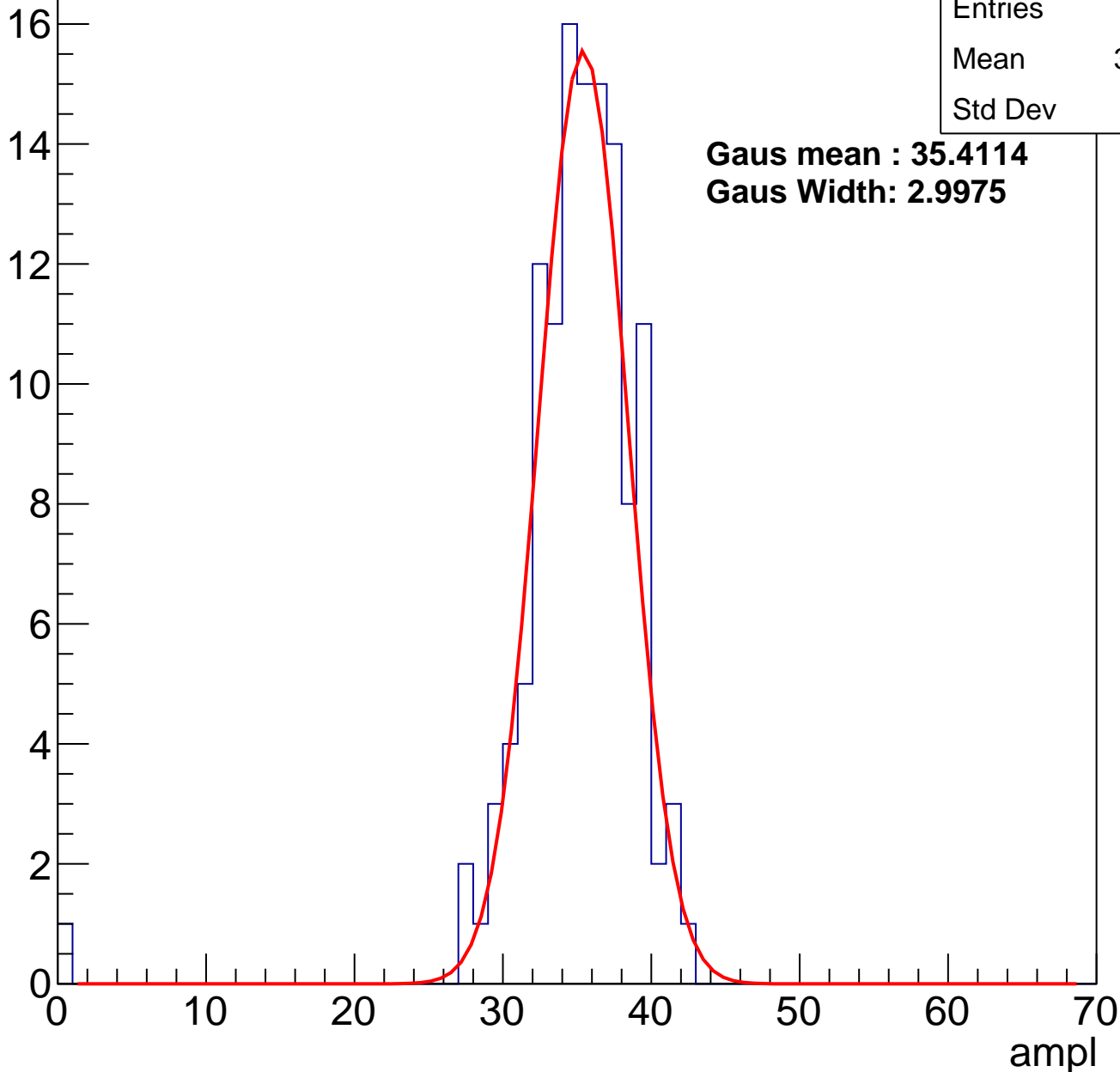
B1L001S, U16-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	34.65
Std Dev	4.37

Gaus mean : 35.4114
Gaus Width: 2.9975

Entry



B1L001S, U16-ch62, adc2

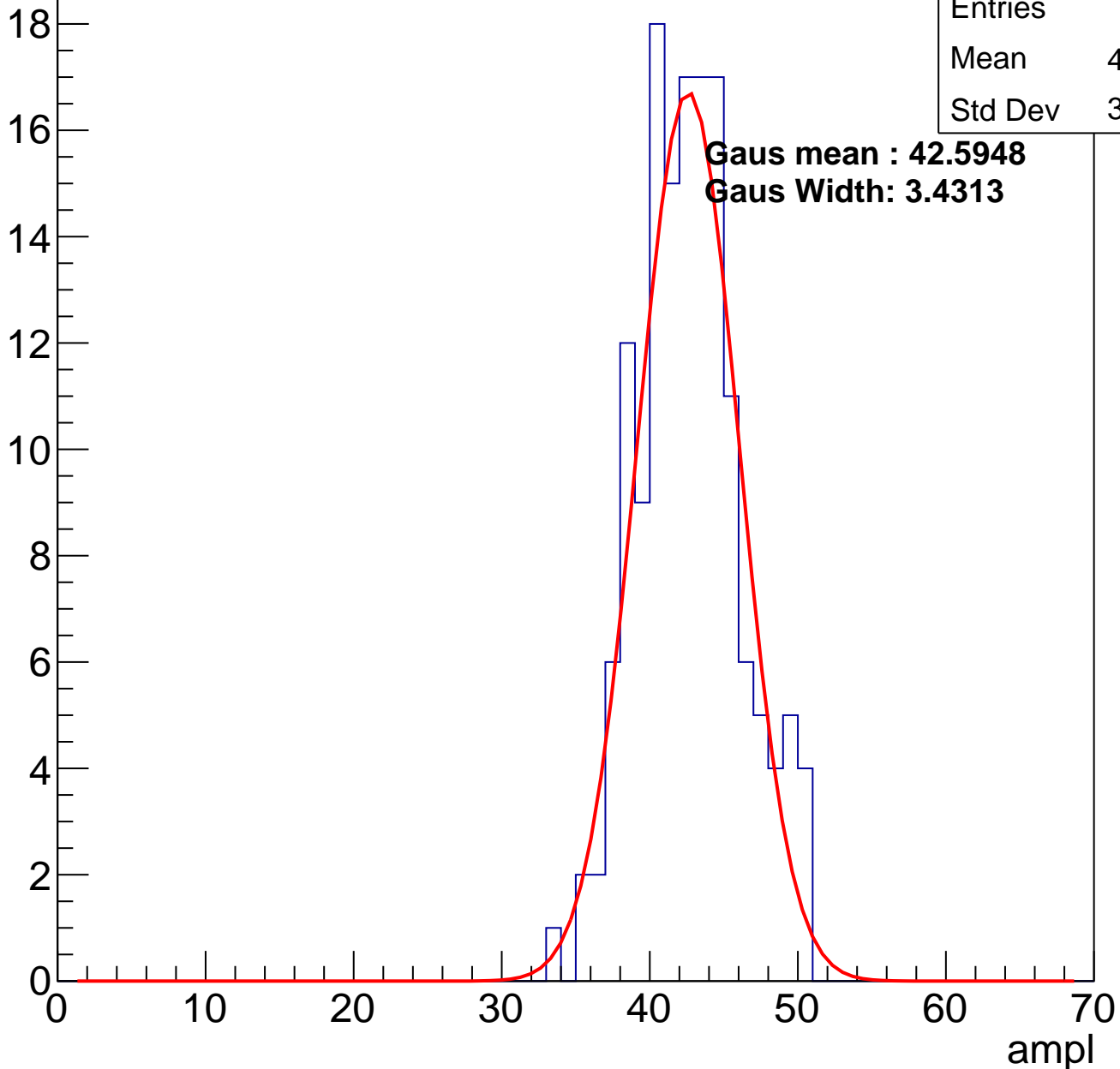
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	42.22
Std Dev	3.456

Gaus mean : 42.5948

Gaus Width: 3.4313

Entry



B1L001S, U16-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

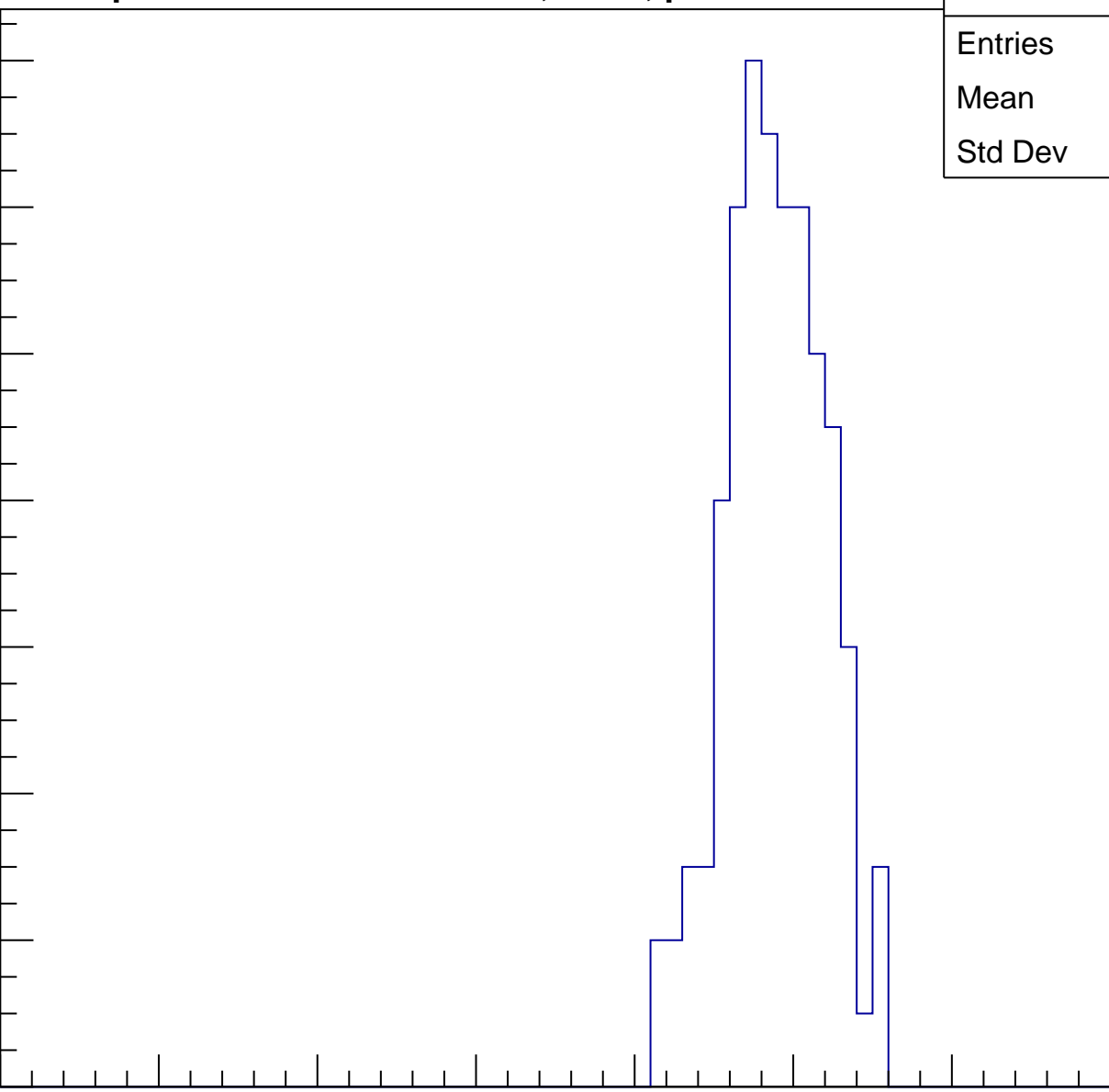
Entries	110
Mean	48.4
Std Dev	3.069

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

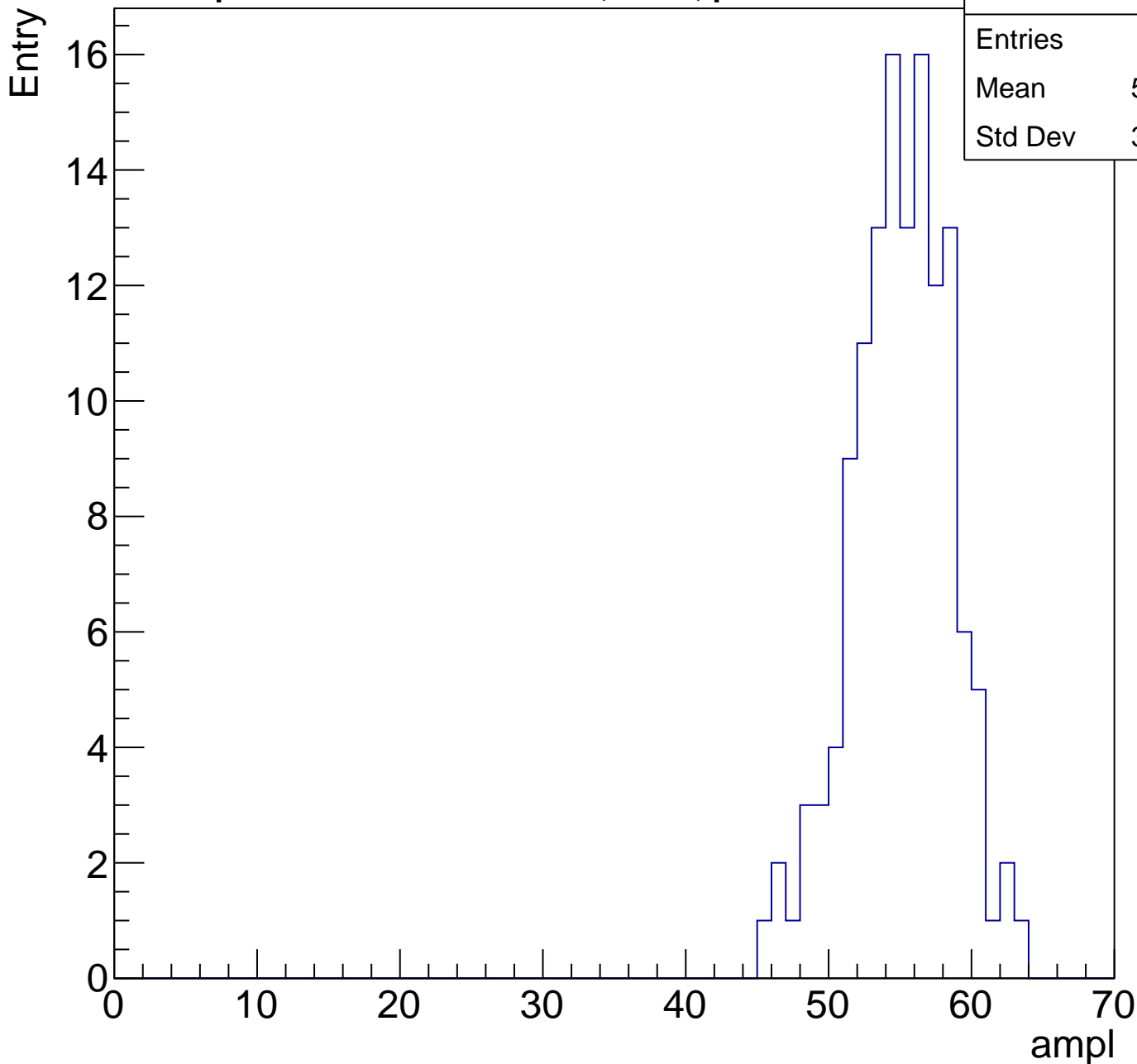
ampl



B1L001S, U16-ch62, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	54.62
Std Dev	3.452



B1L001S, U16-ch62, adc5

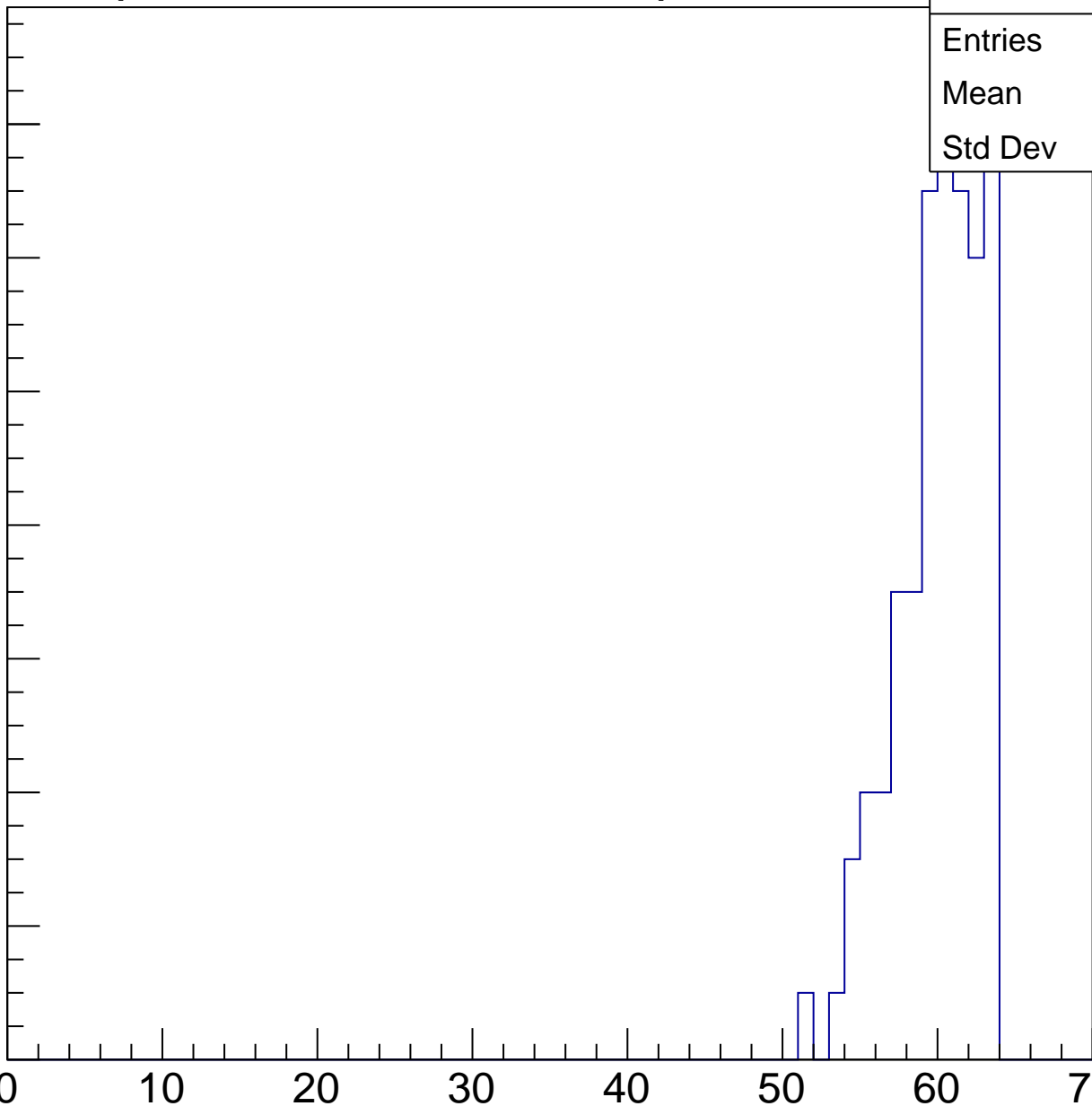
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	59.62
Std Dev	2.706

ampl

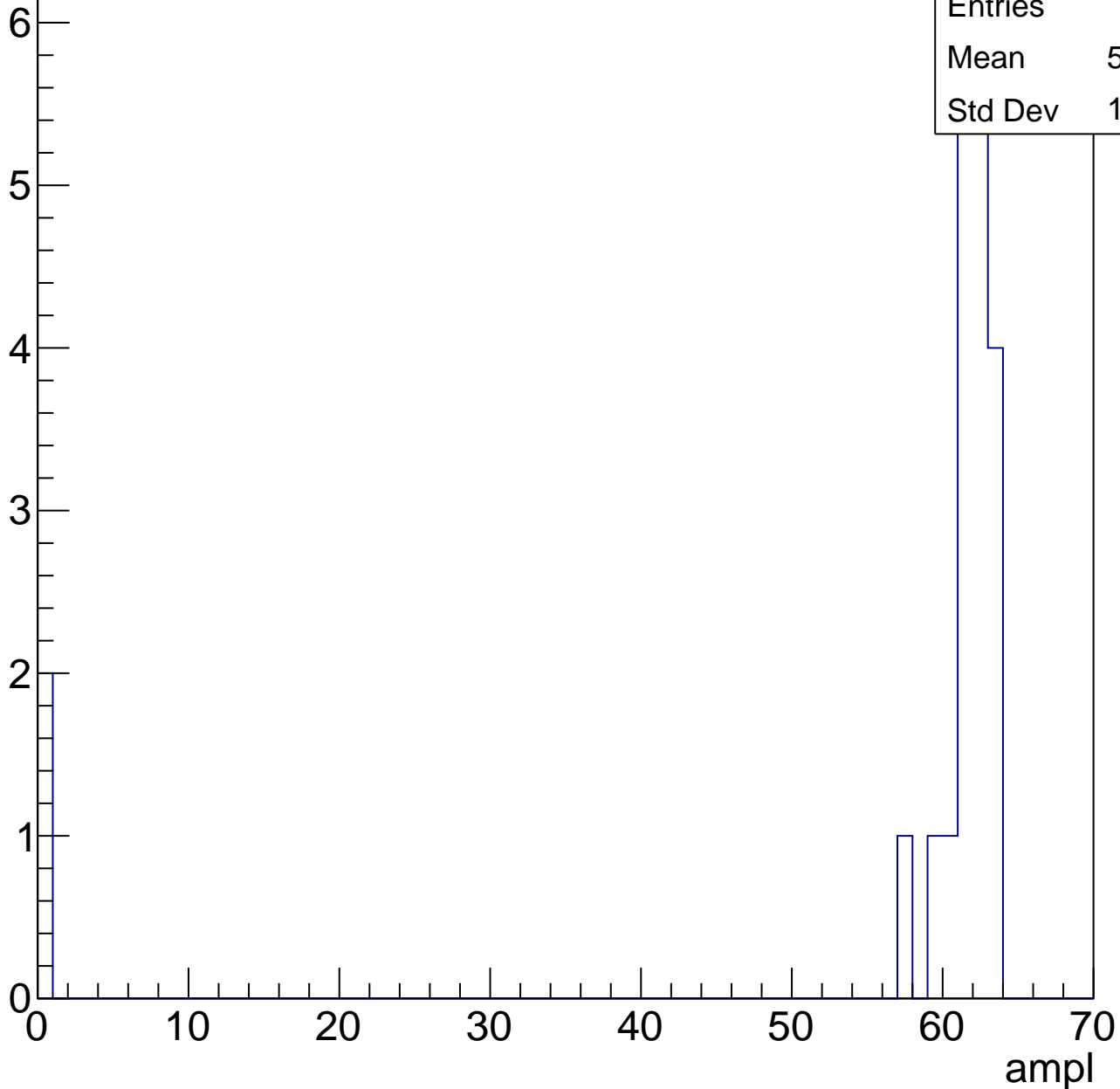


B1L001S, U16-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

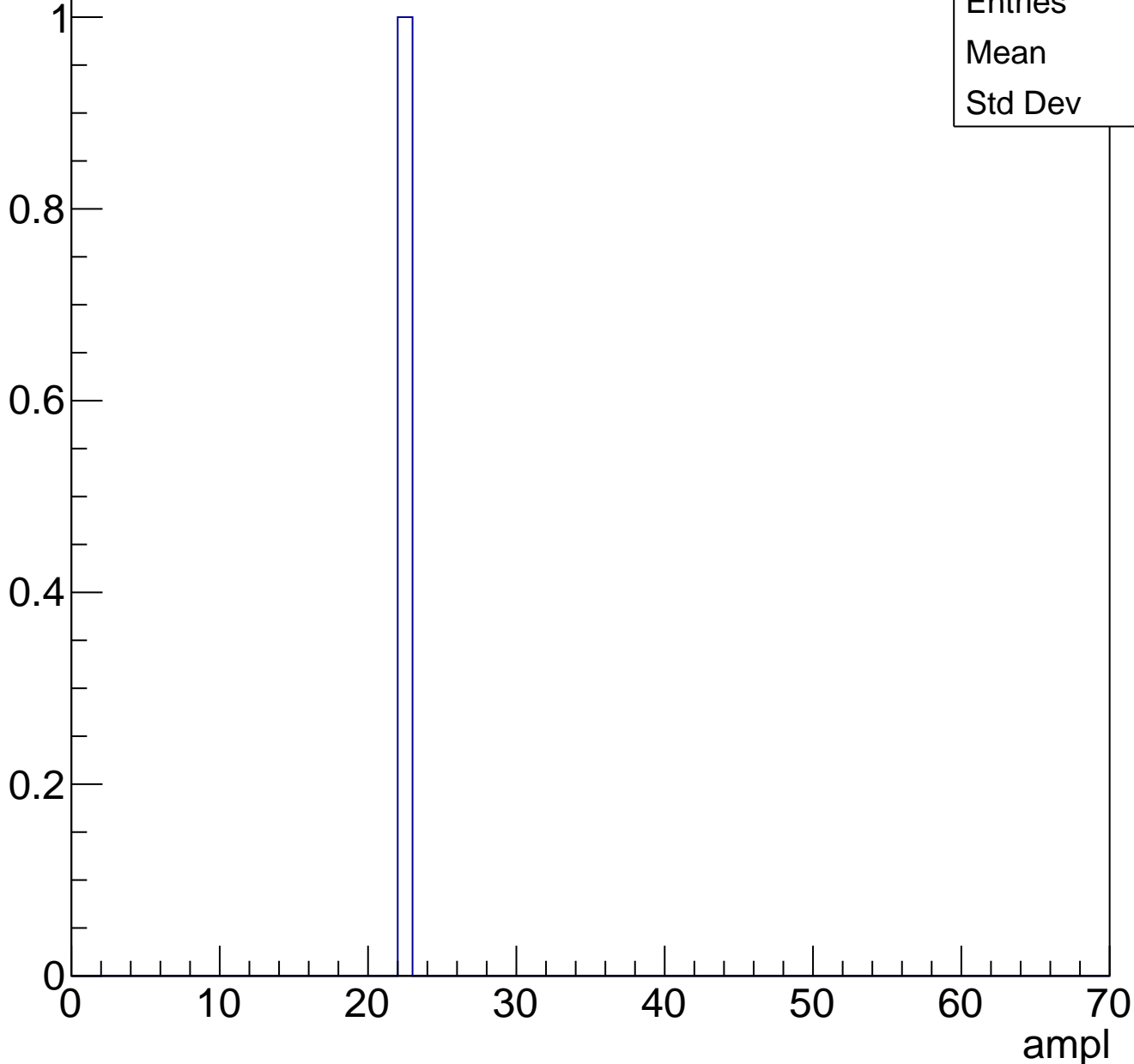
Entries	21
Mean	55.52
Std Dev	18.07



B1L001S, U16-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch63, adc0

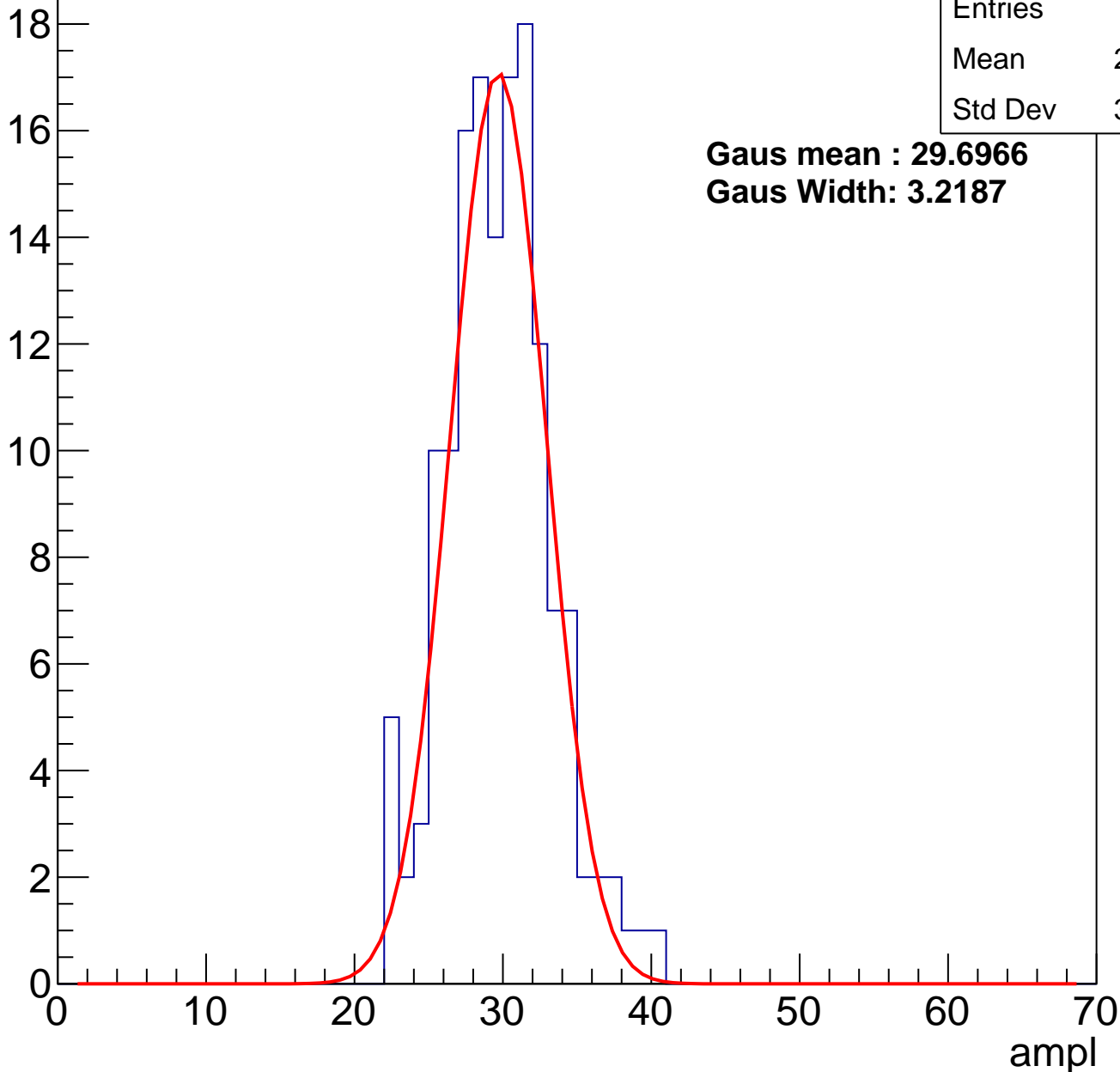
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.29
Std Dev	3.494

Gaus mean : 29.6966

Gaus Width: 3.2187

Entry



B1L001S, U16-ch63, adc1

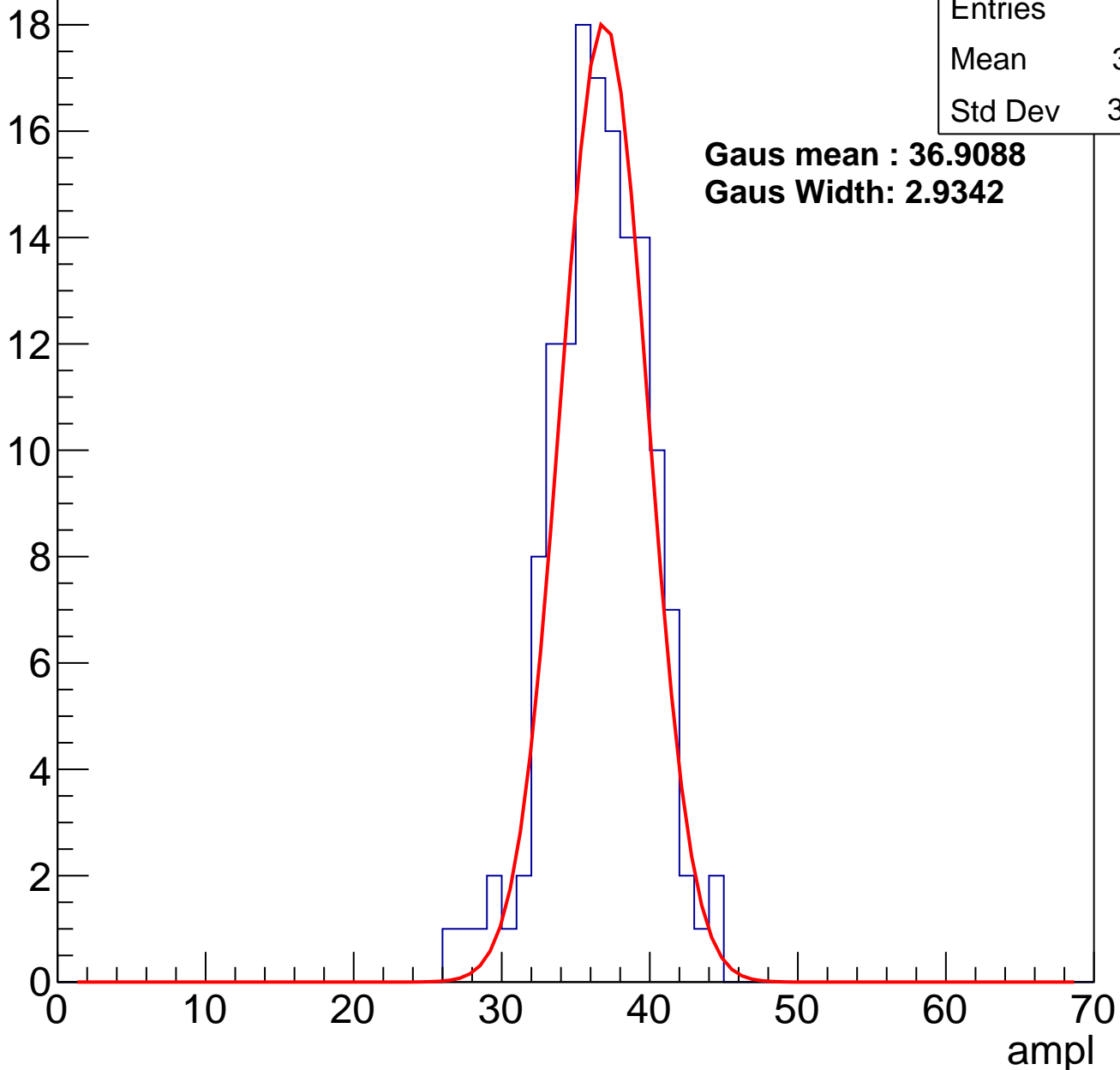
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	36.21
Std Dev	3.269

Gaus mean : 36.9088

Gaus Width: 2.9342

Entry



B1L001S, U16-ch63, adc2

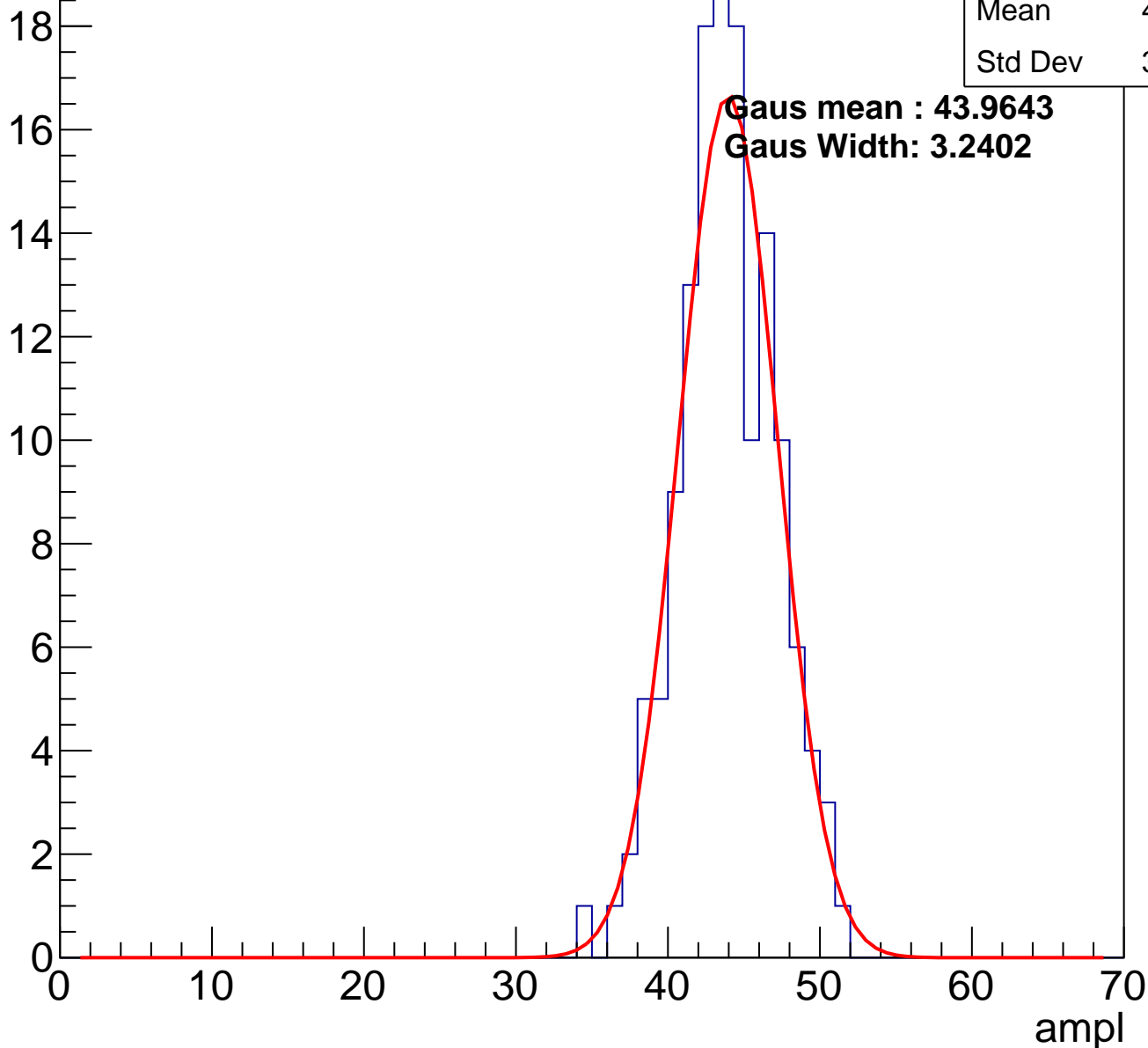
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.42
Std Dev	3.153

Gaus mean : 43.9643

Gaus Width: 3.2402

Entry

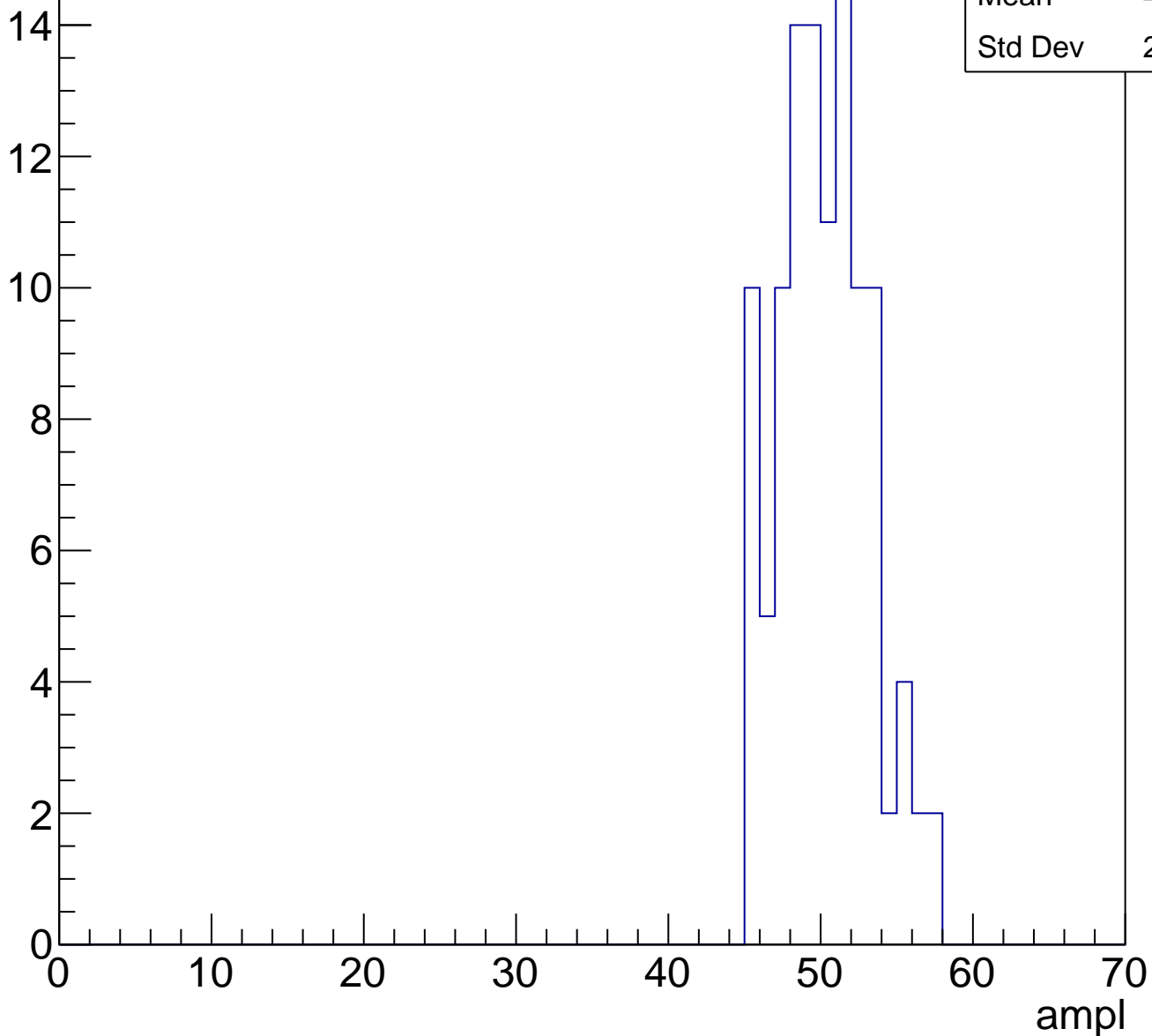


B1L001S, U16-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	49.79
Std Dev	2.924

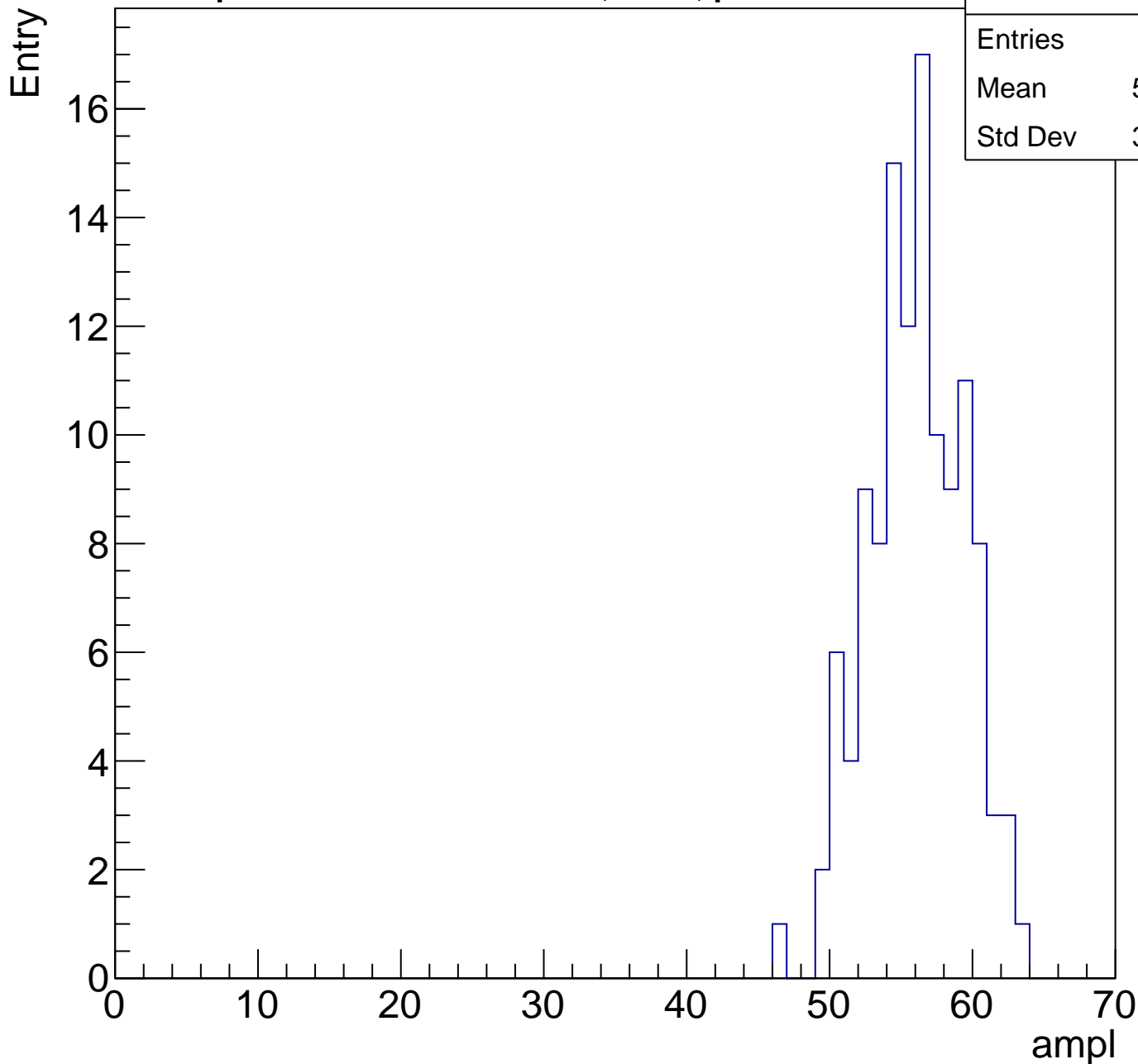
Entry



B1L001S, U16-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.59
Std Dev	3.288



B1L001S, U16-ch63, adc5

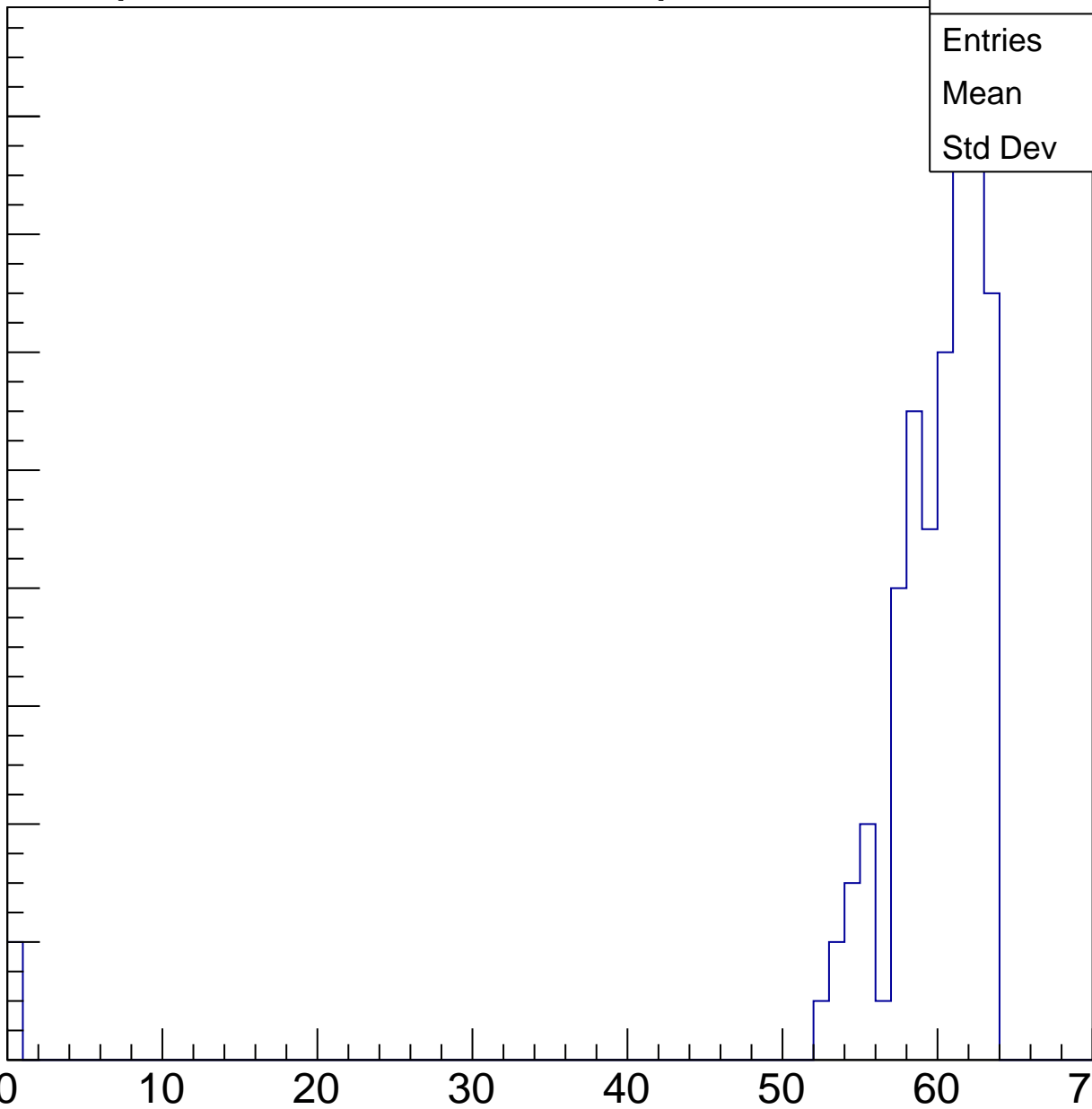
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.47
Std Dev	8.807

ampl

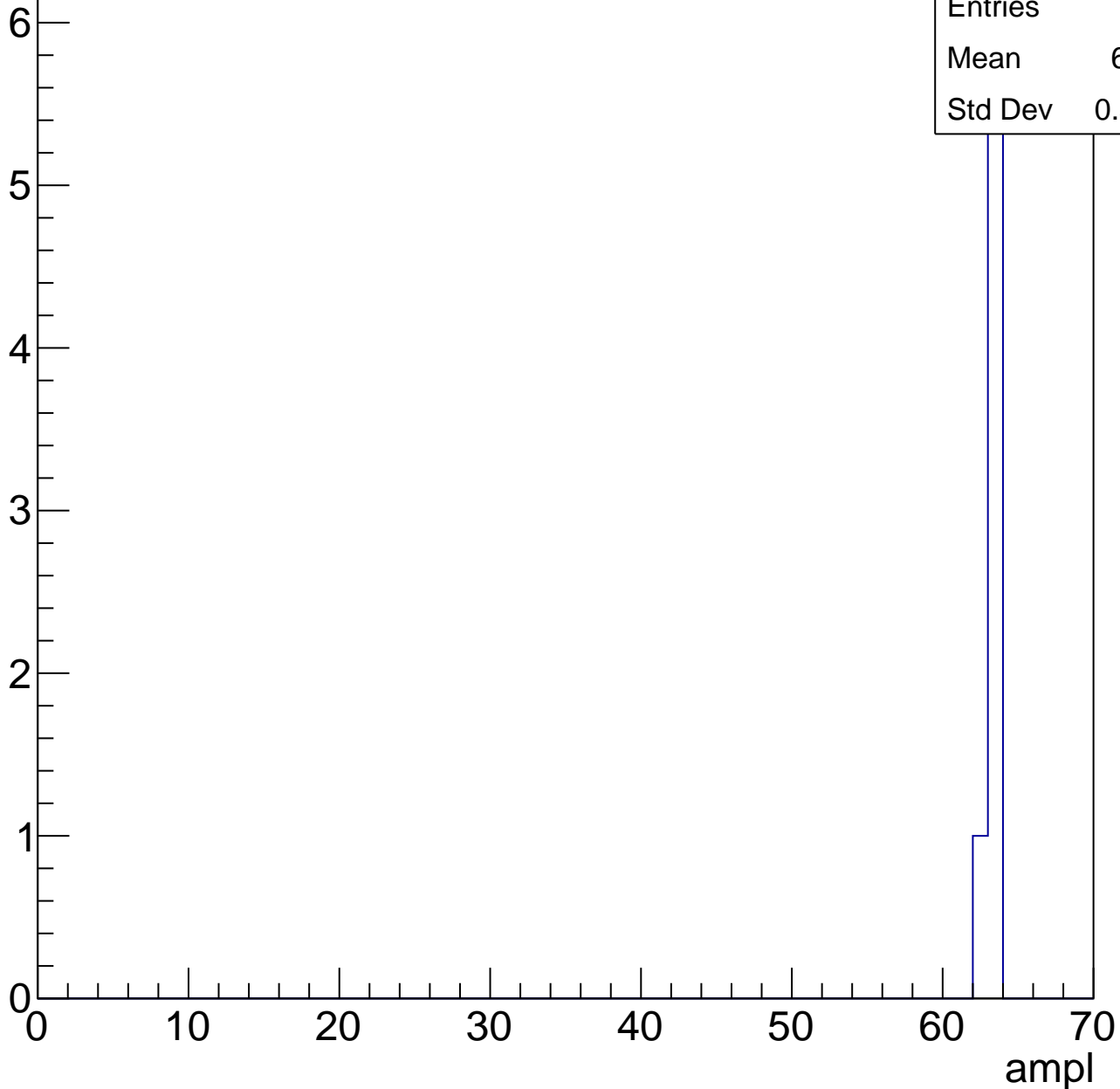


B1L001S, U16-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.86
Std Dev	0.3499



B1L001S, U16-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

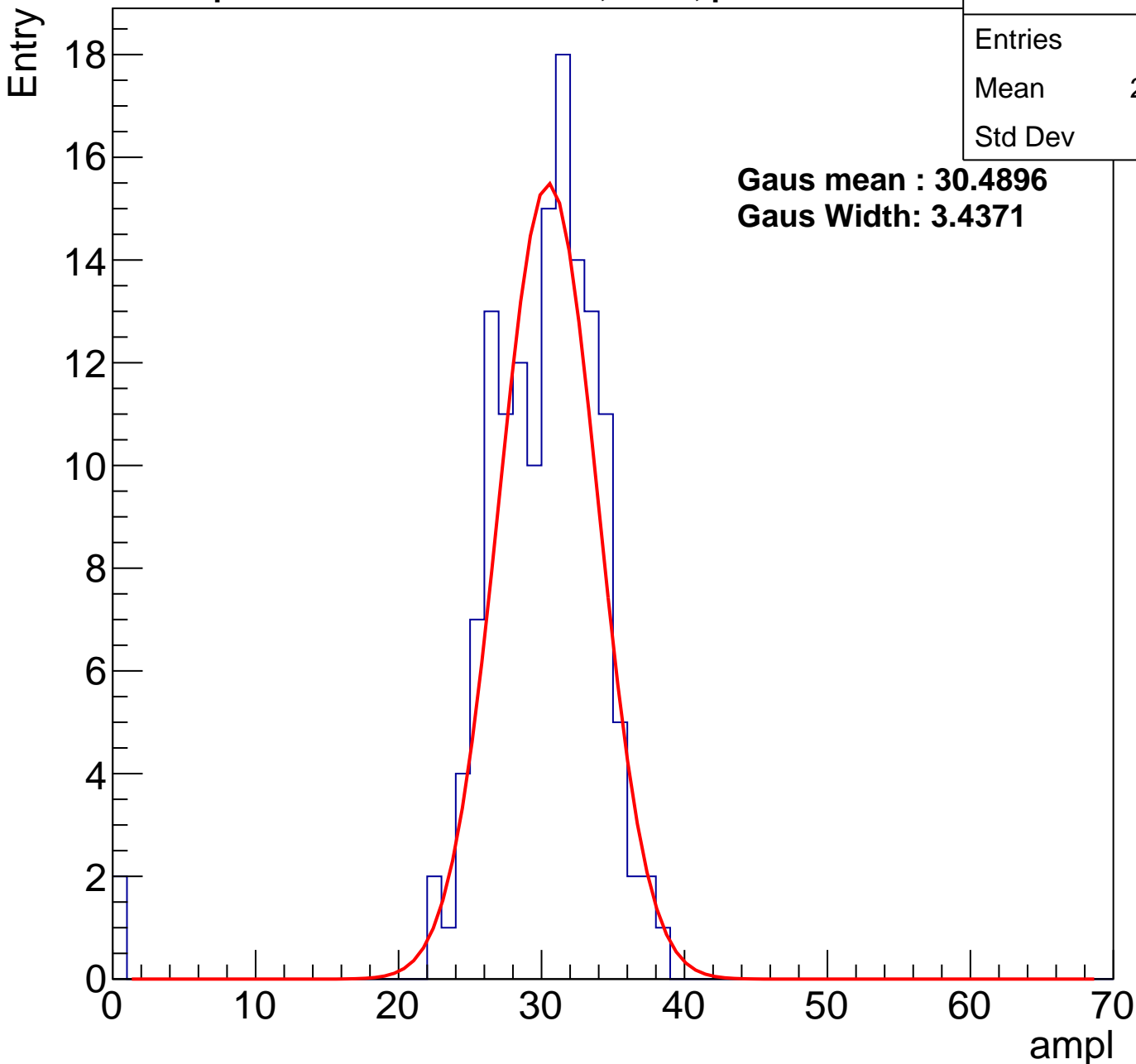
B1L001S, U16-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.49
Std Dev	4.84

Gaus mean : 30.4896

Gaus Width: 3.4371



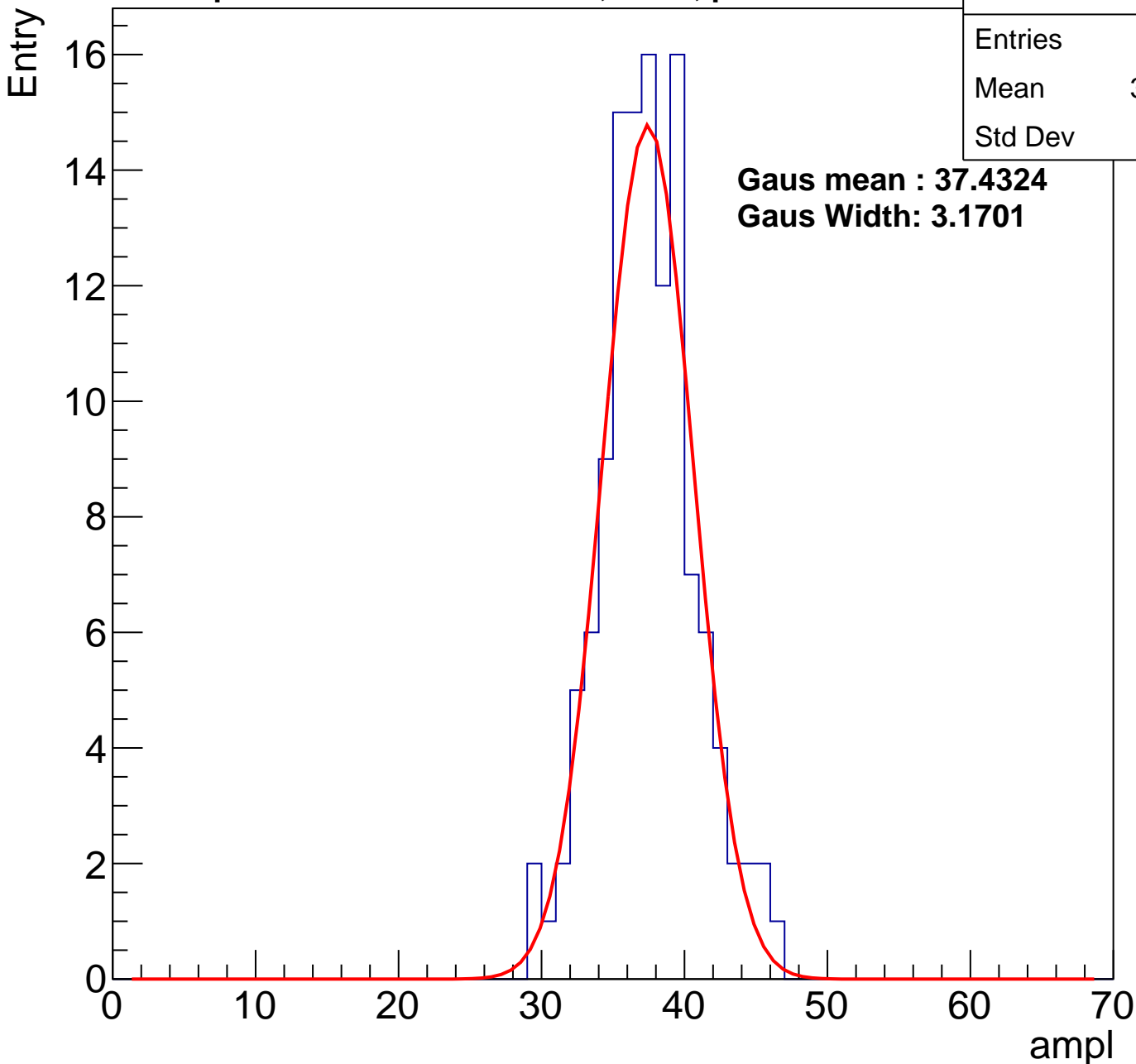
B1L001S, U16-ch64, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	37.03
Std Dev	3.3

Gaus mean : 37.4324

Gaus Width: 3.1701



B1L001S, U16-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	43.42
Std Dev	3.307

Gaus mean : 44.0710

Gaus Width: 3.4249

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch64, adc3

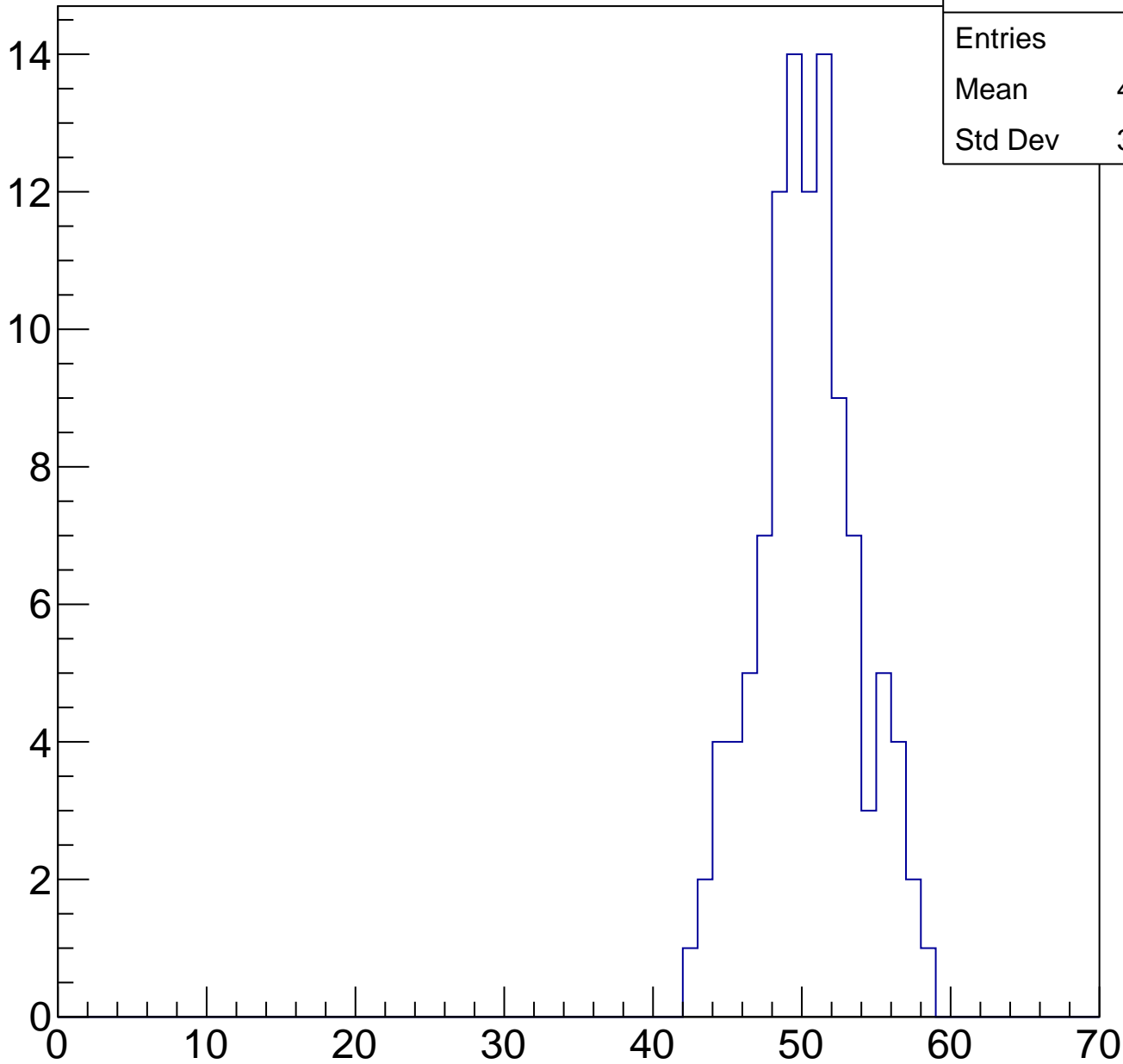
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	49.92
Std Dev	3.368

ampl



B1L001S, U16-ch64, adc4

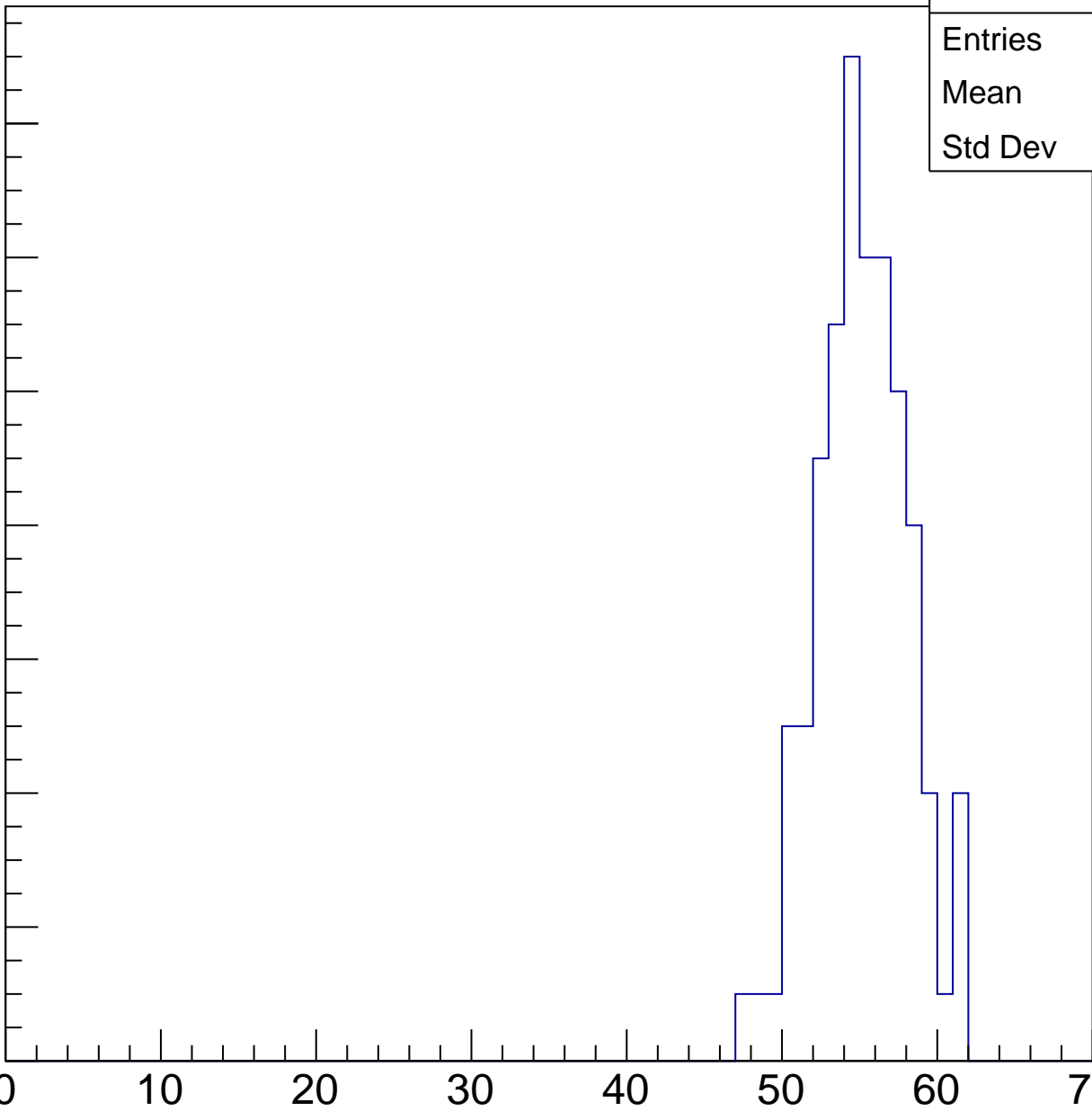
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	54.71
Std Dev	2.917

ampl



B1L001S, U16-ch64, adc5

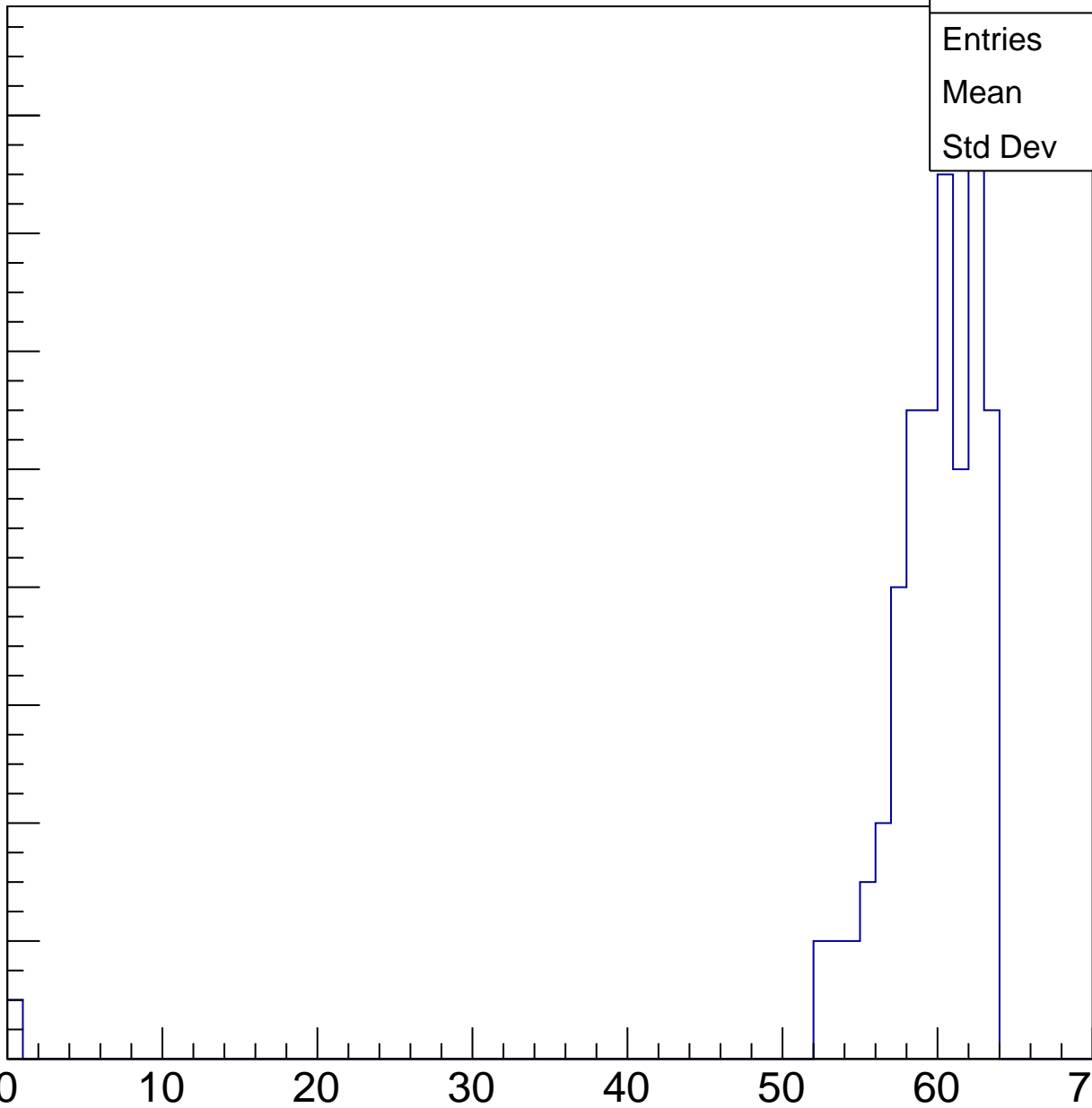
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.84
Std Dev	6.588

ampl

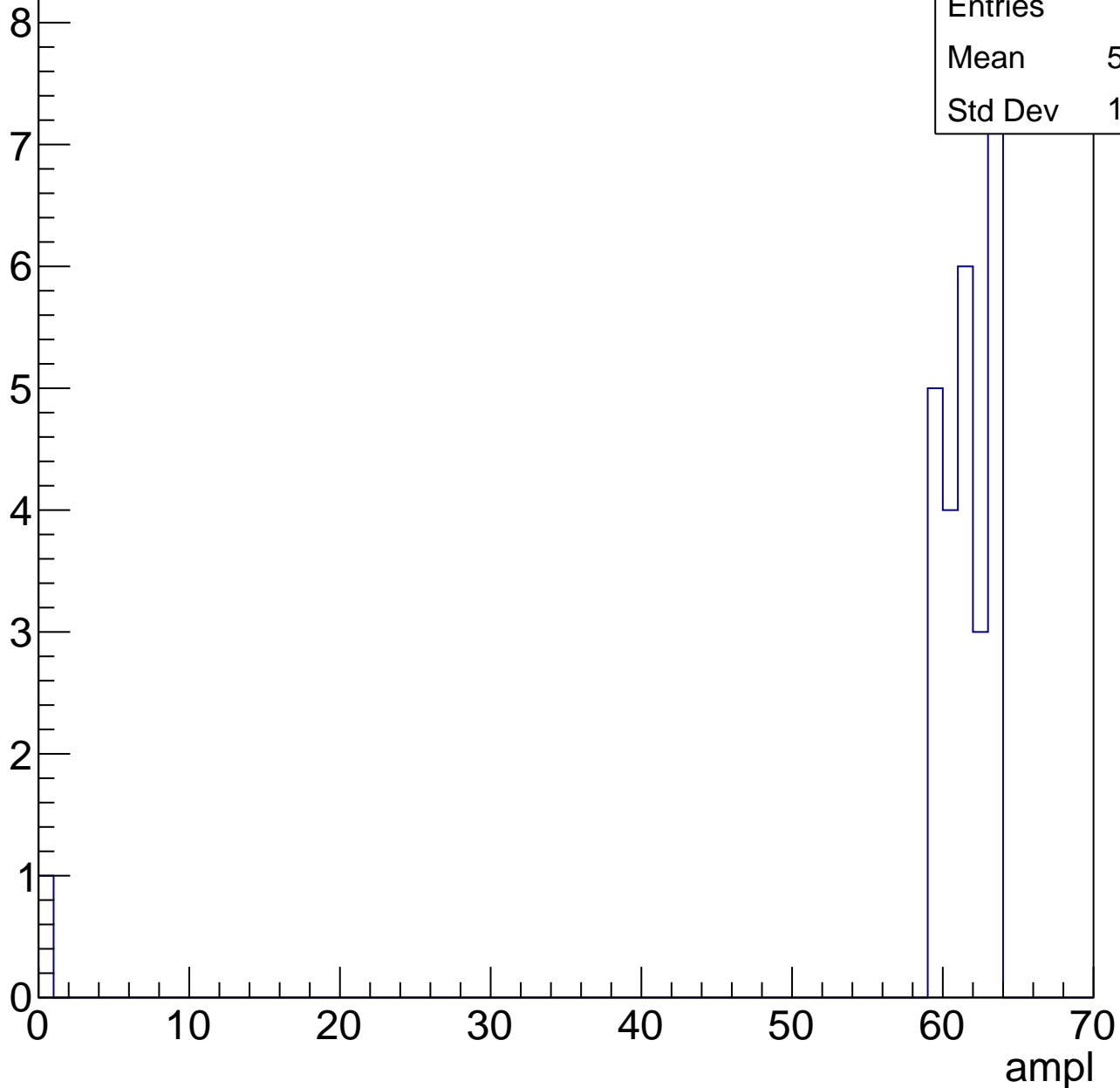


B1L001S, U16-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

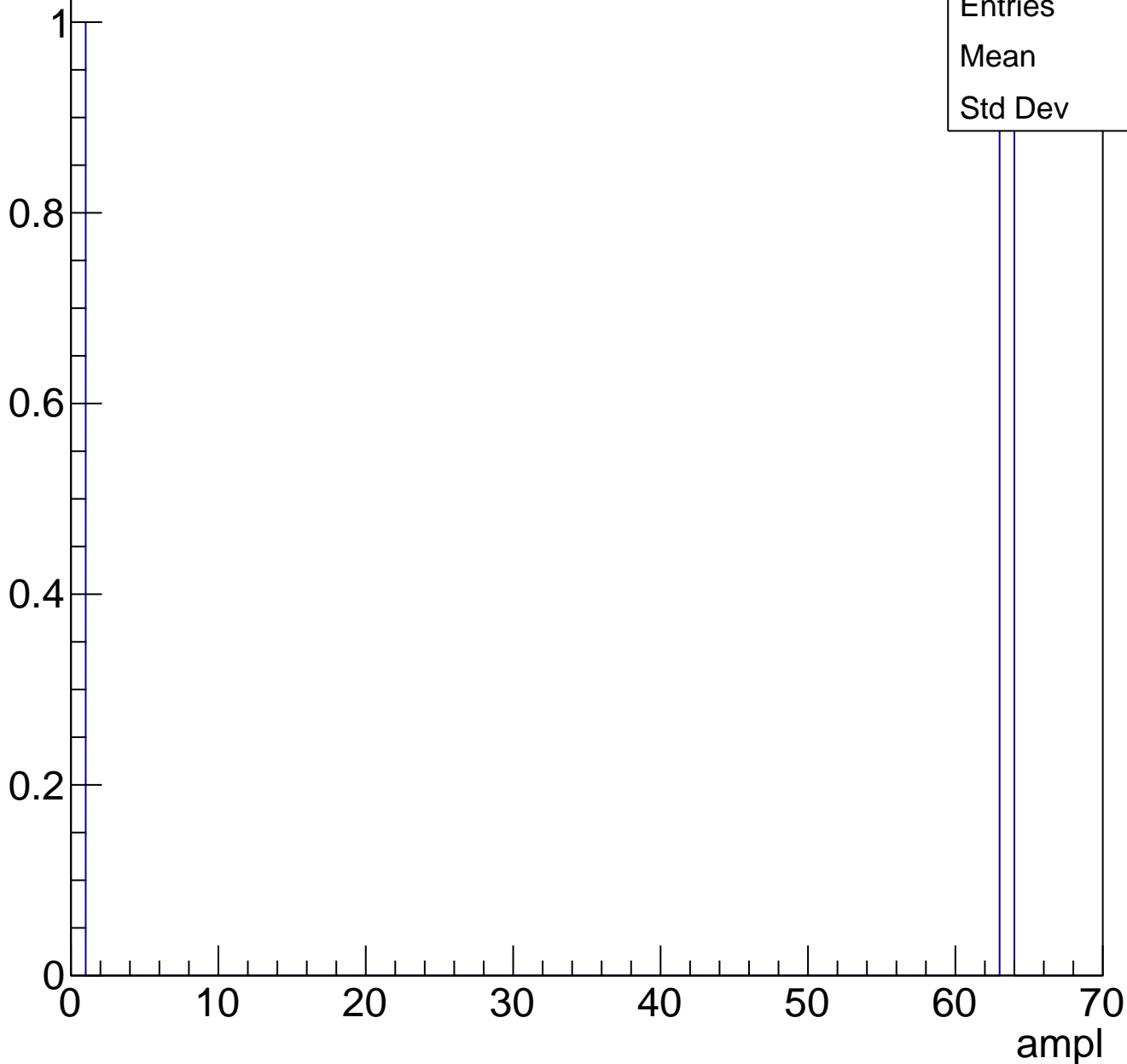
Entries	27
Mean	58.93
Std Dev	11.65



B1L001S, U16-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



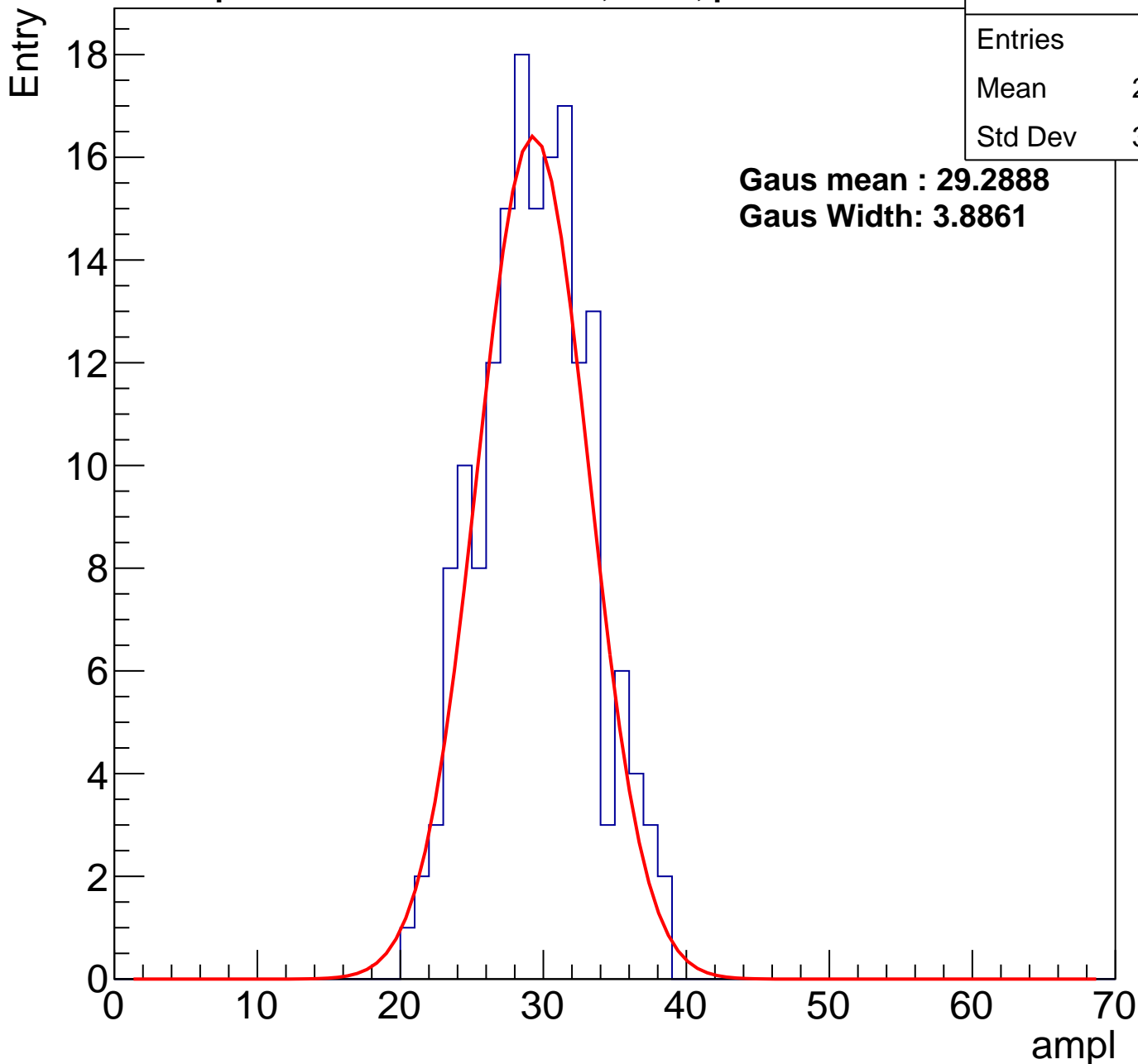
B1L001S, U16-ch65, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	28.99
Std Dev	3.818

Gaus mean : 29.2888

Gaus Width: 3.8861



B1L001S, U16-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	36.63
Std Dev	3.537

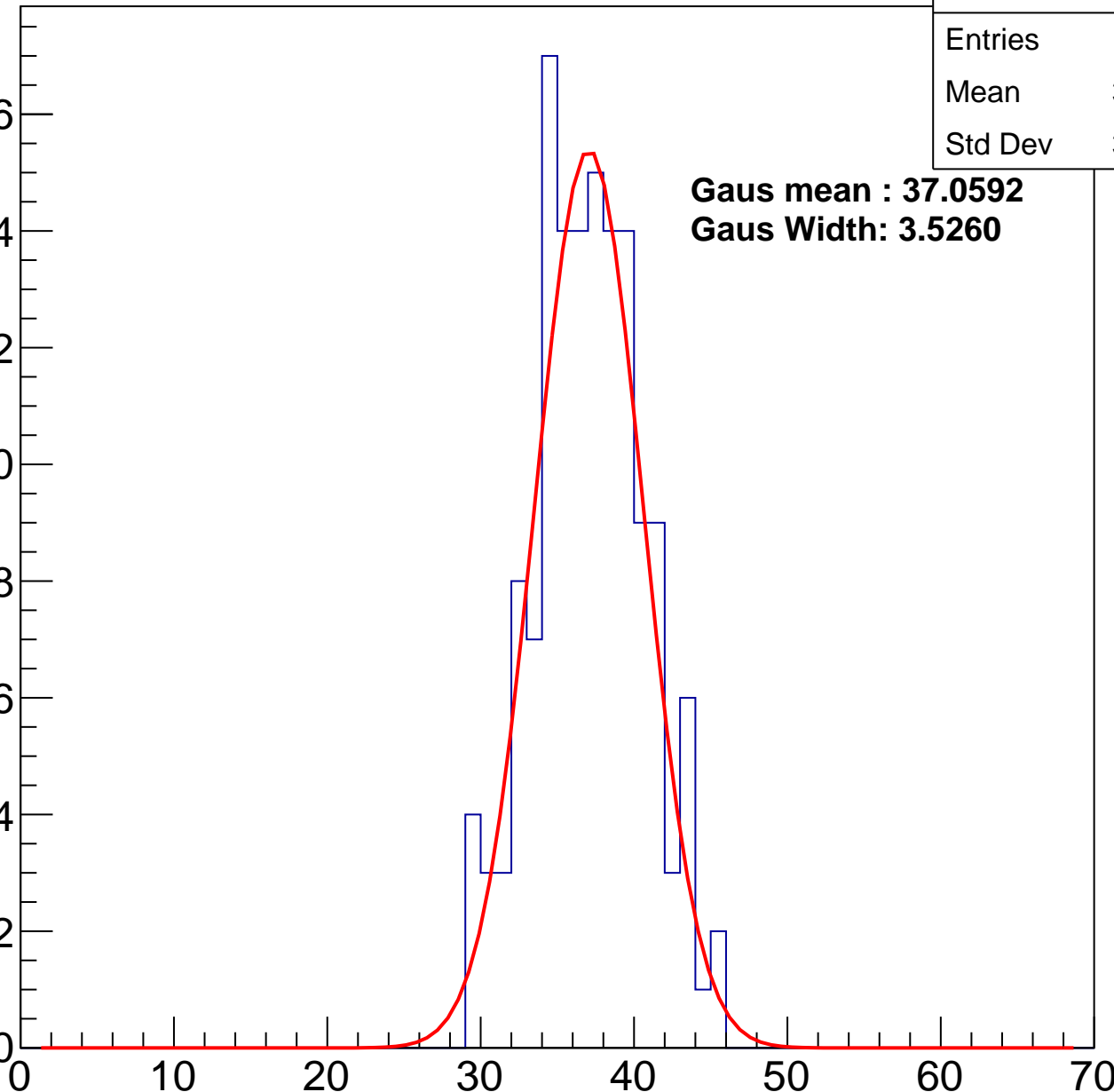
Gaus mean : 37.0592

Gaus Width: 3.5260

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch65, adc2

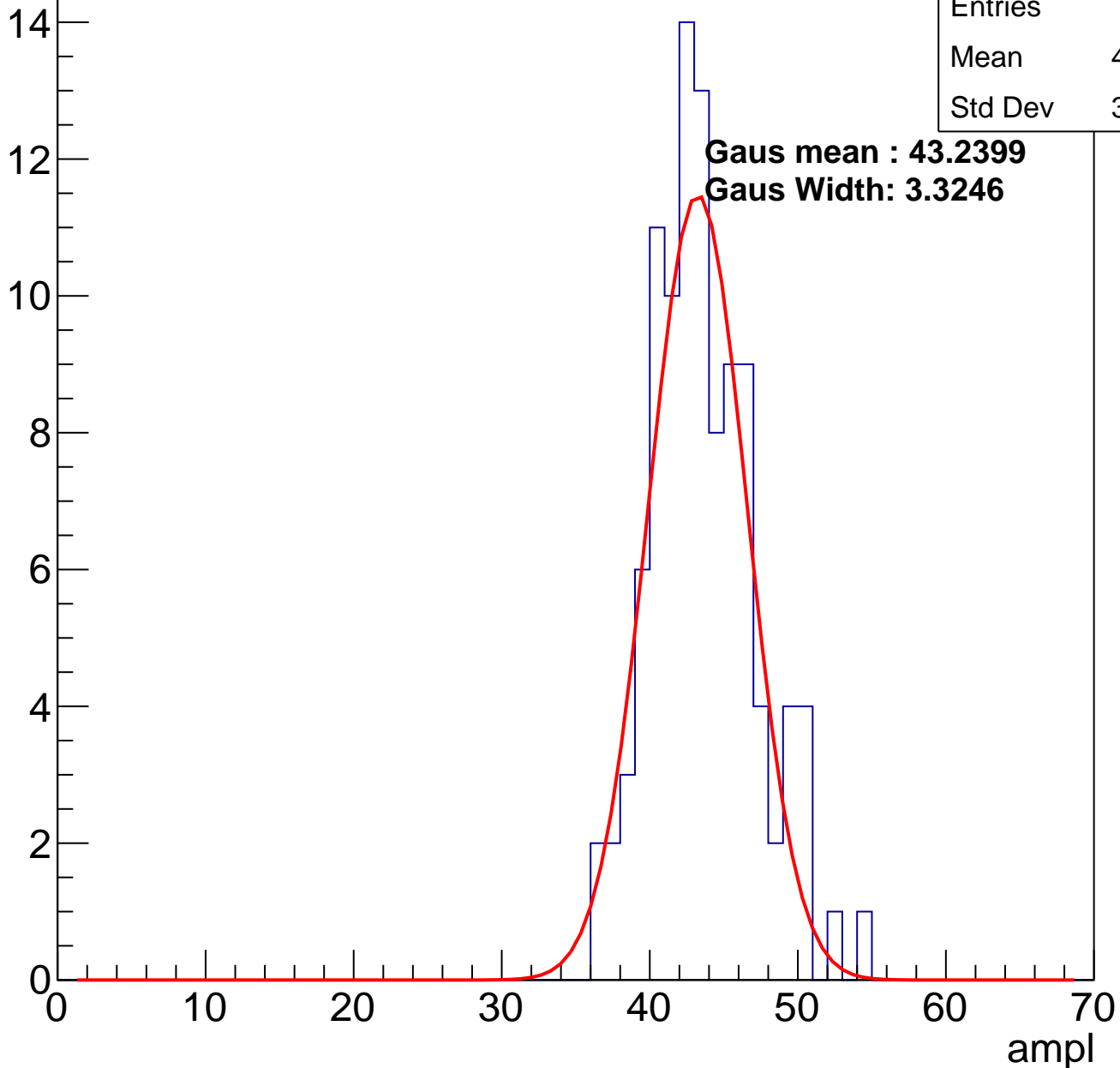
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	103
Mean	43.18
Std Dev	3.513

Gaus mean : 43.2399

Gaus Width: 3.3246



B1L001S, U16-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

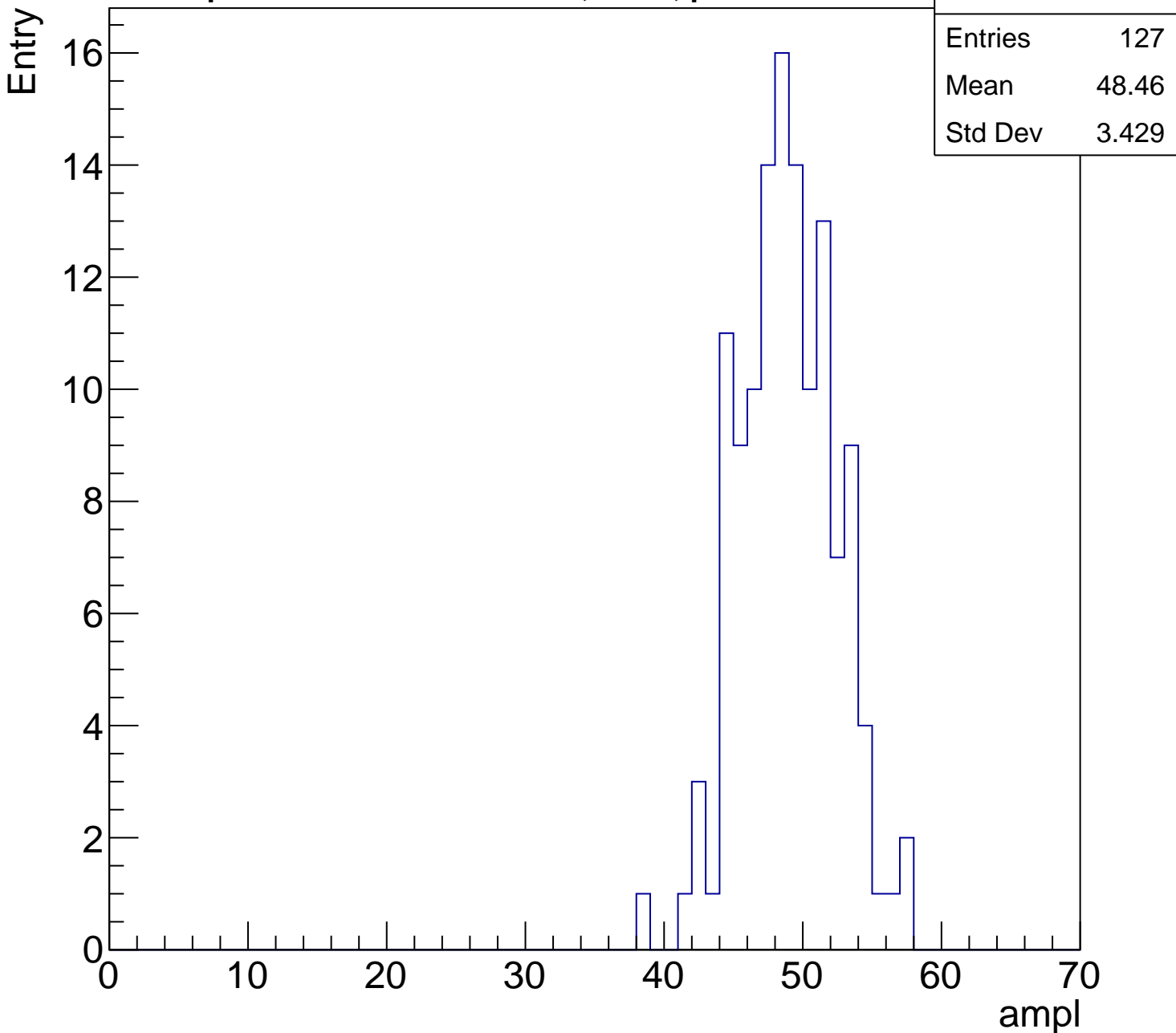
Entries	127
Mean	48.46
Std Dev	3.429

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

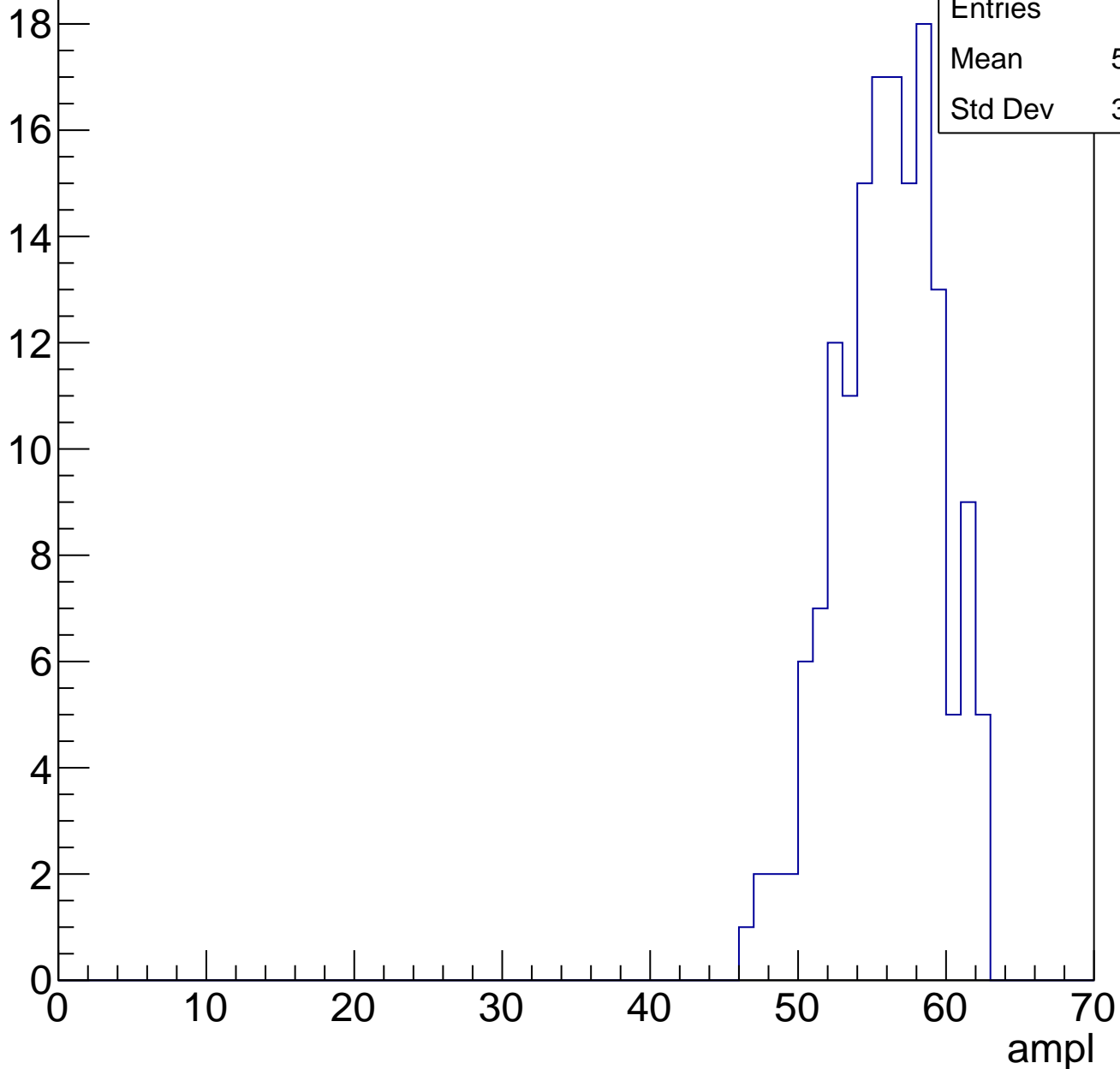


B1L001S, U16-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	55.54
Std Dev	3.482

Entry



B1L001S, U16-ch65, adc5

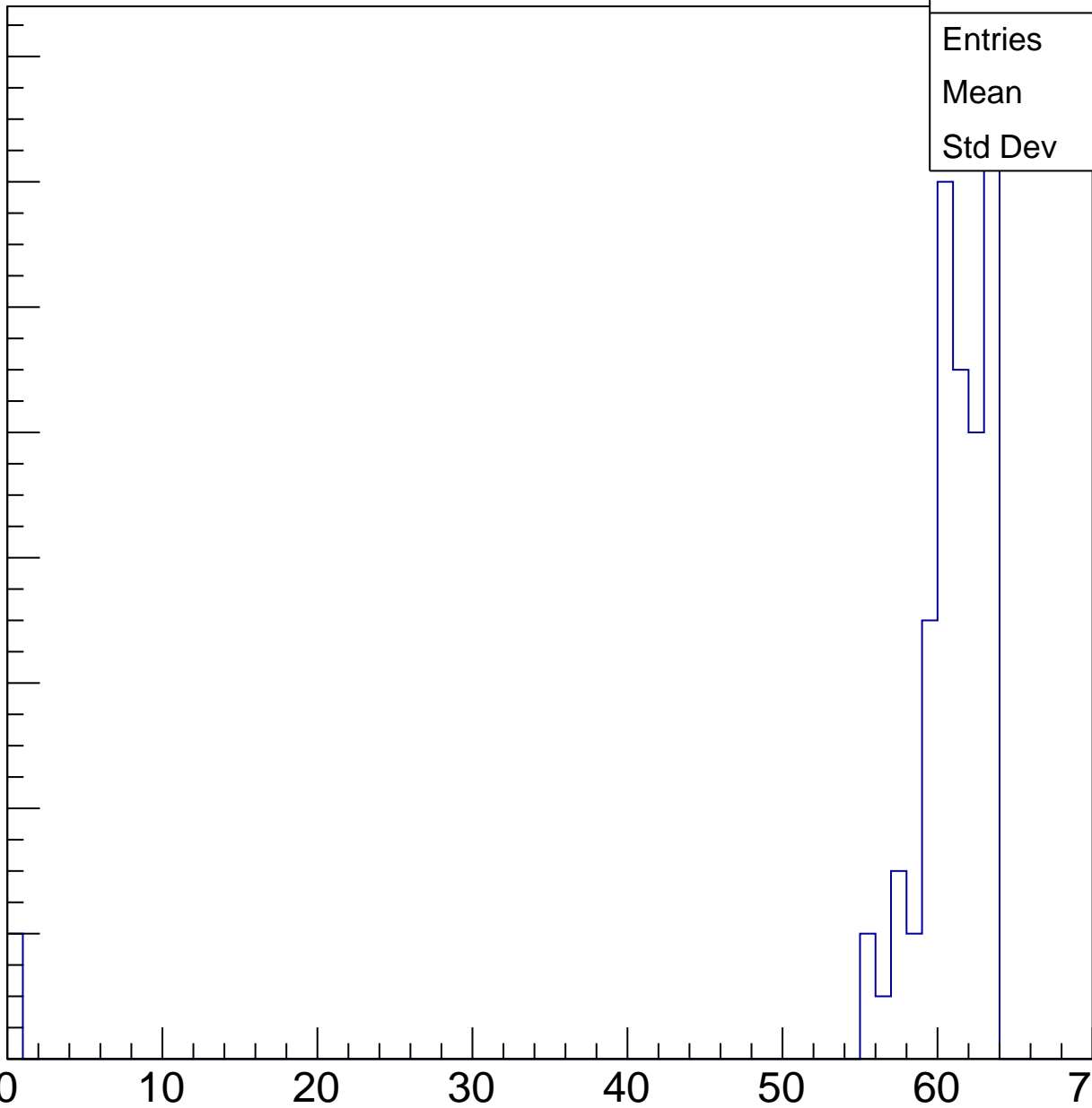
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	58.9
Std Dev	10.45

ampl

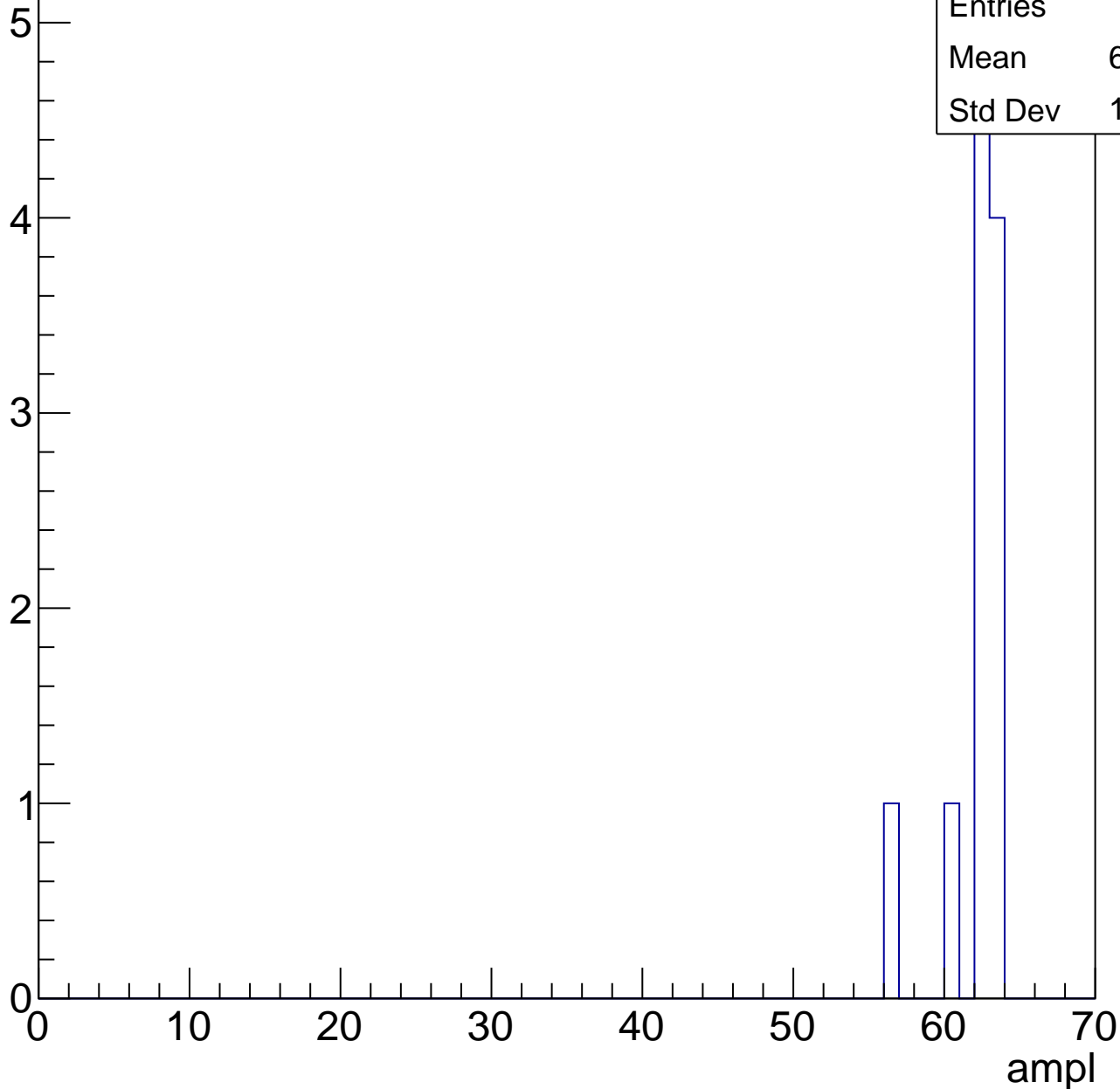


B1L001S, U16-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	61.64
Std Dev	1.967



B1L001S, U16-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U16-ch66, adc0

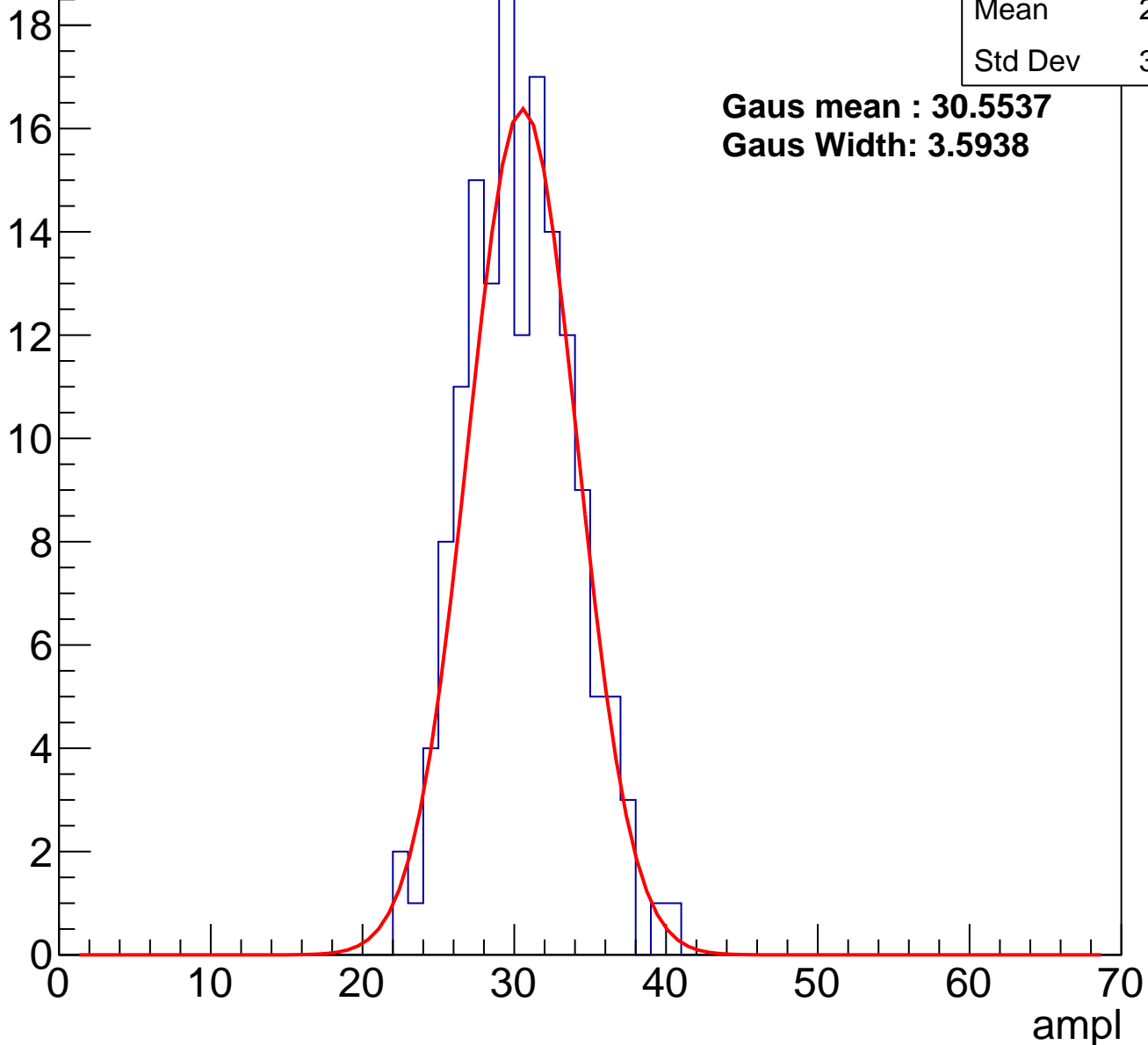
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.94
Std Dev	3.499

Gaus mean : 30.5537

Gaus Width: 3.5938

Entry



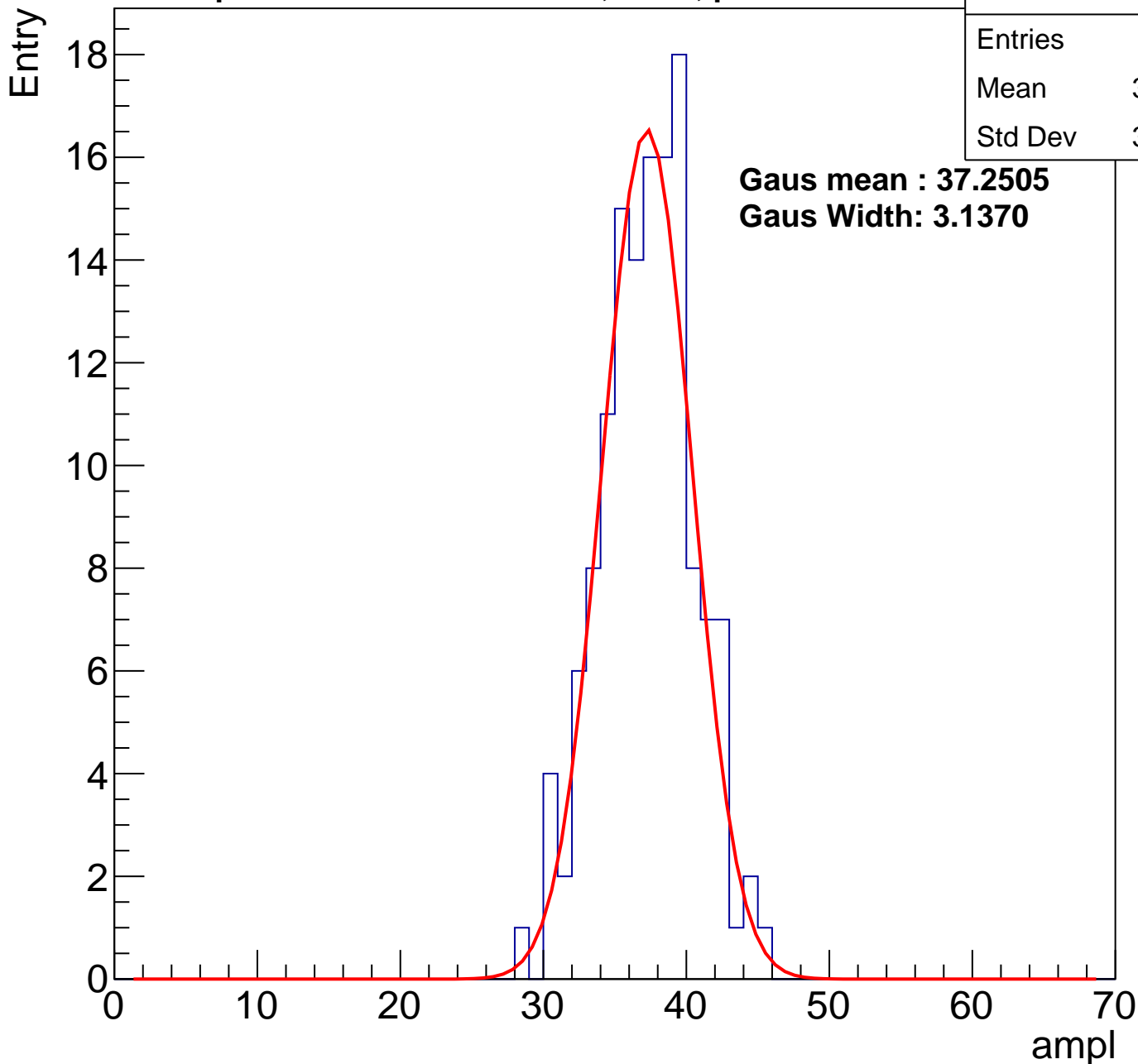
B1L001S, U16-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	36.85
Std Dev	3.244

Gaus mean : 37.2505

Gaus Width: 3.1370



B1L001S, U16-ch66, adc2

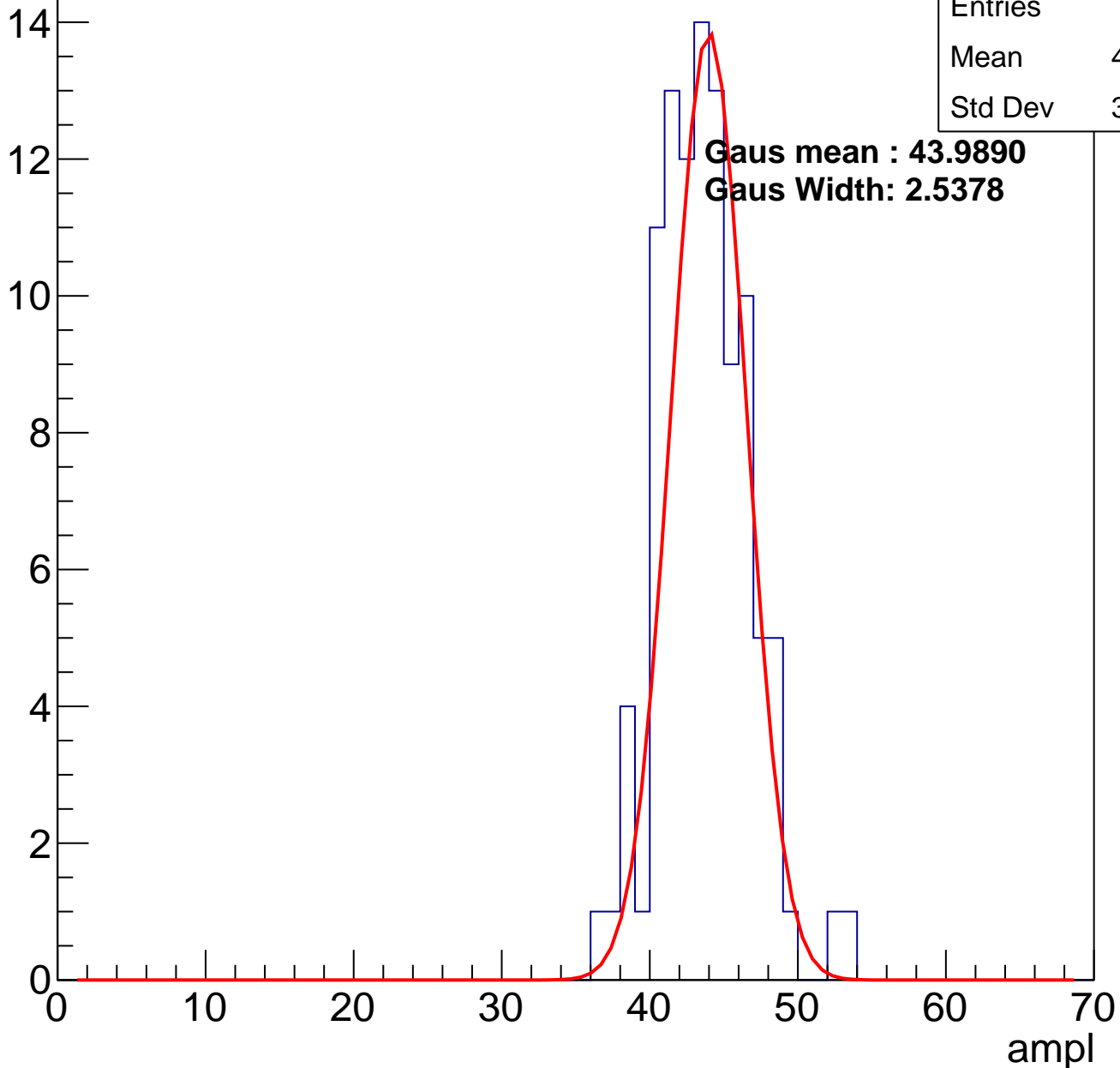
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	43.23
Std Dev	3.003

Gaus mean : 43.9890

Gaus Width: 2.5378

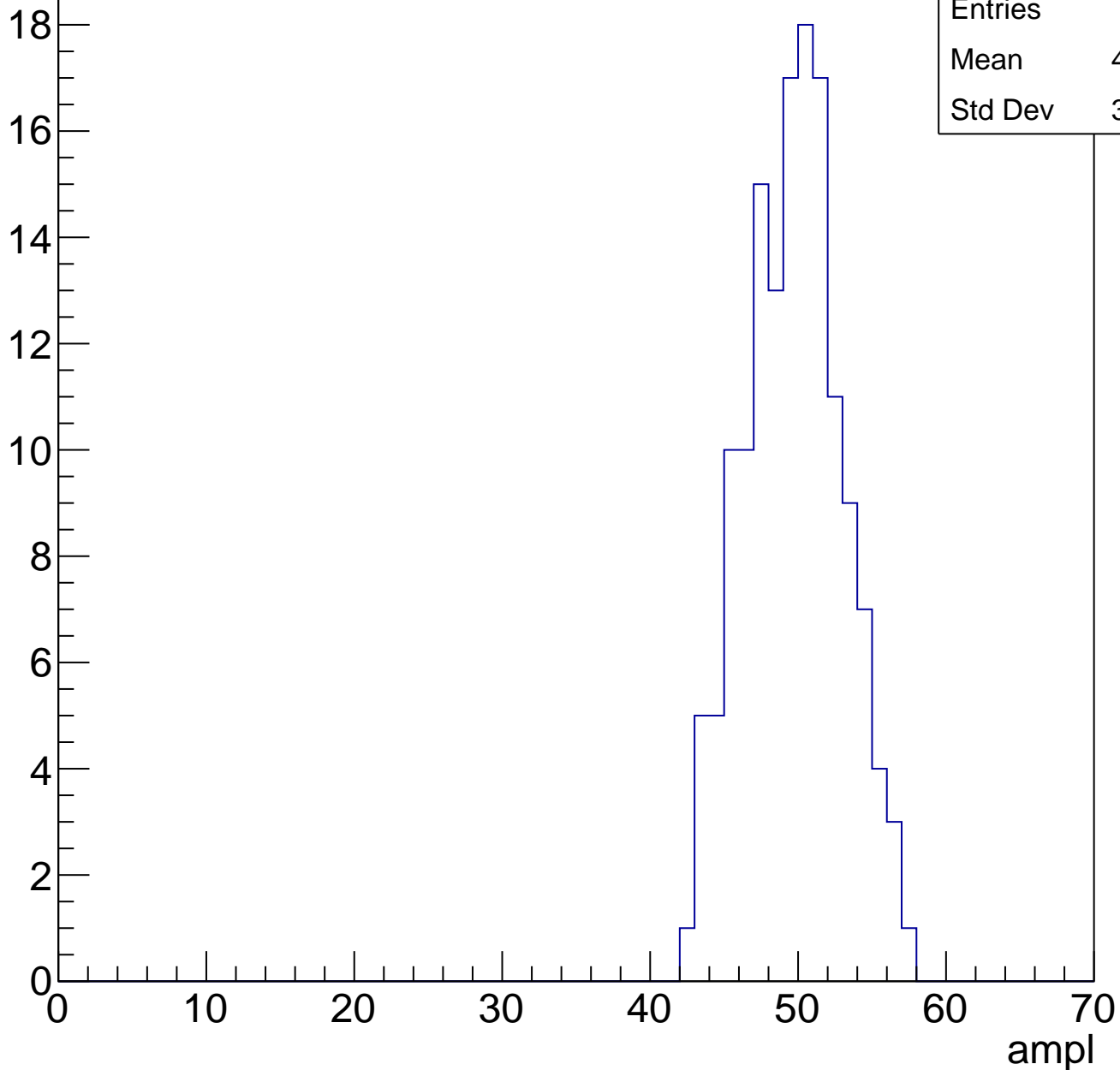
Entry



B1L001S, U16-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

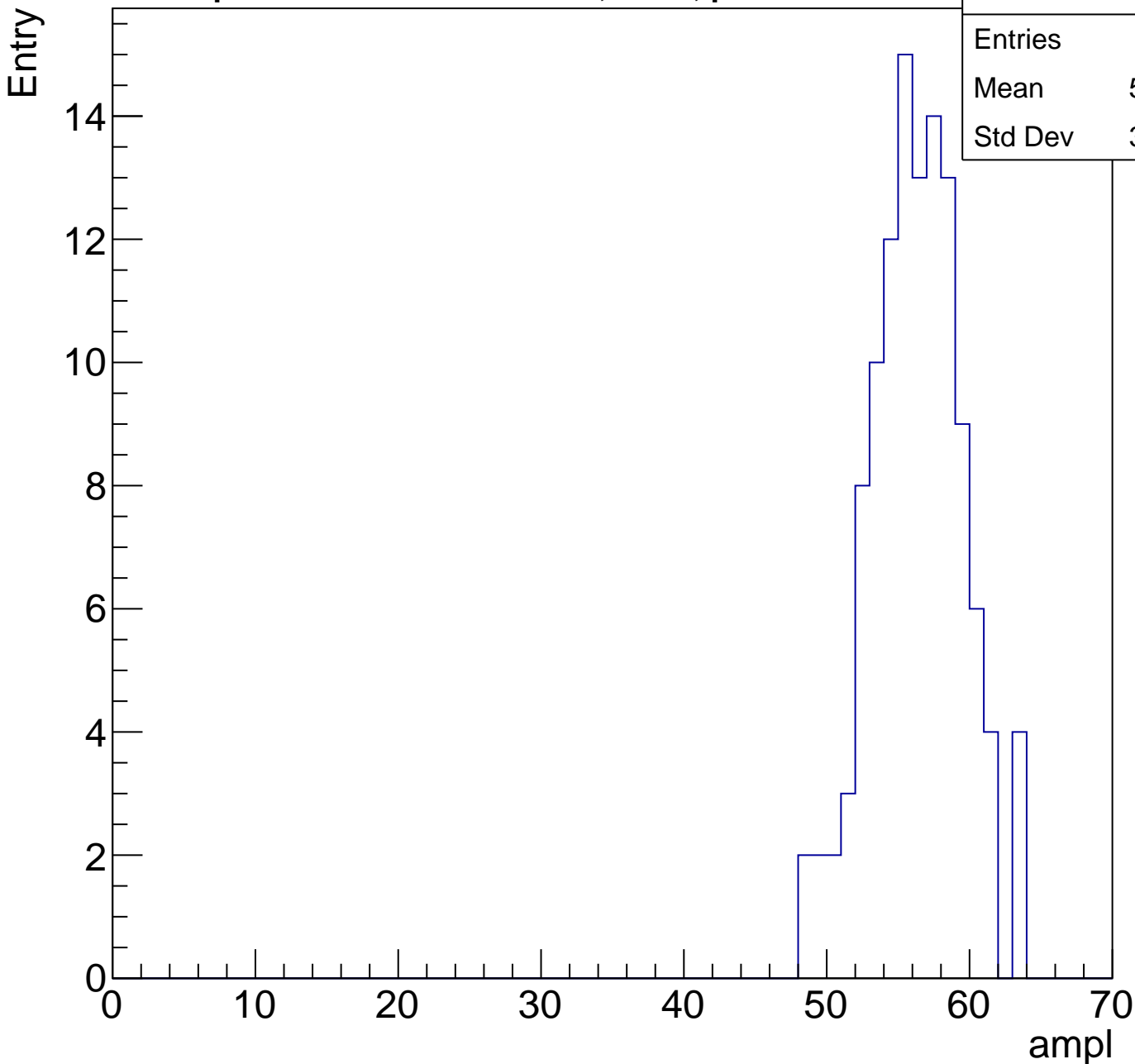
Entry



B1L001S, U16-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	55.84
Std Dev	3.189



B1L001S, U16-ch66, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

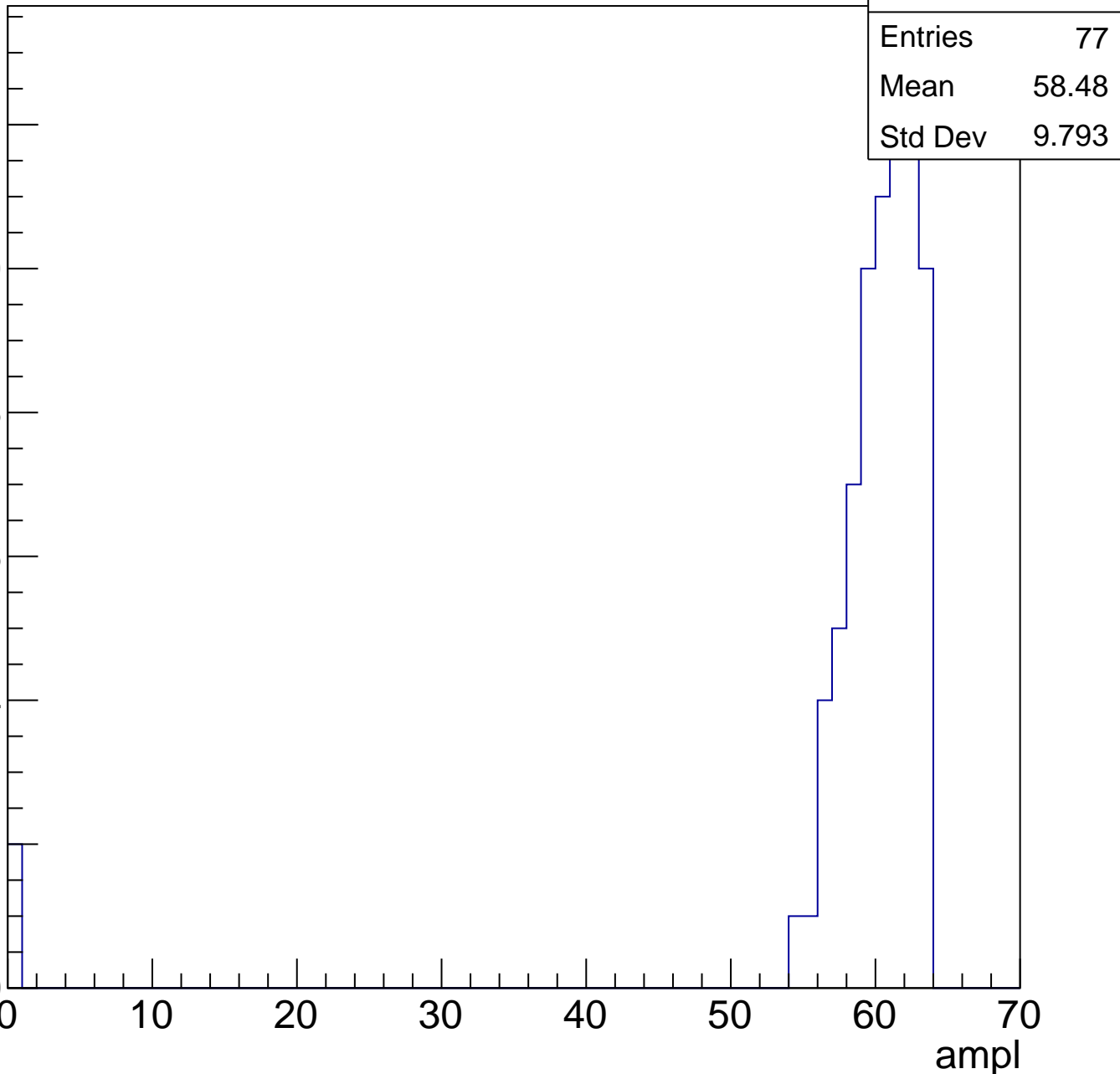
40

50

60

ampl

Entries	77
Mean	58.48
Std Dev	9.793

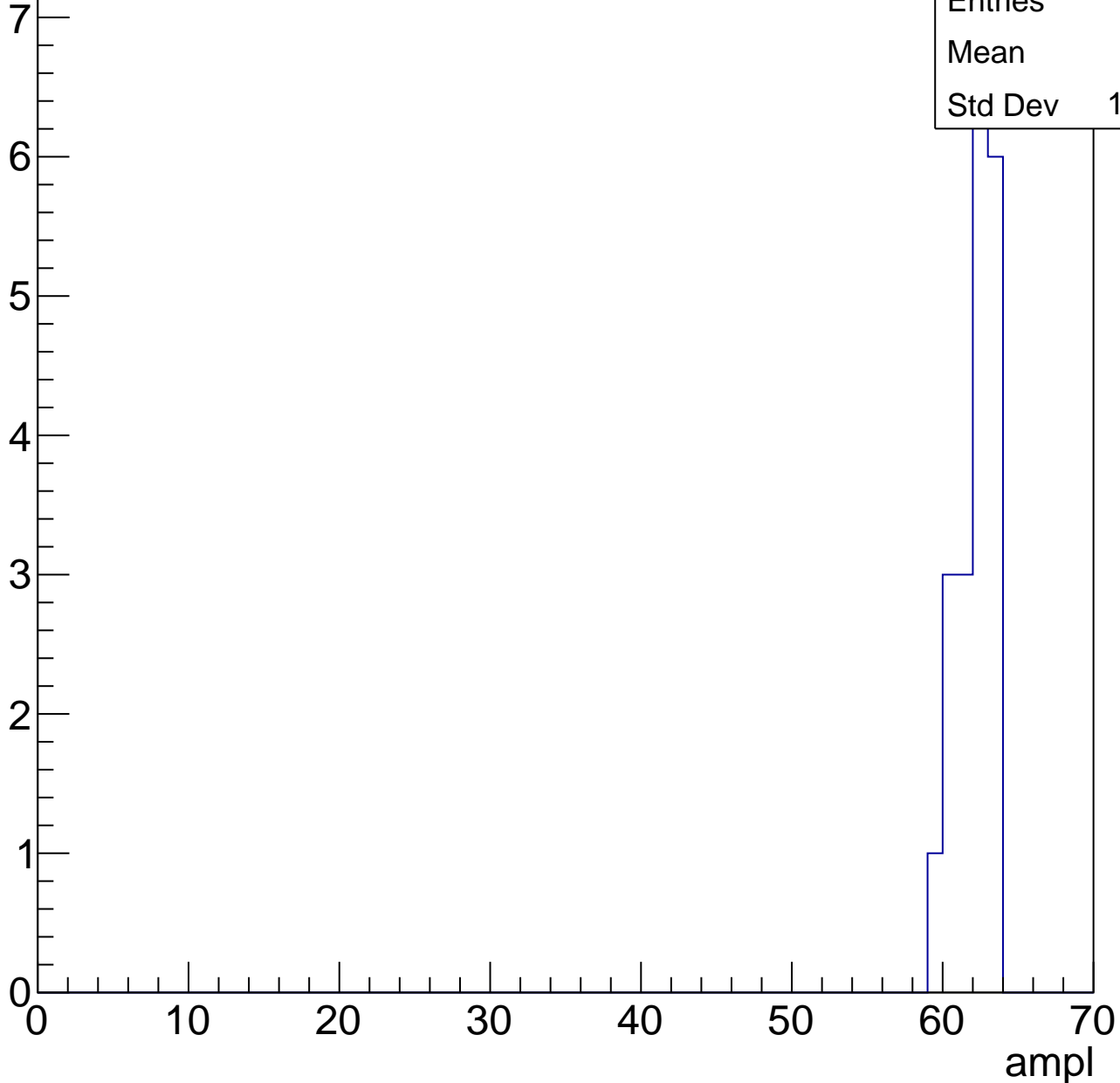


B1L001S, U16-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	61.7
Std Dev	1.187



B1L001S, U16-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U16-ch67, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.7
Std Dev	4.149

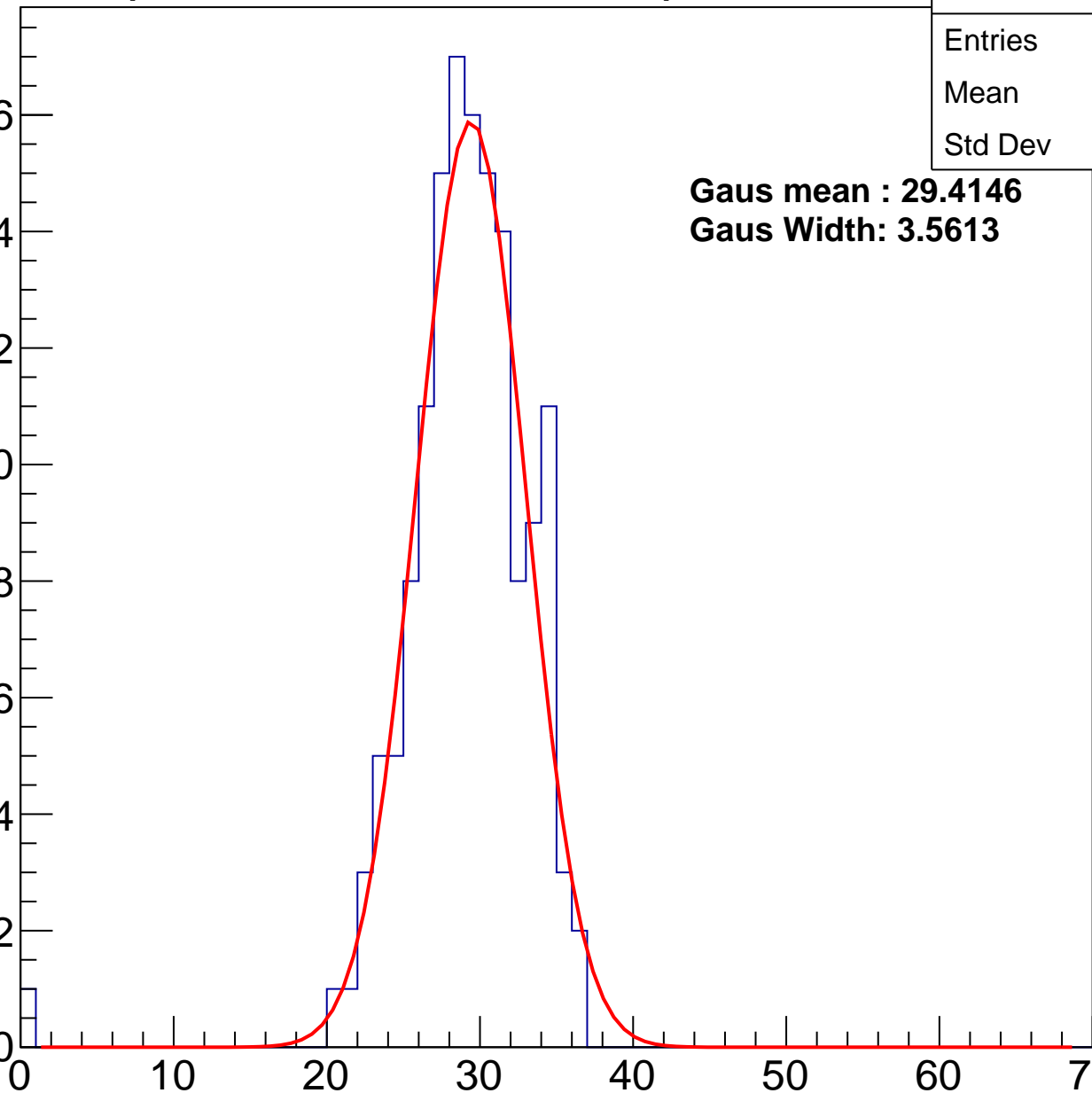
Gaus mean : 29.4146

Gaus Width: 3.5613

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch67, adc1

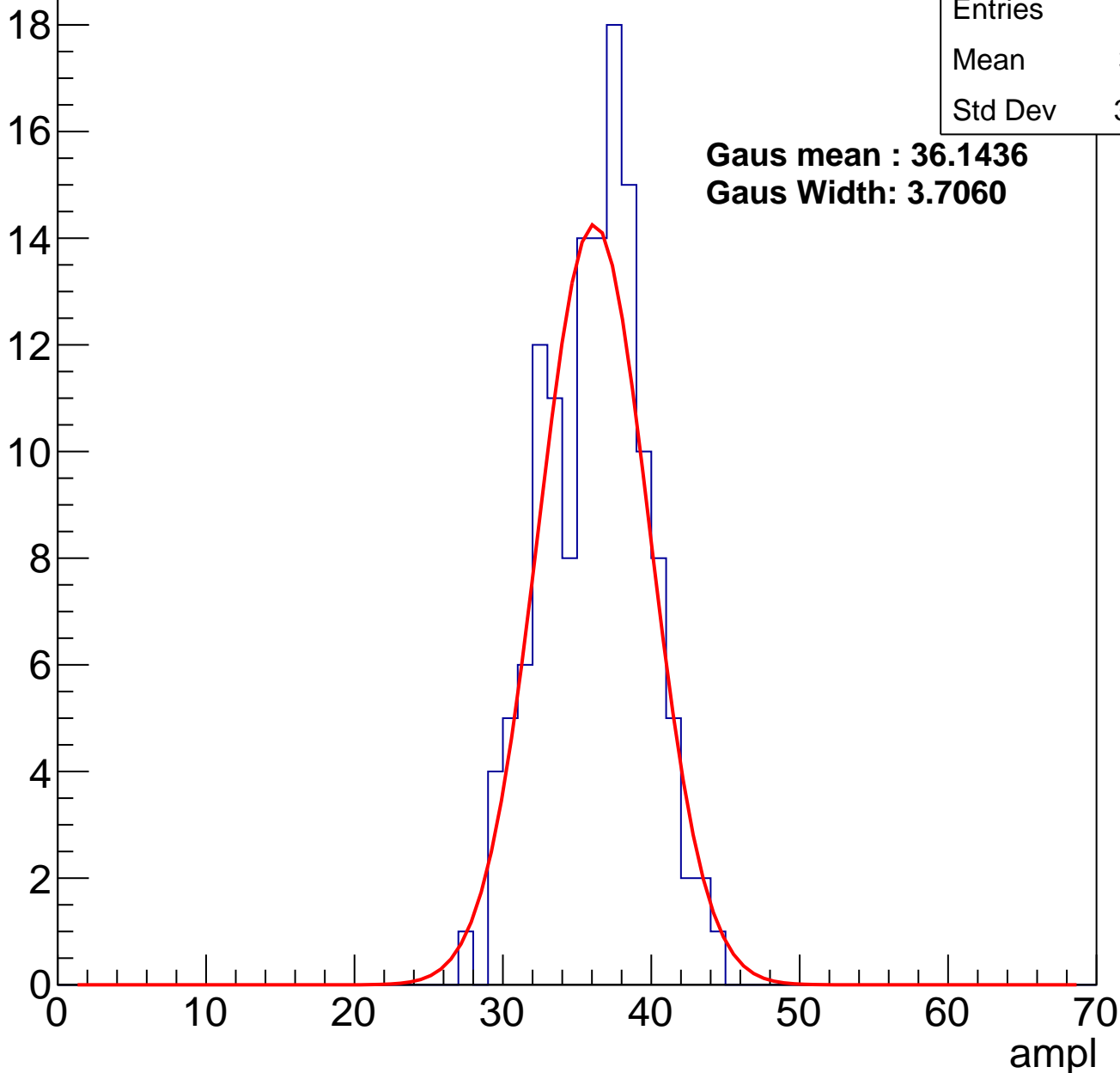
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	35.71
Std Dev	3.417

Gaus mean : 36.1436

Gaus Width: 3.7060

Entry



B1L001S, U16-ch67, adc2

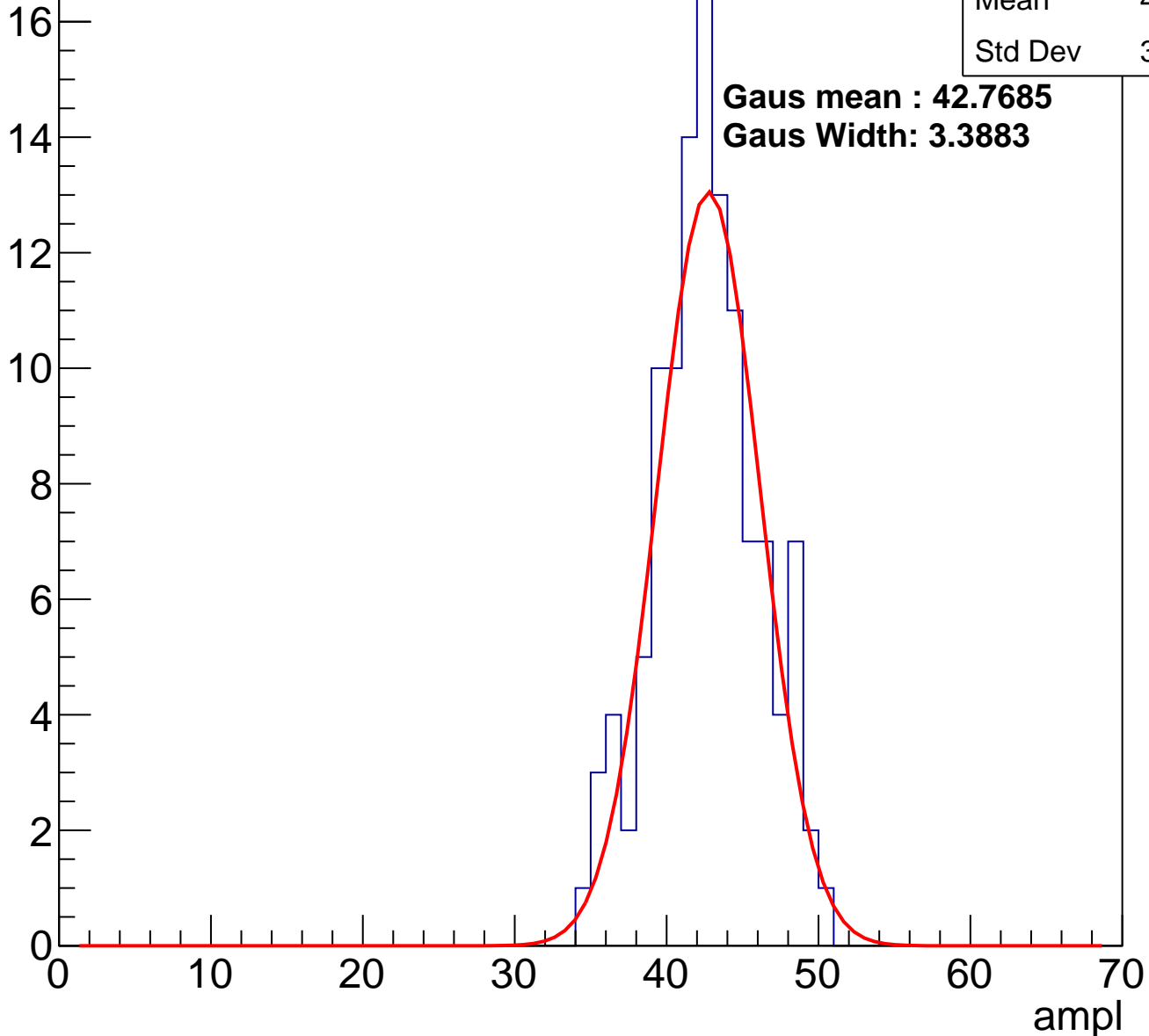
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.18
Std Dev	3.409

Gaus mean : 42.7685

Gaus Width: 3.3883

Entry



B1L001S, U16-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

125

Mean

47.92

Std Dev

3.191

ampl

0

10

20

30

40

50

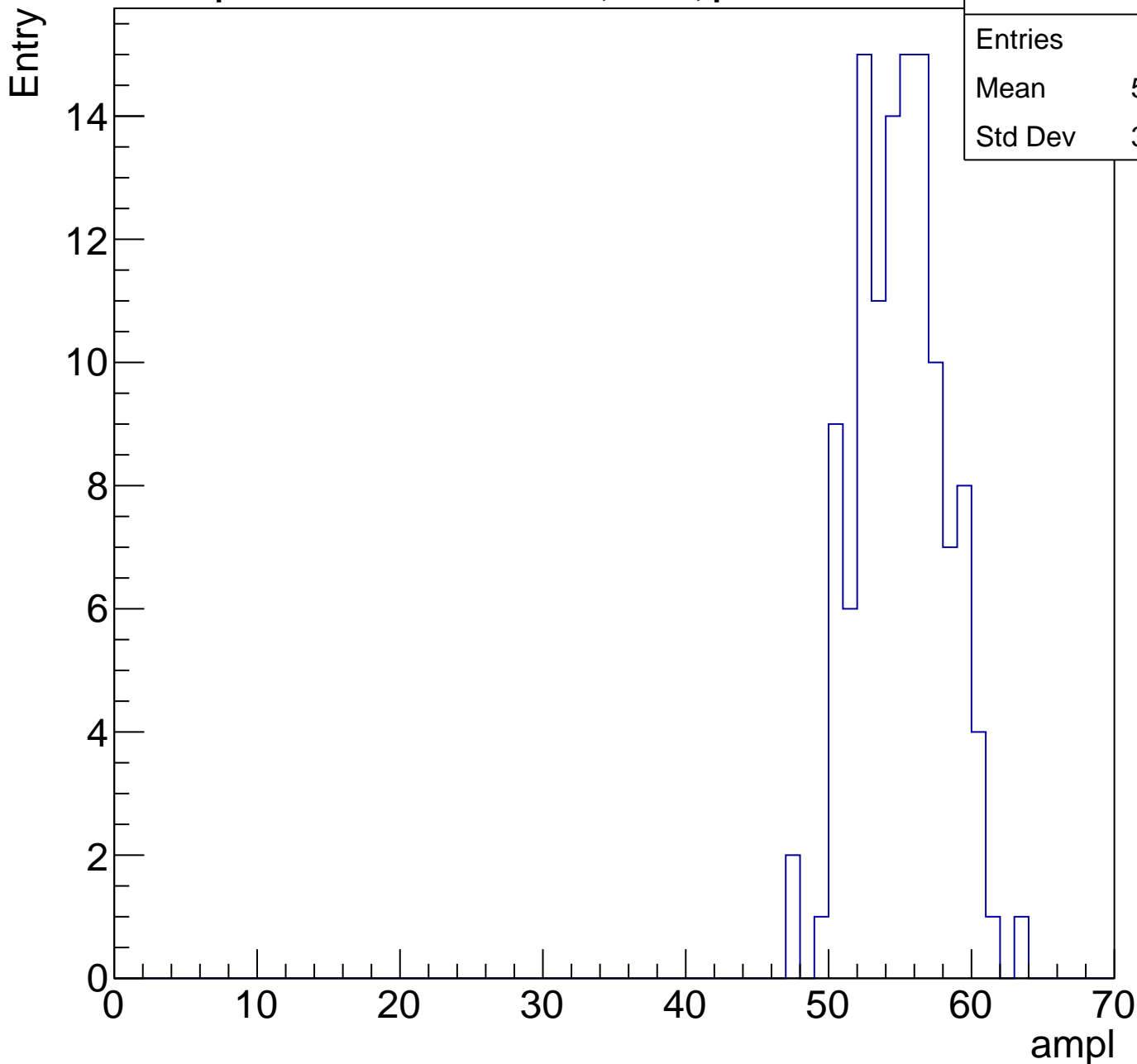
60

70

B1L001S, U16-ch67, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.58
Std Dev	3.044



B1L001S, U16-ch67, adc5

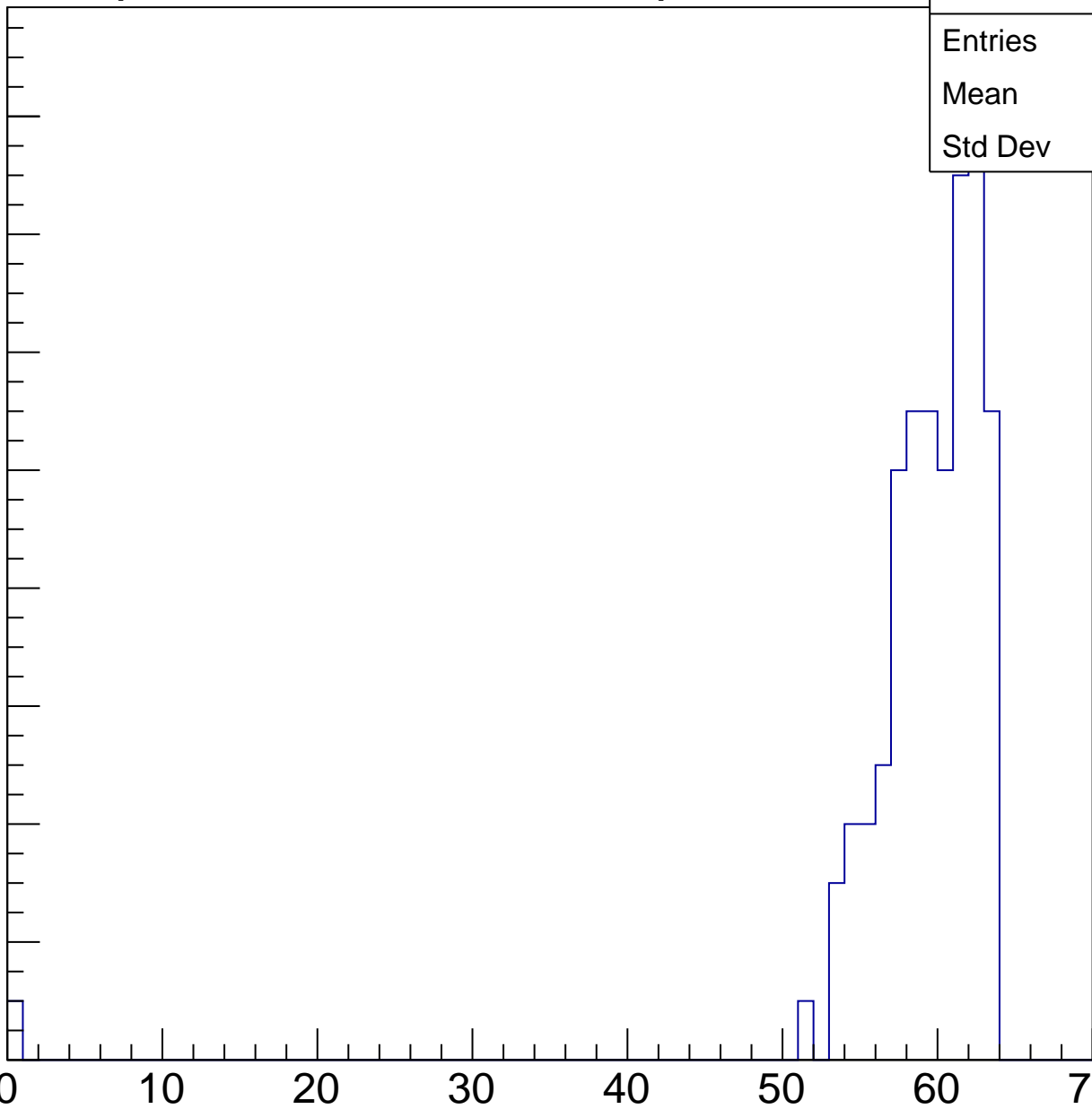
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.69
Std Dev	6.463

ampl

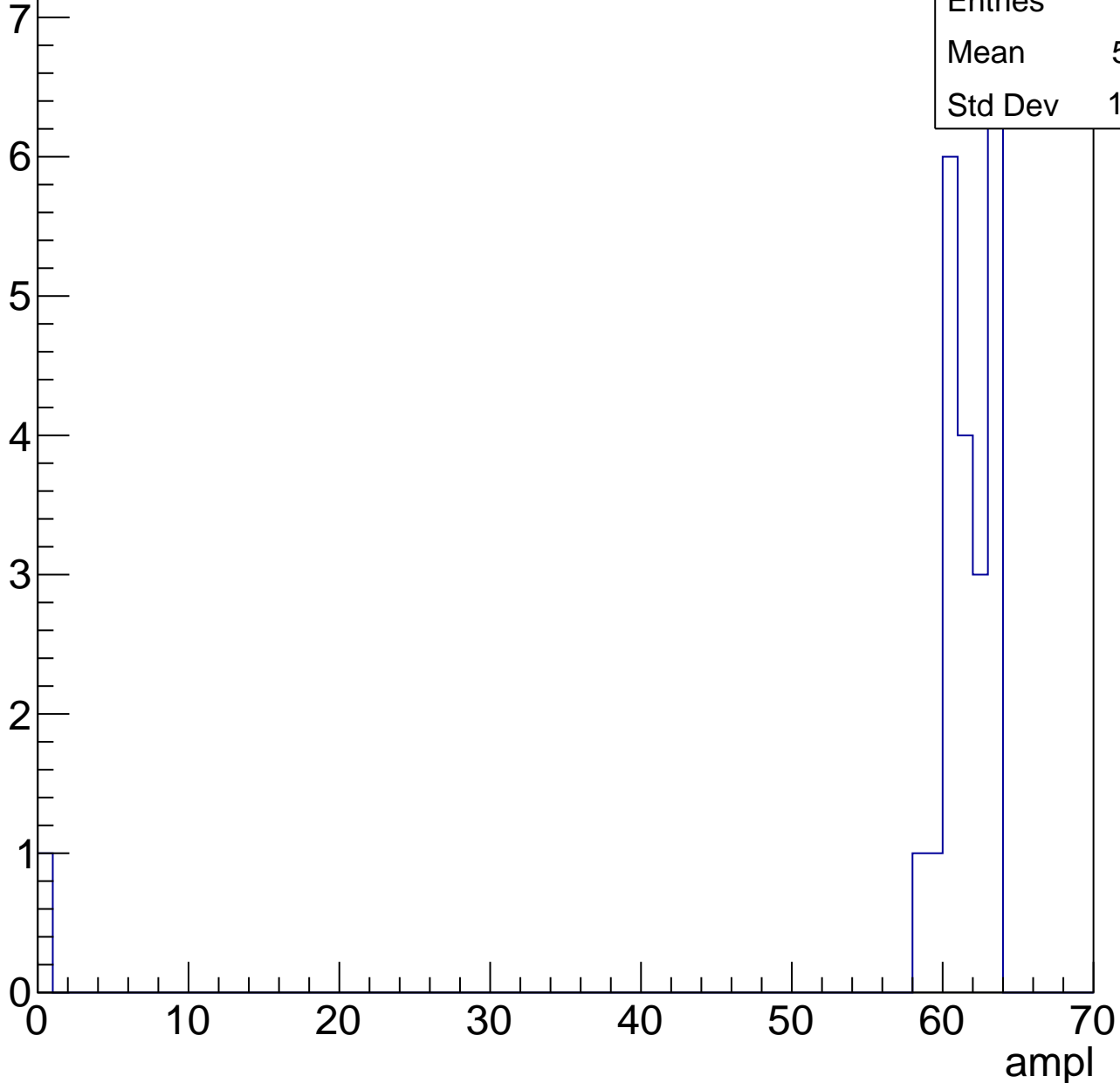


B1L001S, U16-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

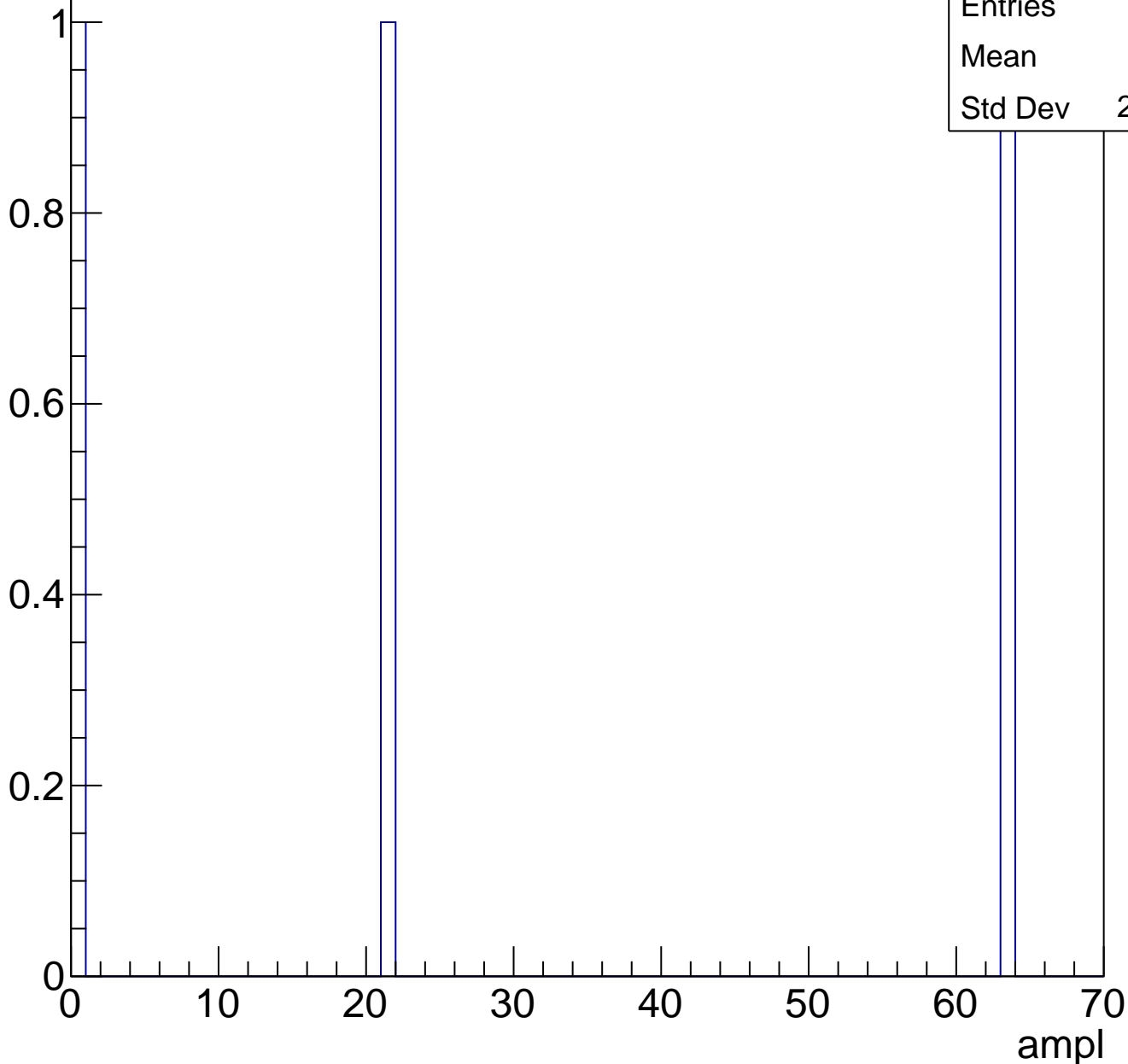
Entries	23
Mean	58.61
Std Dev	12.58



B1L001S, U16-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L001S, U16-ch68, adc0

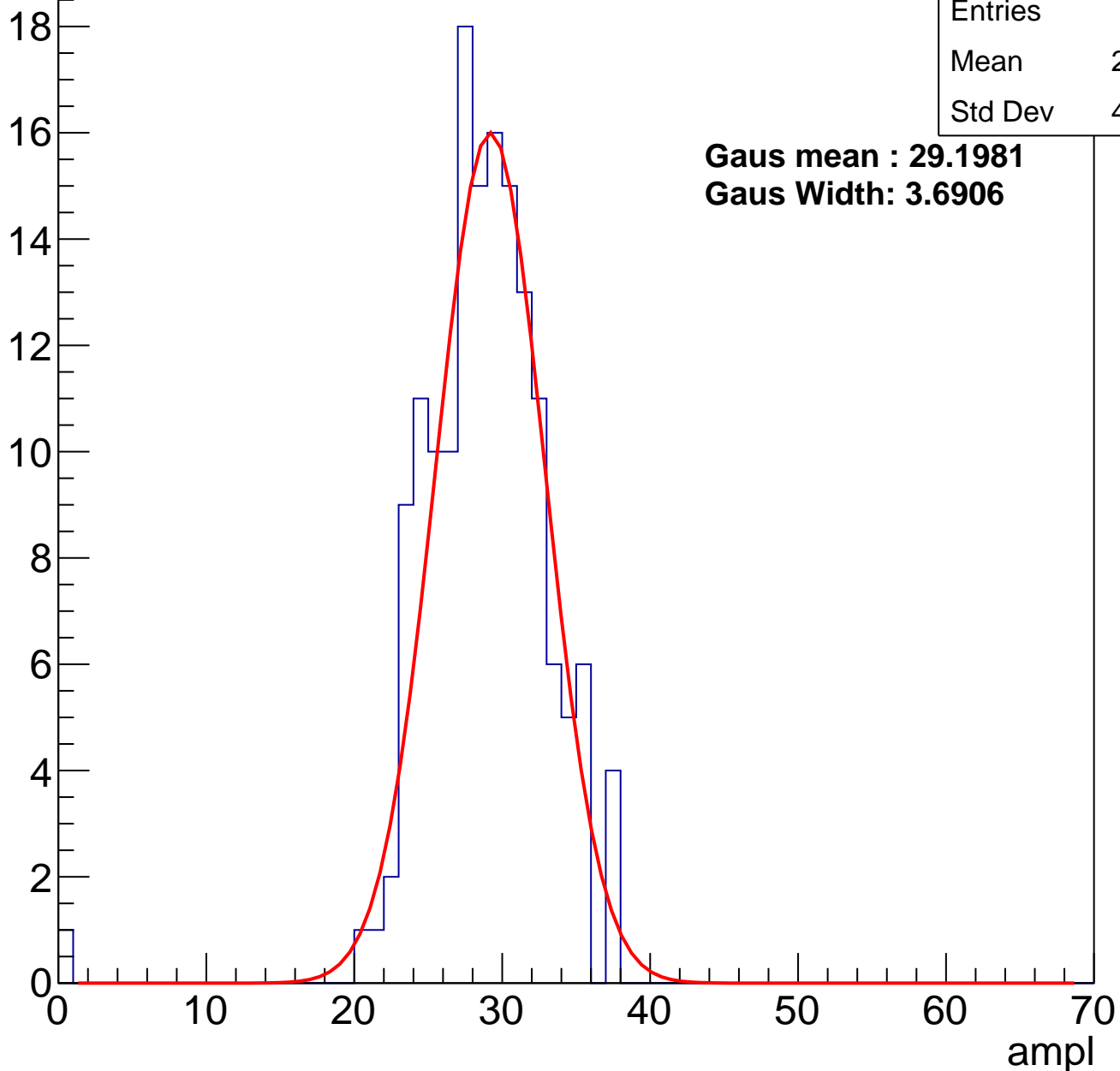
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	28.36
Std Dev	4.276

Gaus mean : 29.1981

Gaus Width: 3.6906

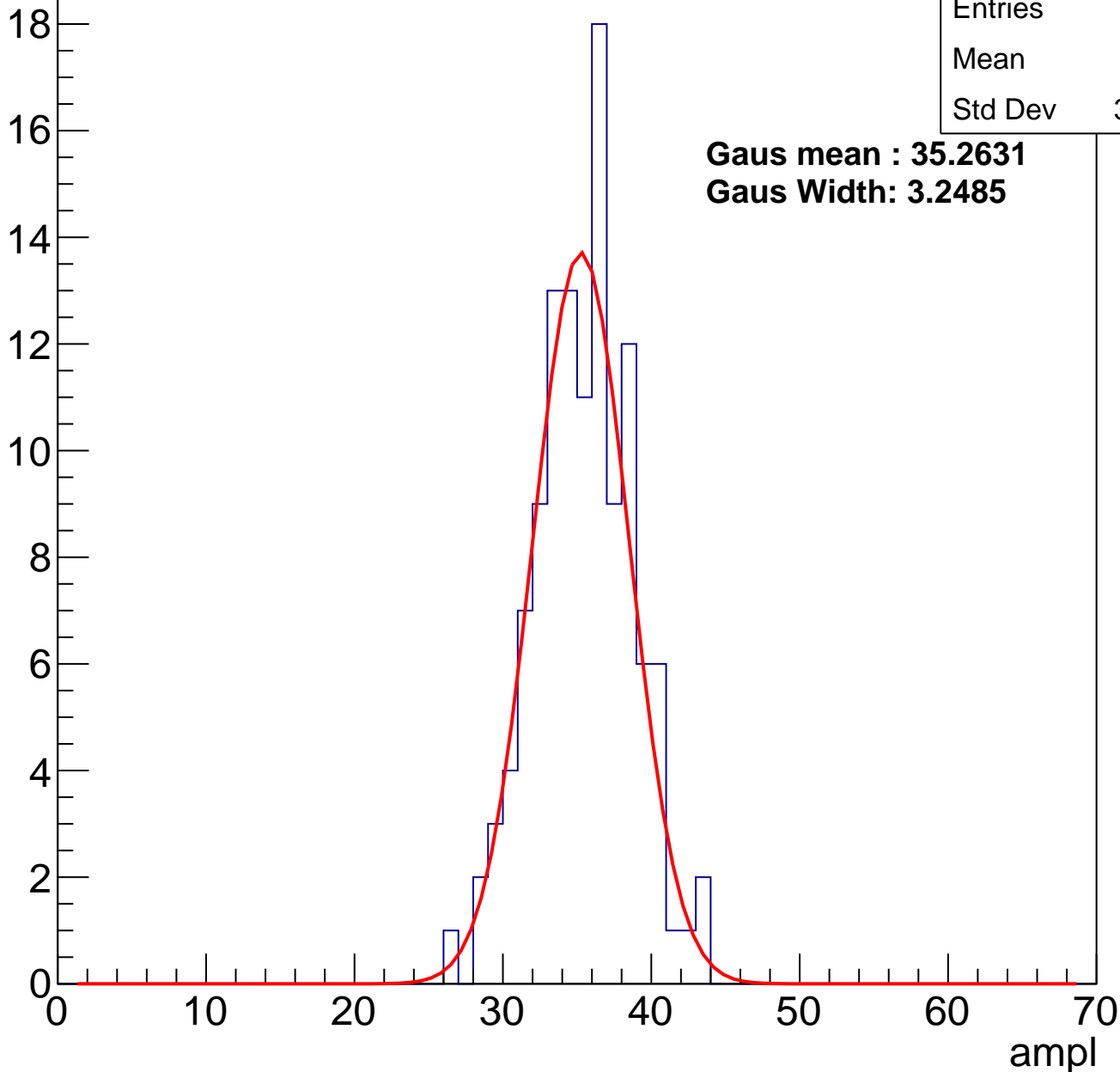
Entry



B1L001S, U16-ch68, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch68, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

112

Mean

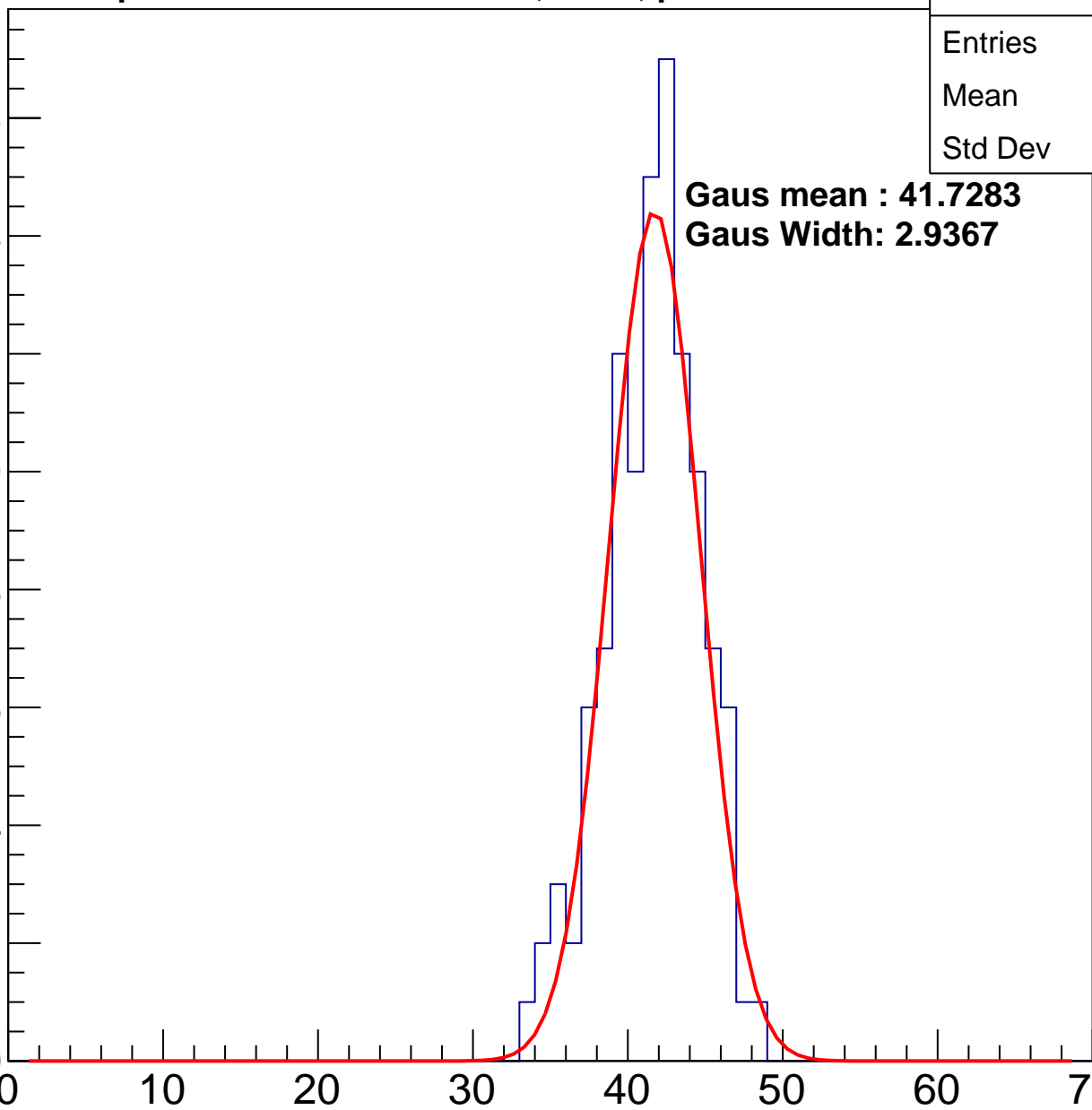
41.12

Std Dev

3.044

Gaus mean : 41.7283

Gaus Width: 2.9367



B1L001S, U16-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

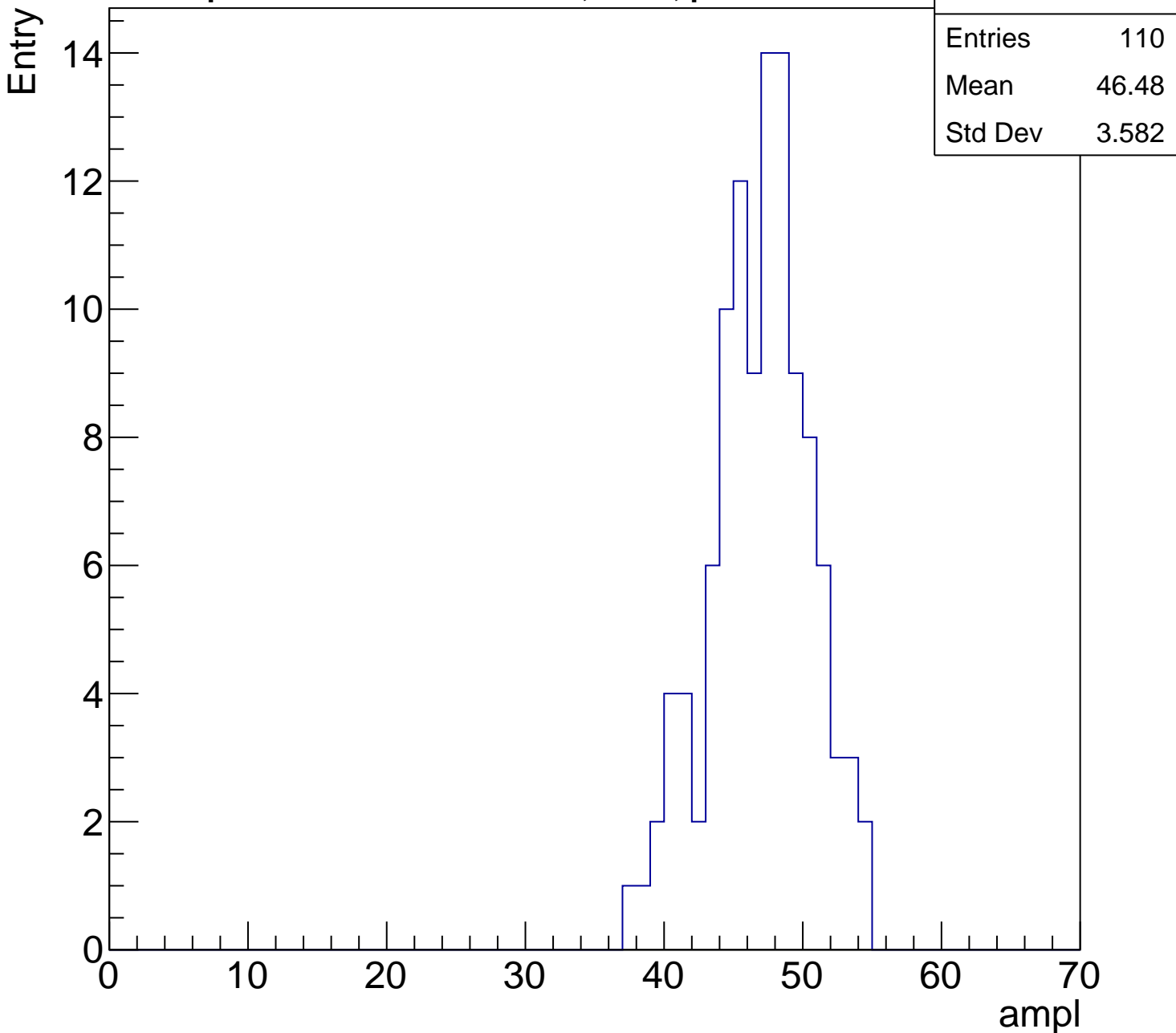
Entries	110
Mean	46.48
Std Dev	3.582

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

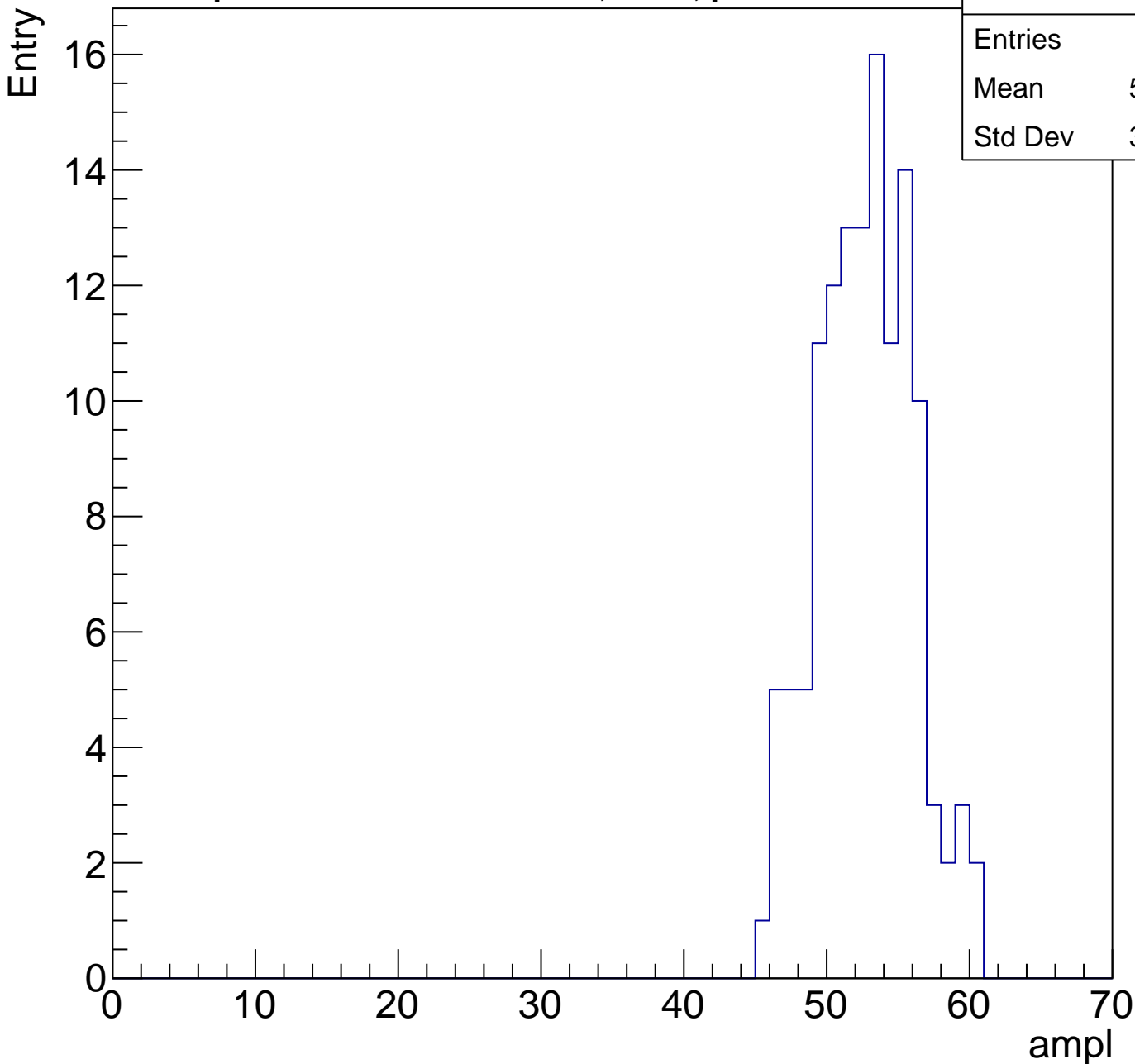
ampl



B1L001S, U16-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	52.25
Std Dev	3.278



B1L001S, U16-ch68, adc5

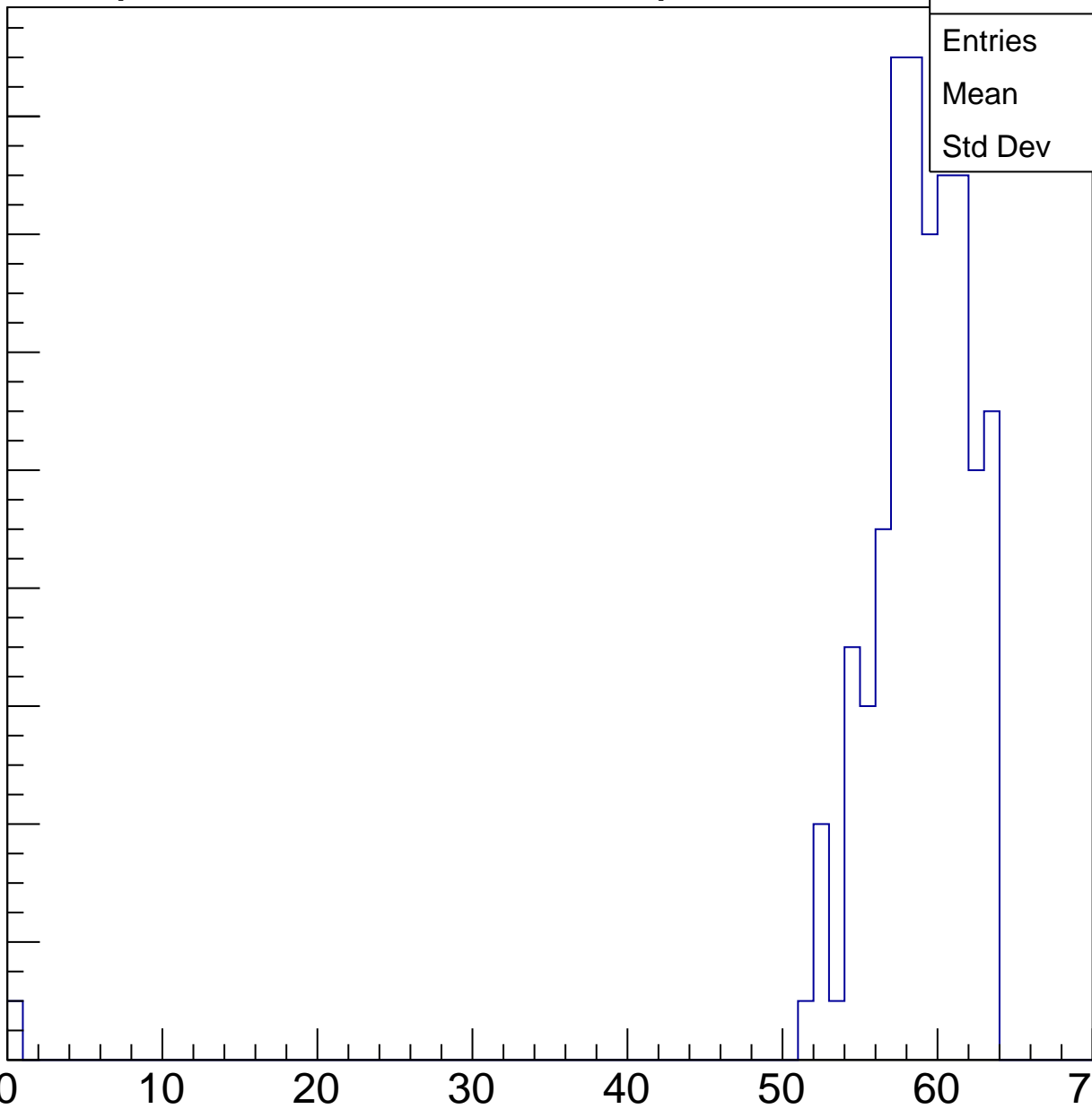
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	58.07
Std Dev	5.893

ampl

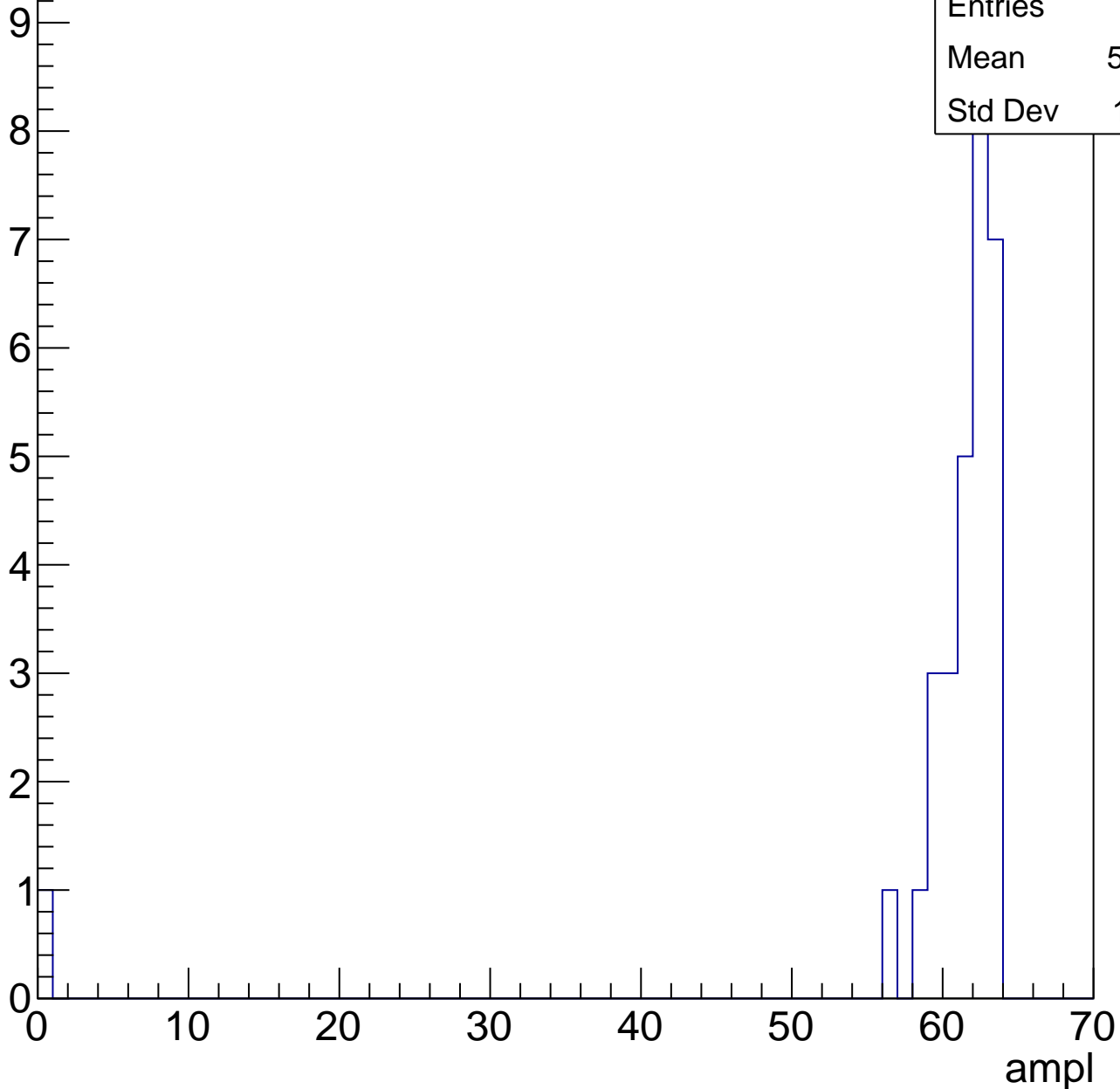


B1L001S, U16-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	30
Mean	59.17
Std Dev	11.11



B1L001S, U16-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch69, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	29.35
Std Dev	4.866

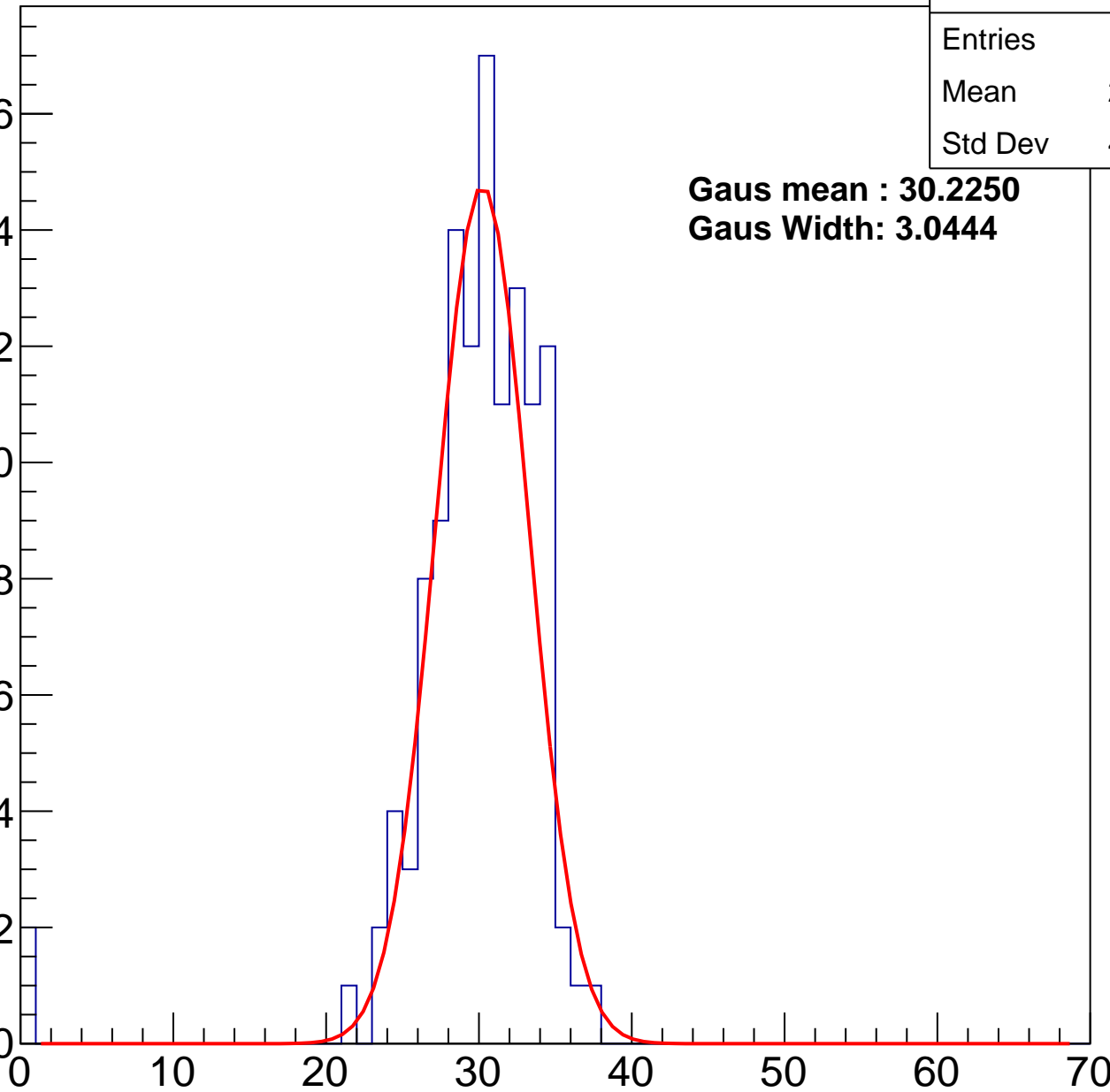
Gaus mean : 30.2250

Gaus Width: 3.0444

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch69, adc1

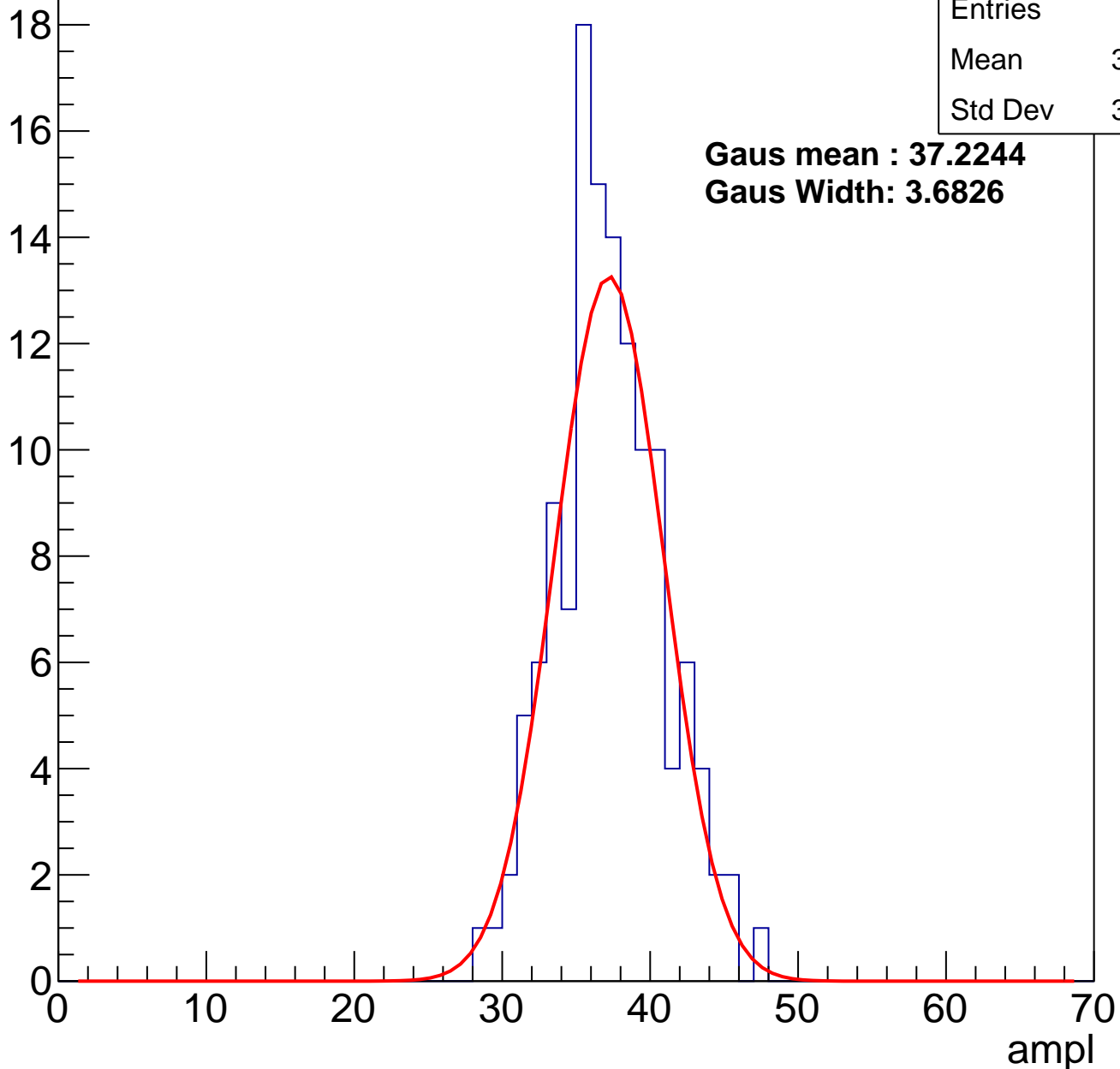
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.79
Std Dev	3.617

Gaus mean : 37.2244

Gaus Width: 3.6826

Entry



B1L001S, U16-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

137

Mean

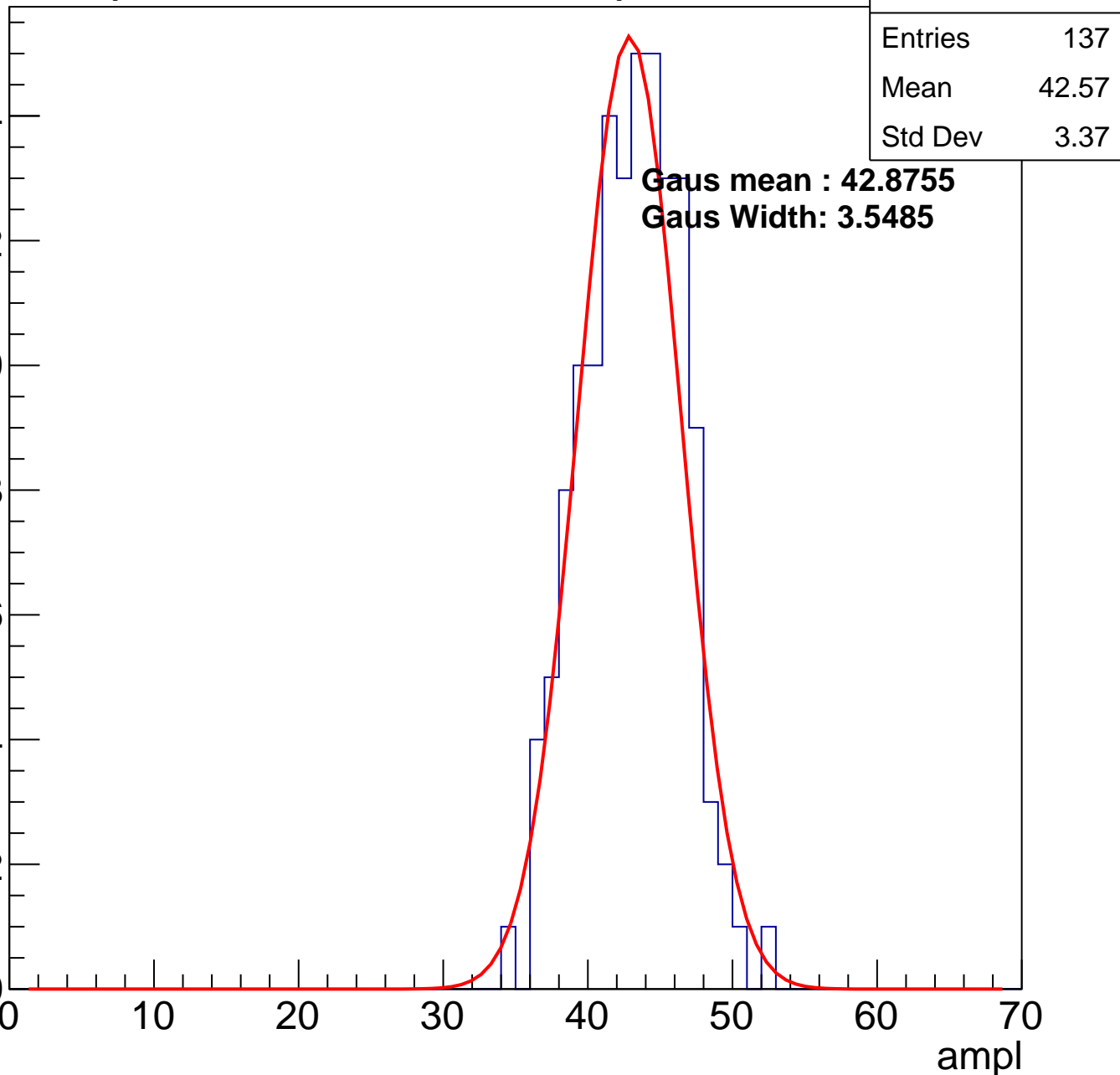
42.57

Std Dev

3.37

Gaus mean : 42.8755

Gaus Width: 3.5485

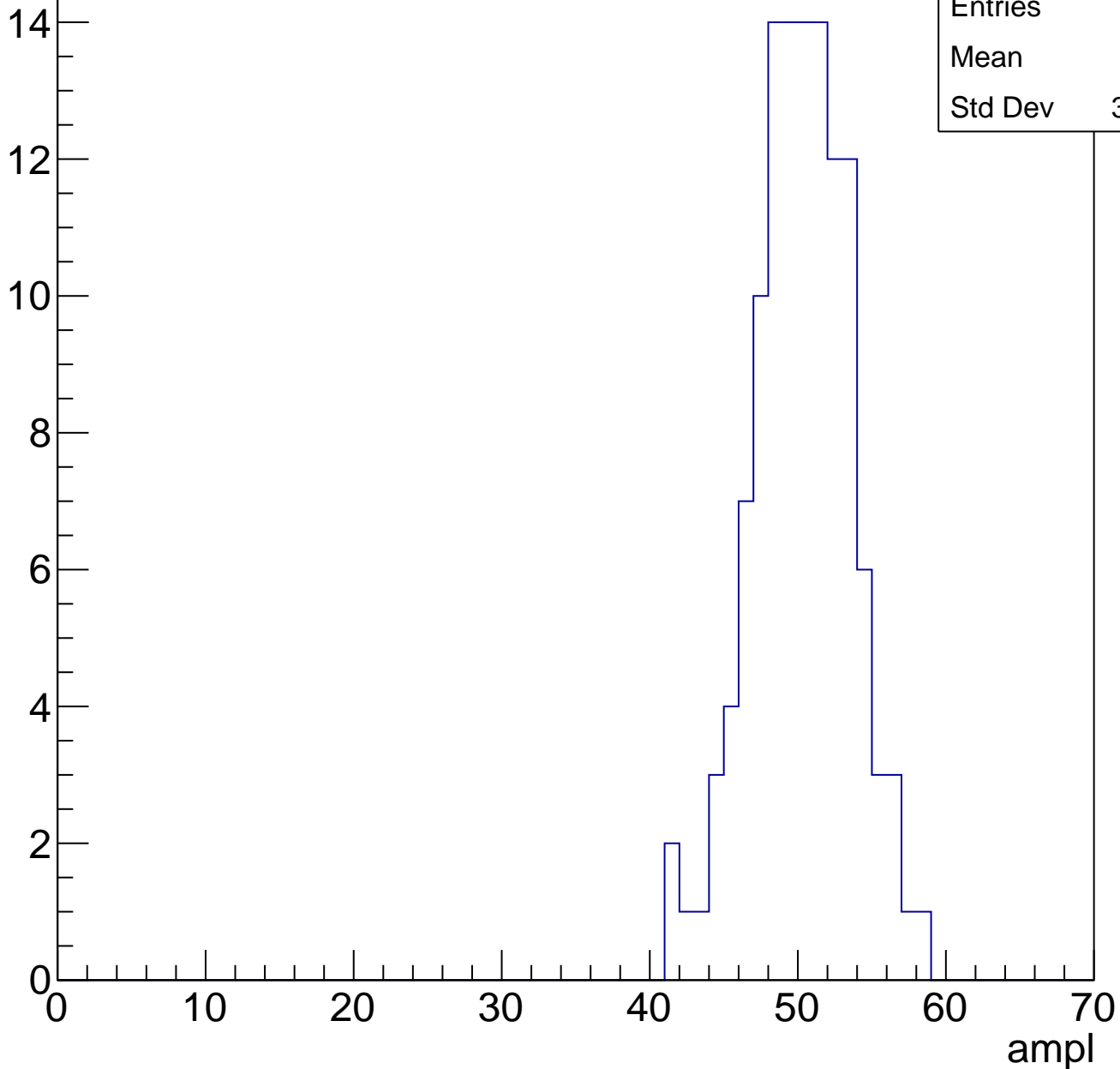


B1L001S, U16-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	49.8
Std Dev	3.279

Entry



B1L001S, U16-ch69, adc4

calib_packv5_042523_0143.root, FC#2, port C2

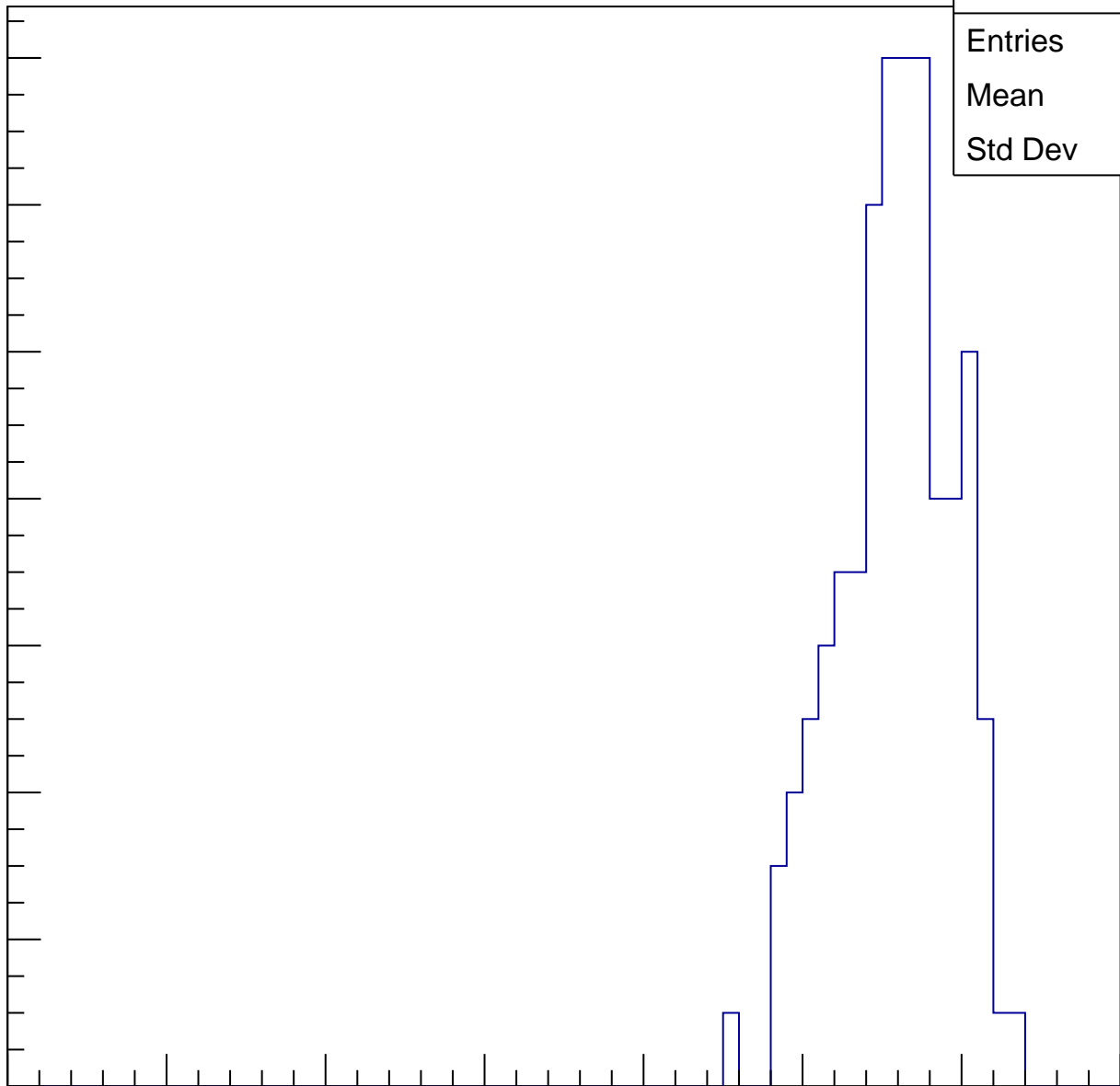
Entries	120
Mean	55.35
Std Dev	3.551

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch69, adc5

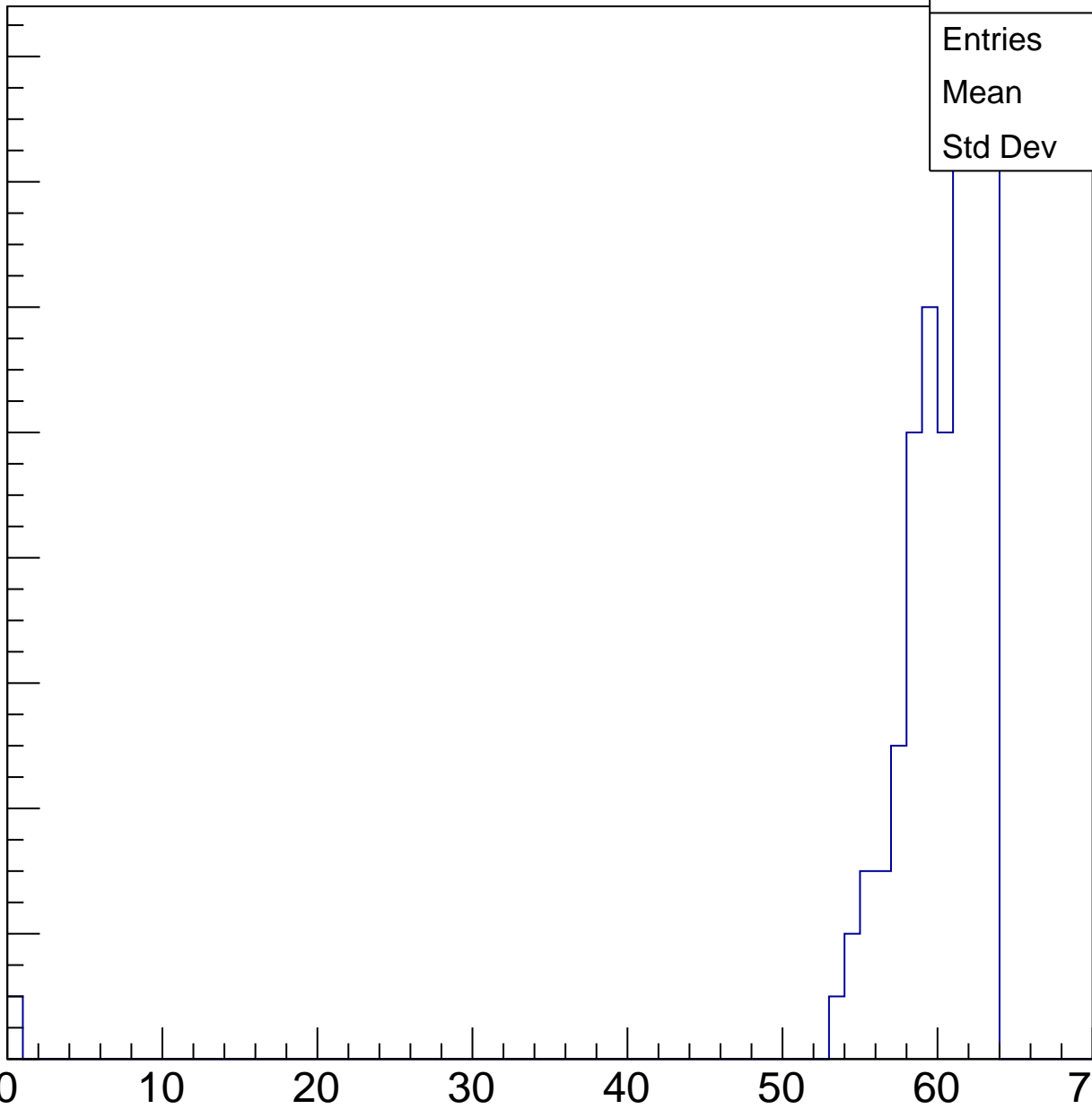
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.34
Std Dev	6.652

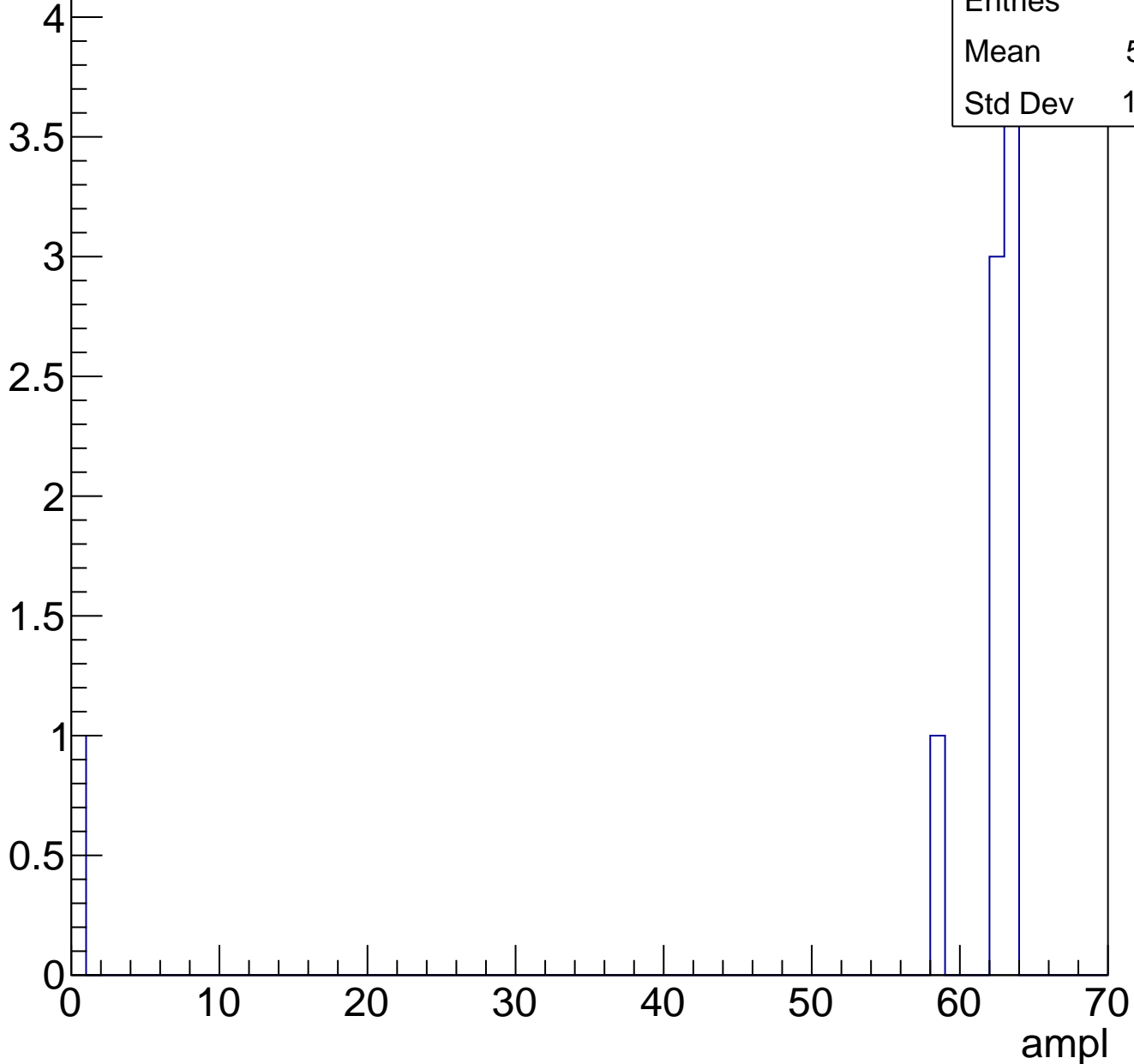
ampl



B1L001S, U16-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch70, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.44
Std Dev	4.373

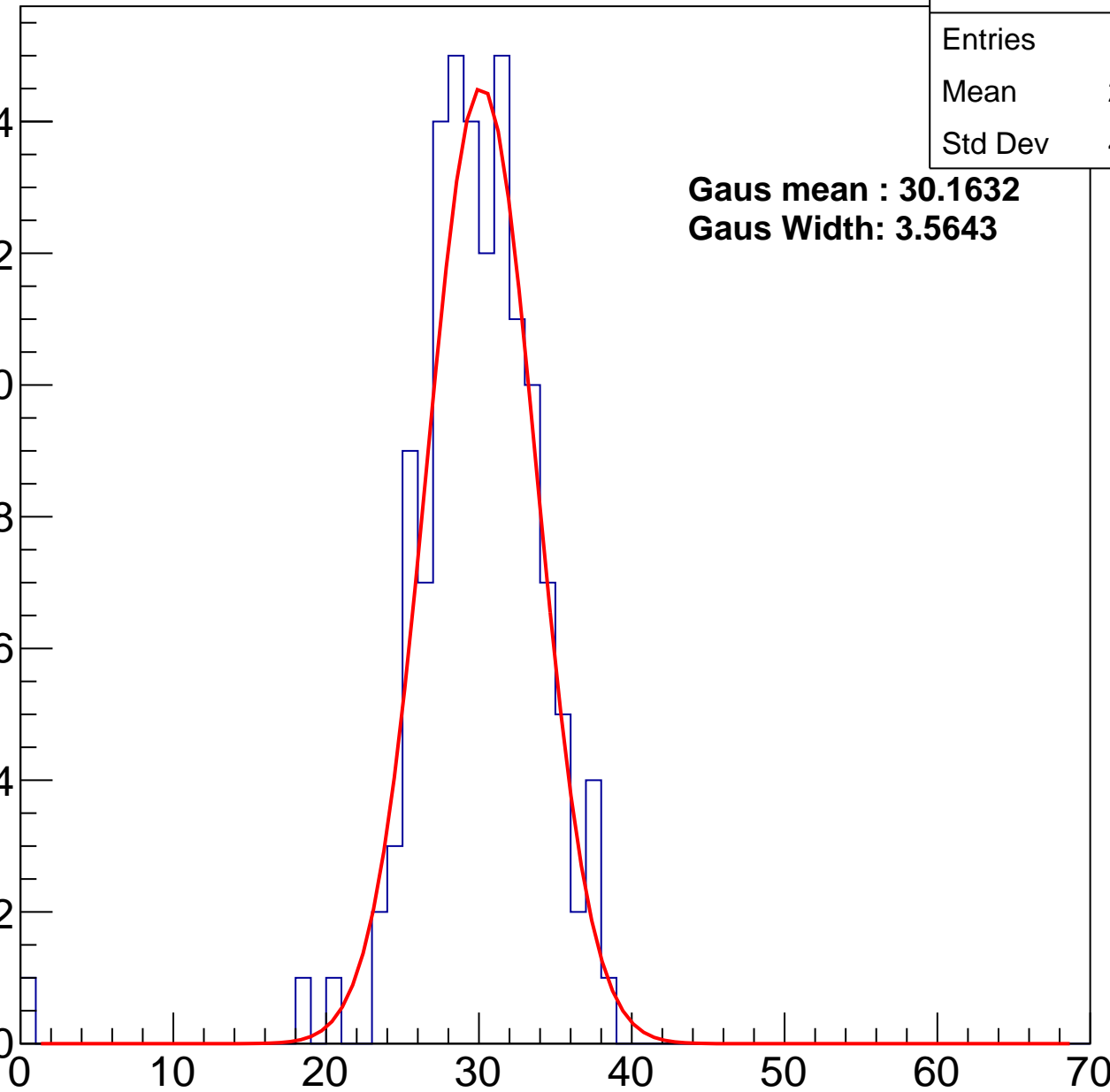
Gaus mean : 30.1632

Gaus Width: 3.5643

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch70, adc1

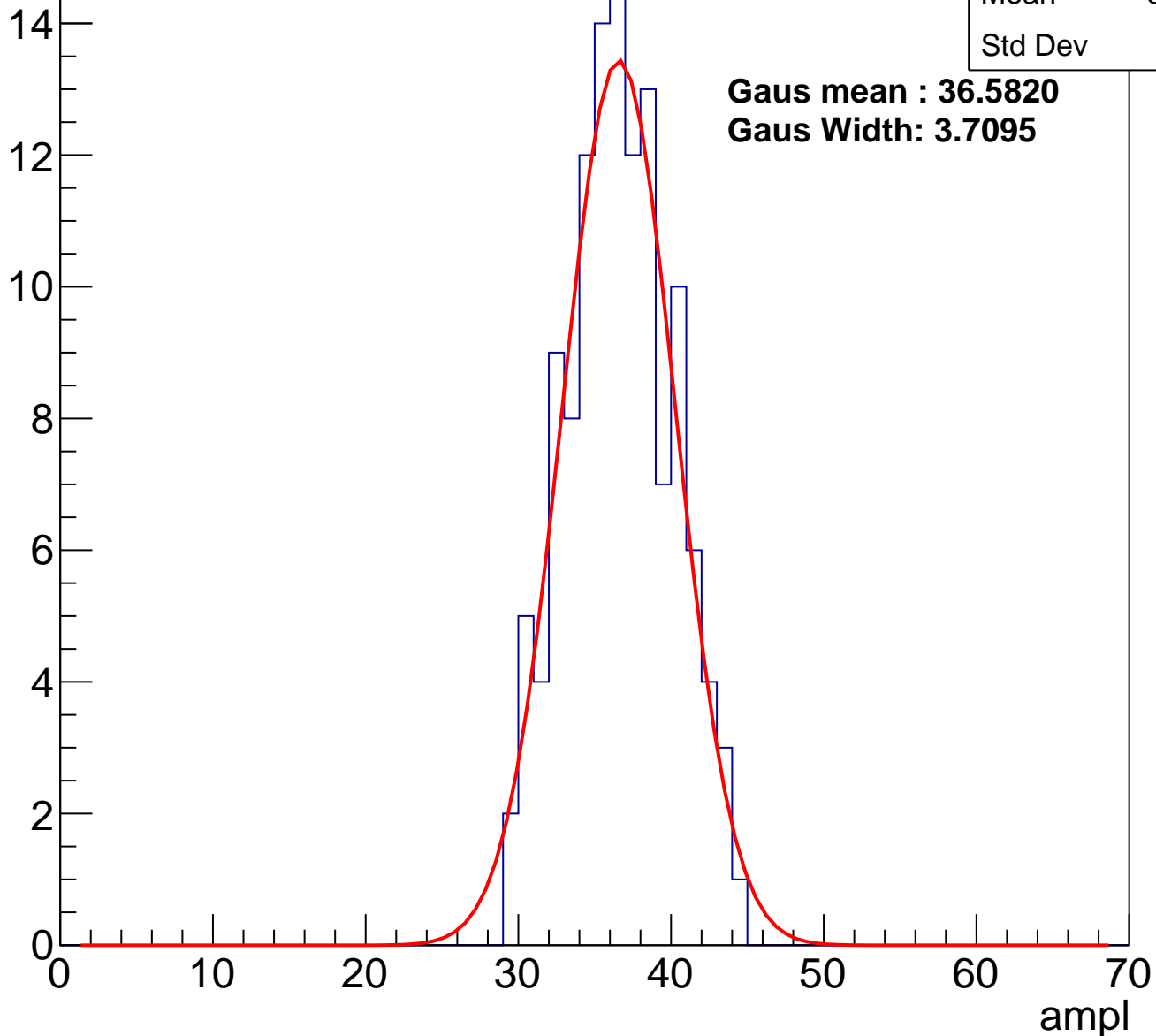
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.16
Std Dev	3.4

Gaus mean : 36.5820

Gaus Width: 3.7095

Entry



B1L001S, U16-ch70, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

42.45

Std Dev

3.524

Gaus mean : 42.9750

Gaus Width: 3.3870

0

10

20

30

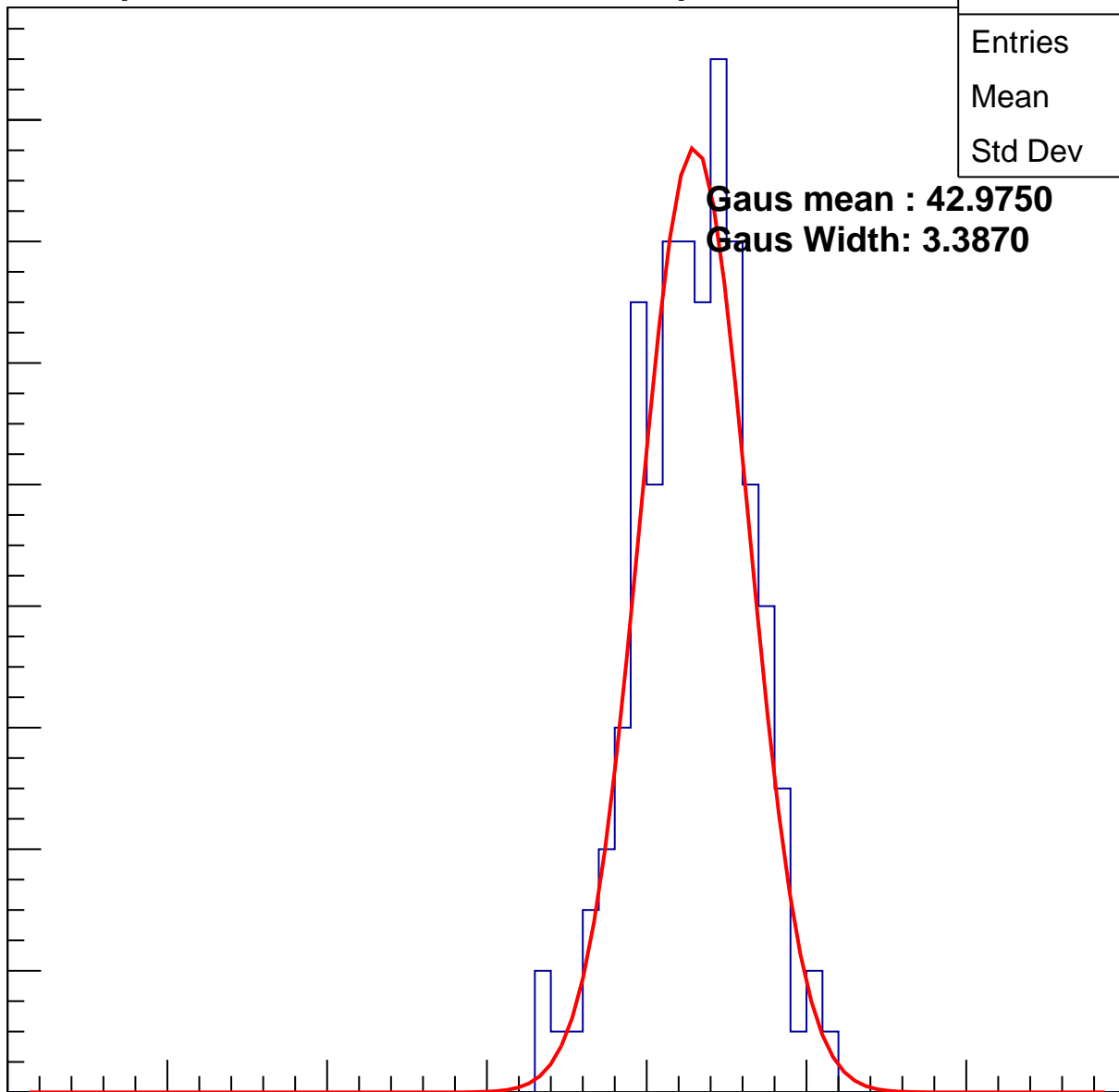
40

50

60

70

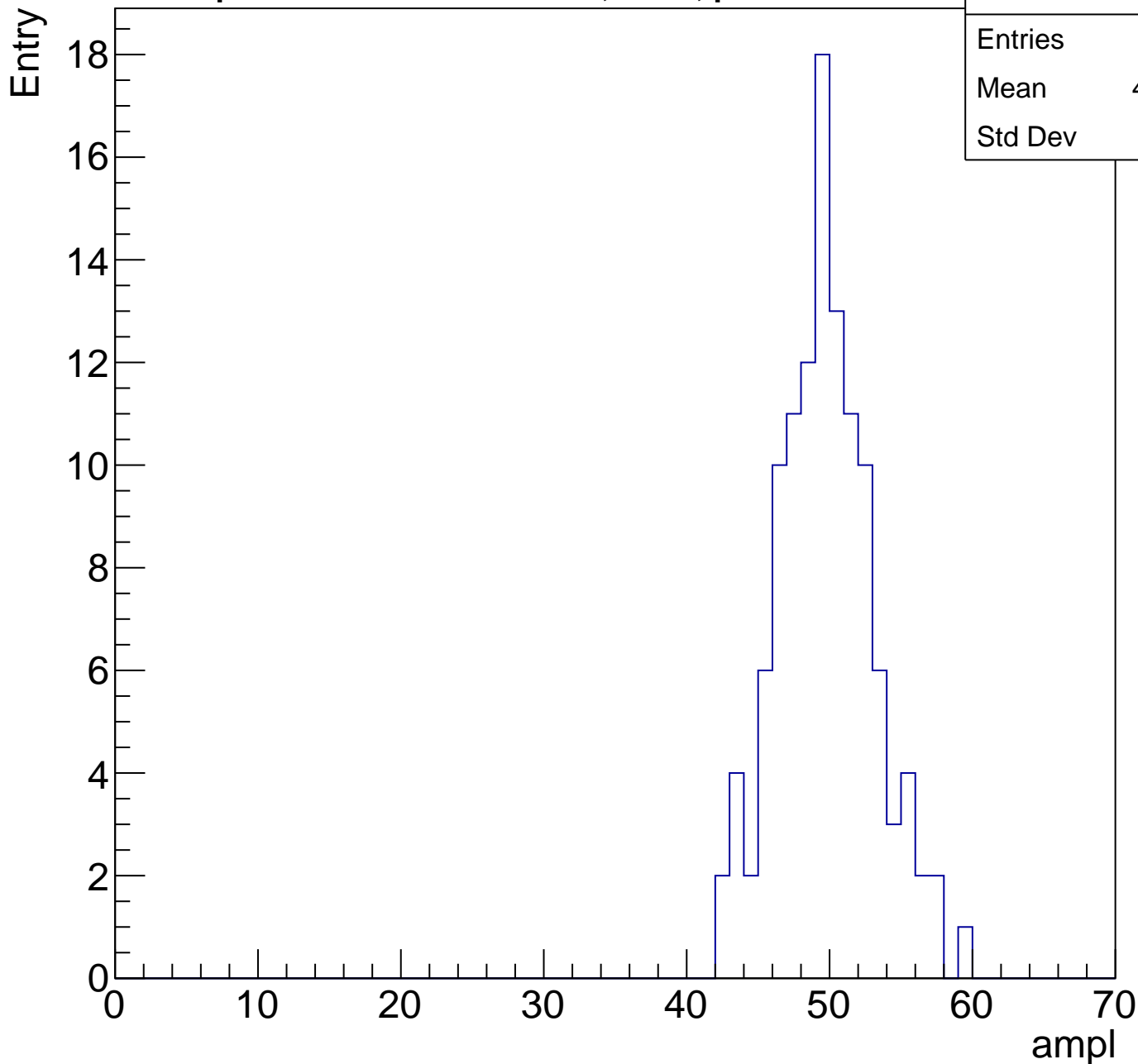
ampl



B1L001S, U16-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

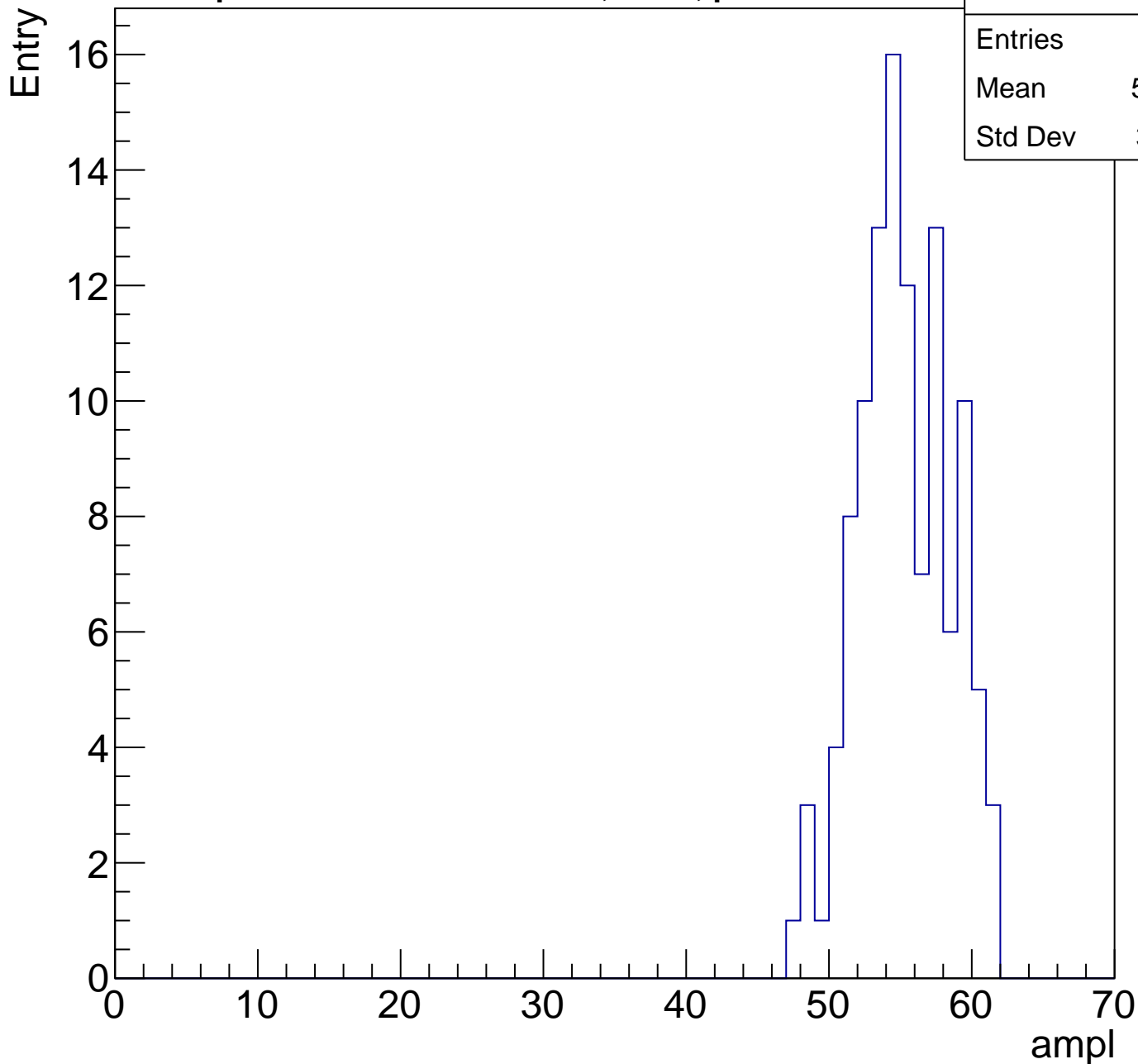
Entries	117
Mean	49.27
Std Dev	3.35



B1L001S, U16-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	54.78
Std Dev	3.181



B1L001S, U16-ch70, adc5

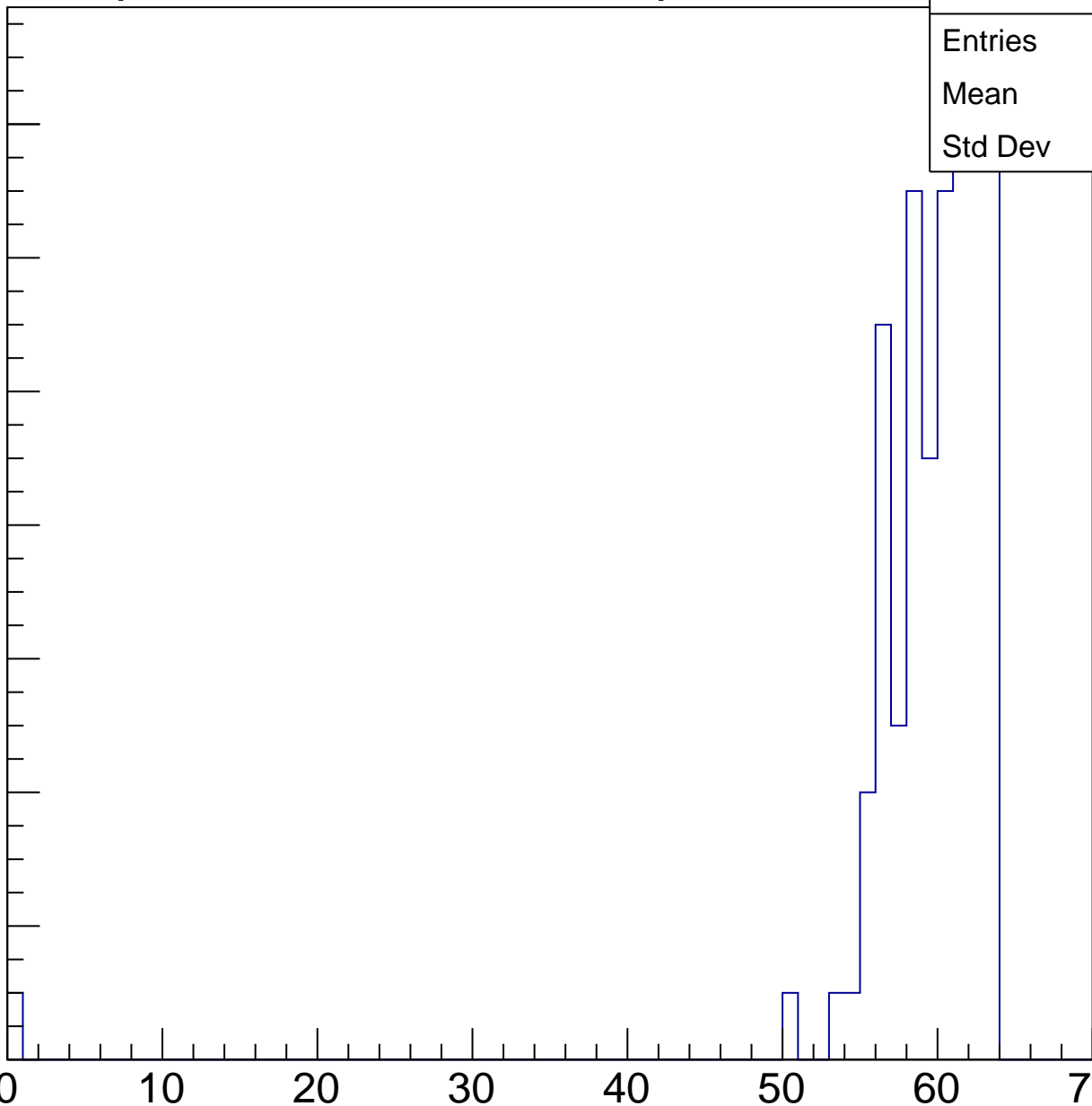
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	58.93
Std Dev	6.424

ampl

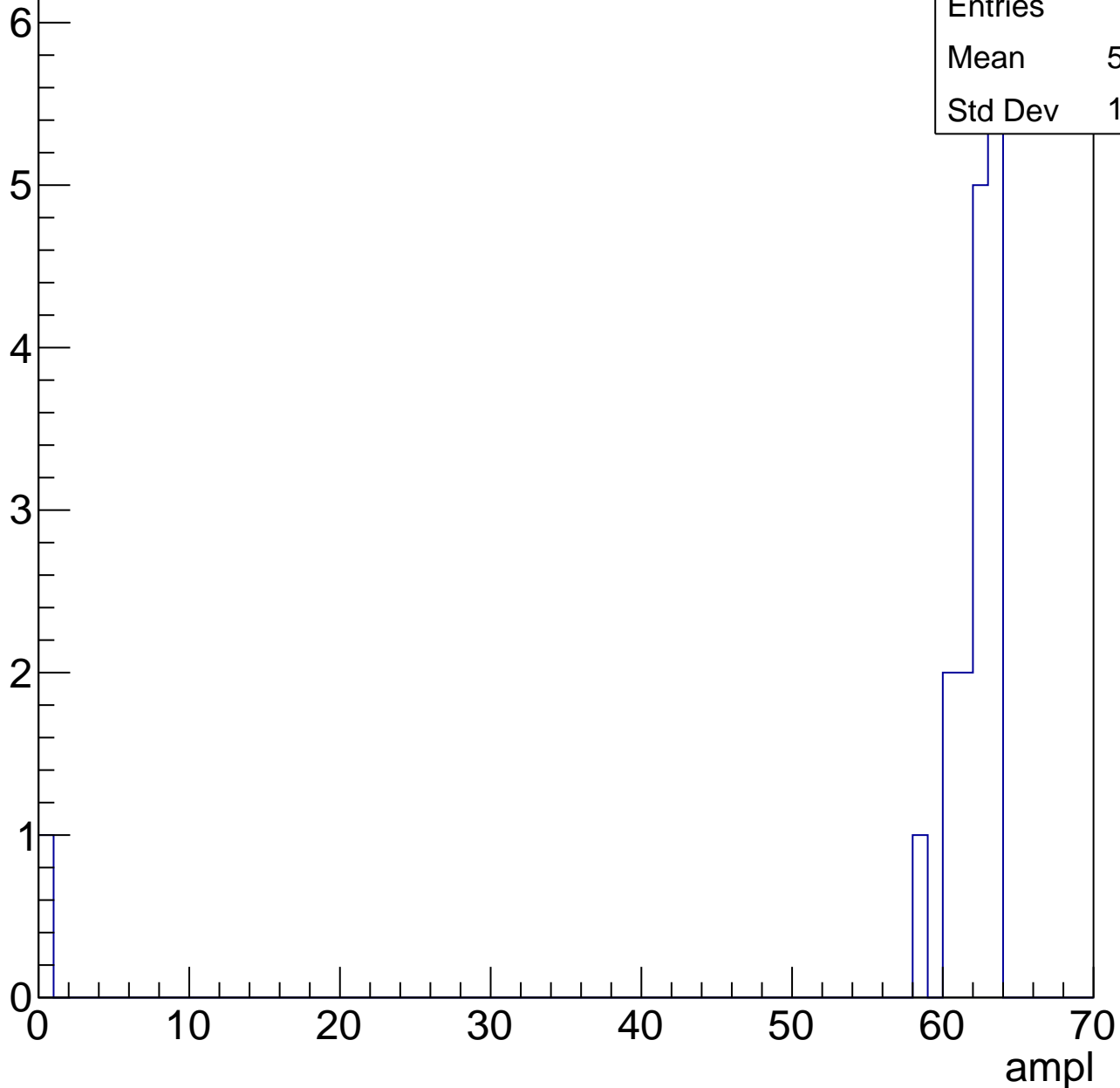


B1L001S, U16-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.12
Std Dev	14.59



B1L001S, U16-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U16-ch71, adc0

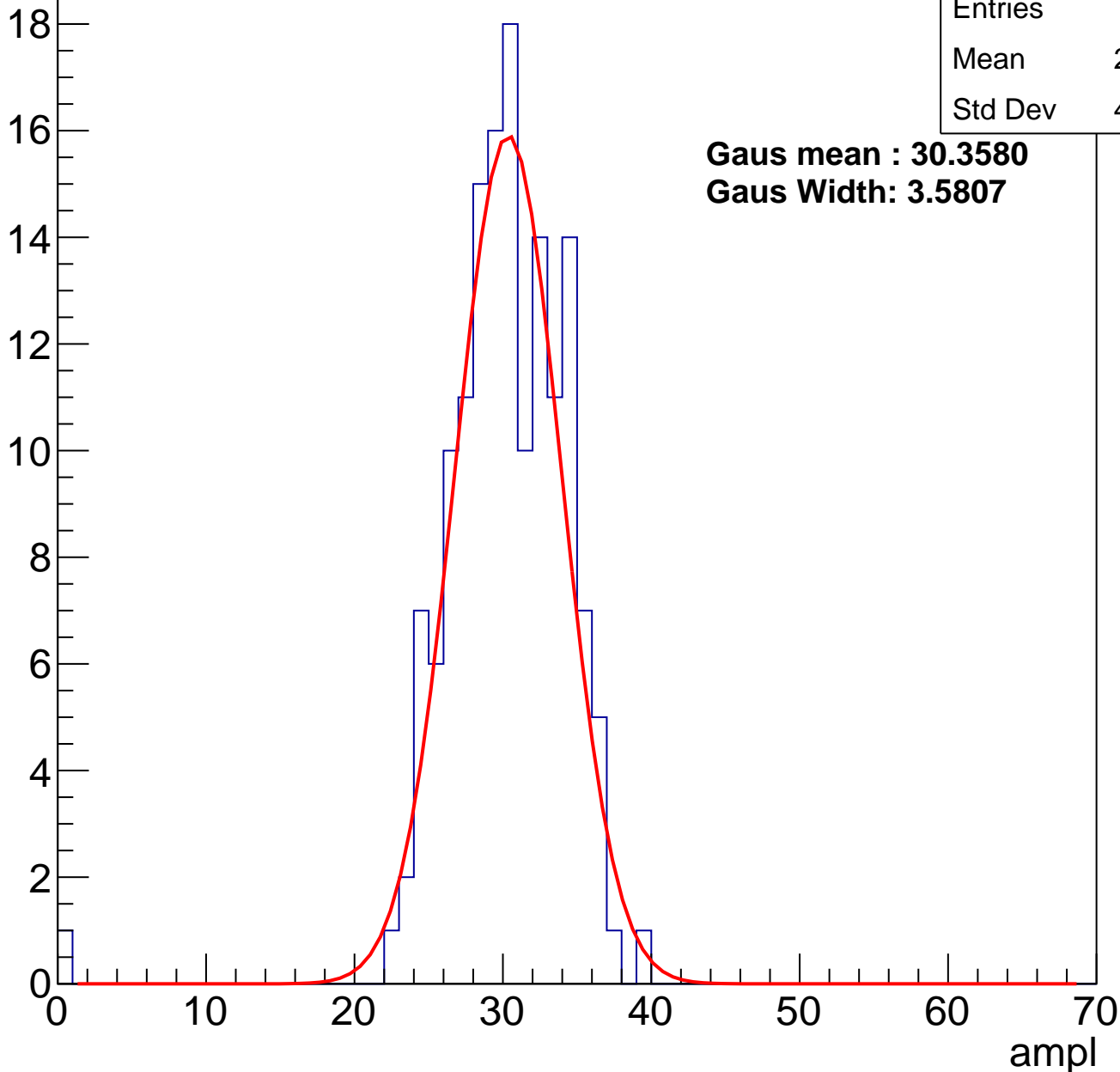
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	29.77
Std Dev	4.215

Gaus mean : 30.3580

Gaus Width: 3.5807

Entry



B1L001S, U16-ch71, adc1

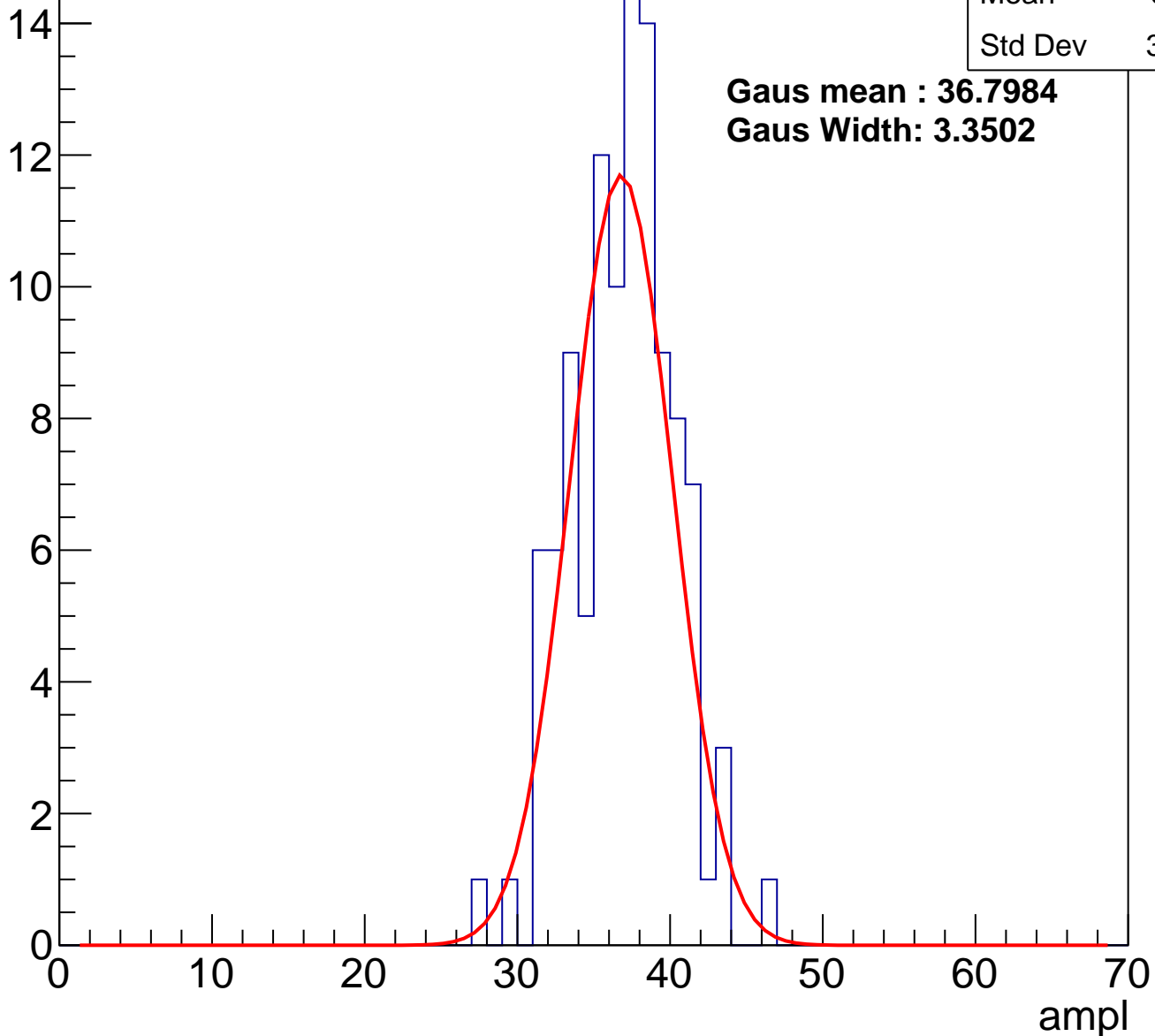
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	36.51
Std Dev	3.332

Gaus mean : 36.7984

Gaus Width: 3.3502

Entry



B1L001S, U16-ch71, adc2

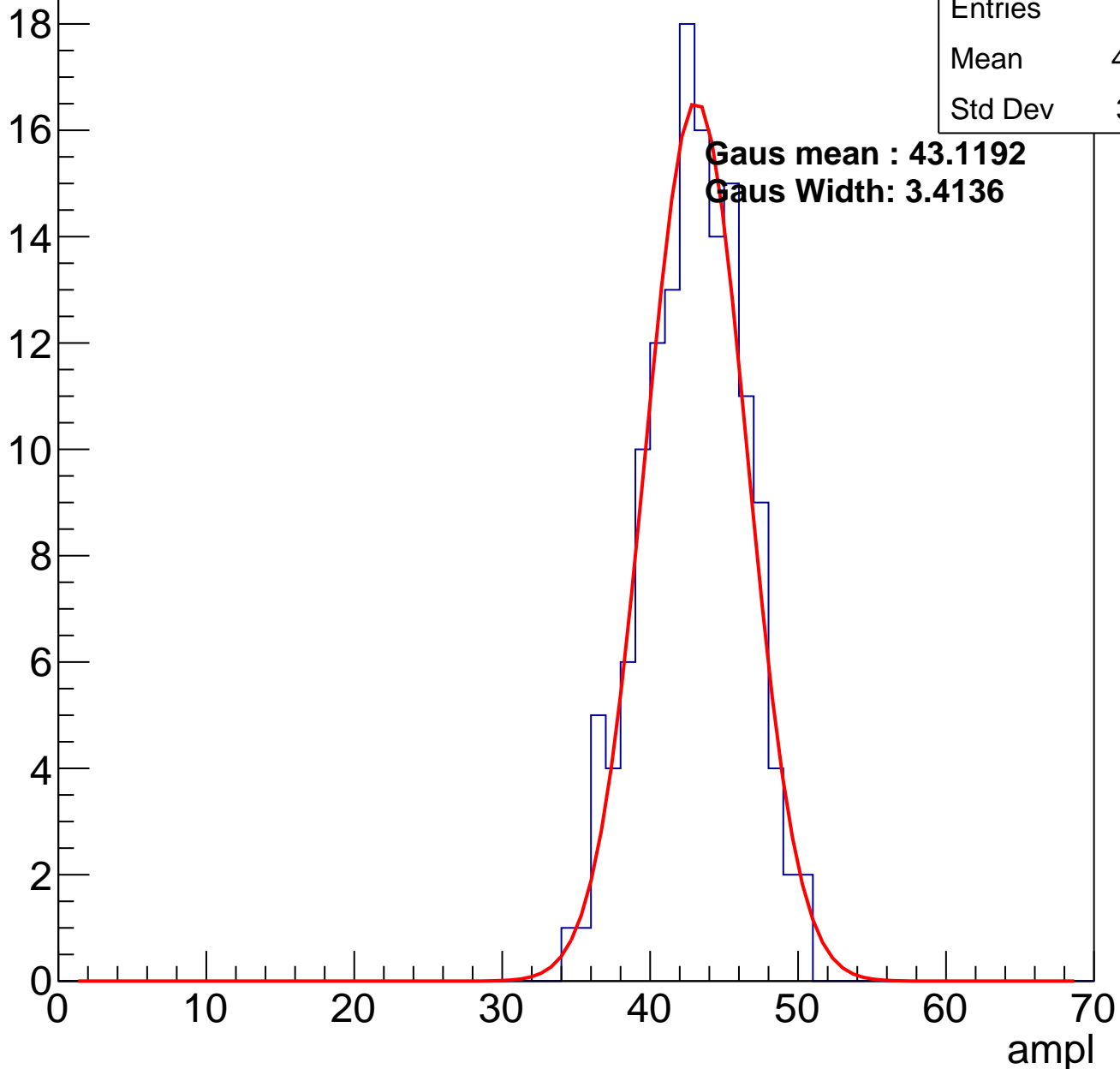
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	42.53
Std Dev	3.321

Gaus mean : 43.1192

Gaus Width: 3.4136

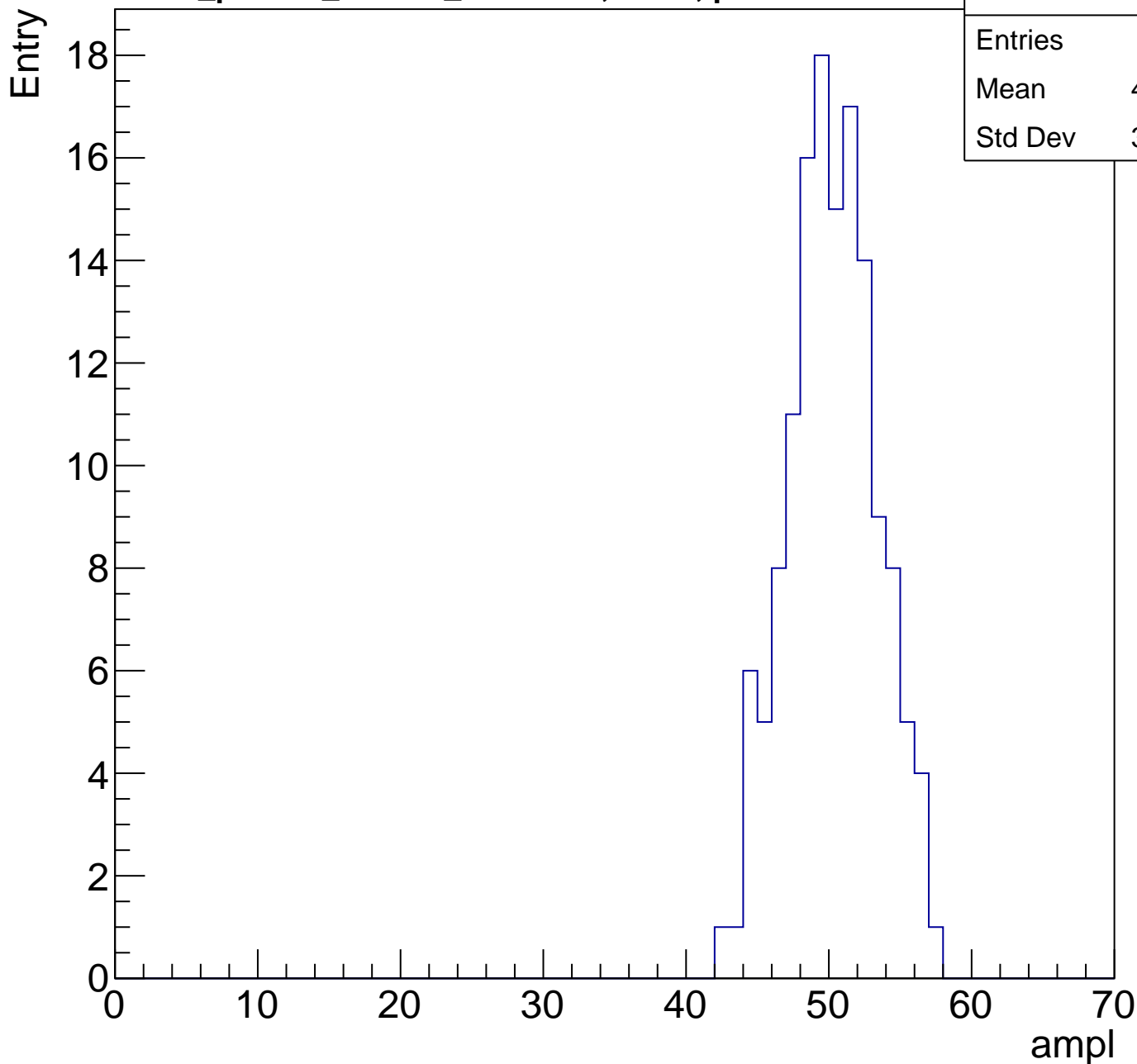
Entry



B1L001S, U16-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	49.78
Std Dev	3.123



B1L001S, U16-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

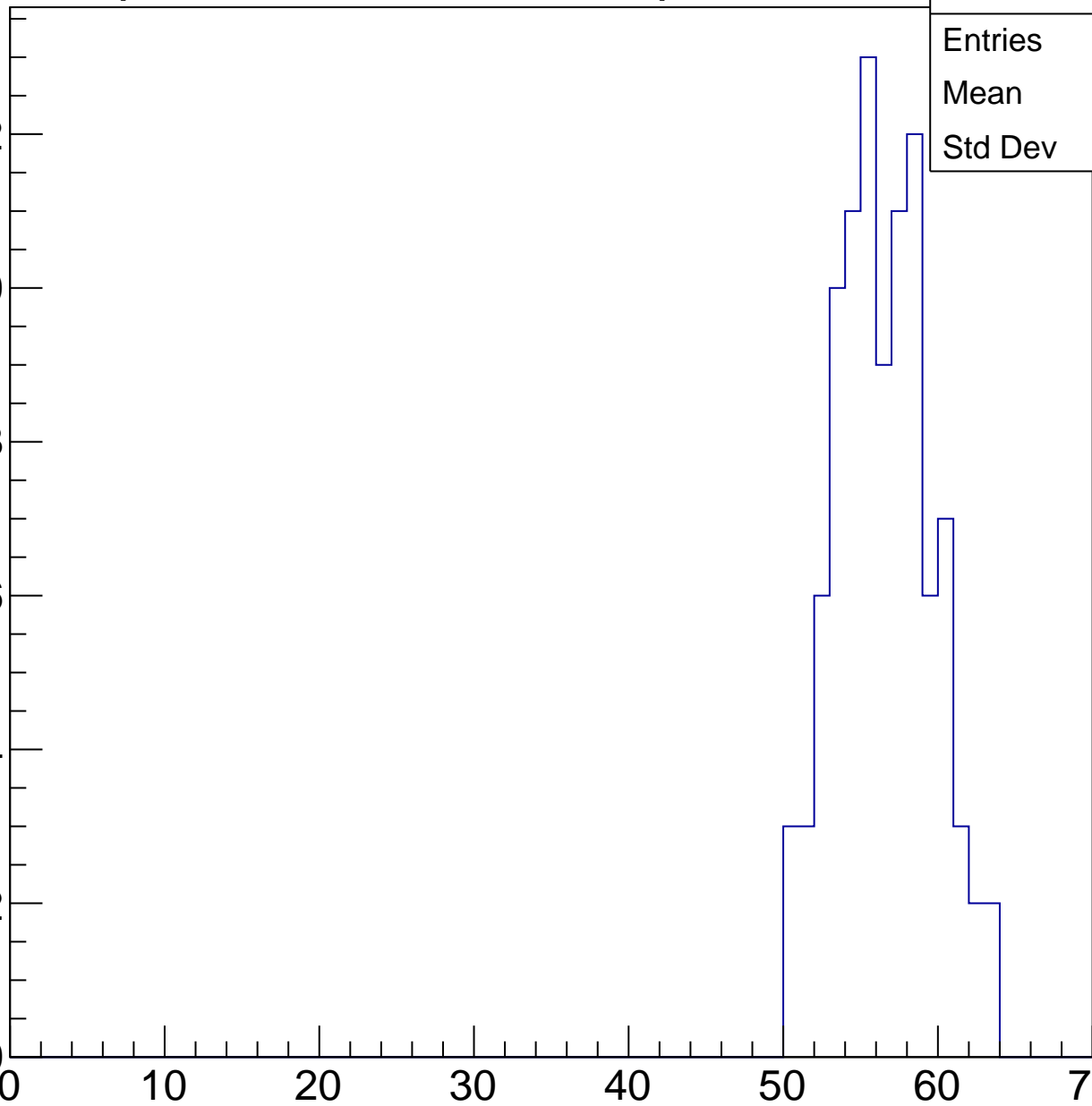
40

50

60

ampl

Entries	98
Mean	56
Std Dev	3.024



B1L001S, U16-ch71, adc5

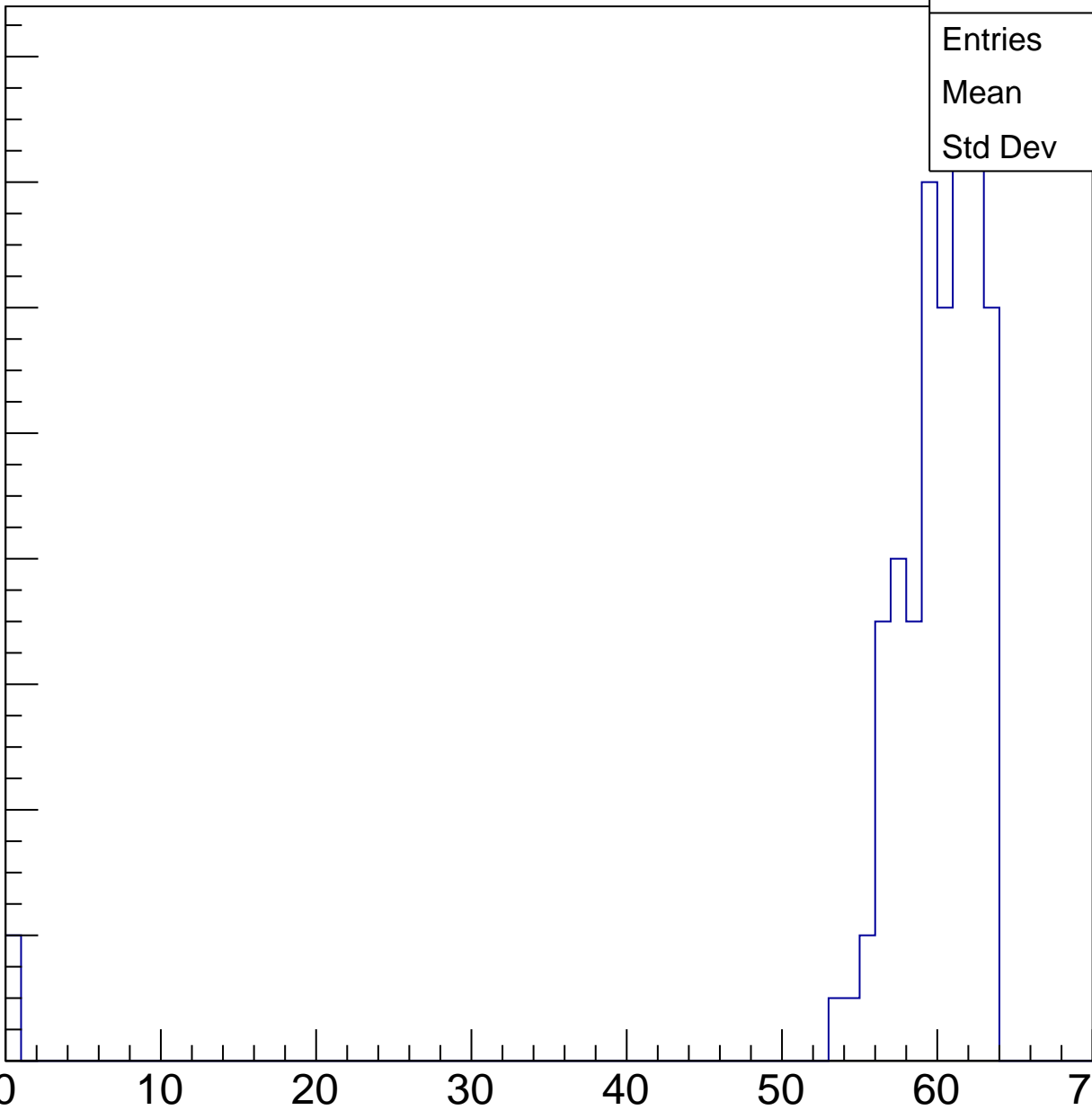
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.55
Std Dev	8.817

ampl

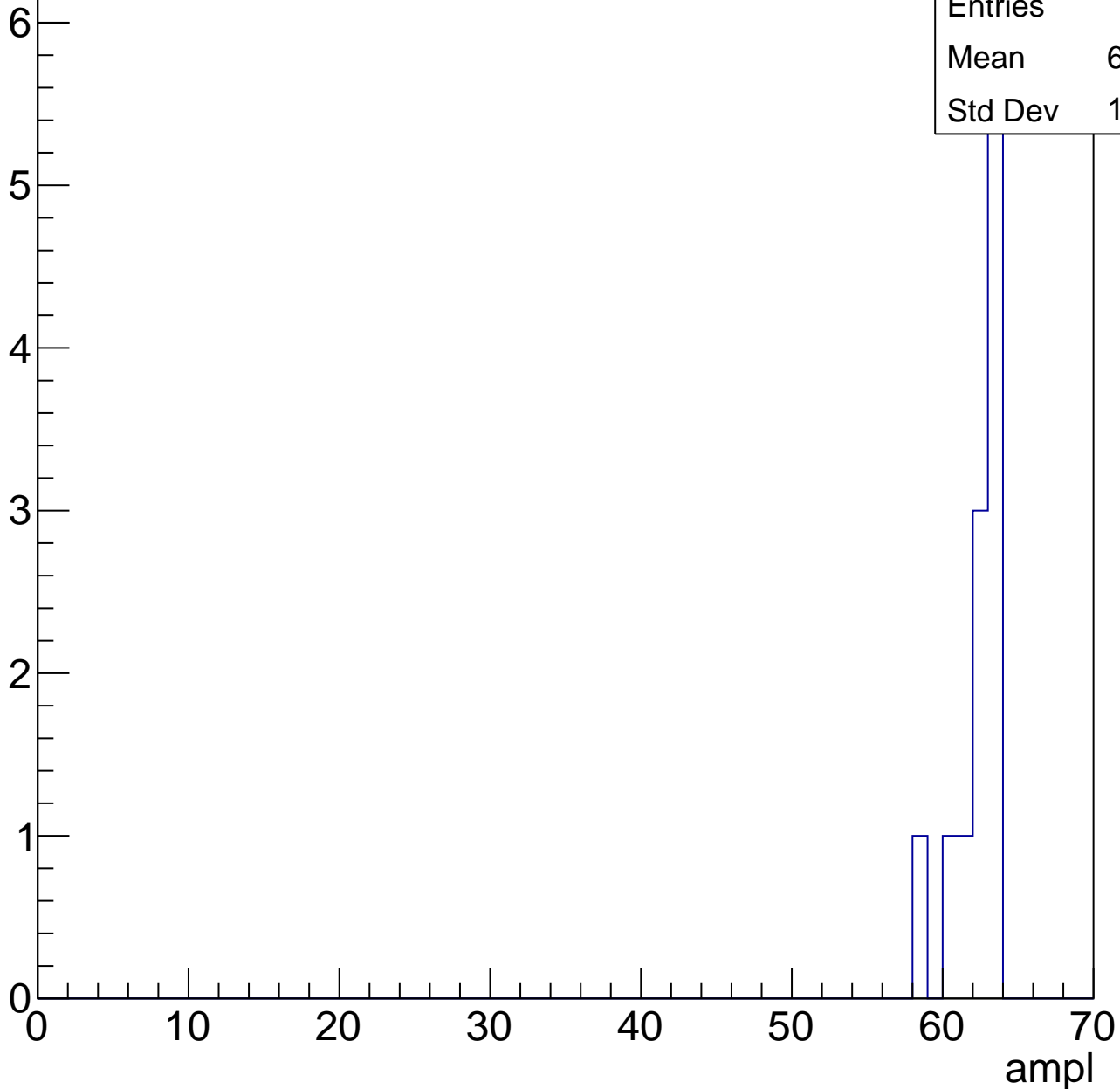


B1L001S, U16-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.92
Std Dev	1.498



B1L001S, U16-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch72, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	28.89
Std Dev	4.065

Gaus mean : 29.7043

Gaus Width: 3.0795

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

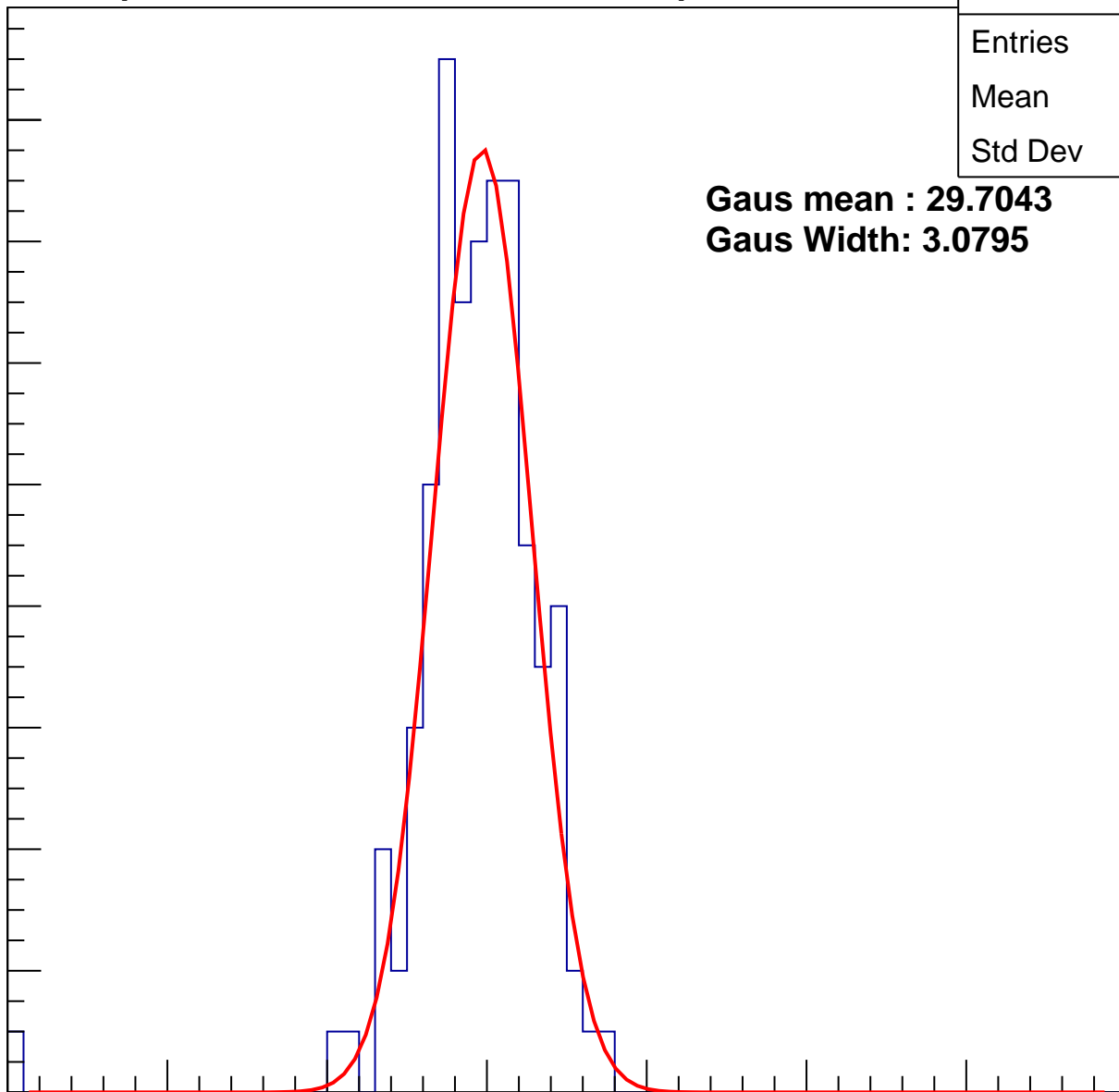
40

50

60

70

ampl



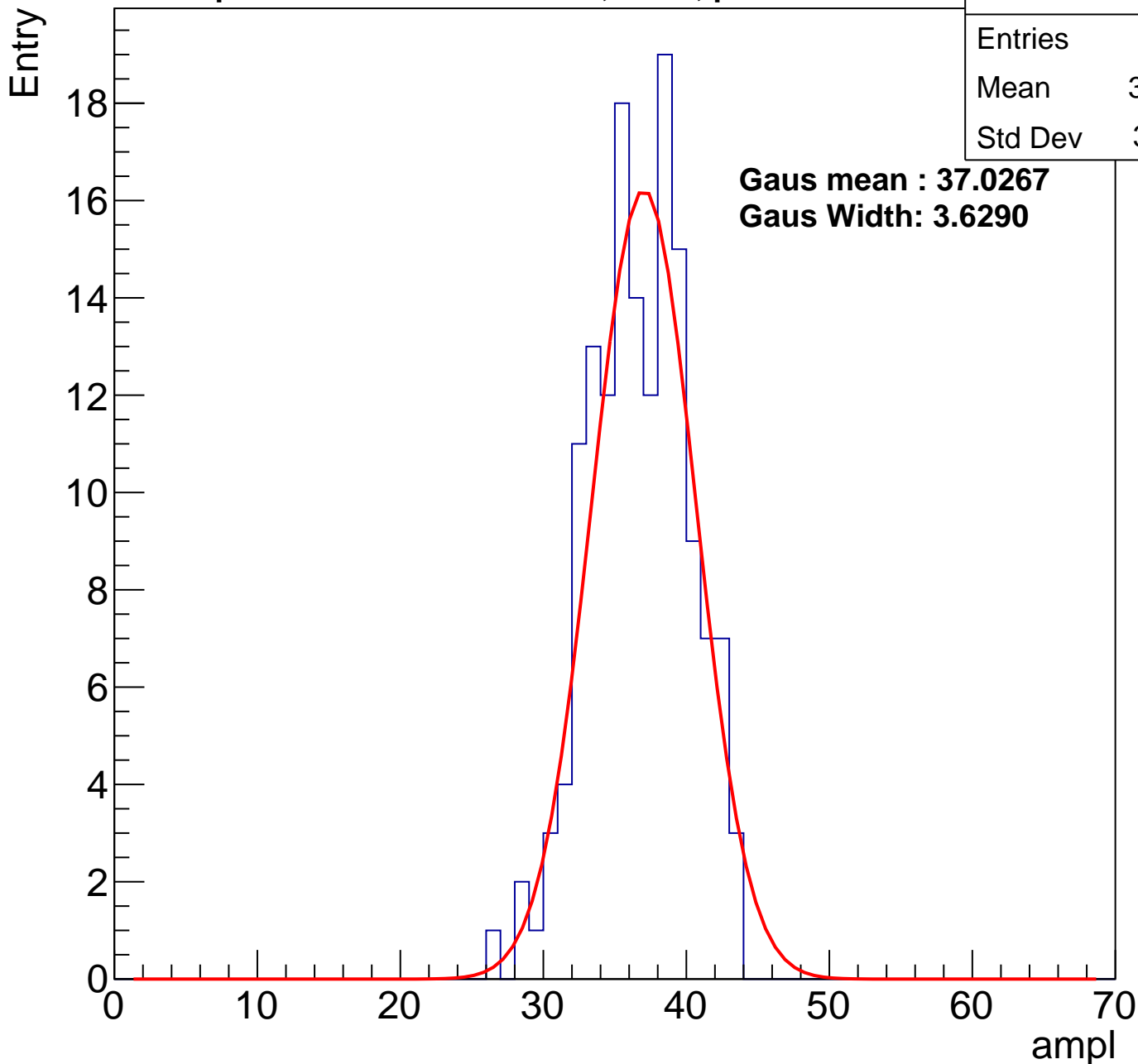
B1L001S, U16-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	36.22
Std Dev	3.431

Gaus mean : 37.0267

Gaus Width: 3.6290



B1L001S, U16-ch72, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	42.59
Std Dev	3.235

Gaus mean : 43.6462

Gaus Width: 2.5924

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

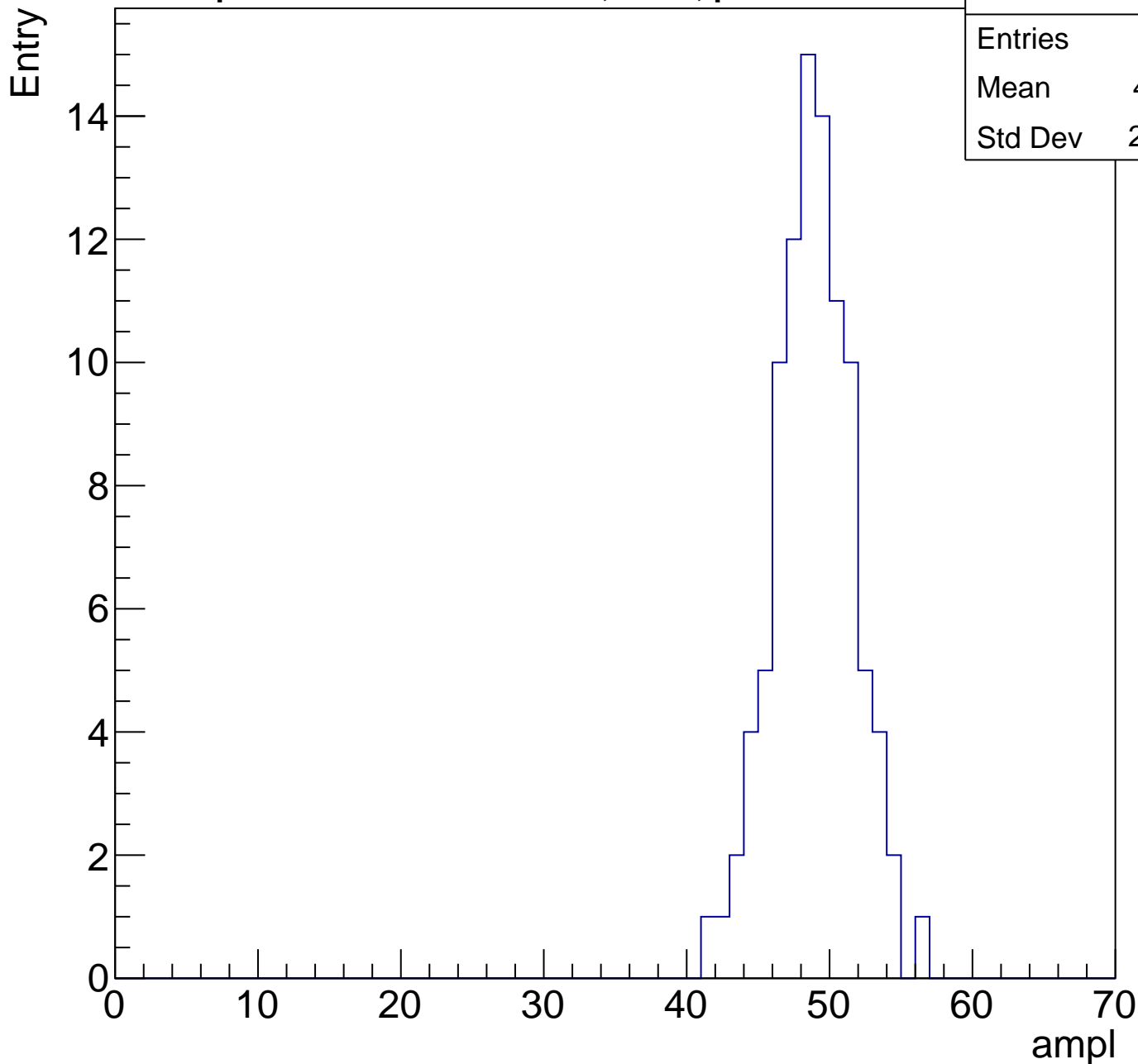
60

70

B1L001S, U16-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	48.41
Std Dev	2.769



B1L001S, U16-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

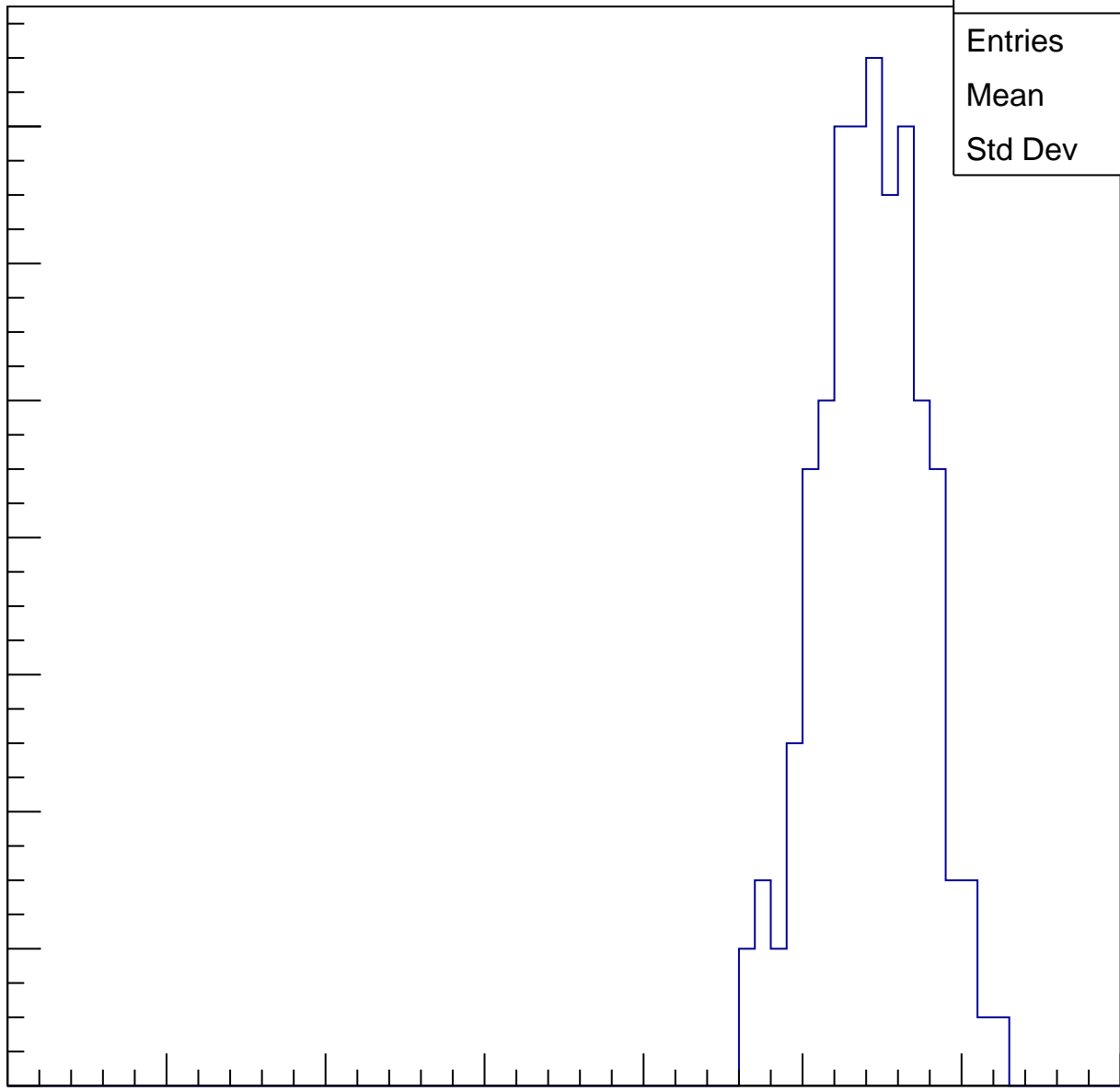
Entries	128
Mean	53.79
Std Dev	3.271

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch72, adc5

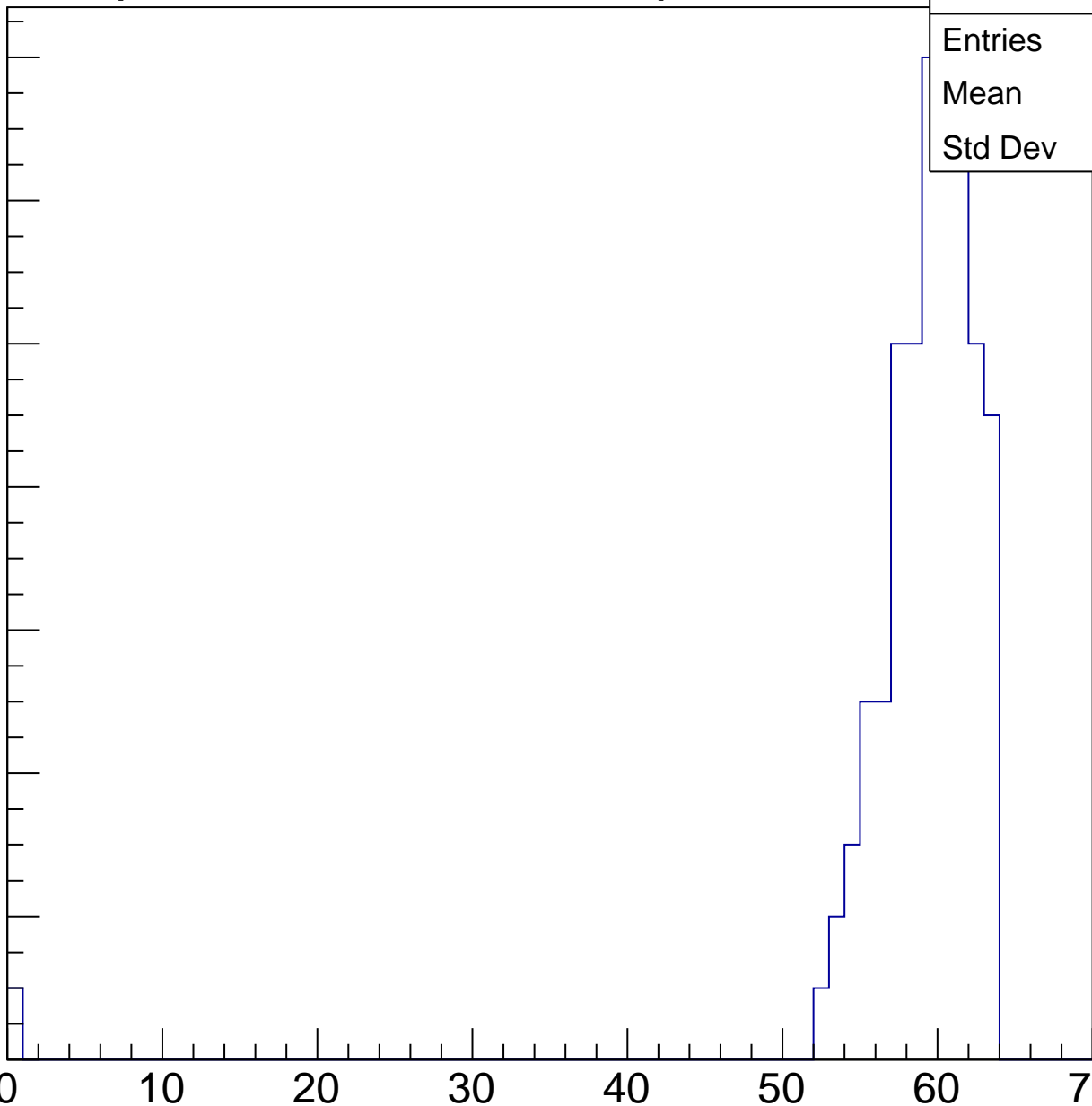
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	58.47
Std Dev	6.524

ampl



B1L001S, U16-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

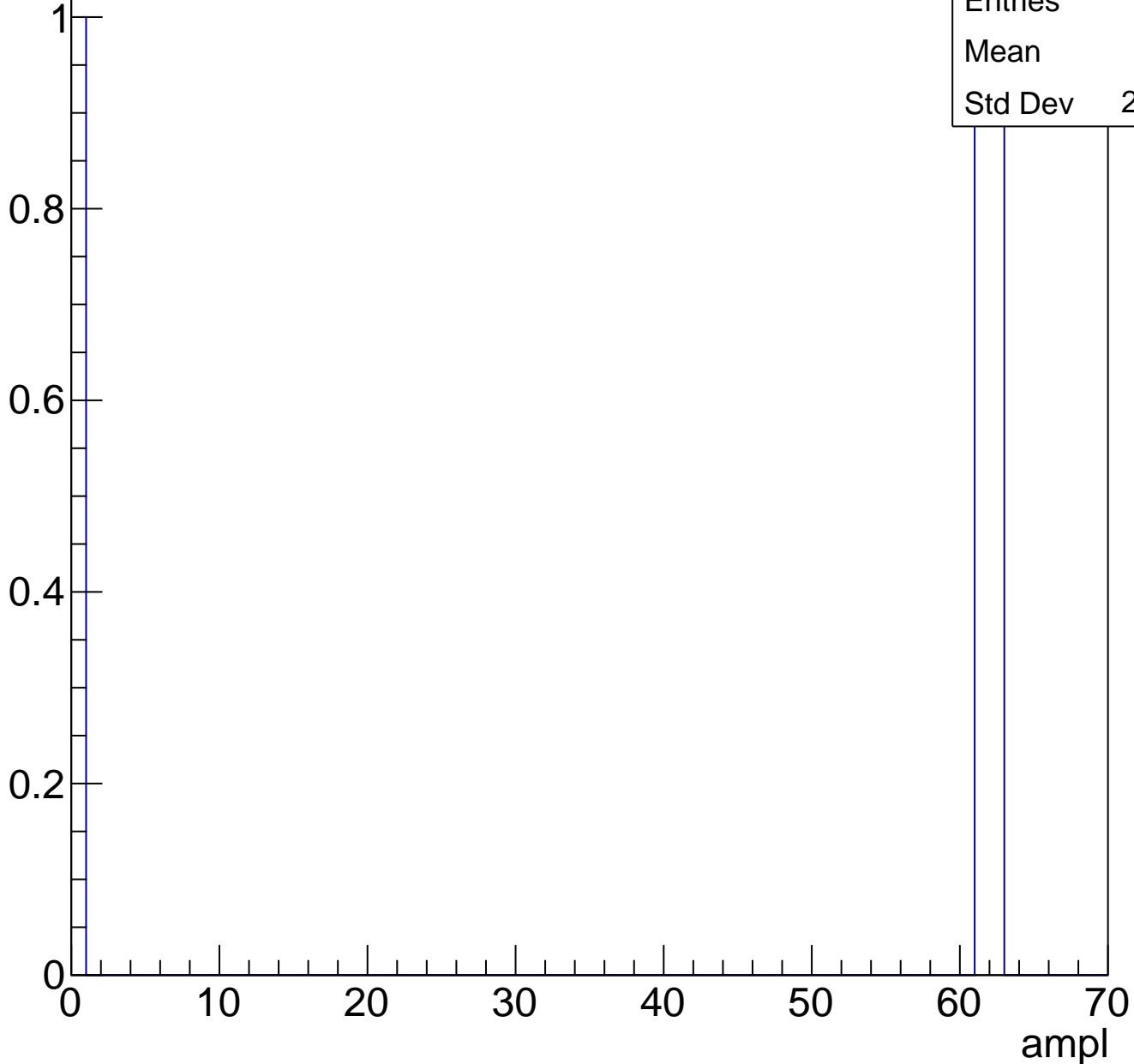
ampl

Entries	33
Mean	59.48
Std Dev	10.67

B1L001S, U16-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.66
Std Dev	4.141

Gaus mean : 29.4875

Gaus Width: 3.3190

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

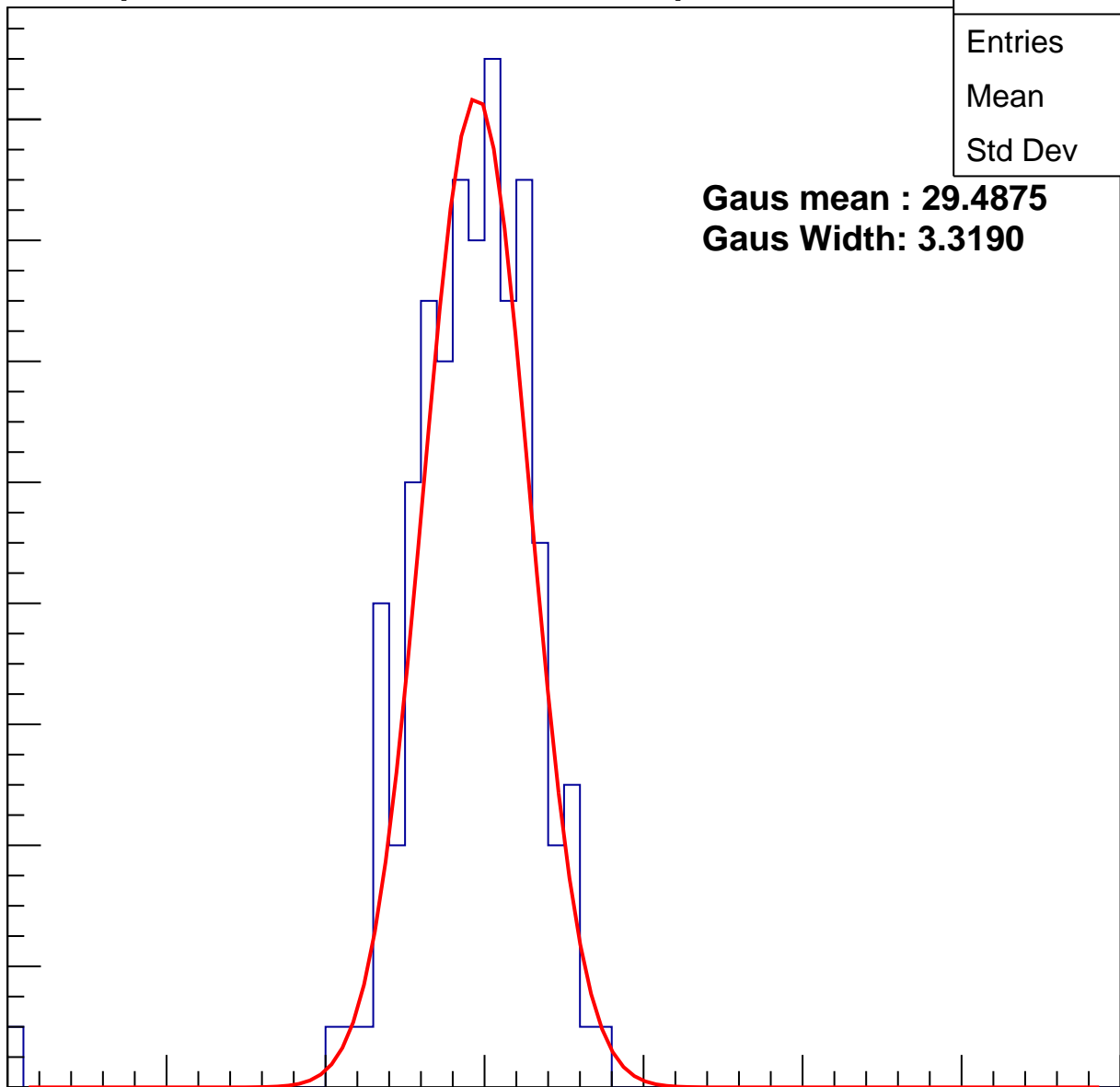
40

50

60

70

ampl



B1L001S, U16-ch73, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	36.11
Std Dev	3.359

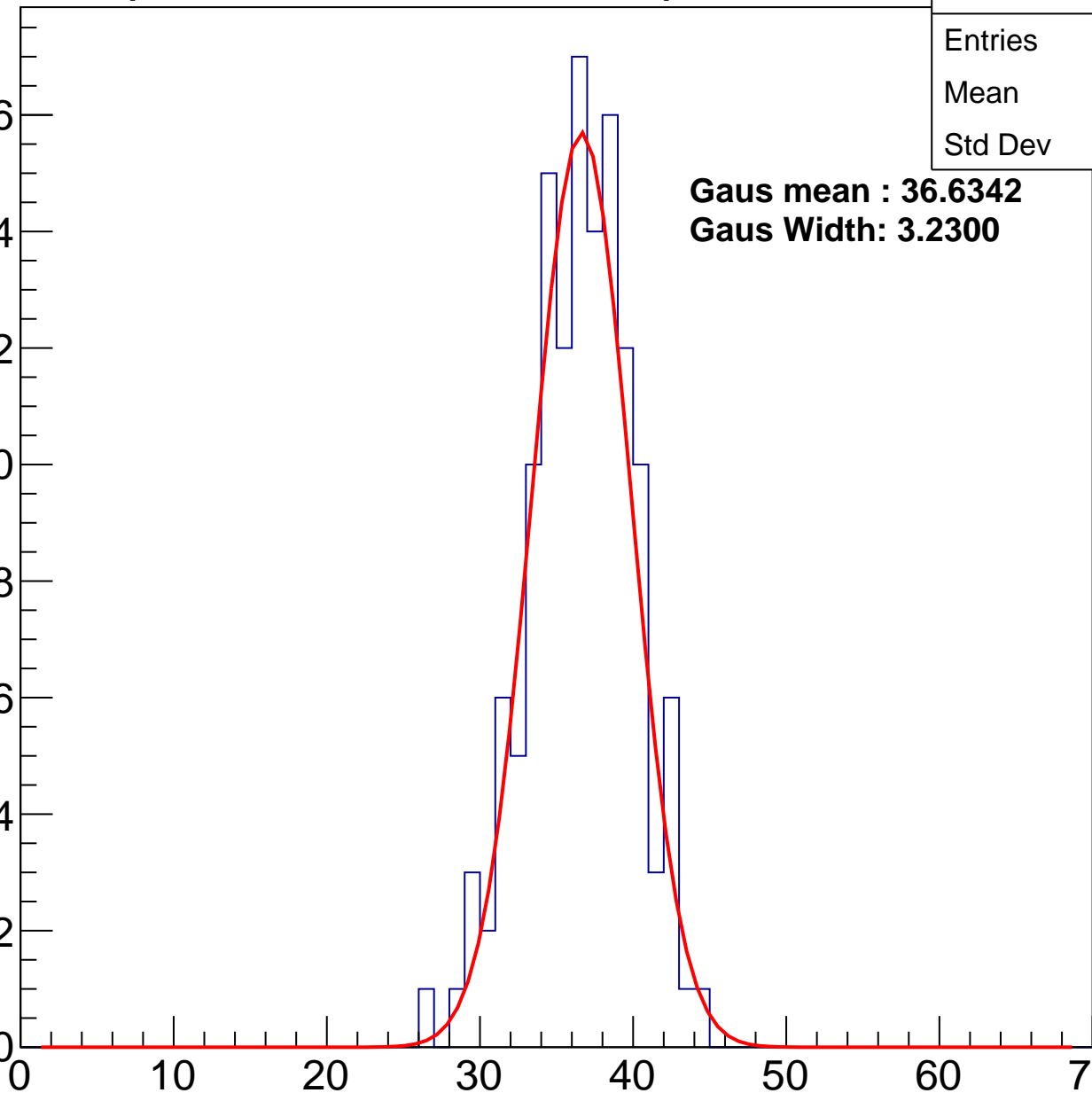
Gaus mean : 36.6342

Gaus Width: 3.2300

Entry

16
14
12
10
8
6
4
2
0

ampl



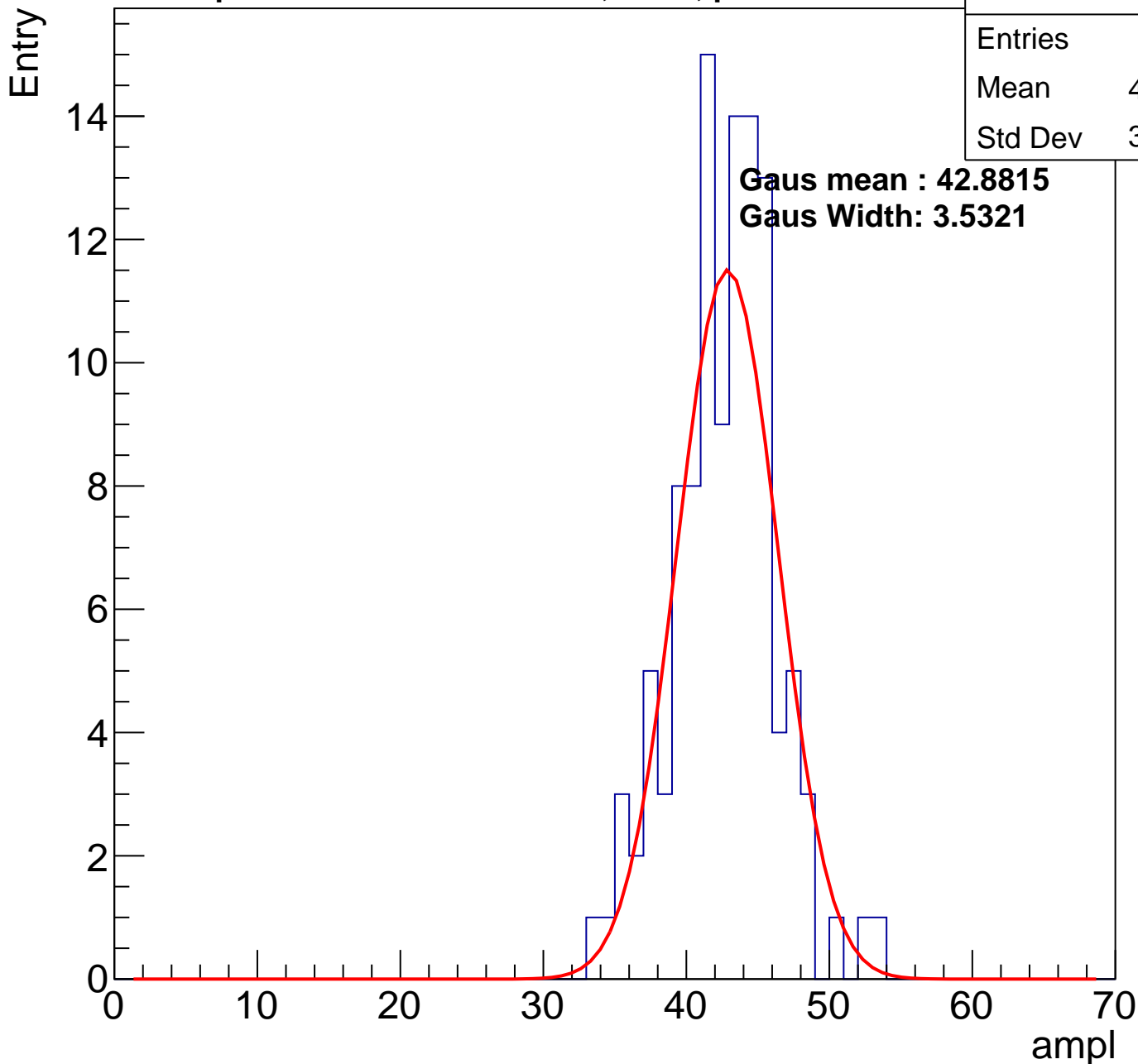
B1L001S, U16-ch73, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	42.24
Std Dev	3.575

Gaus mean : 42.8815

Gaus Width: 3.5321



B1L001S, U16-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	47.79
Std Dev	3.418

Entry

14

12

10

8

6

4

2

0

0

10

20

30

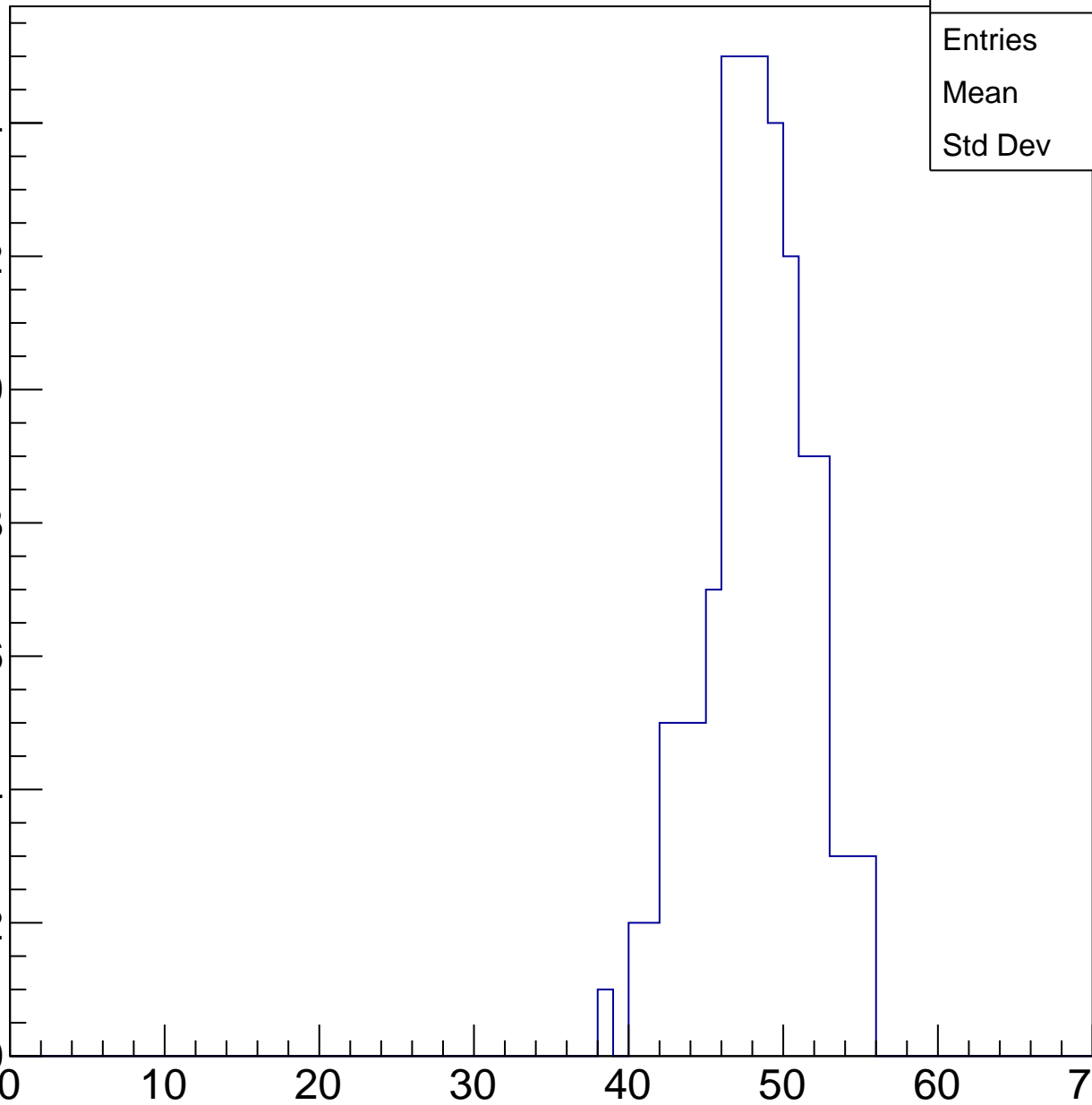
40

50

60

70

ampl



B1L001S, U16-ch73, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

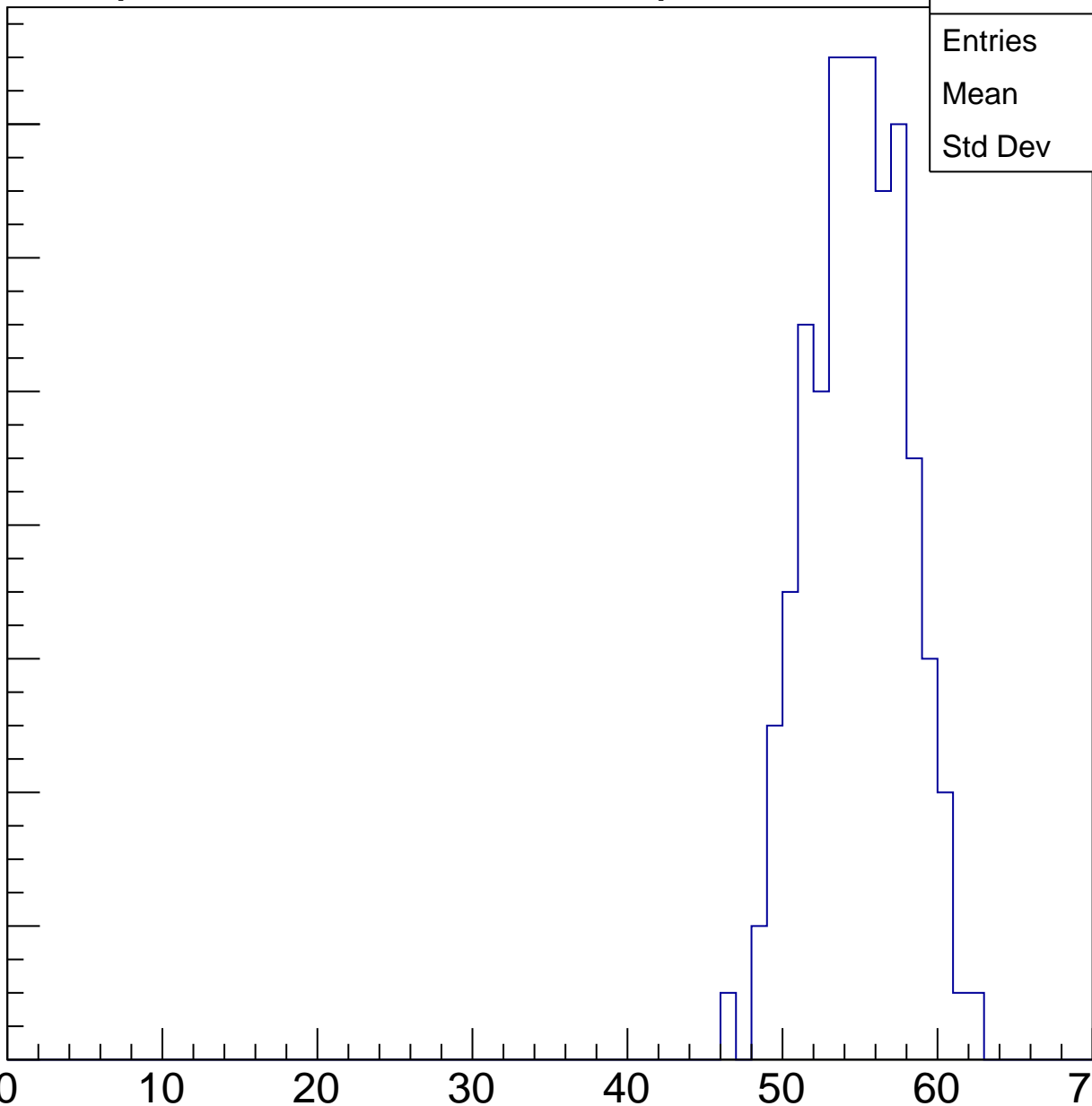
4

2

0

Entries	129
Mean	54.36
Std Dev	3.118

ampl



B1L001S, U16-ch73, adc5

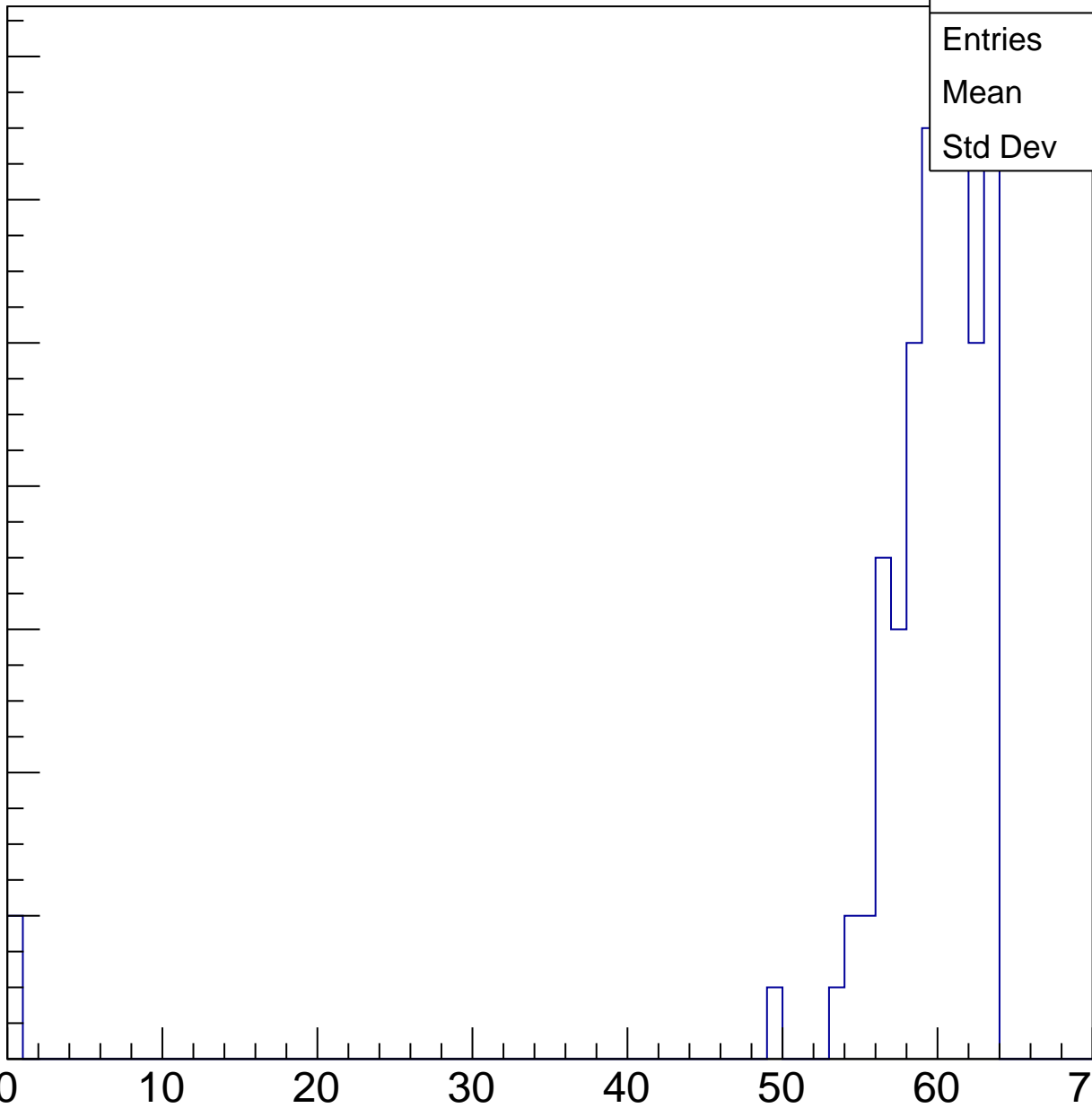
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	58.23
Std Dev	8.985

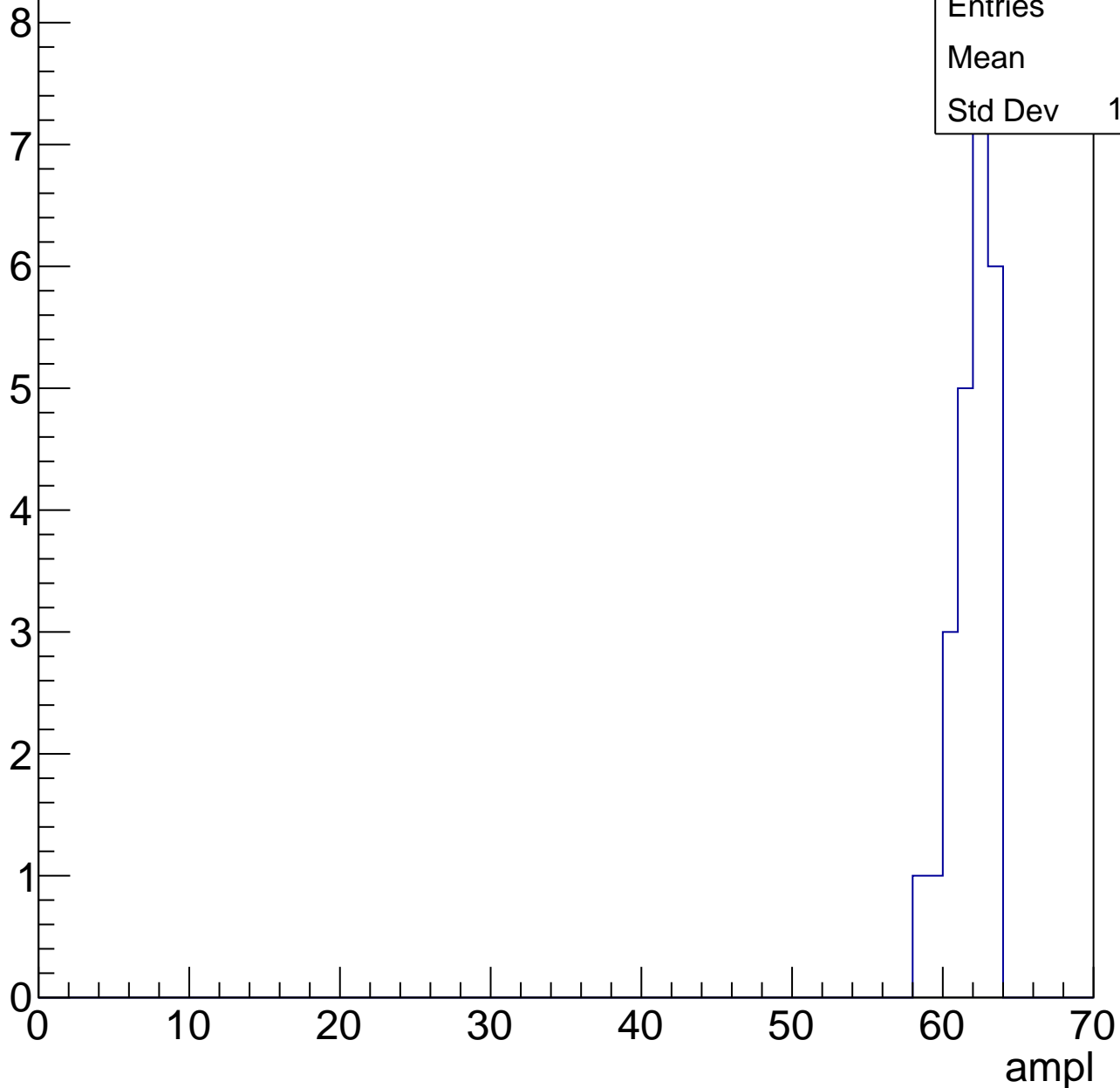
ampl



B1L001S, U16-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

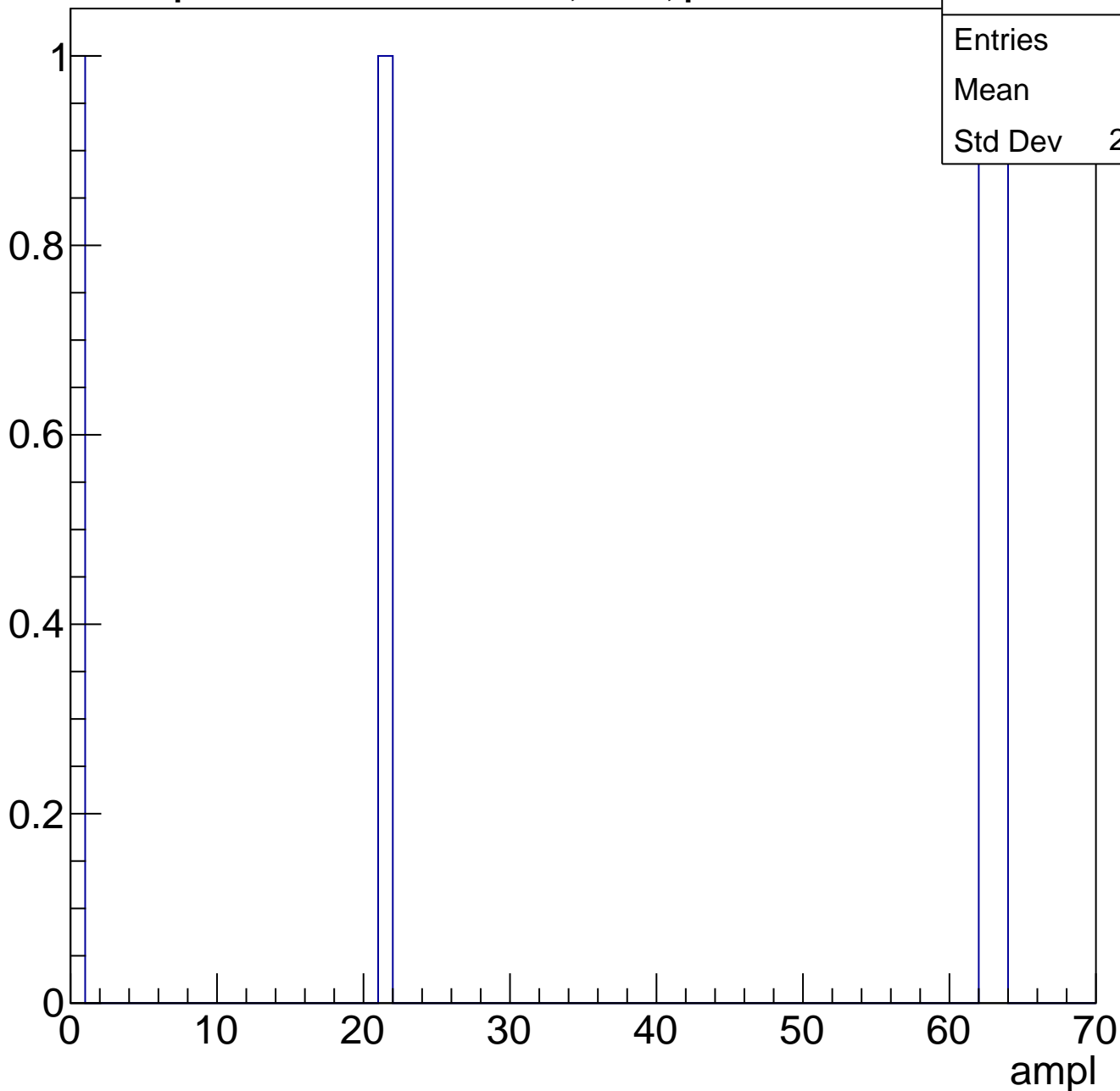
Entry



B1L001S, U16-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch74, adc0

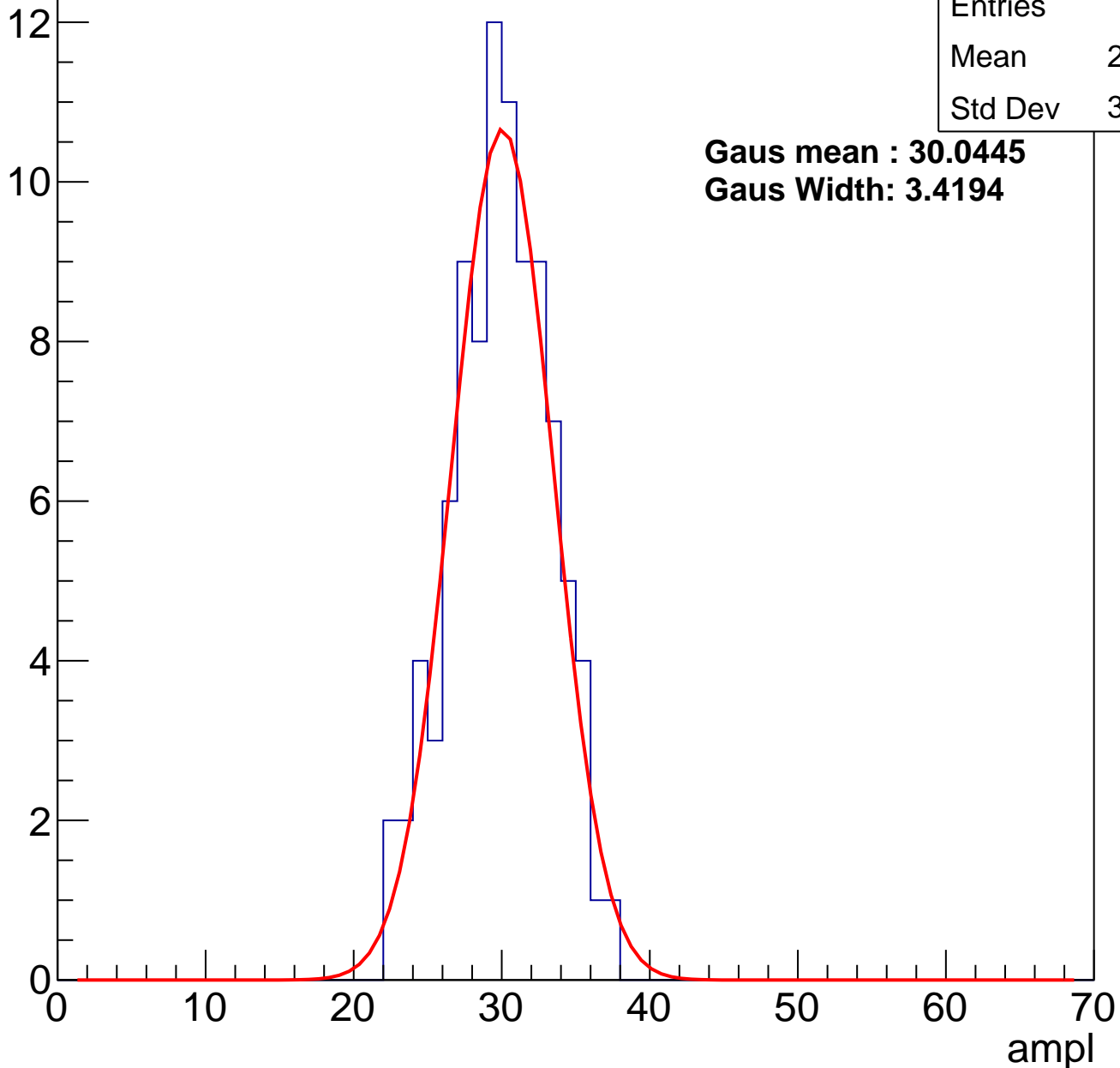
calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	29.49
Std Dev	3.304

Gaus mean : 30.0445

Gaus Width: 3.4194

Entry

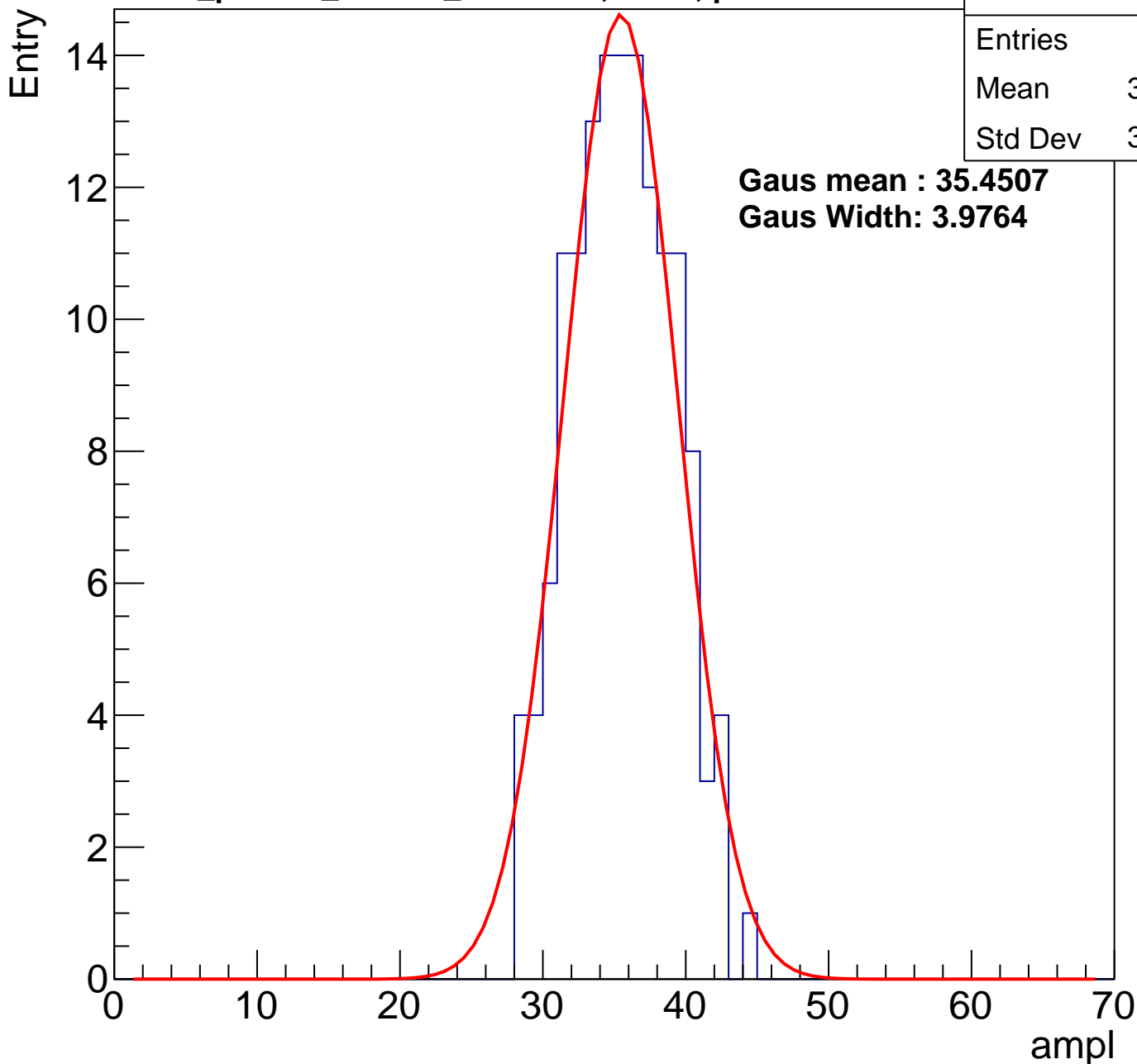


B1L001S, U16-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	35.08
Std Dev	3.525

Gaus mean : 35.4507
Gaus Width: 3.9764



B1L001S, U16-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

141

Mean

42.15

Std Dev

3.488

Gaus mean : 42.6614

Gaus Width: 3.7009

0

10

20

30

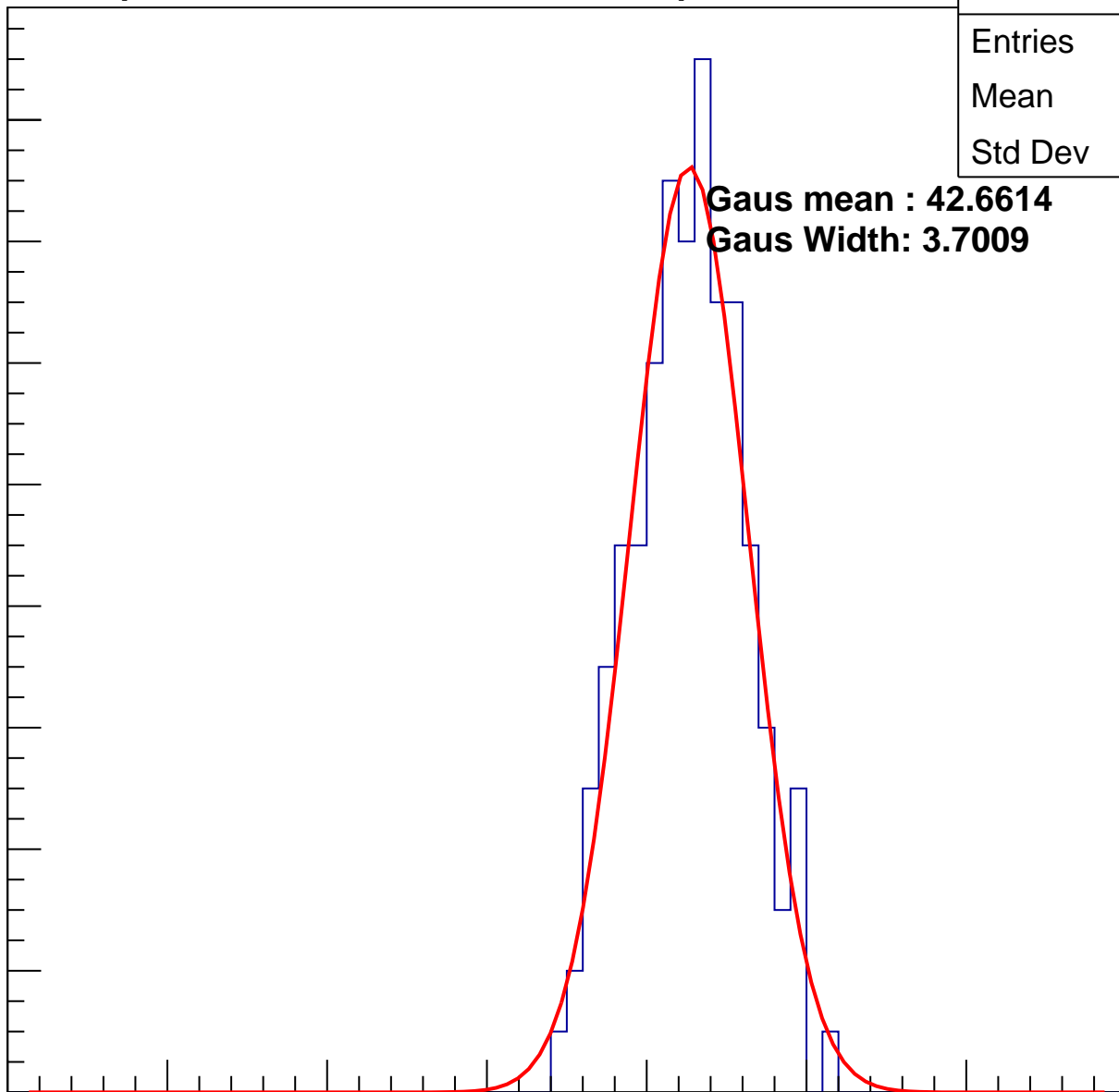
40

50

60

70

ampl

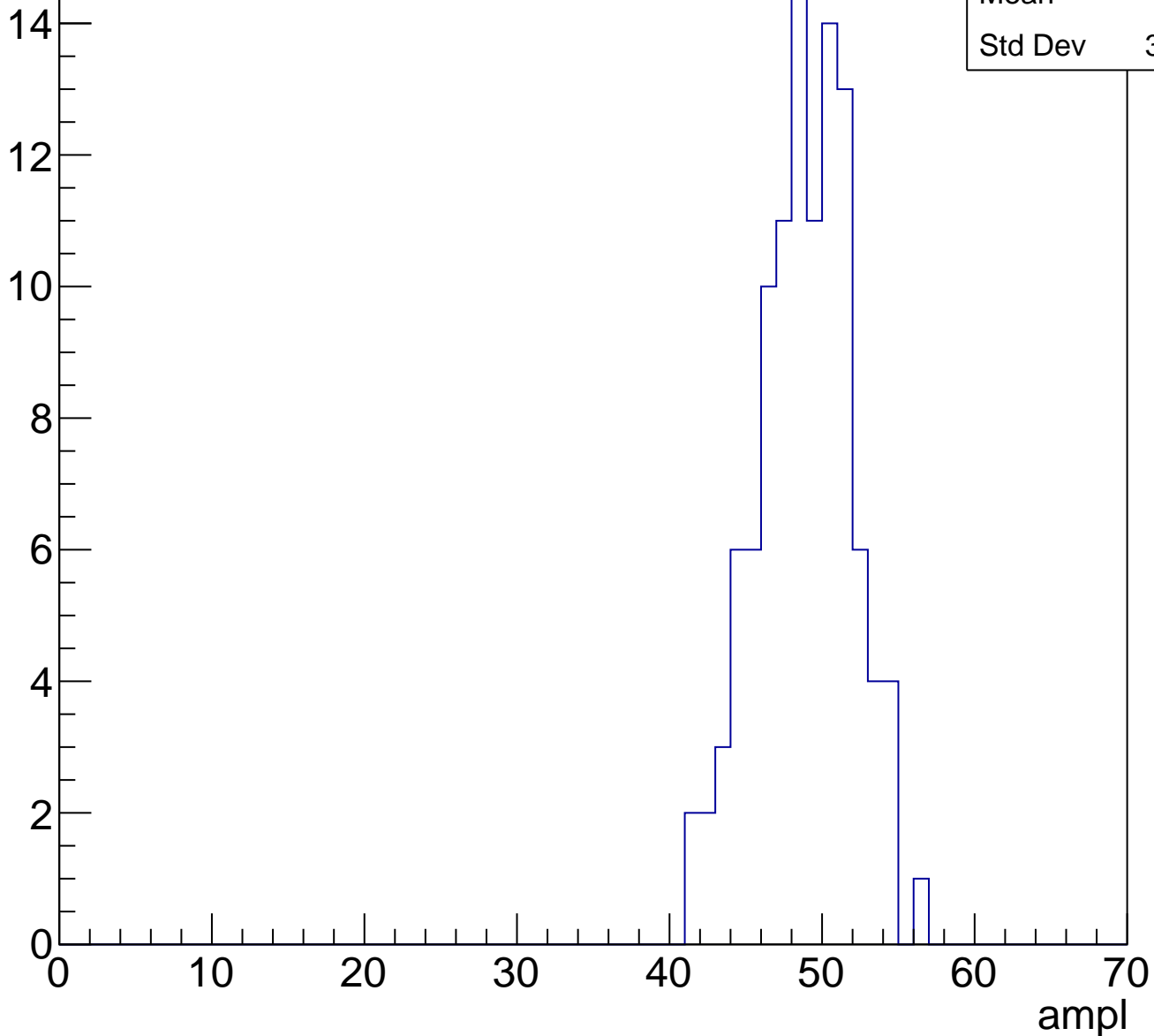


B1L001S, U16-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	48.37
Std Dev	3.072

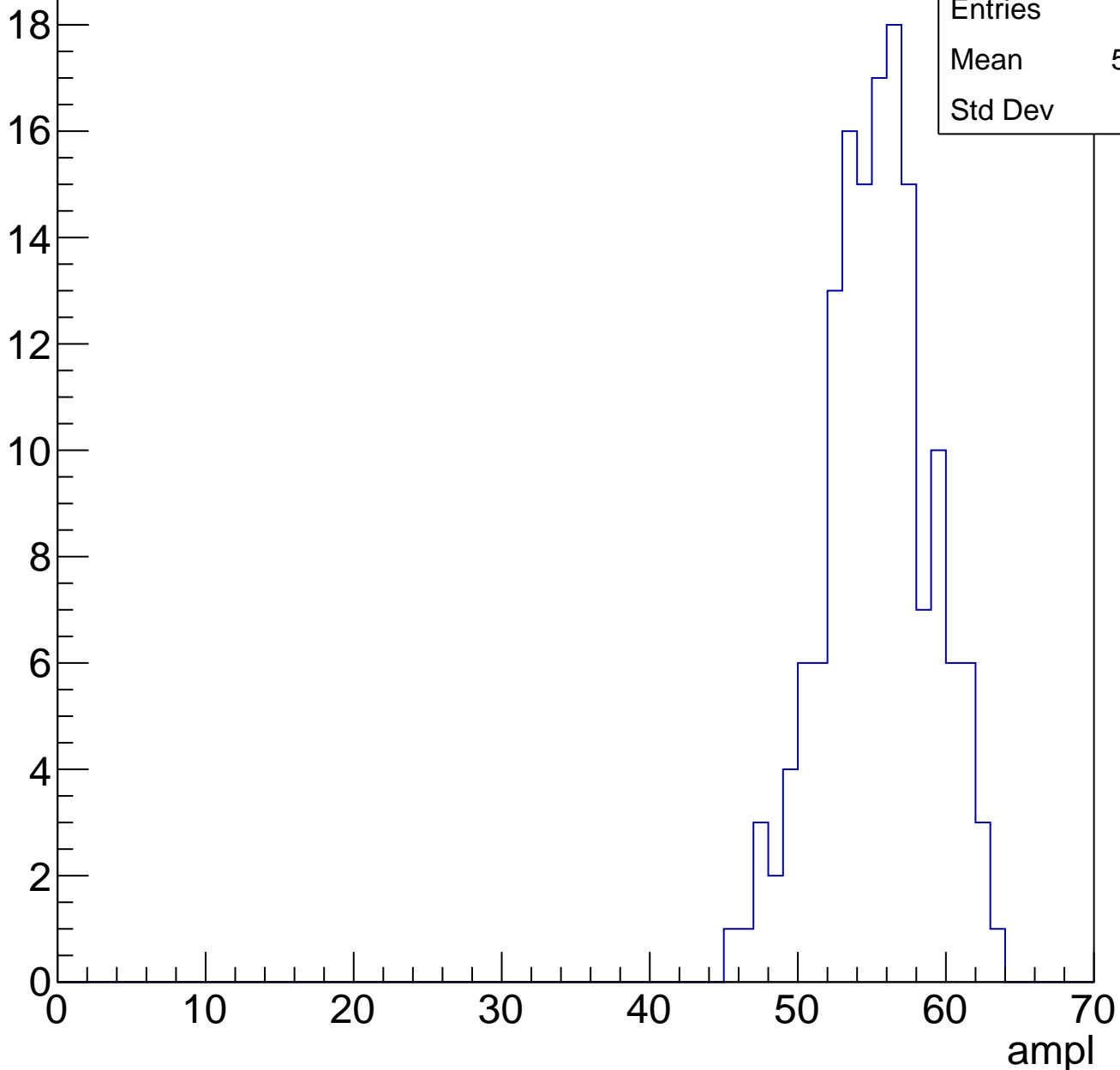
Entry



B1L001S, U16-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch74, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

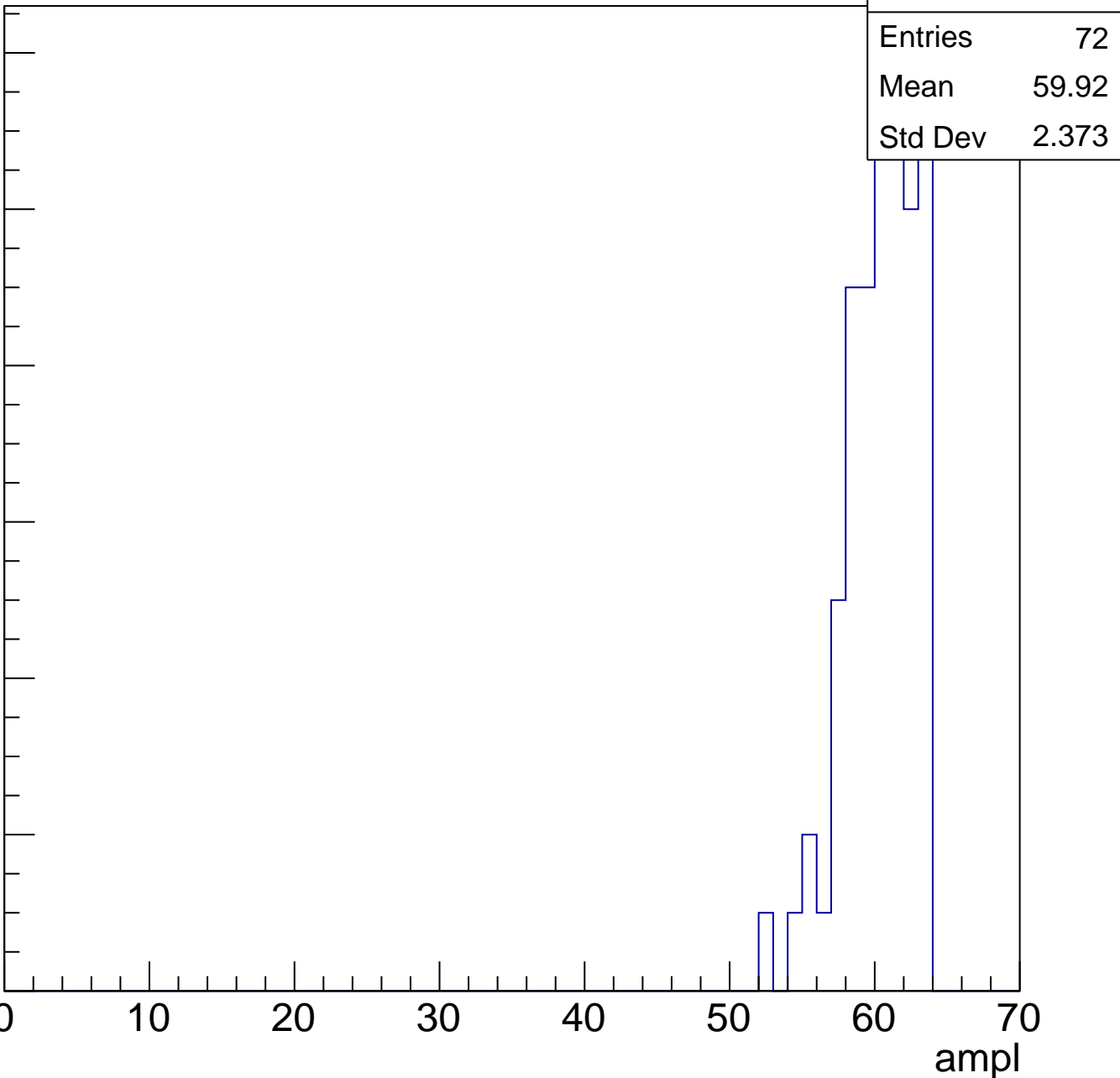
40

50

60

ampl

Entries	72
Mean	59.92
Std Dev	2.373

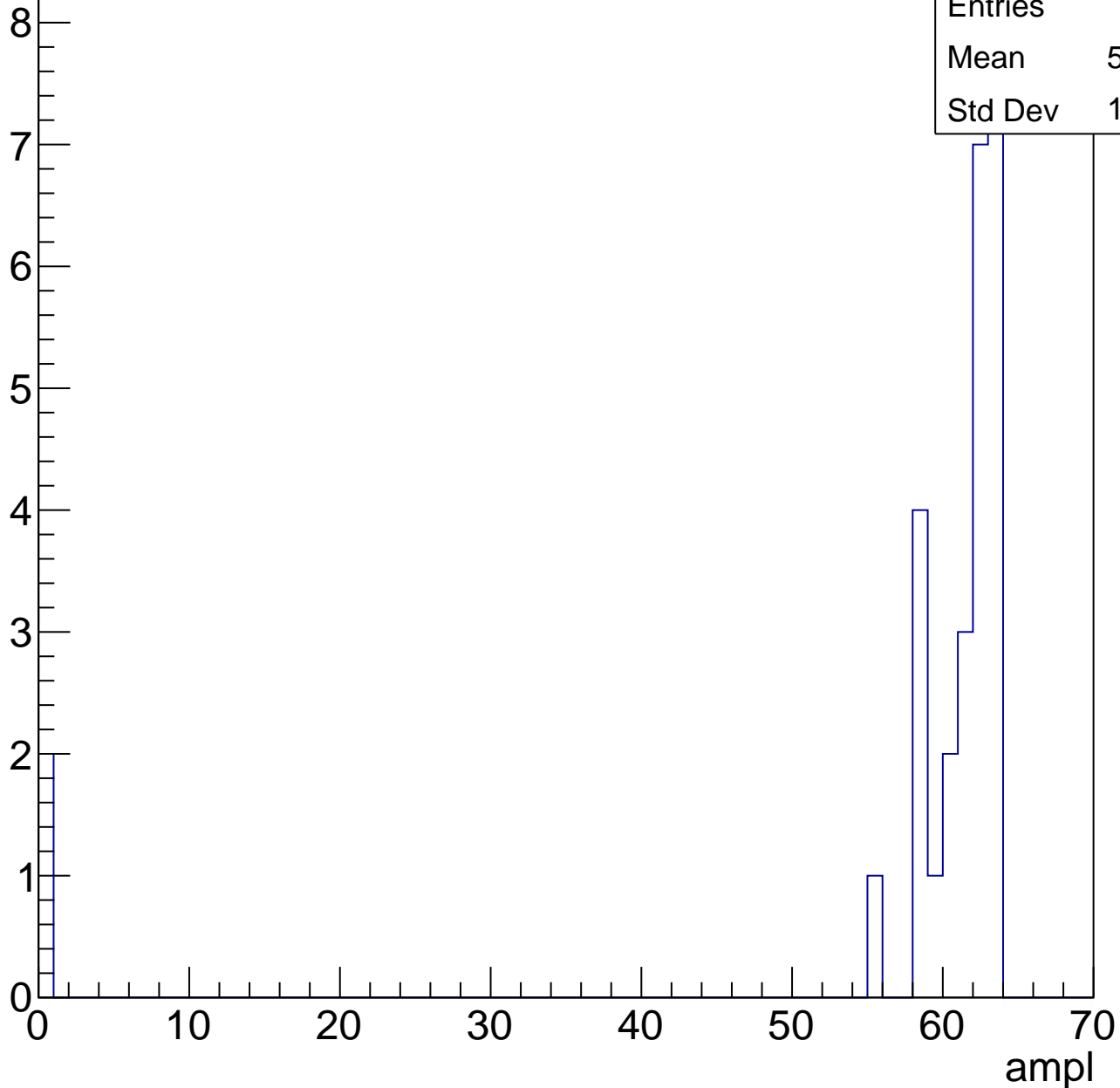


B1L001S, U16-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	28
Mean	56.68
Std Dev	15.85



B1L001S, U16-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L001S, U16-ch75, adc0

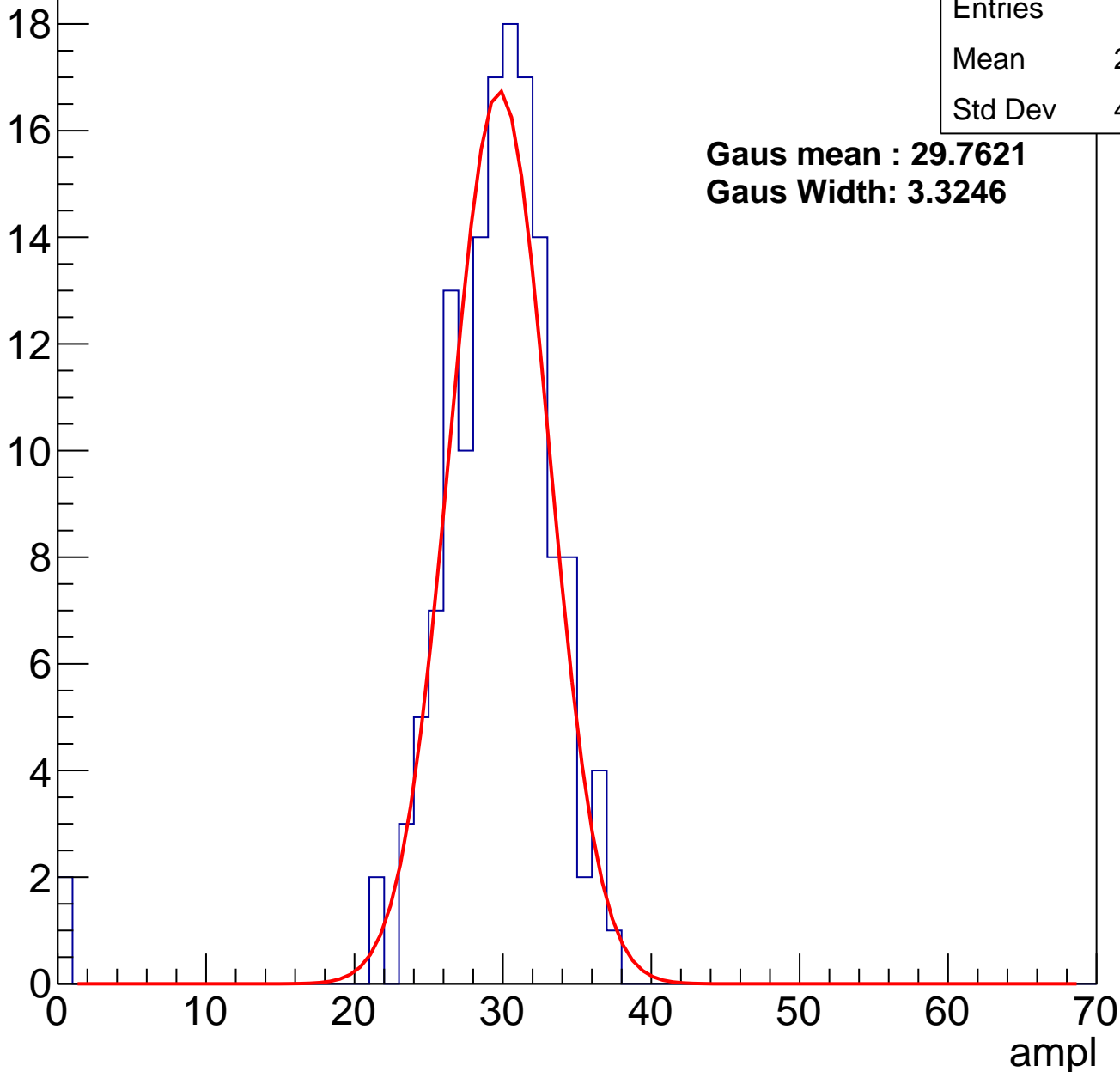
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.97
Std Dev	4.696

Gaus mean : 29.7621

Gaus Width: 3.3246

Entry



B1L001S, U16-ch75, adc1

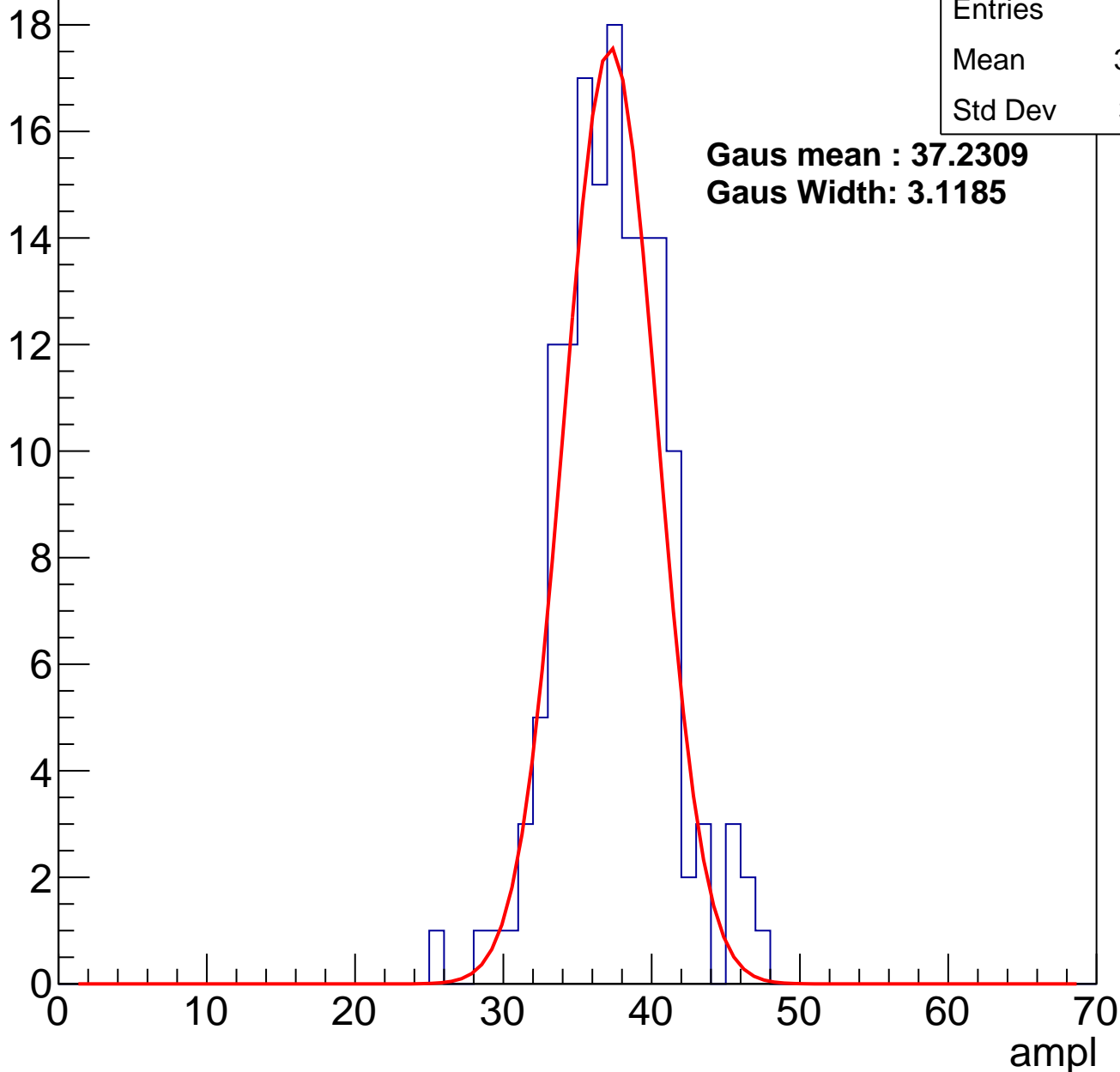
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.95
Std Dev	3.591

Gaus mean : 37.2309

Gaus Width: 3.1185

Entry



B1L001S, U16-ch75, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

107

Mean

43.23

Std Dev

3.225

Gaus mean : 43.7175

Gaus Width: 2.9913

0

10

20

30

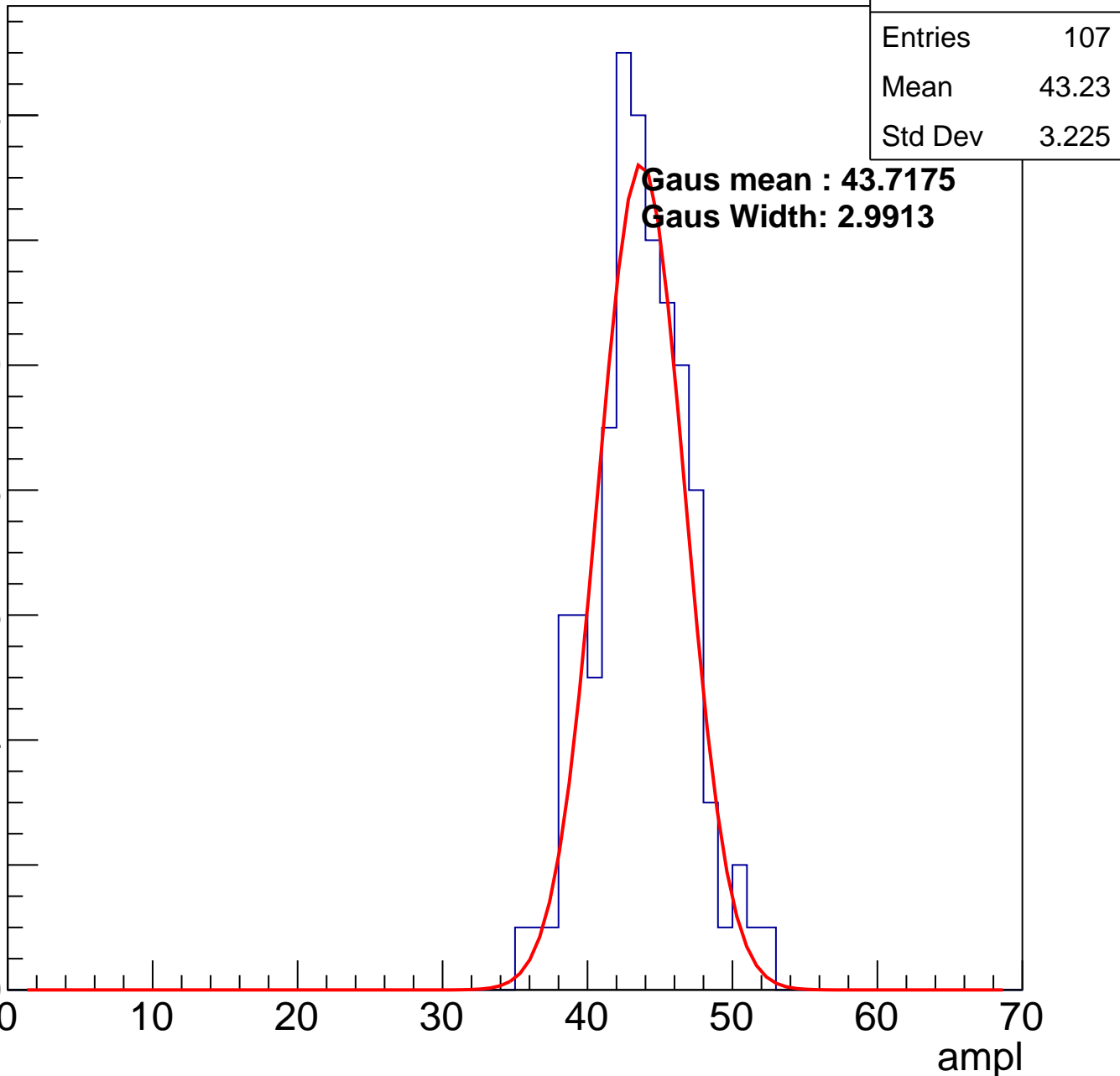
40

50

60

70

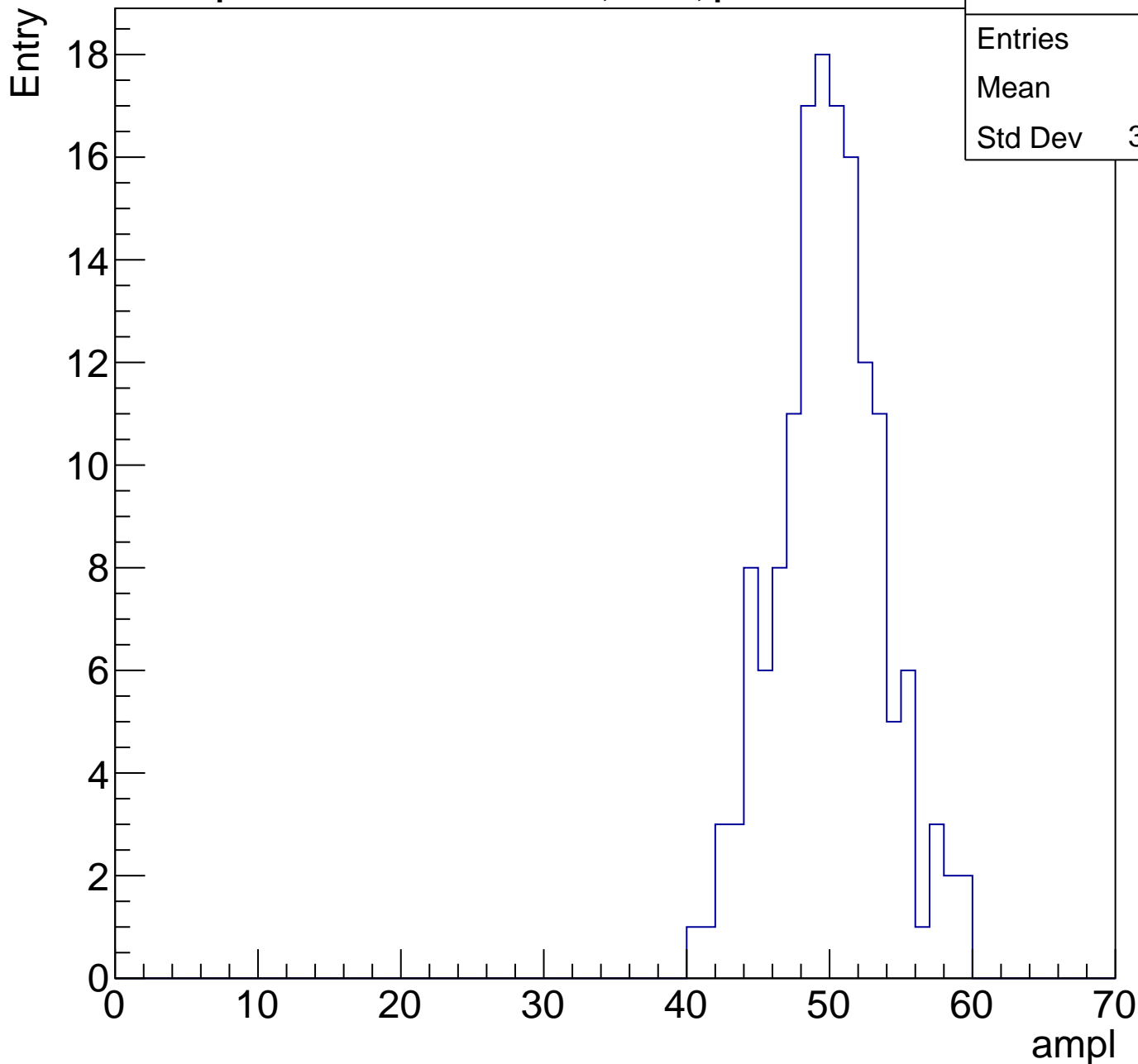
ampl



B1L001S, U16-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	49.5
Std Dev	3.729



B1L001S, U16-ch75, adc4

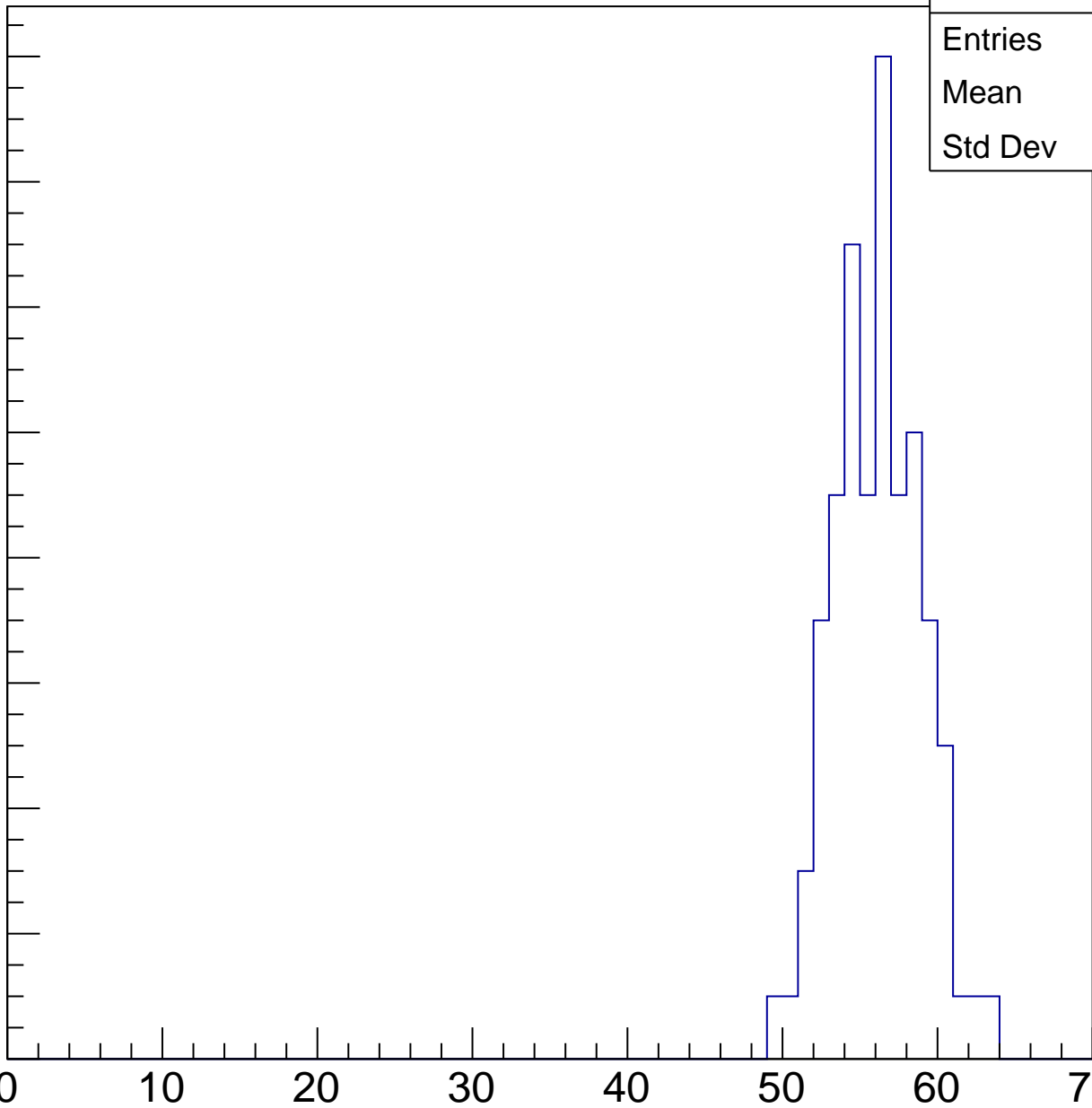
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	55.68
Std Dev	2.756

ampl



B1L001S, U16-ch75, adc5

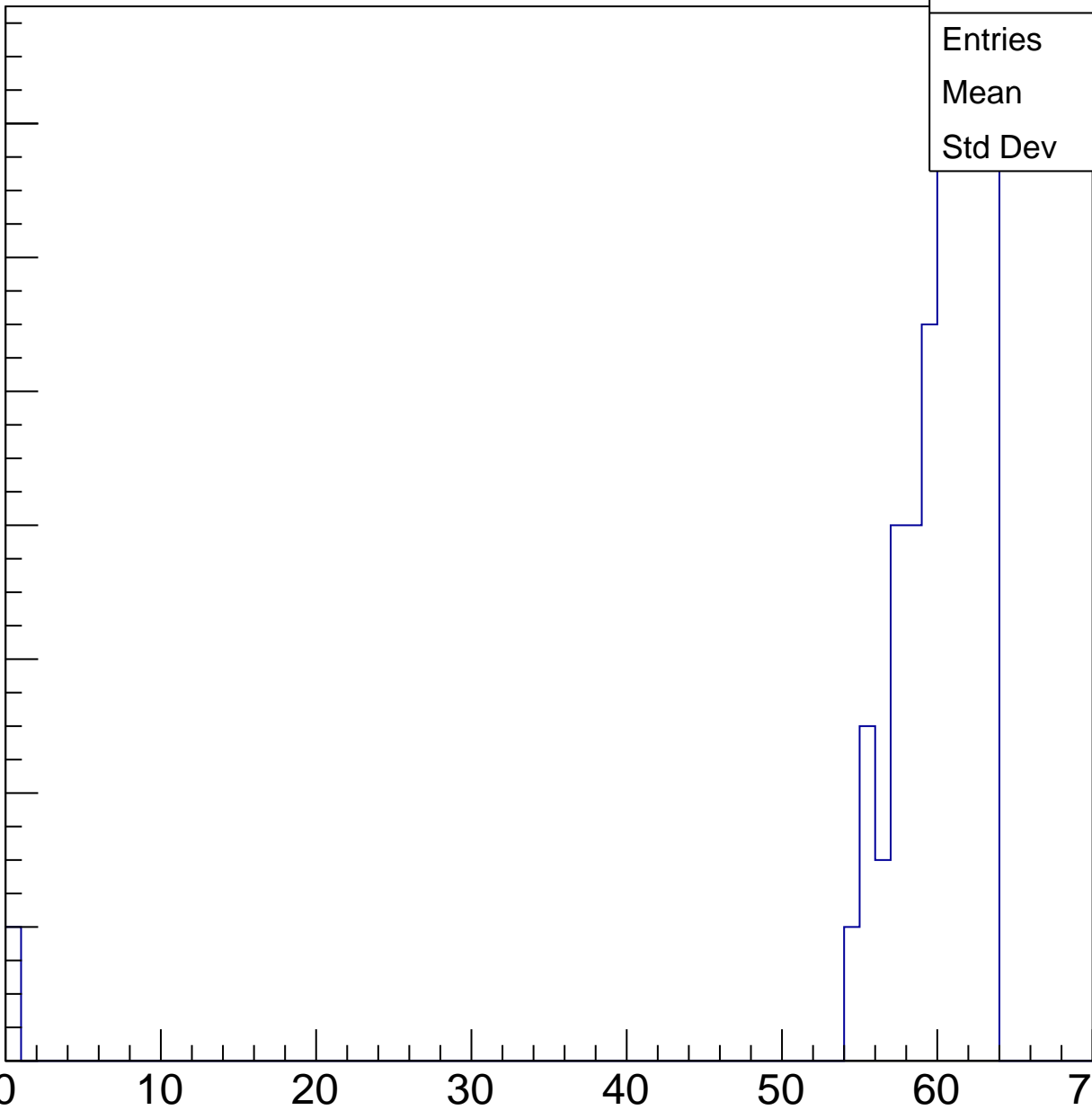
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.59
Std Dev	8.878

ampl

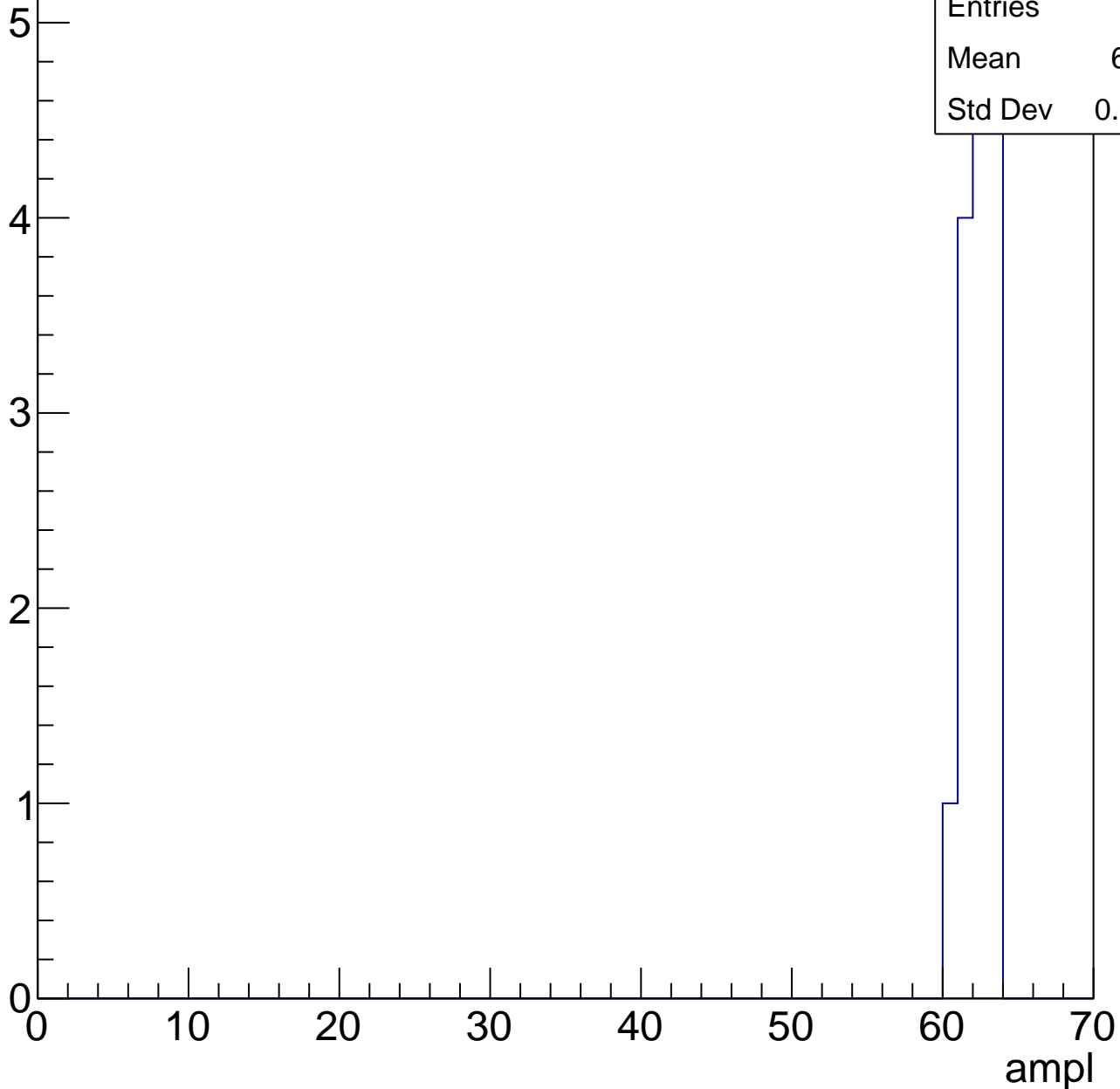


B1L001S, U16-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	61.93
Std Dev	0.9286



B1L001S, U16-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch76, adc0

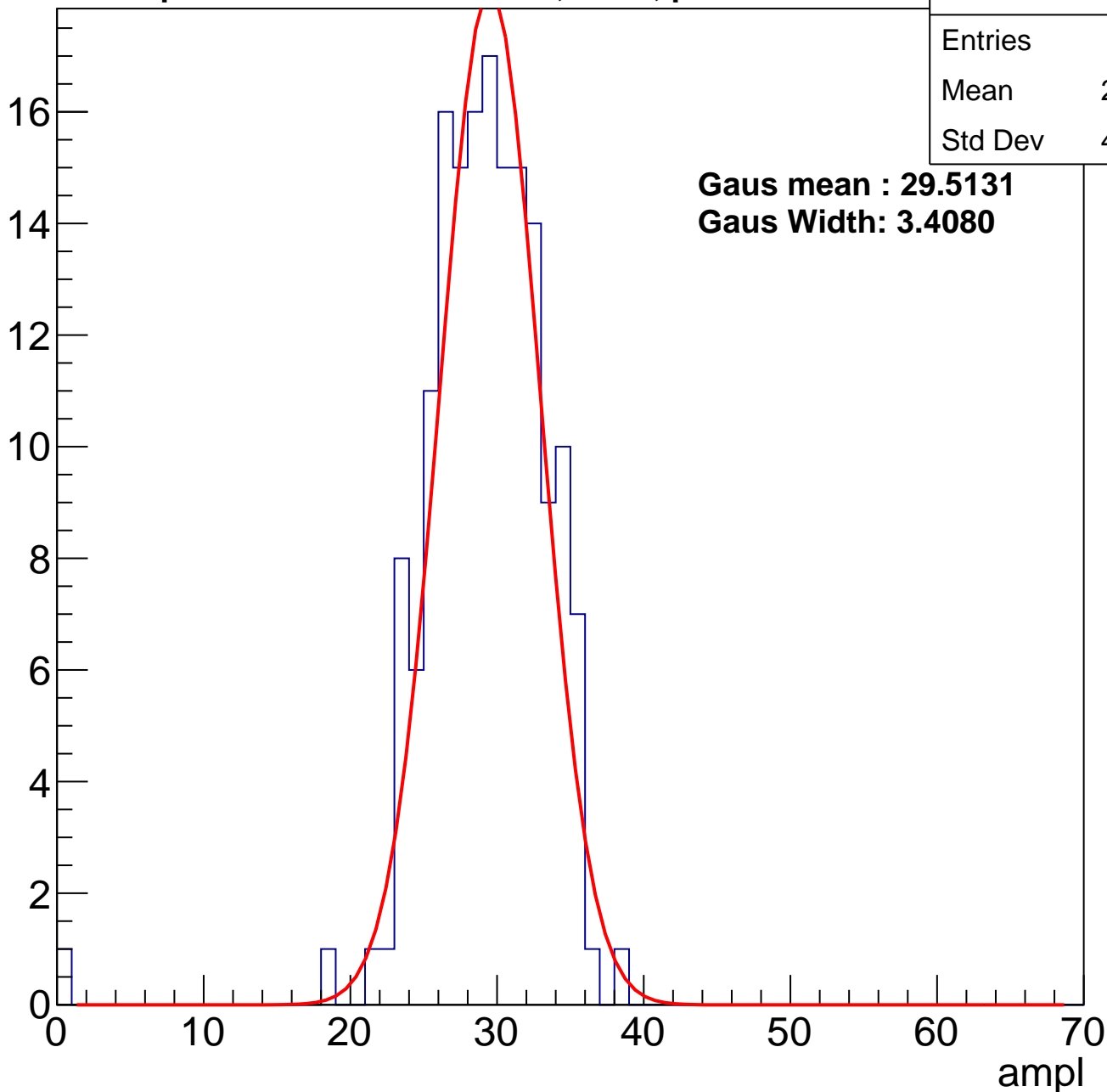
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	28.76
Std Dev	4.175

Gaus mean : 29.5131

Gaus Width: 3.4080

Entry



B1L001S, U16-ch76, adc1

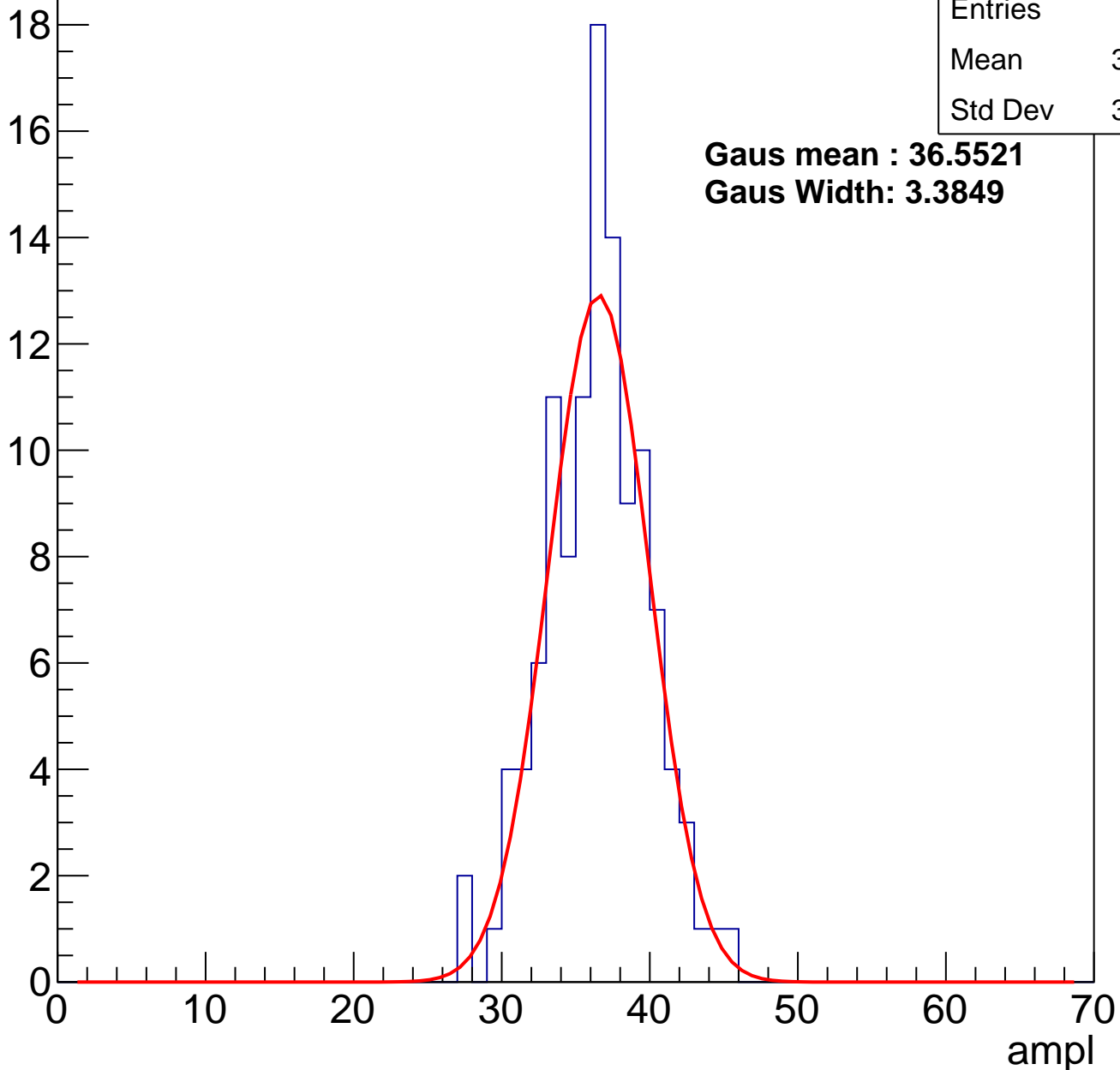
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	35.99
Std Dev	3.422

Gaus mean : 36.5521

Gaus Width: 3.3849

Entry



B1L001S, U16-ch76, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

139

Mean

42.37

Std Dev

3.418

Gaus mean : 43.0967

Gaus Width: 3.3956

0

10

20

30

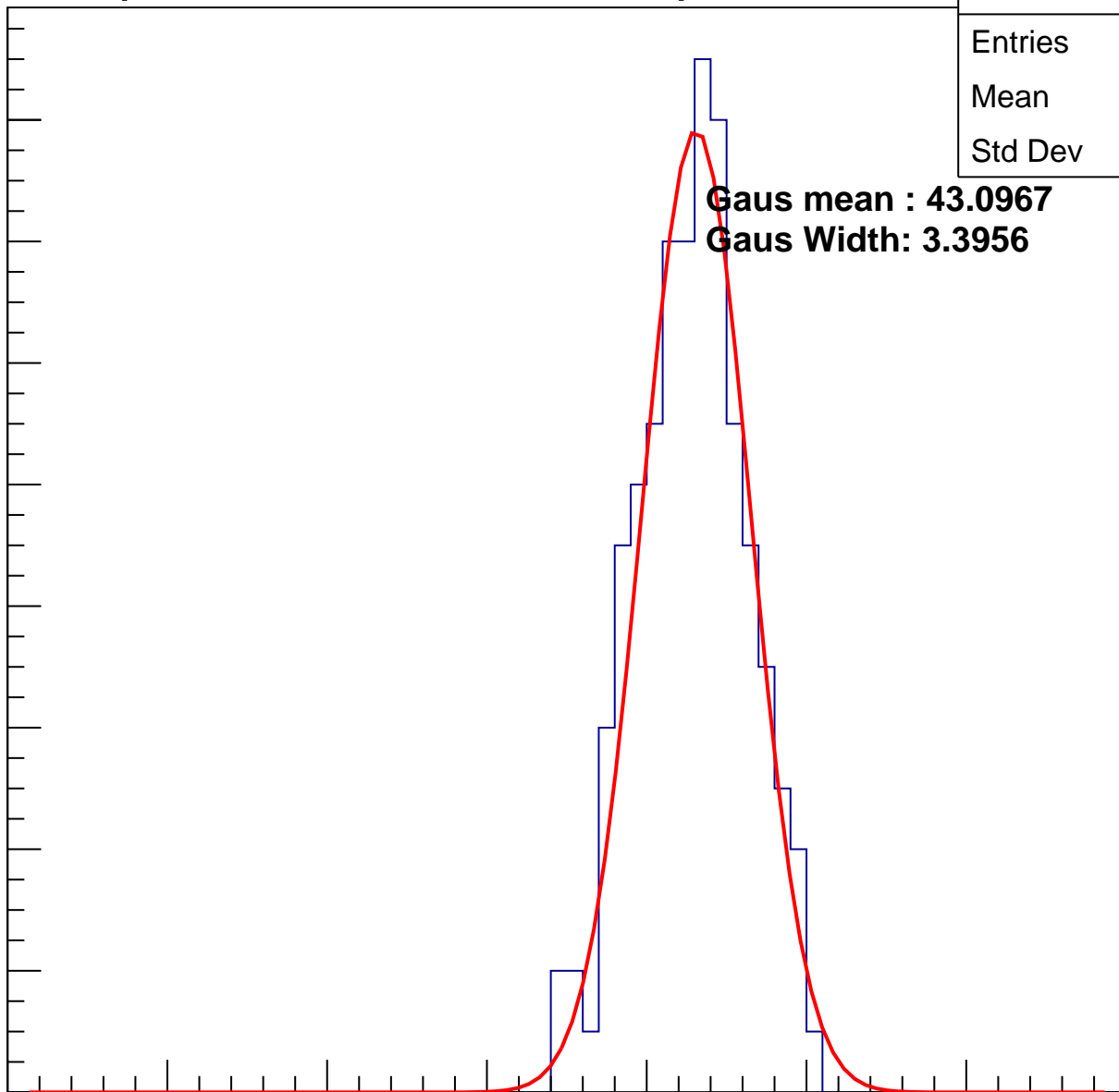
40

50

60

70

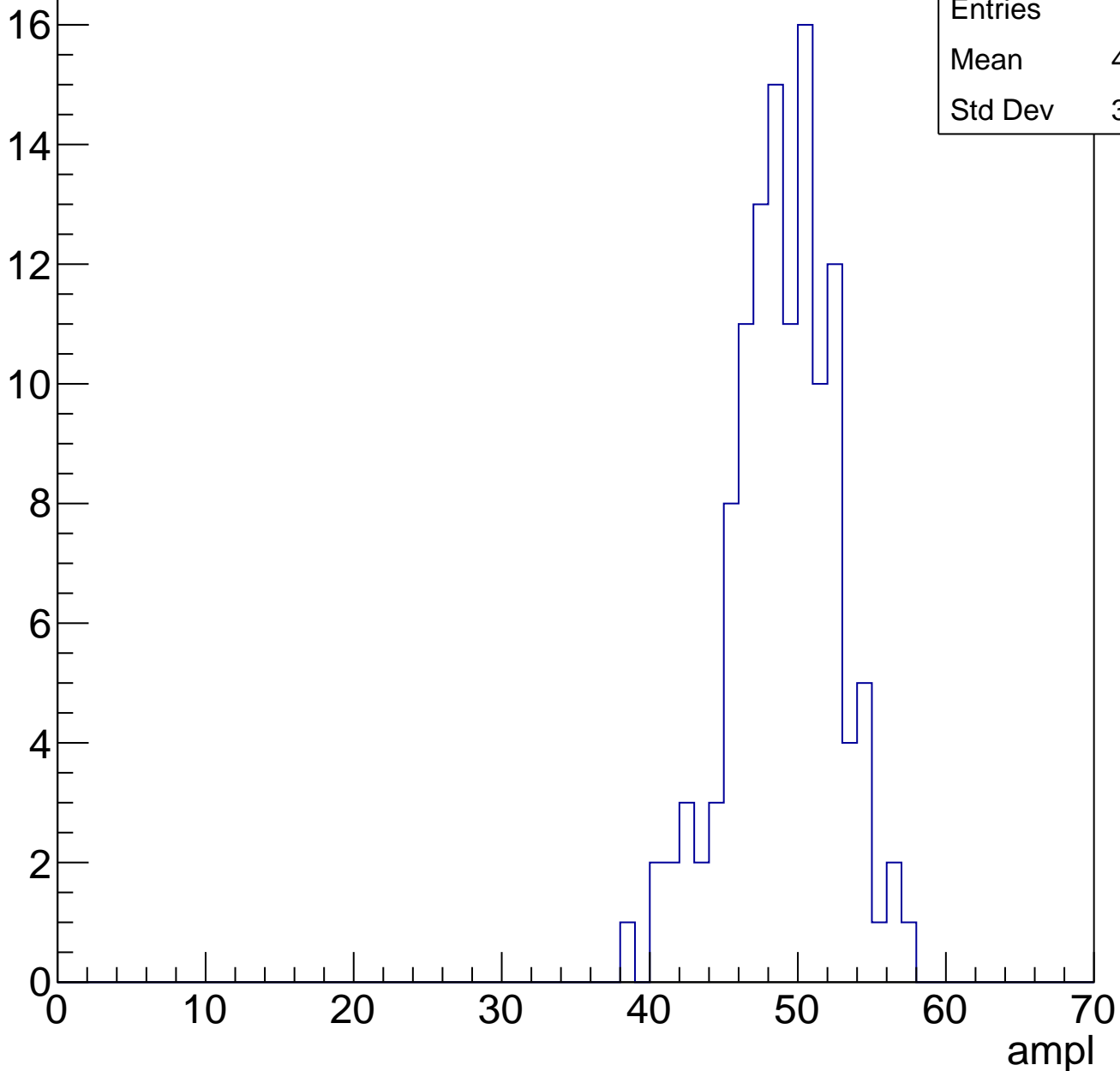
ampl



B1L001S, U16-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

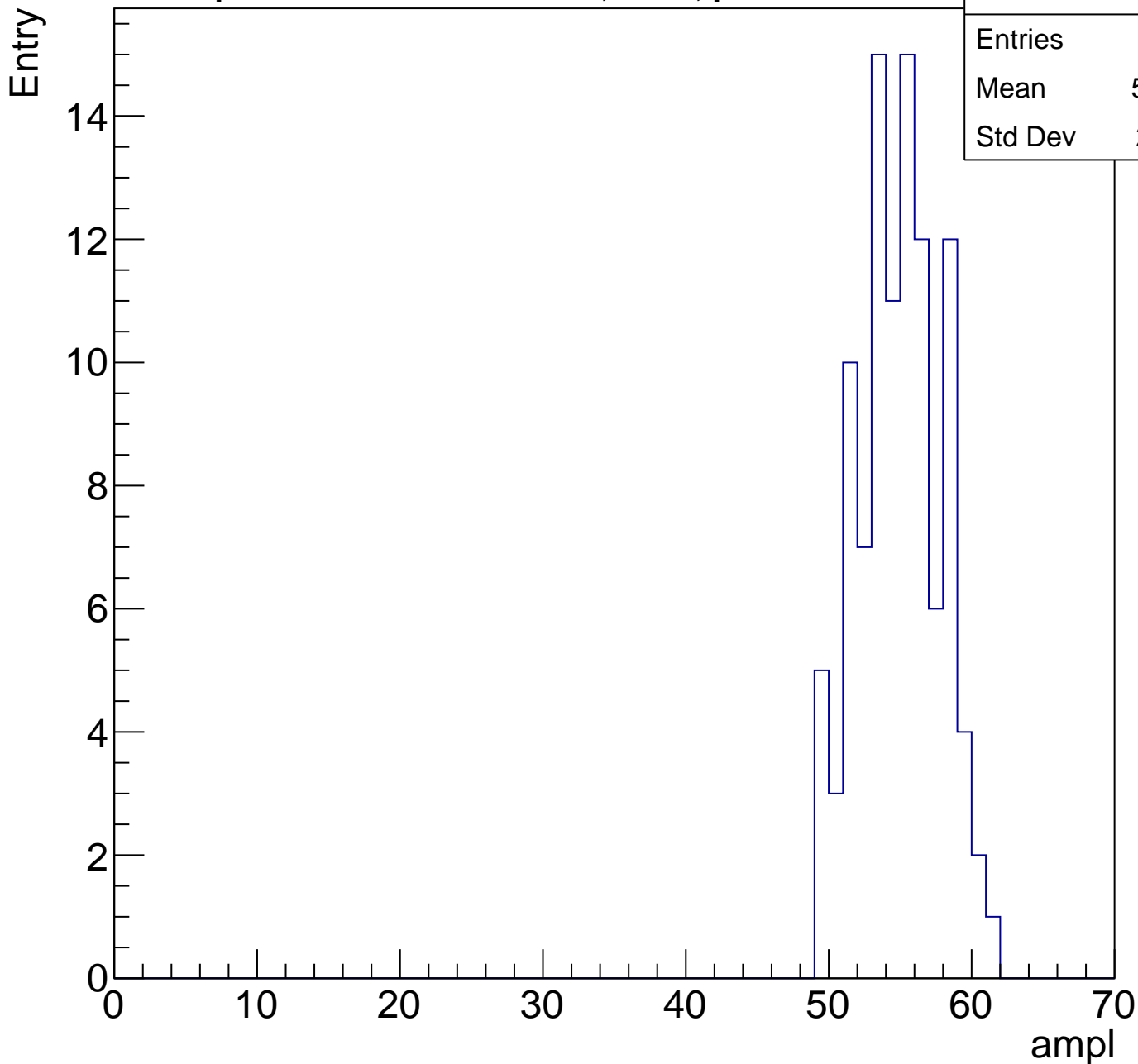
Entry



B1L001S, U16-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	54.47
Std Dev	2.811



B1L001S, U16-ch76, adc5

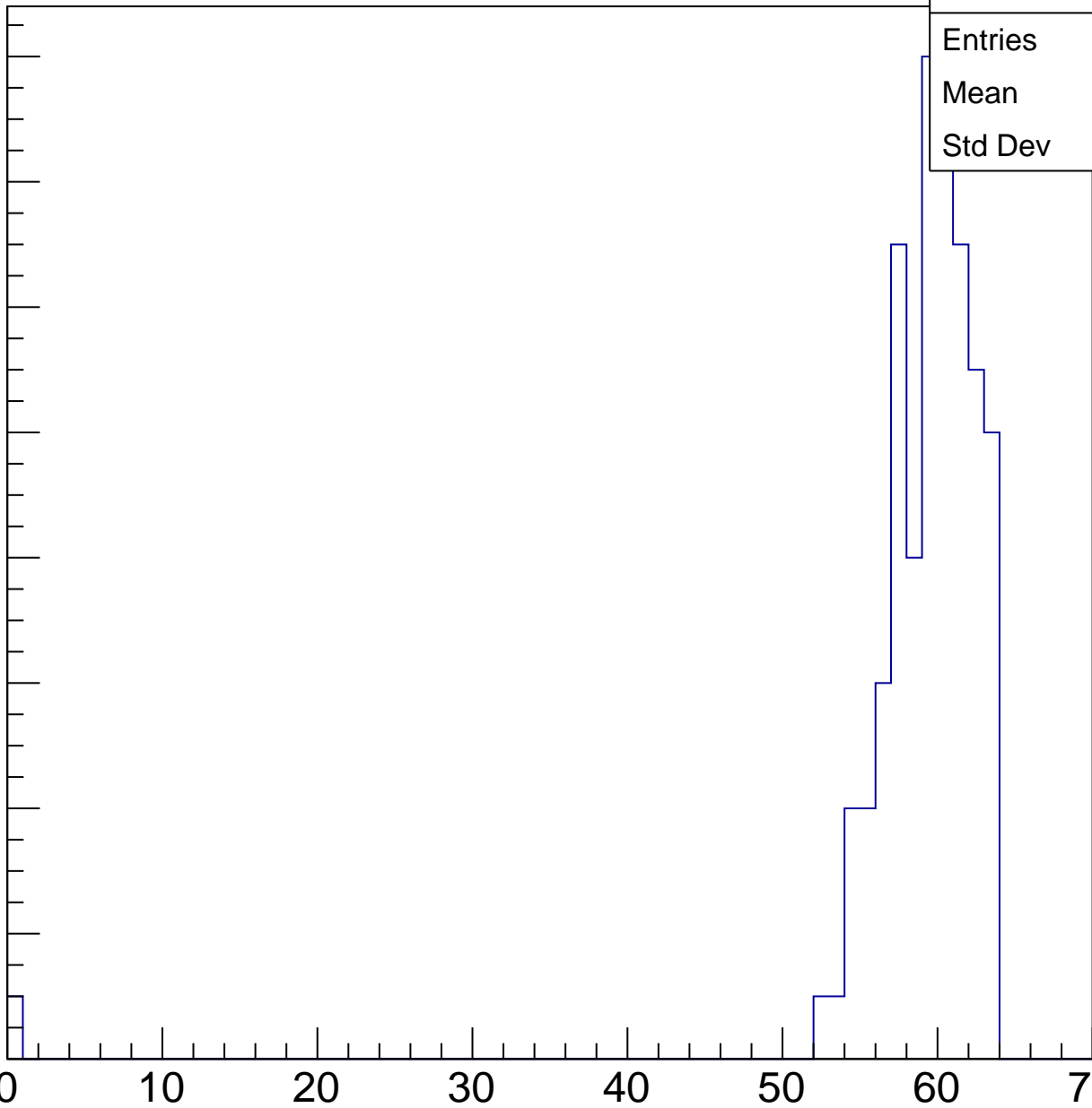
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.57
Std Dev	6.318

ampl

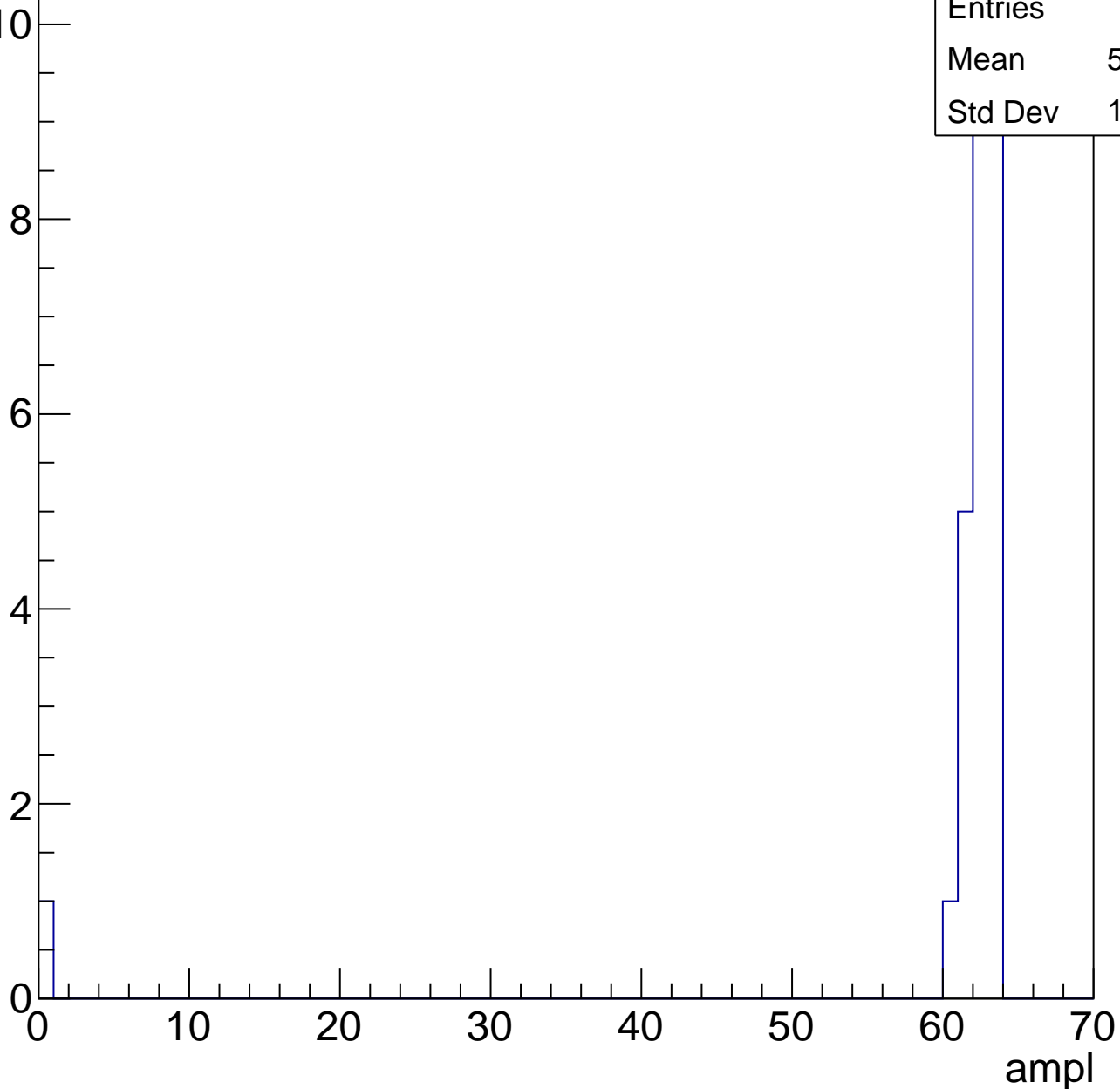


B1L001S, U16-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

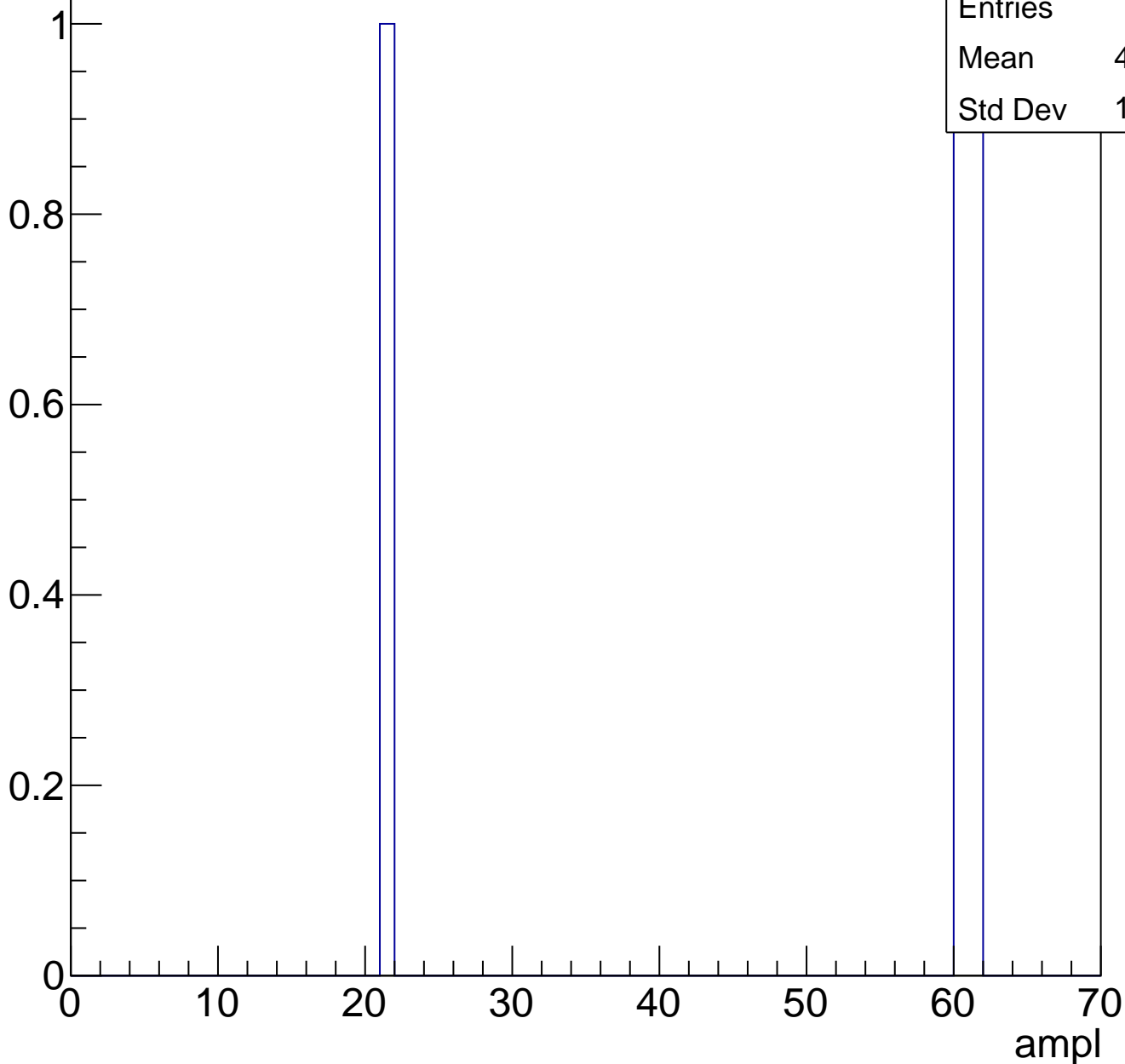
Entries	26
Mean	59.73
Std Dev	11.98



B1L001S, U16-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

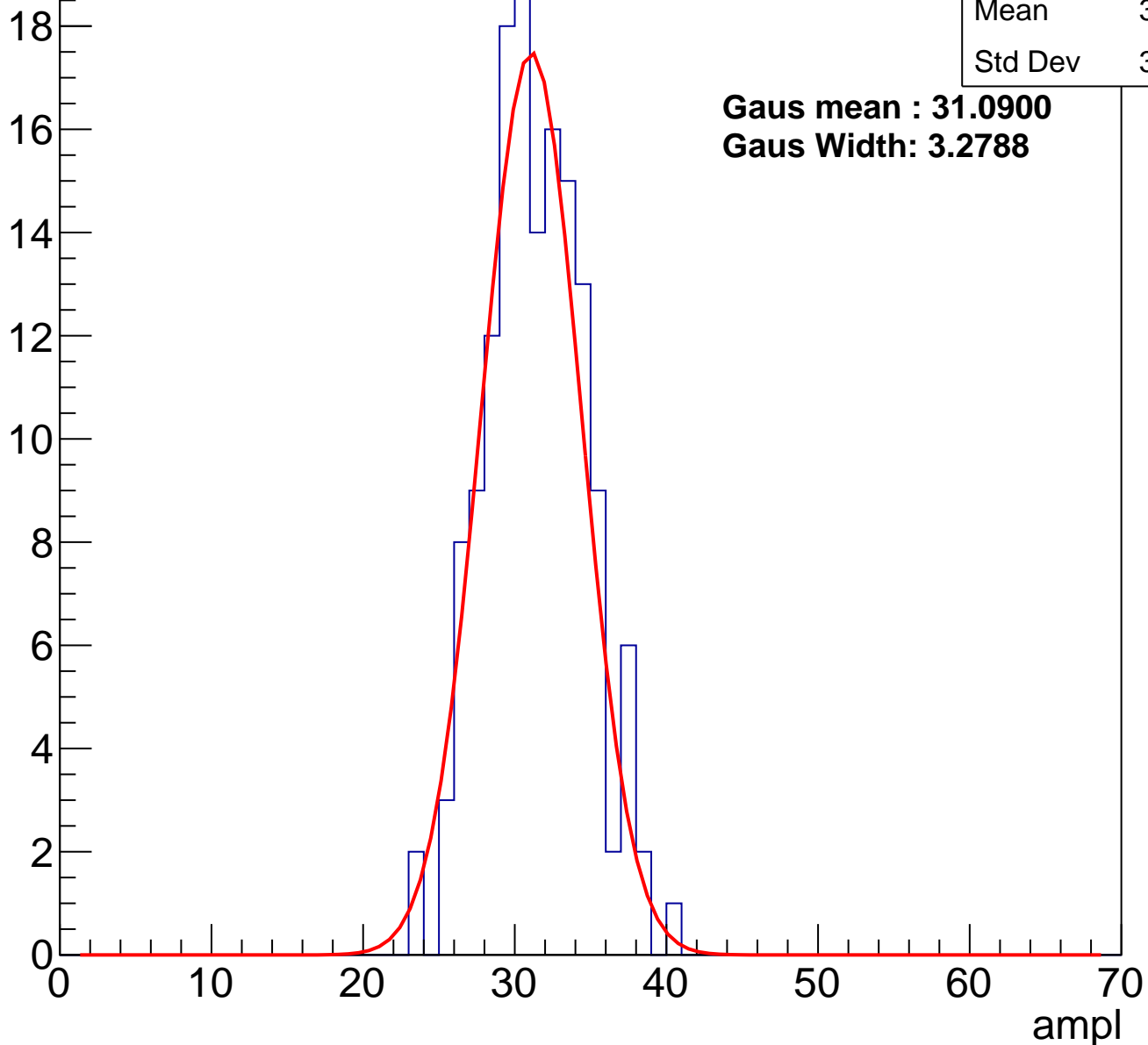
Entry



B1L001S, U16-ch77, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch77, adc1

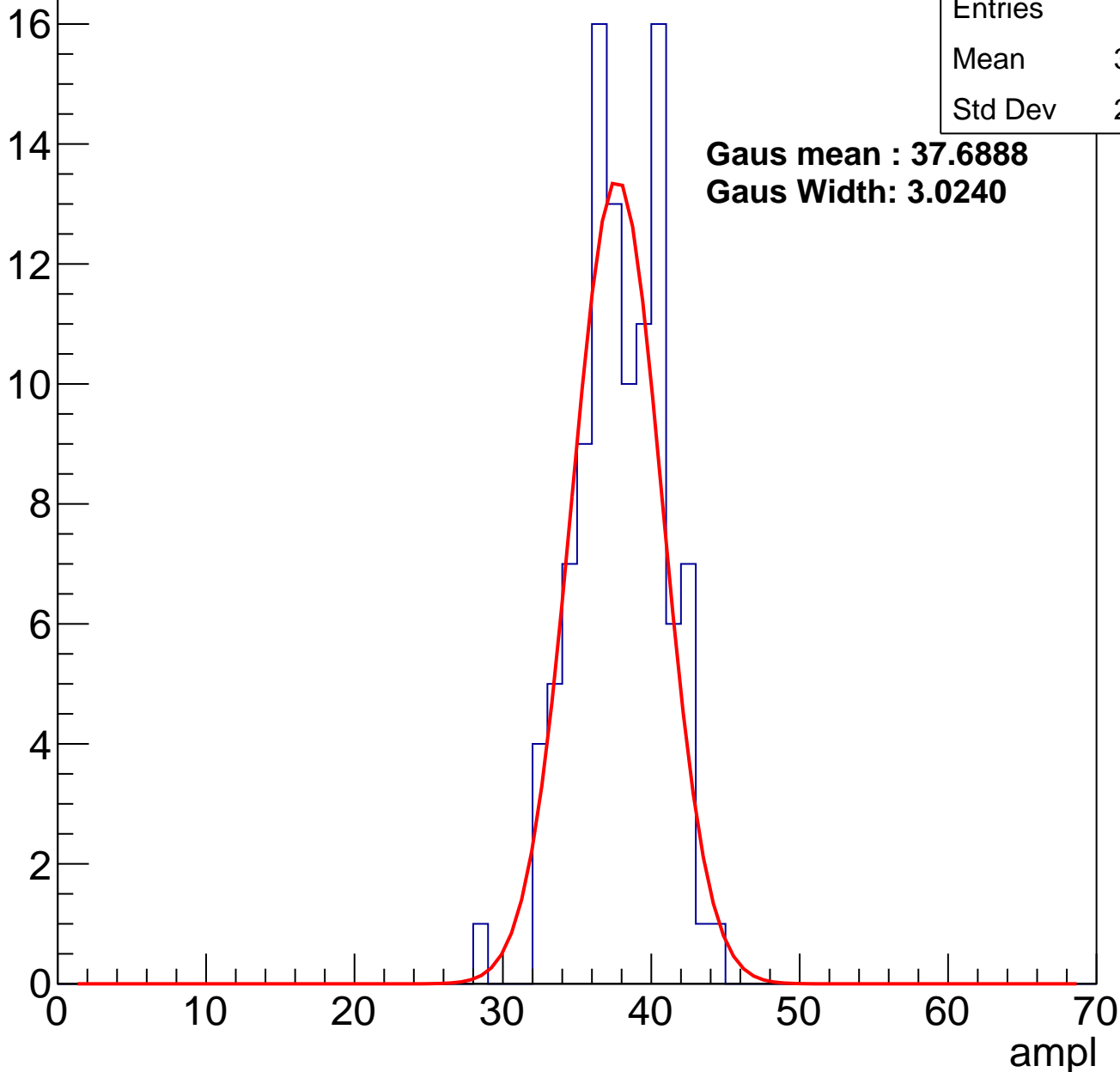
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	37.45
Std Dev	2.914

Gaus mean : 37.6888

Gaus Width: 3.0240

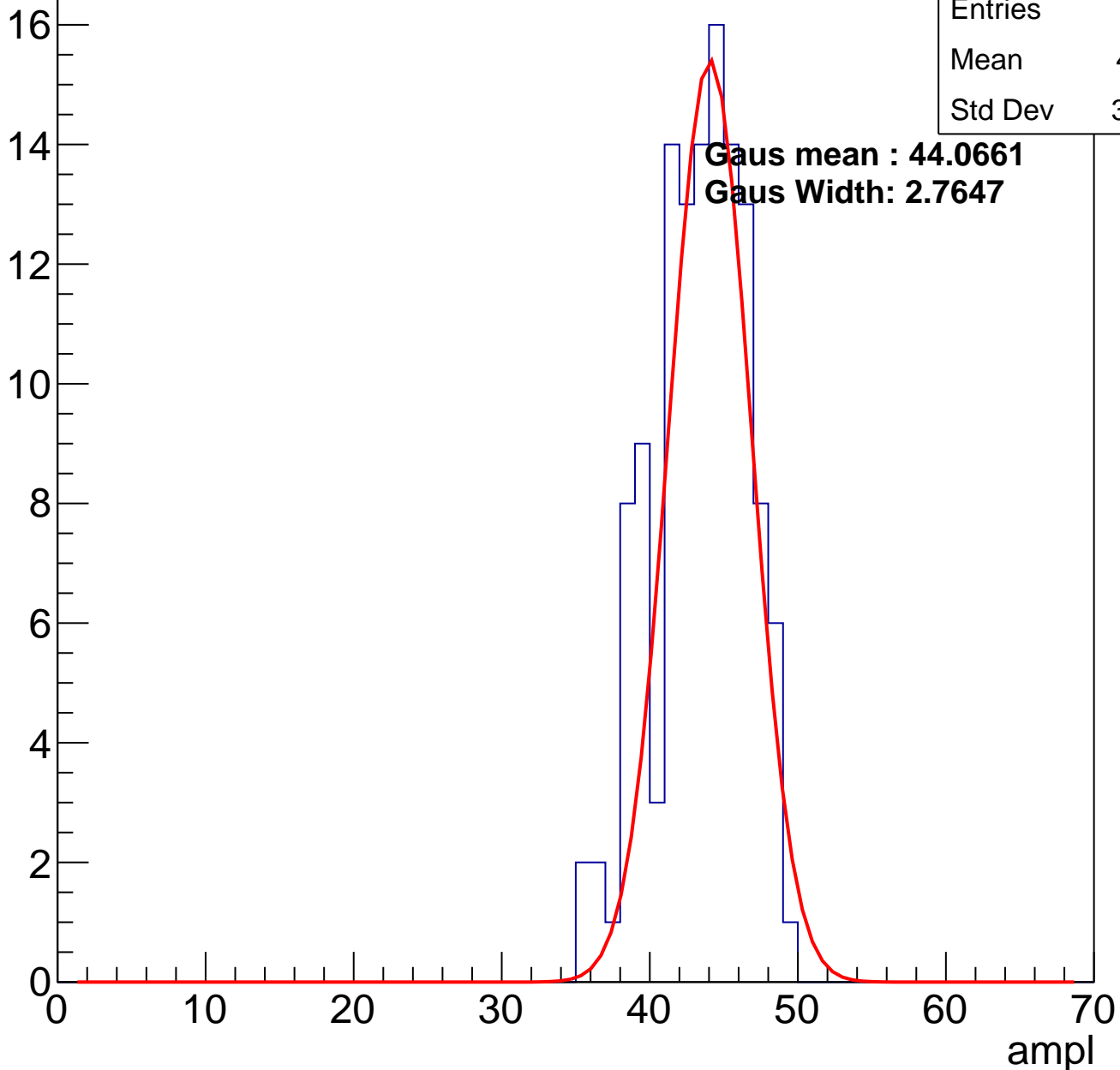
Entry



B1L001S, U16-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

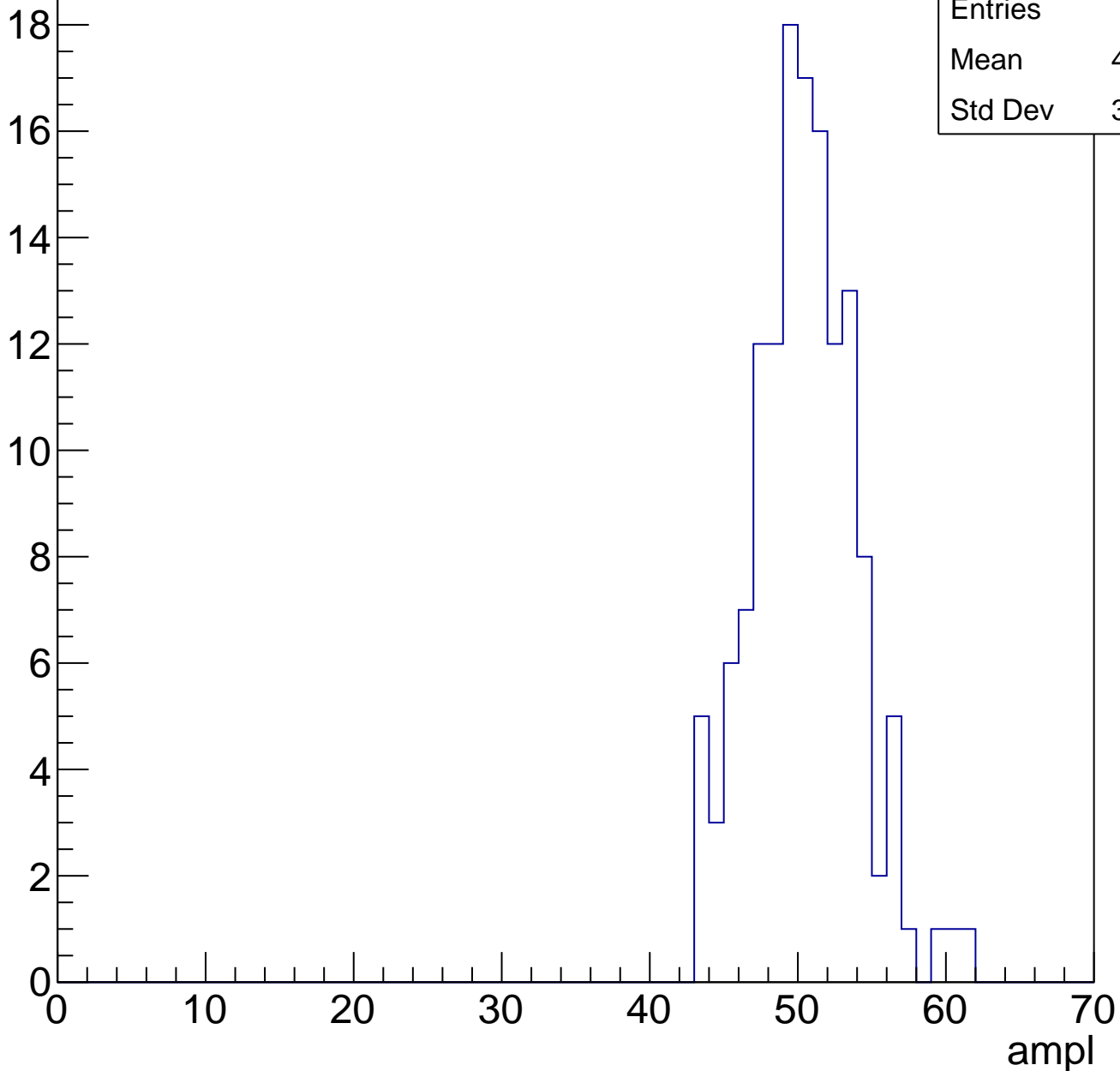
Entry



B1L001S, U16-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

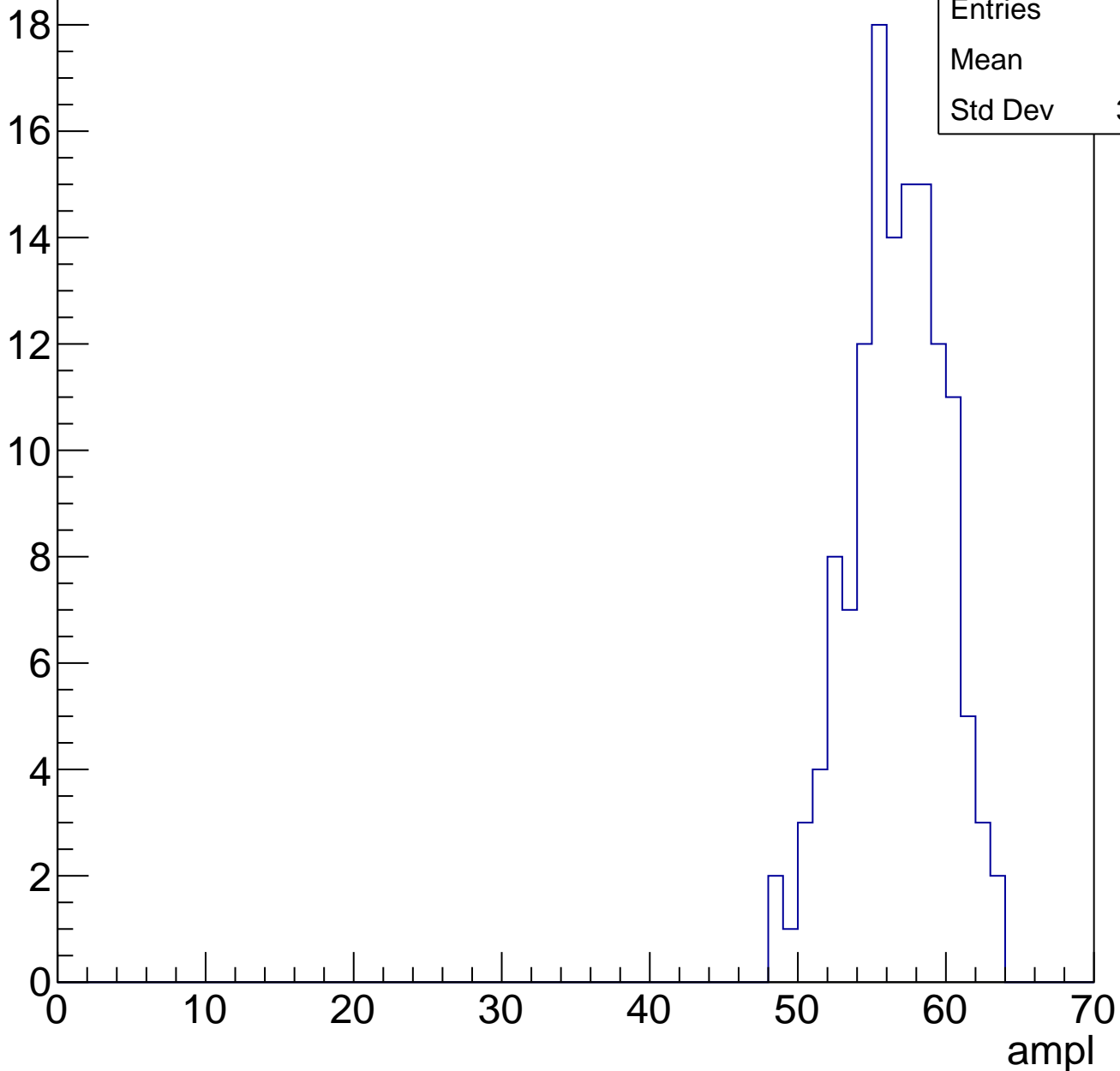
Entry



B1L001S, U16-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	132
Mean	56.2
Std Dev	3.171

B1L001S, U16-ch77, adc5

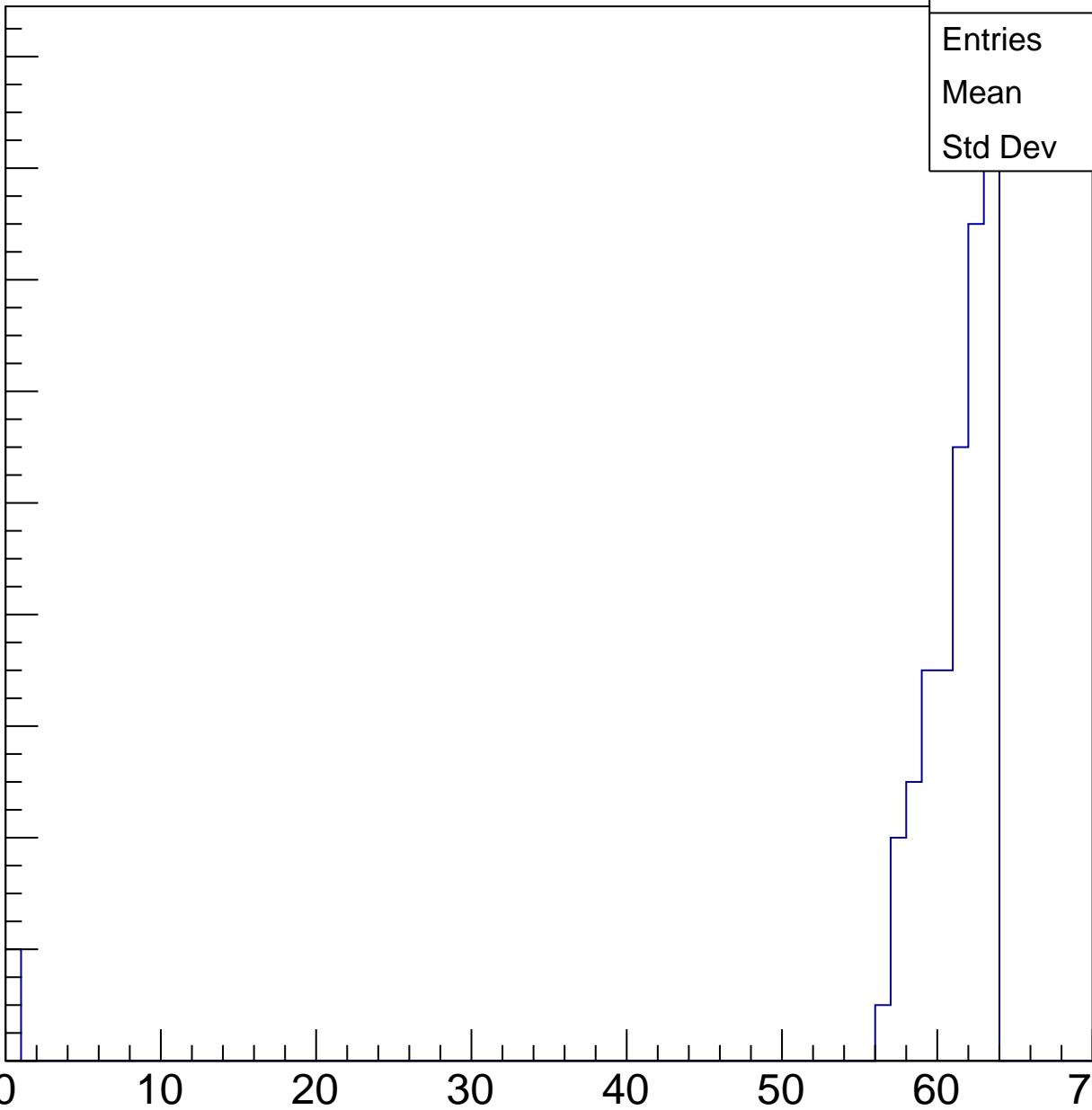
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	70
Mean	59.17
Std Dev	10.33

ampl



B1L001S, U16-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

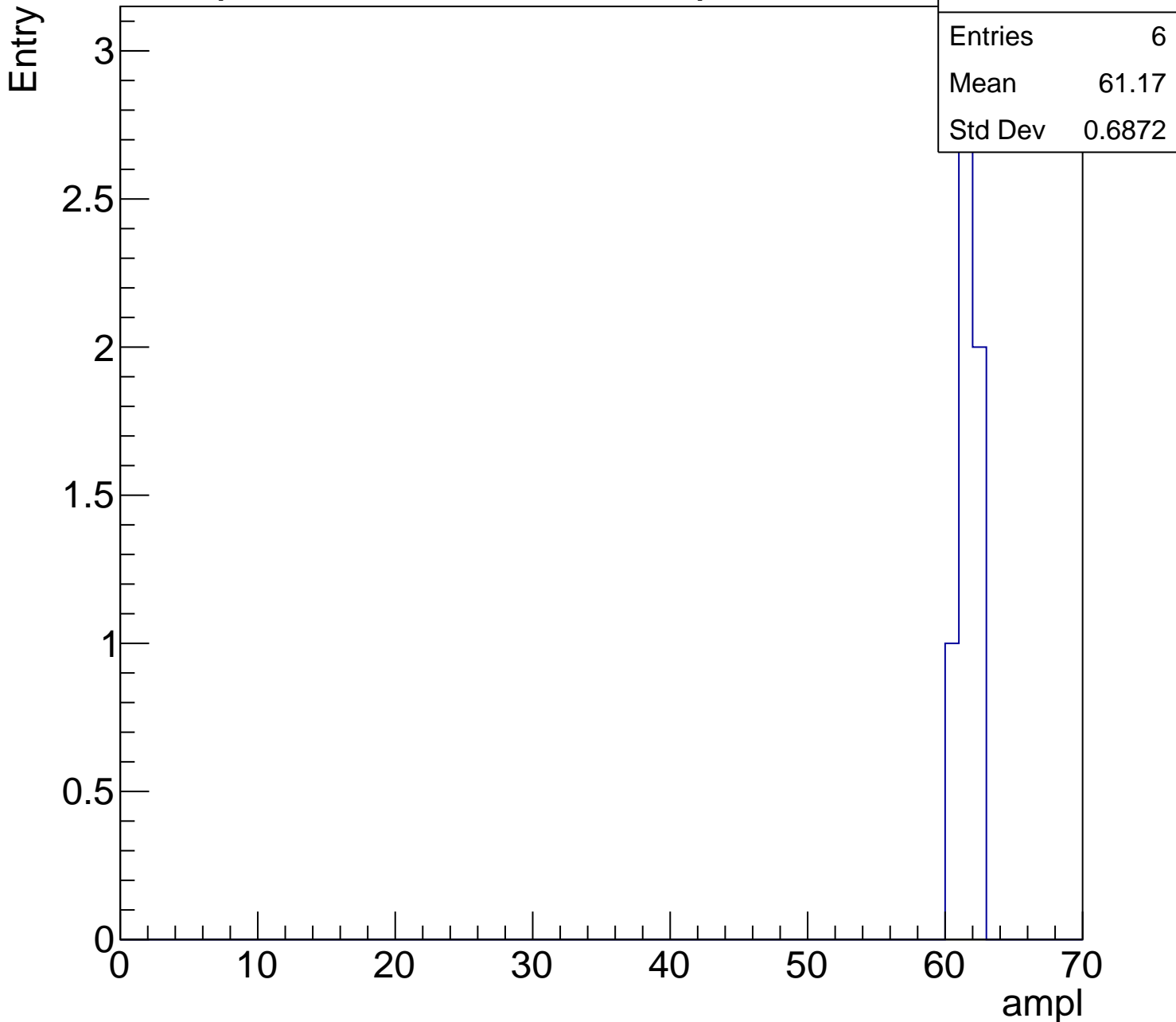
6

Mean

61.17

Std Dev

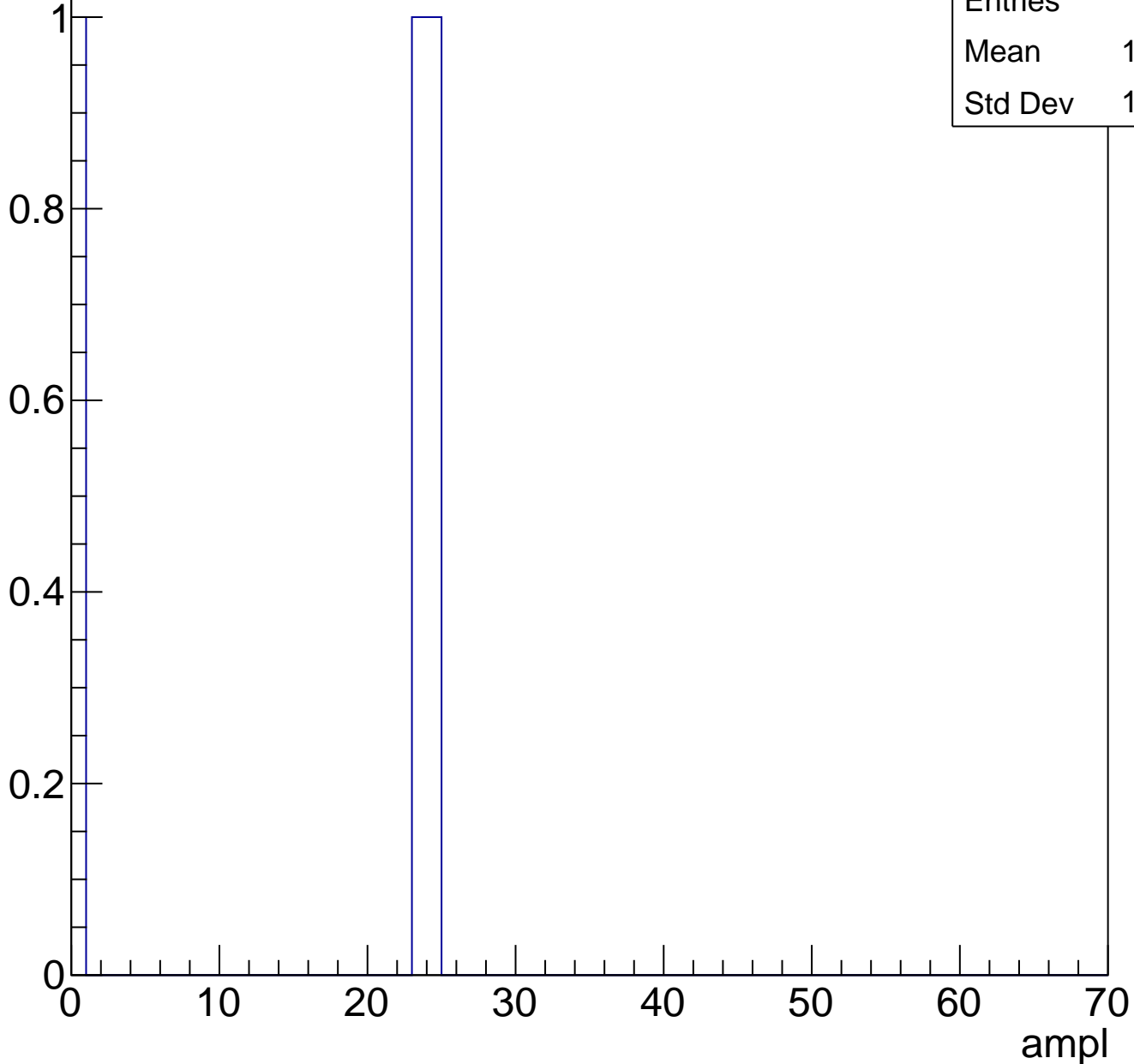
0.6872



B1L001S, U16-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.07
Std Dev	4.728

Gaus mean : 29.0928

Gaus Width: 3.1809

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch78, adc1

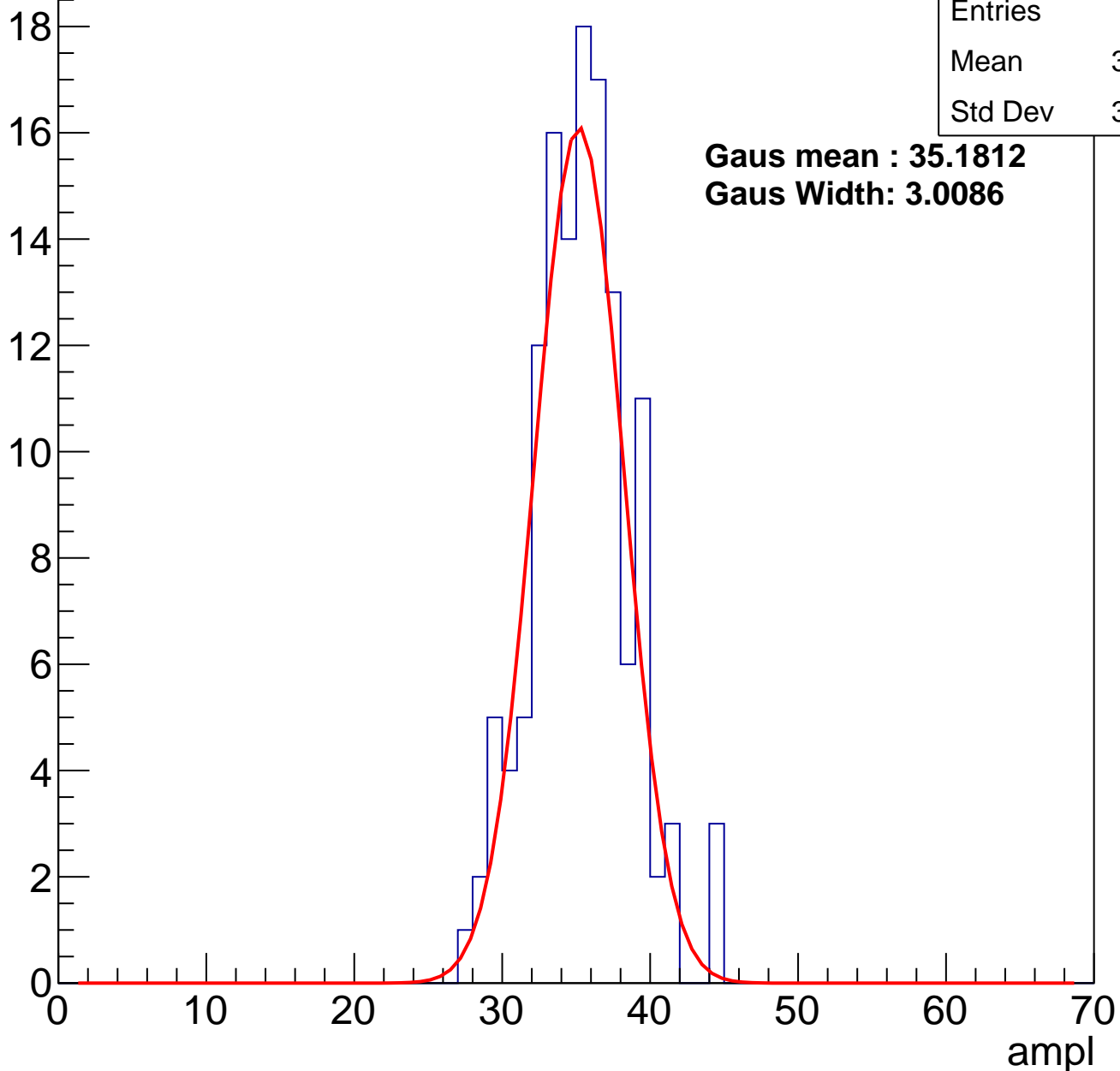
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	34.89
Std Dev	3.264

Gaus mean : 35.1812

Gaus Width: 3.0086

Entry



B1L001S, U16-ch78, adc2

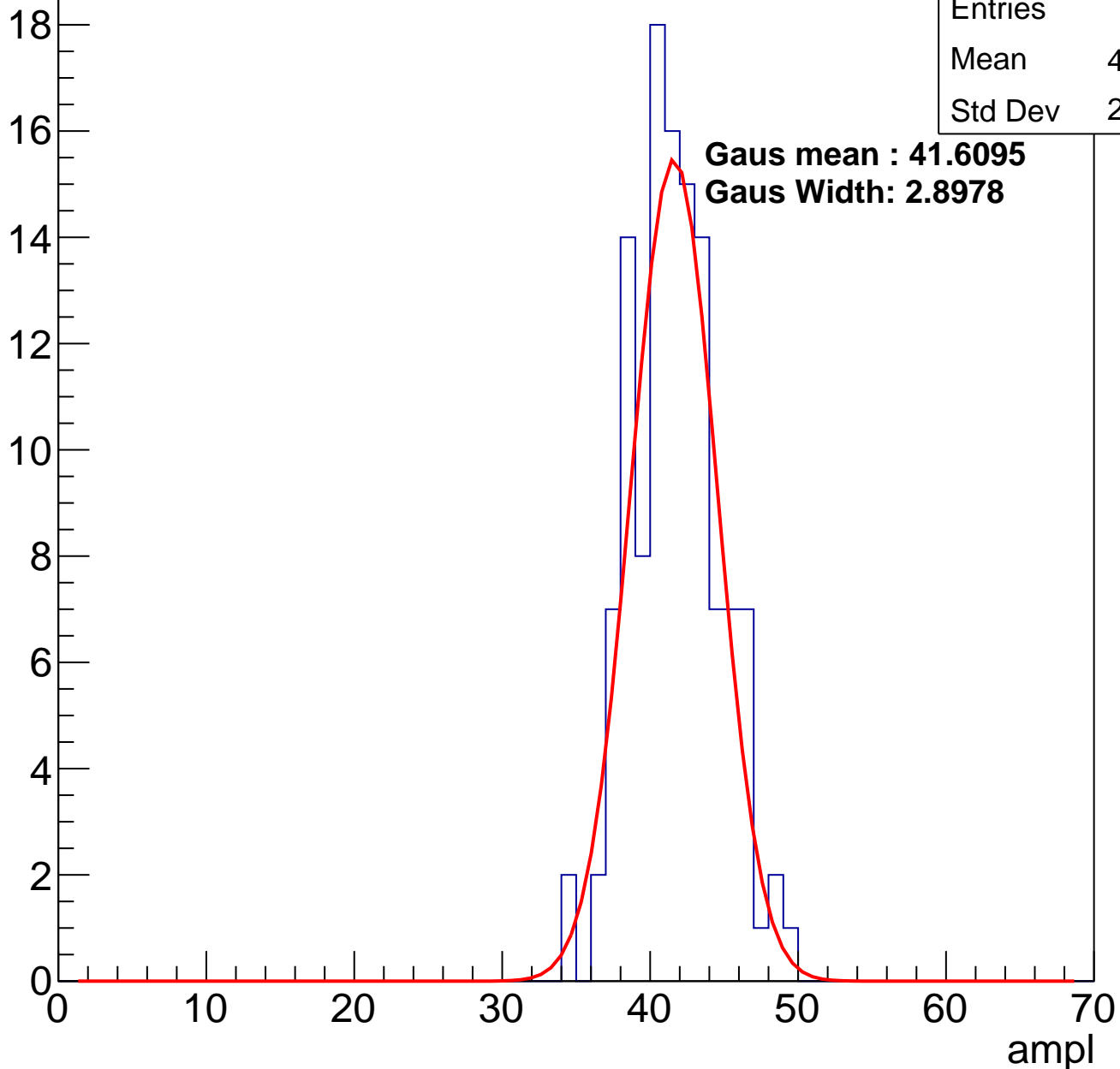
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	41.22
Std Dev	2.944

Gaus mean : 41.6095

Gaus Width: 2.8978

Entry

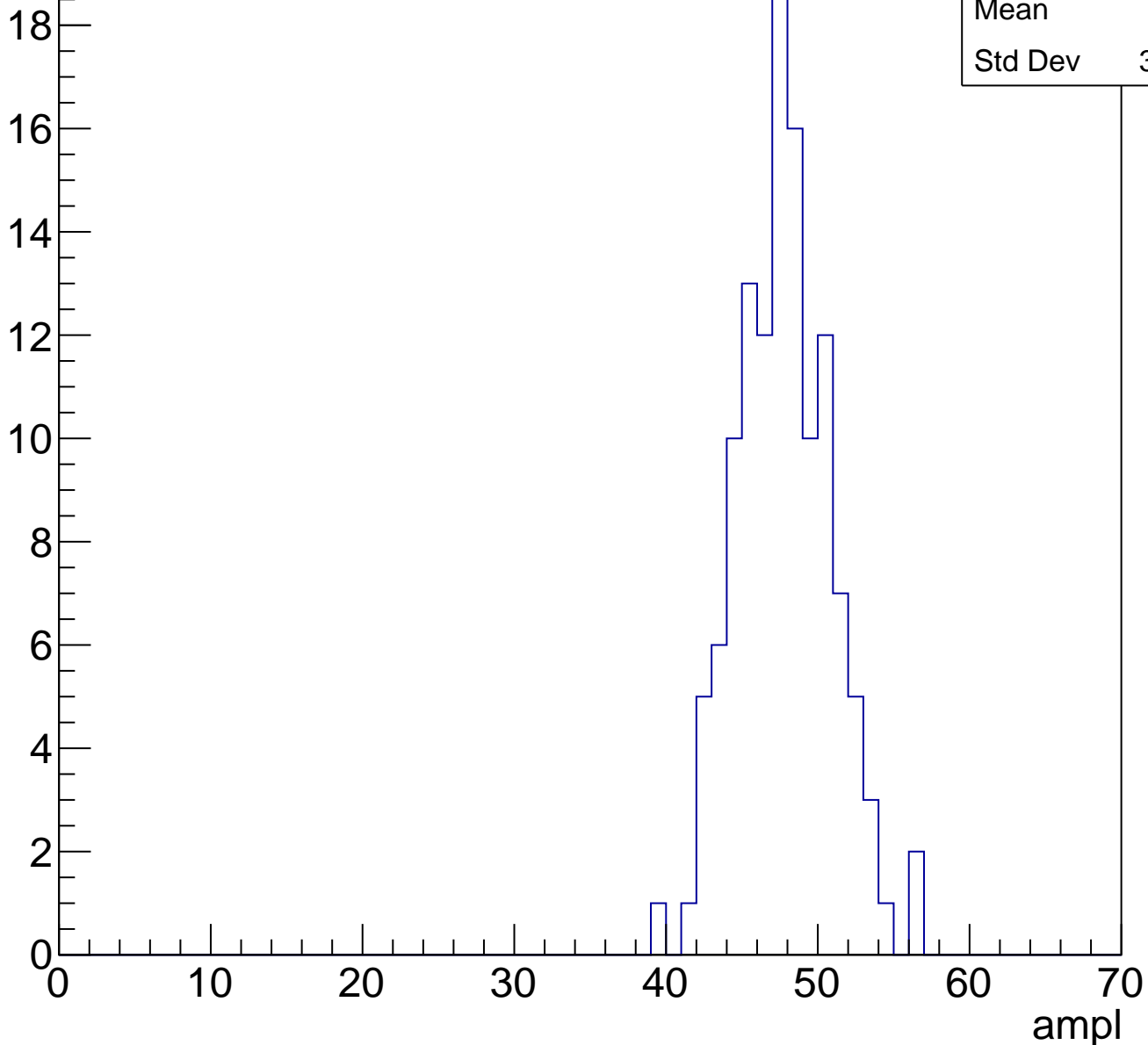


B1L001S, U16-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	47.3
Std Dev	3.102

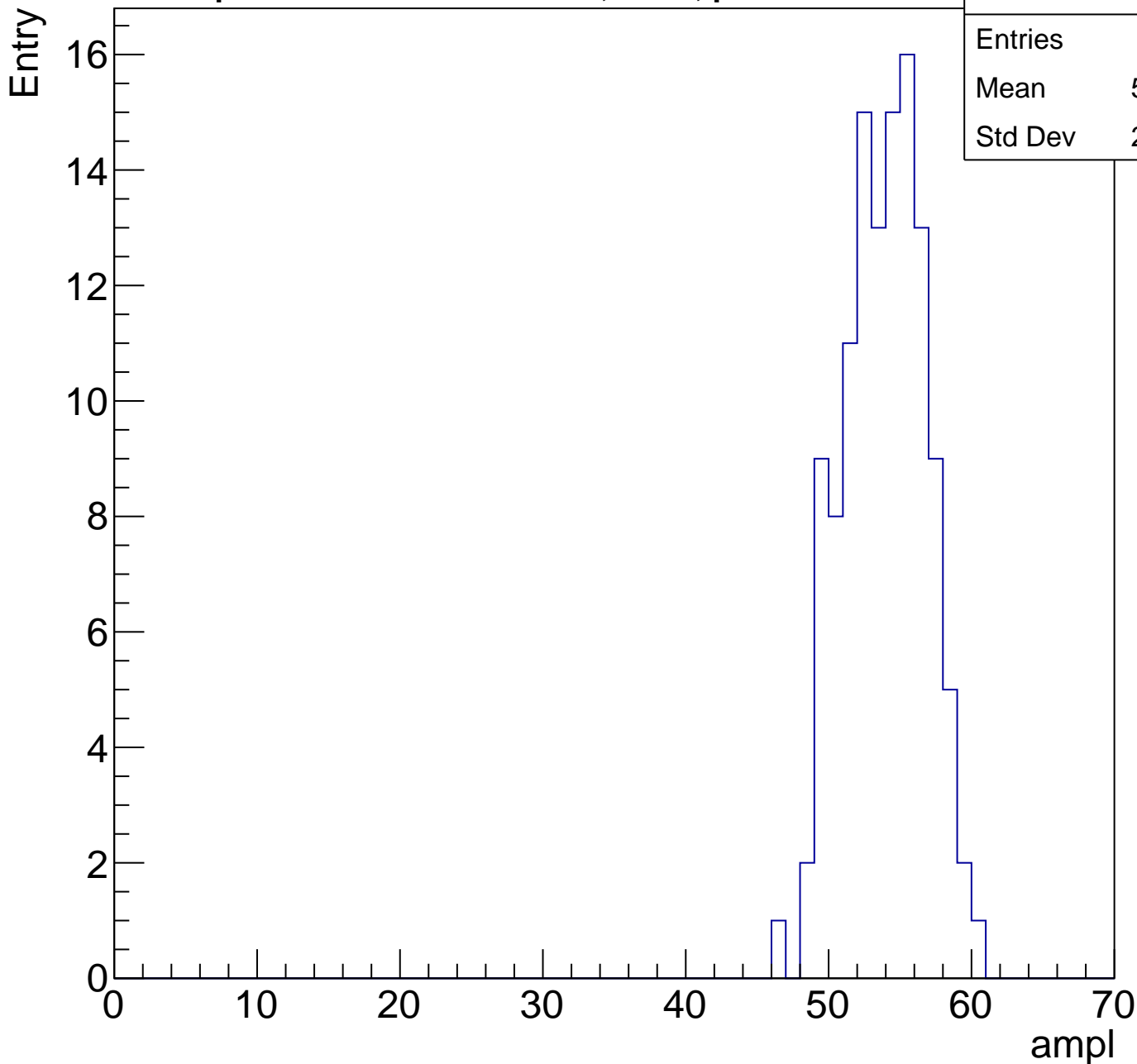
Entry



B1L001S, U16-ch78, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	53.43
Std Dev	2.795



B1L001S, U16-ch78, adc5

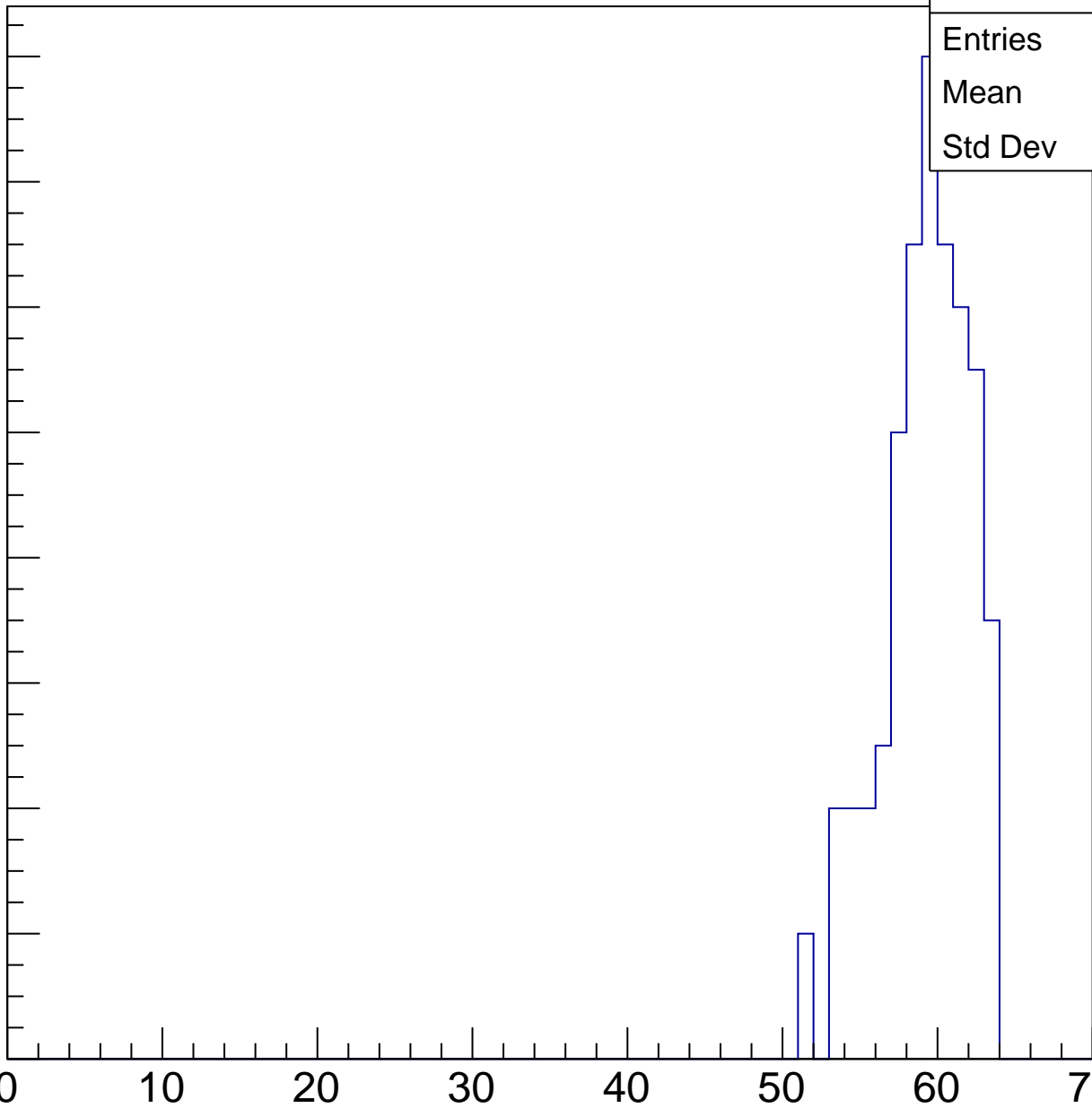
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.74
Std Dev	2.813

ampl



B1L001S, U16-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

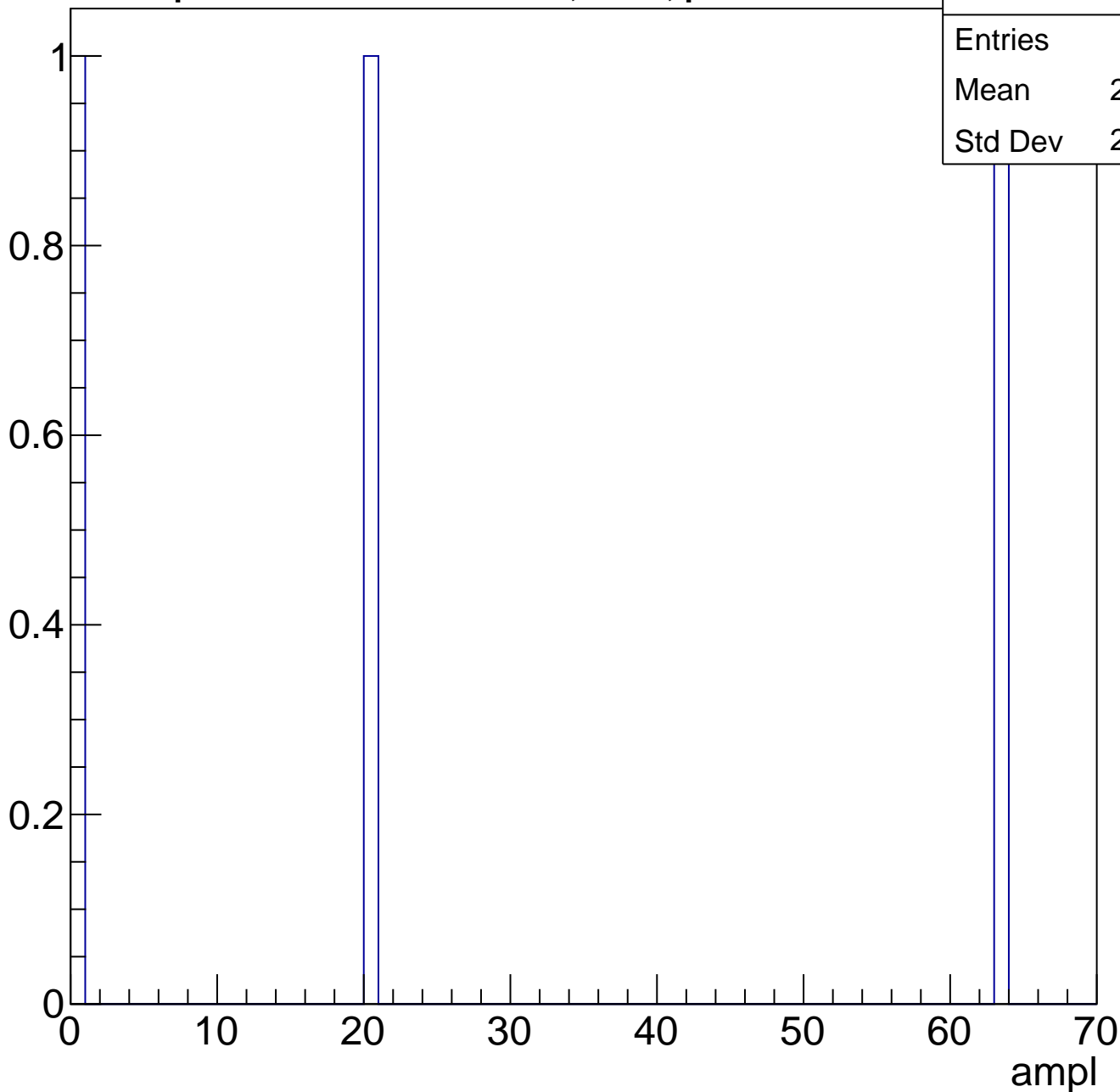
ampl

Entries	42
Mean	58.4
Std Dev	13.15

B1L001S, U16-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B1L001S, U16-ch79, adc0

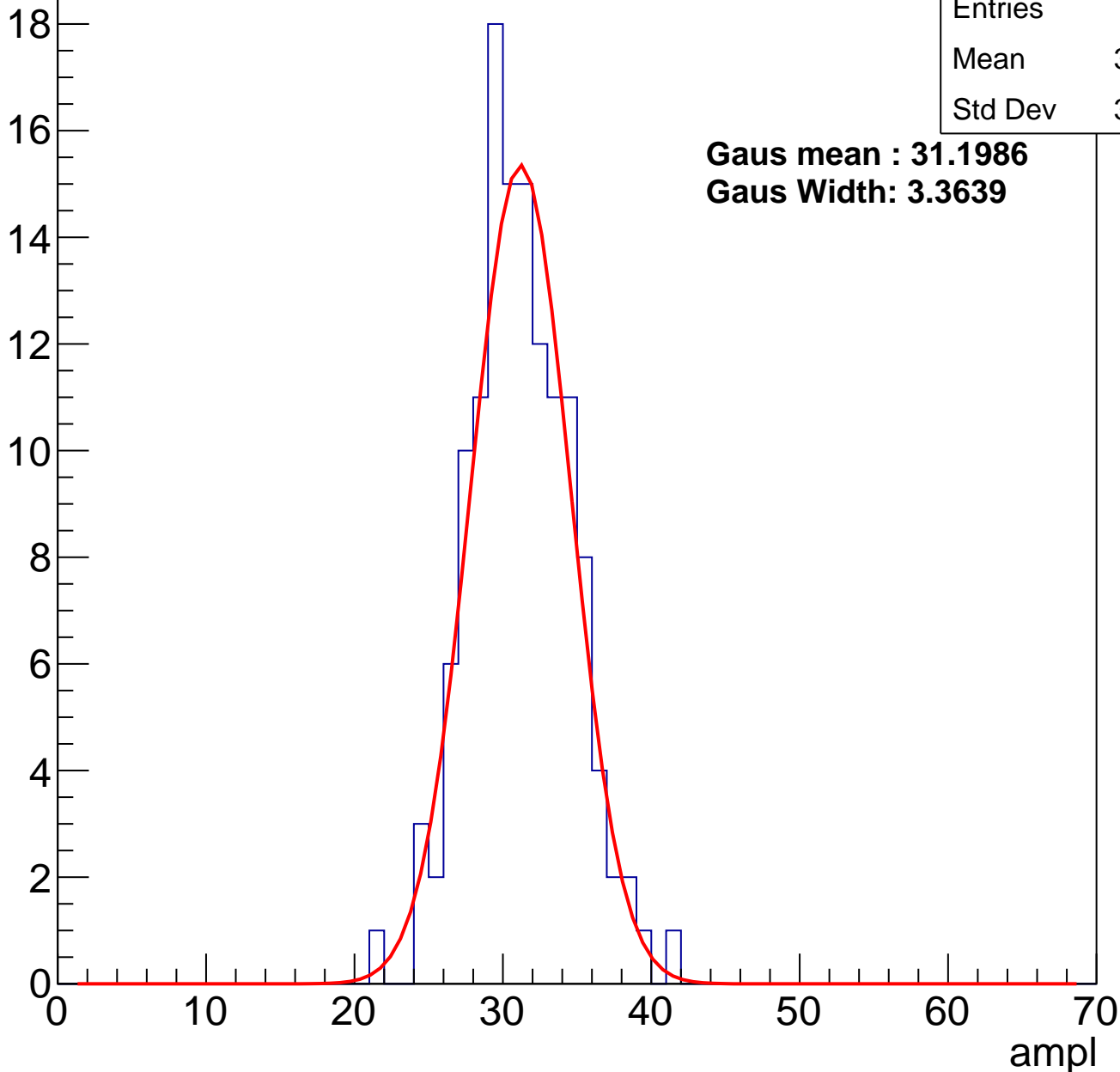
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.74
Std Dev	3.384

Gaus mean : 31.1986

Gaus Width: 3.3639

Entry



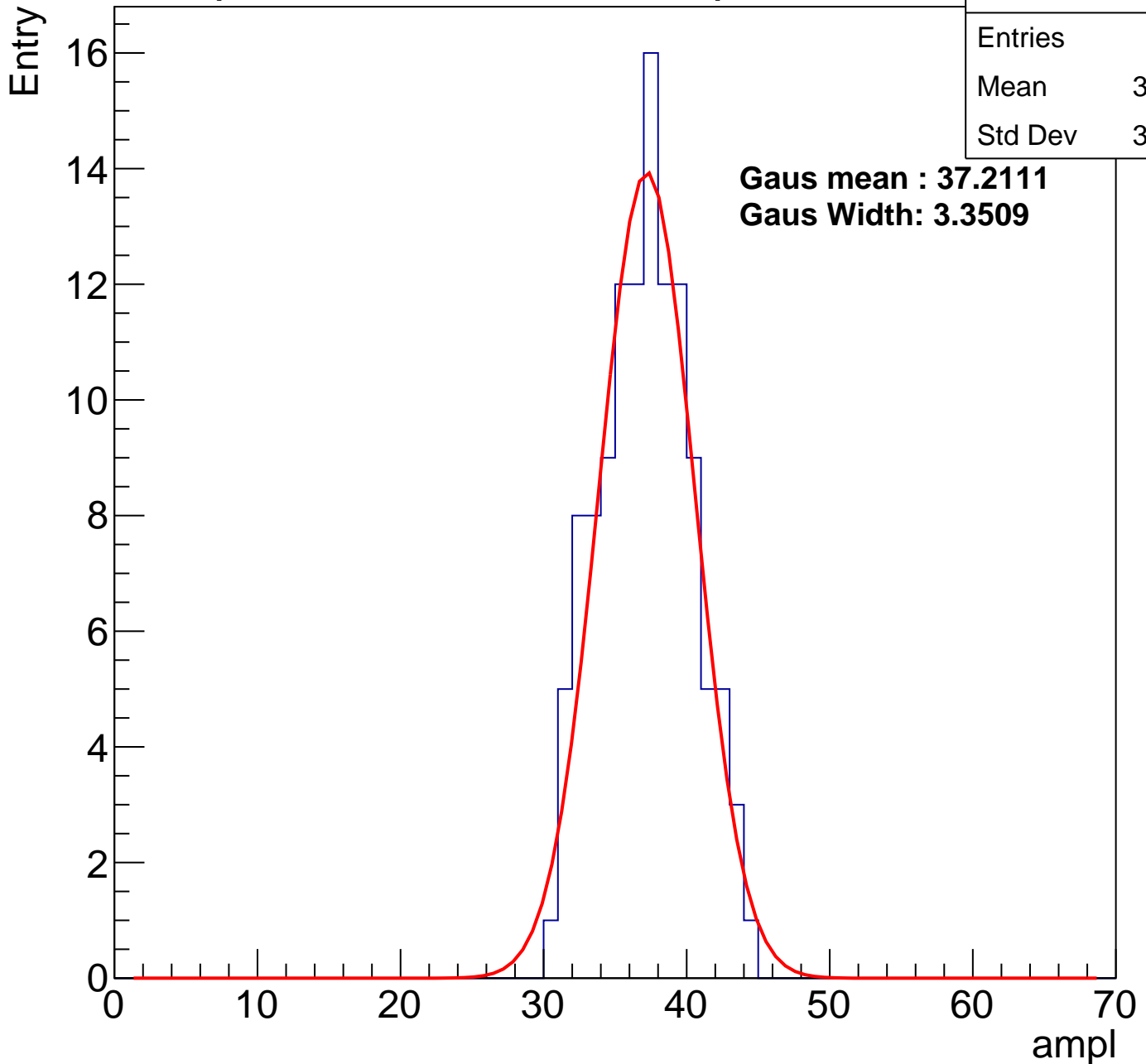
B1L001S, U16-ch79, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	36.67
Std Dev	3.173

Gaus mean : 37.2111

Gaus Width: 3.3509



B1L001S, U16-ch79, adc2

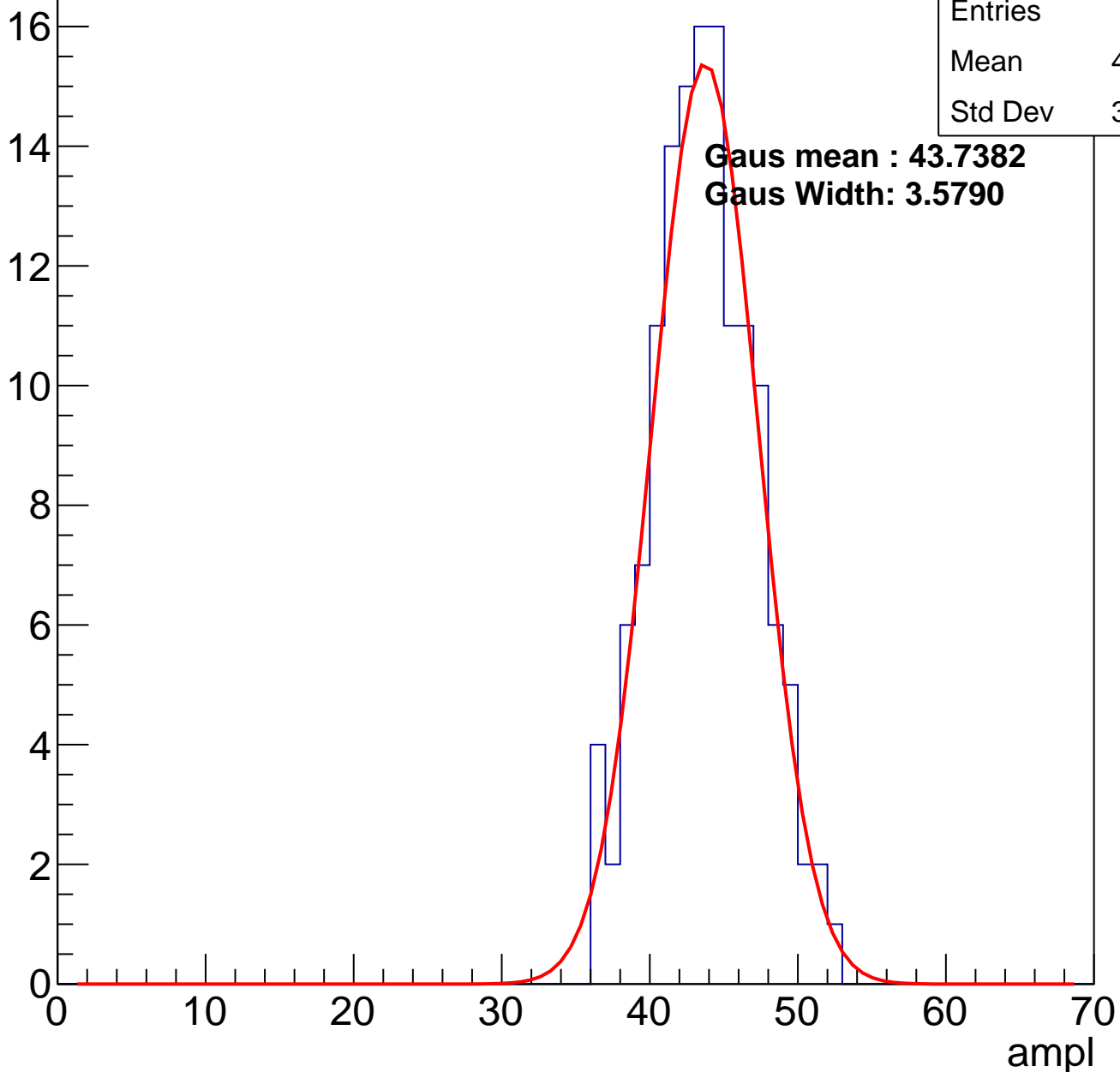
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	43.26
Std Dev	3.442

Gaus mean : 43.7382

Gaus Width: 3.5790

Entry



B1L001S, U16-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.07
Std Dev	3.26

Entry

16
14
12
10
8
6
4
2
0

0

10

20

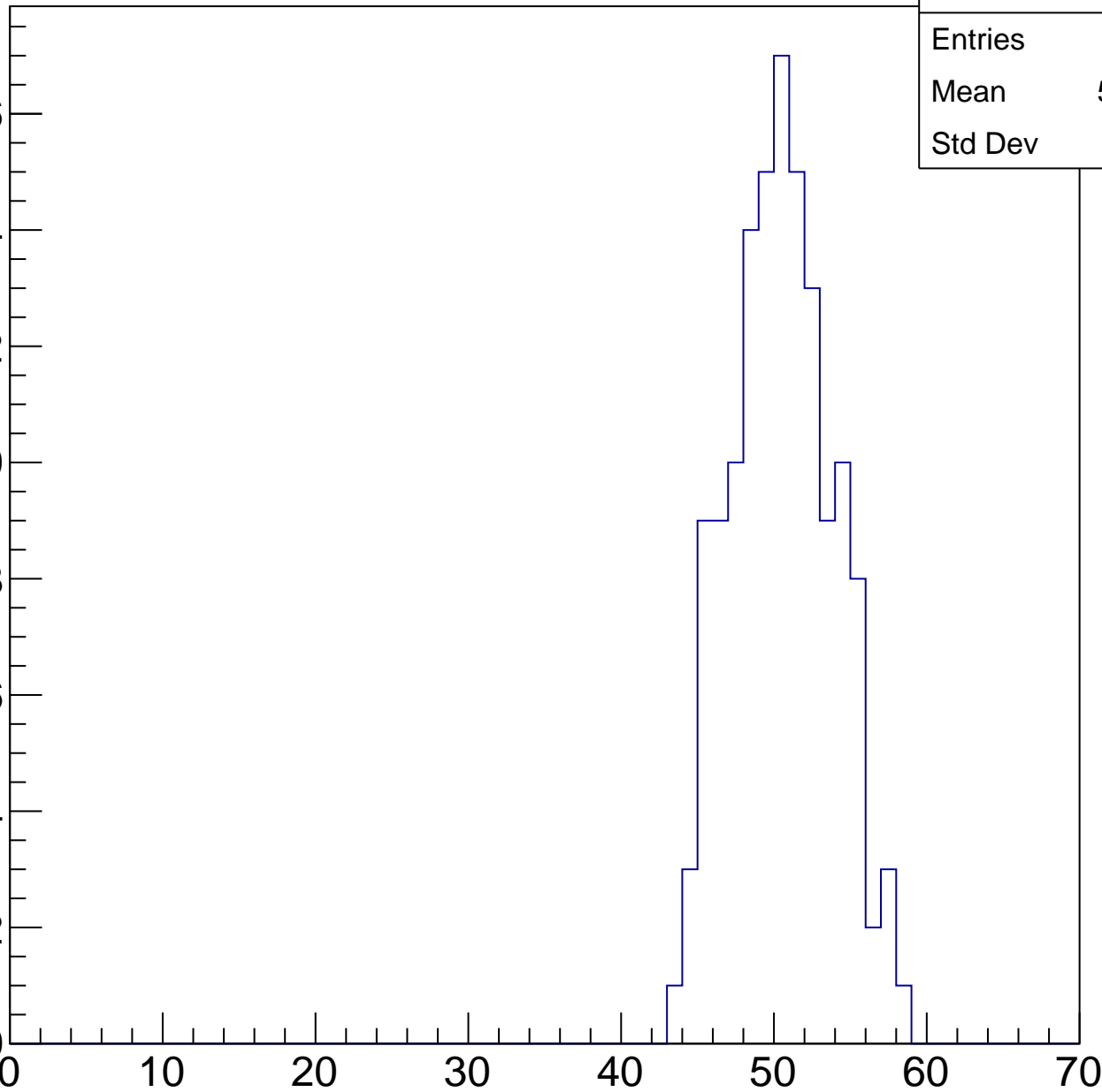
30

40

50

60

ampl



B1L001S, U16-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

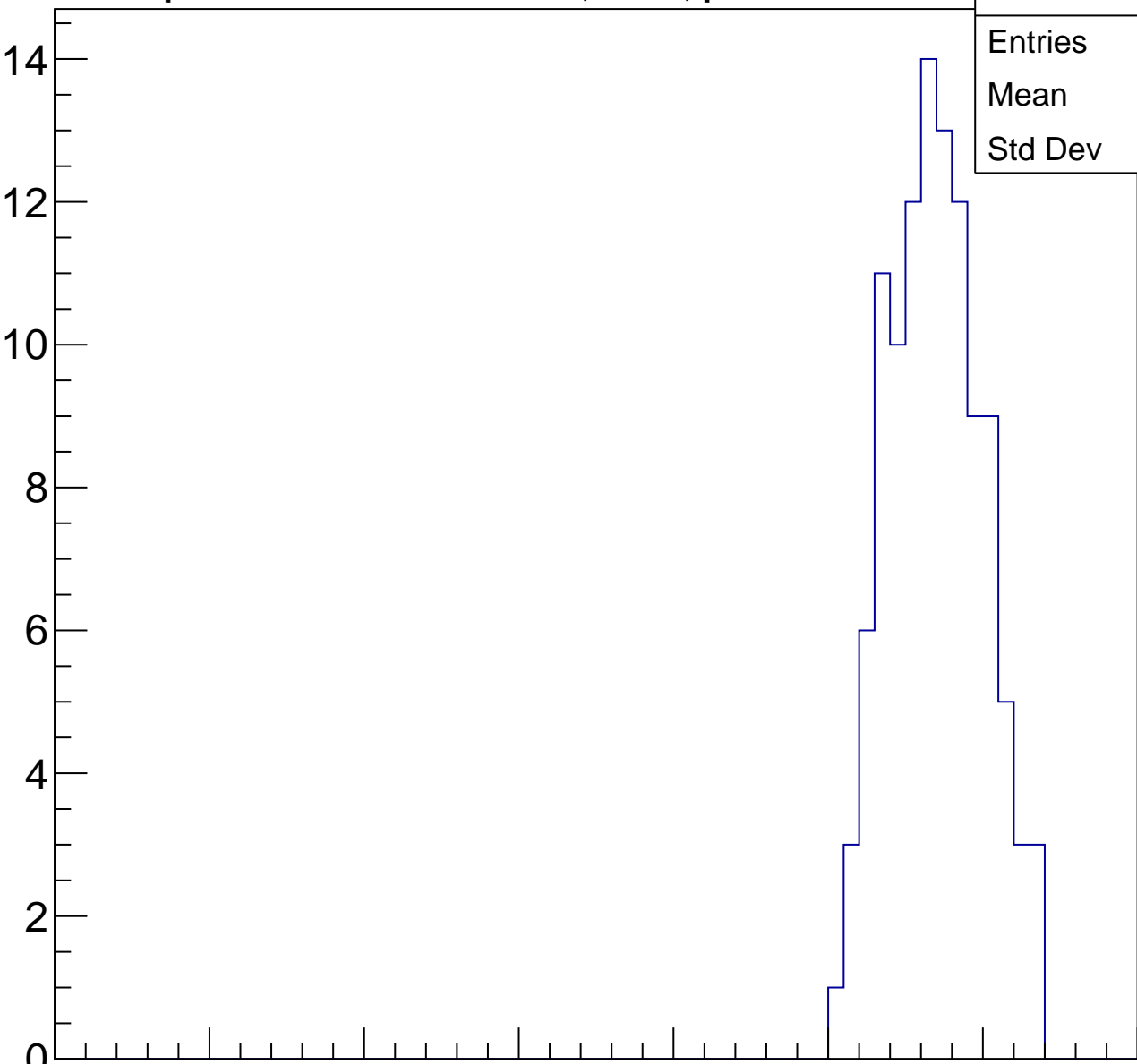
Entries	111
Mean	56.49
Std Dev	2.998

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch79, adc5

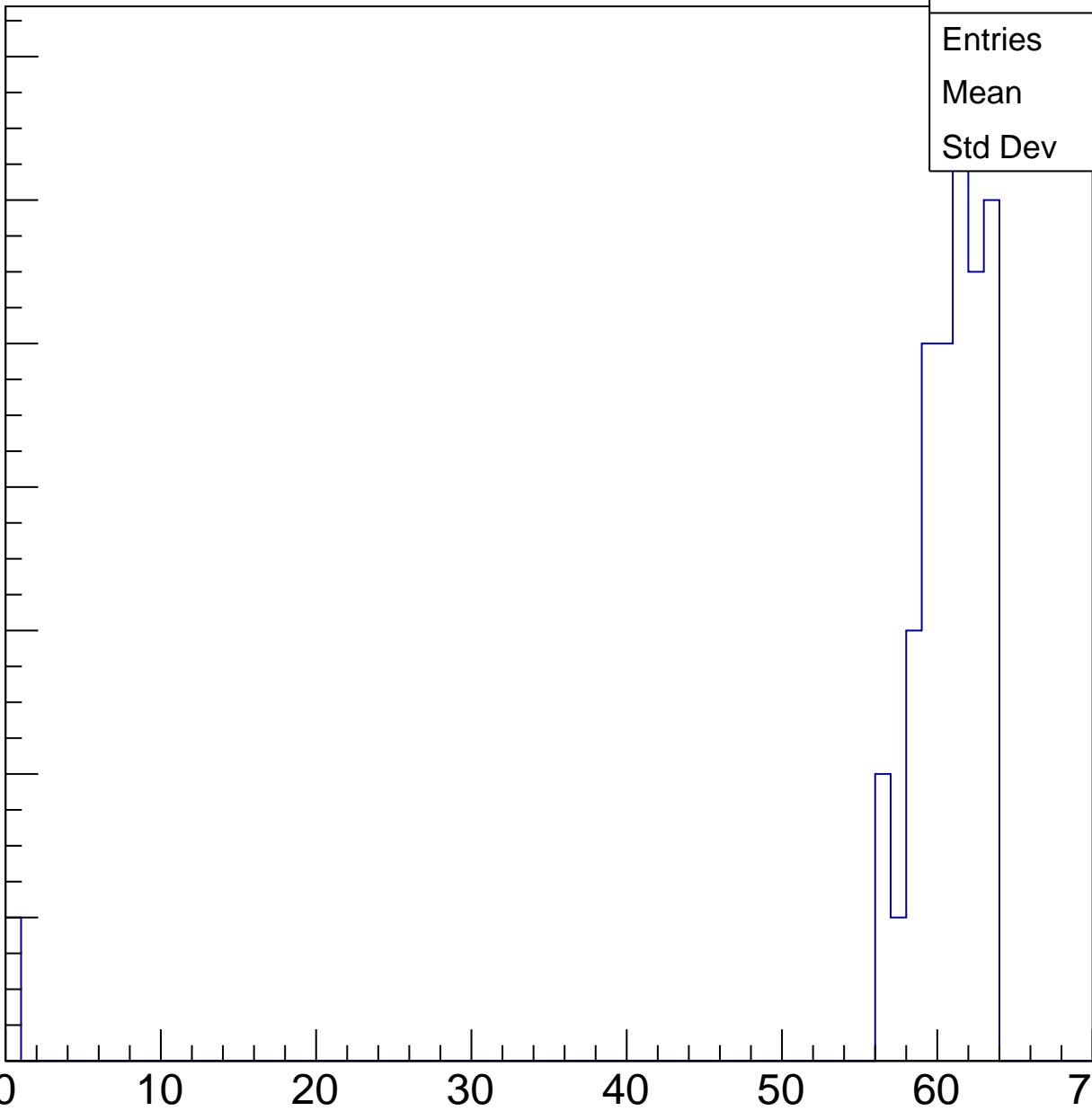
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.7
Std Dev	10.18

ampl

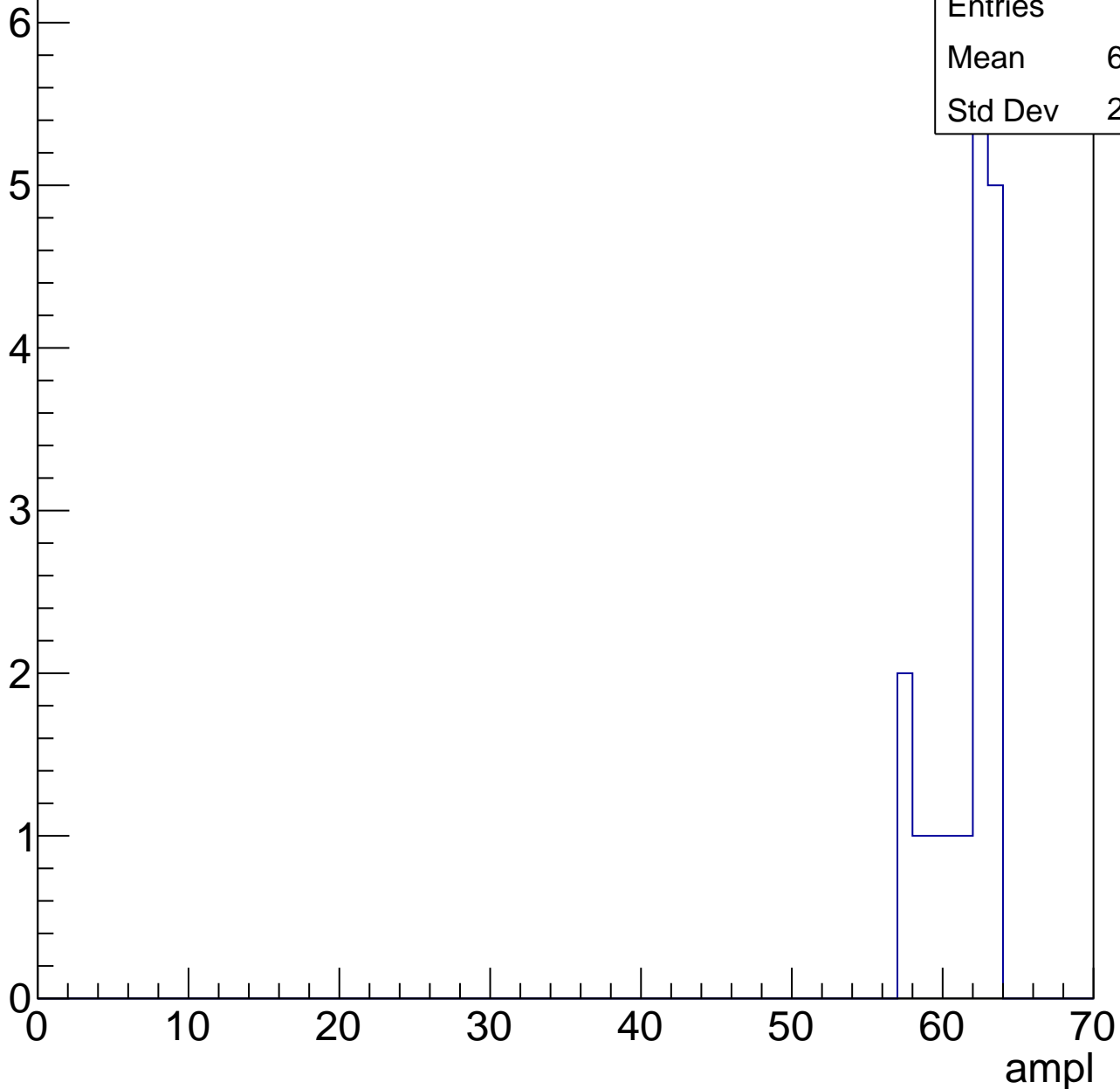


B1L001S, U16-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	61.12
Std Dev	2.055



B1L001S, U16-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



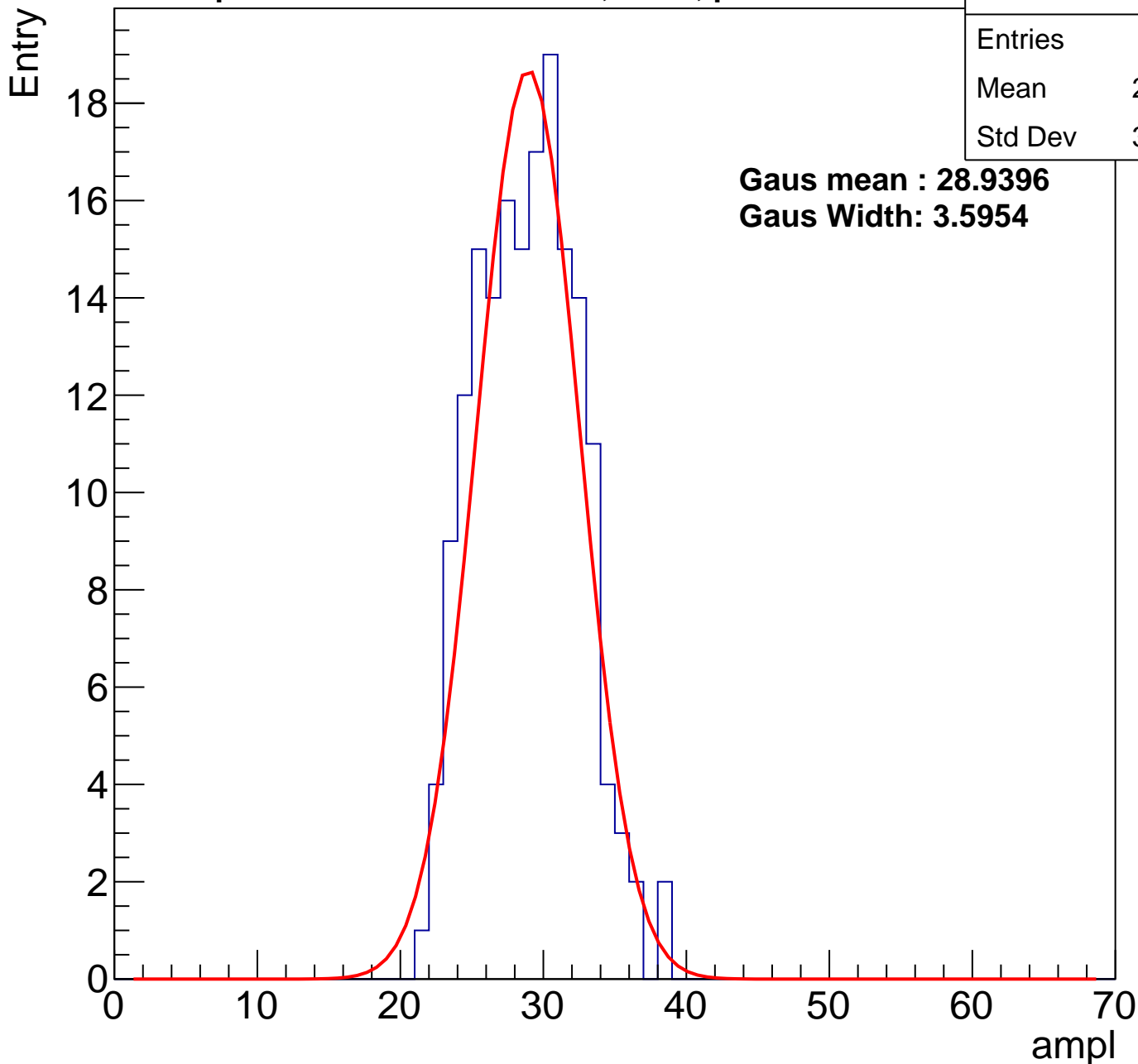
B1L001S, U16-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	173
Mean	28.46
Std Dev	3.506

Gaus mean : 28.9396

Gaus Width: 3.5954



B1L001S, U16-ch80, adc1

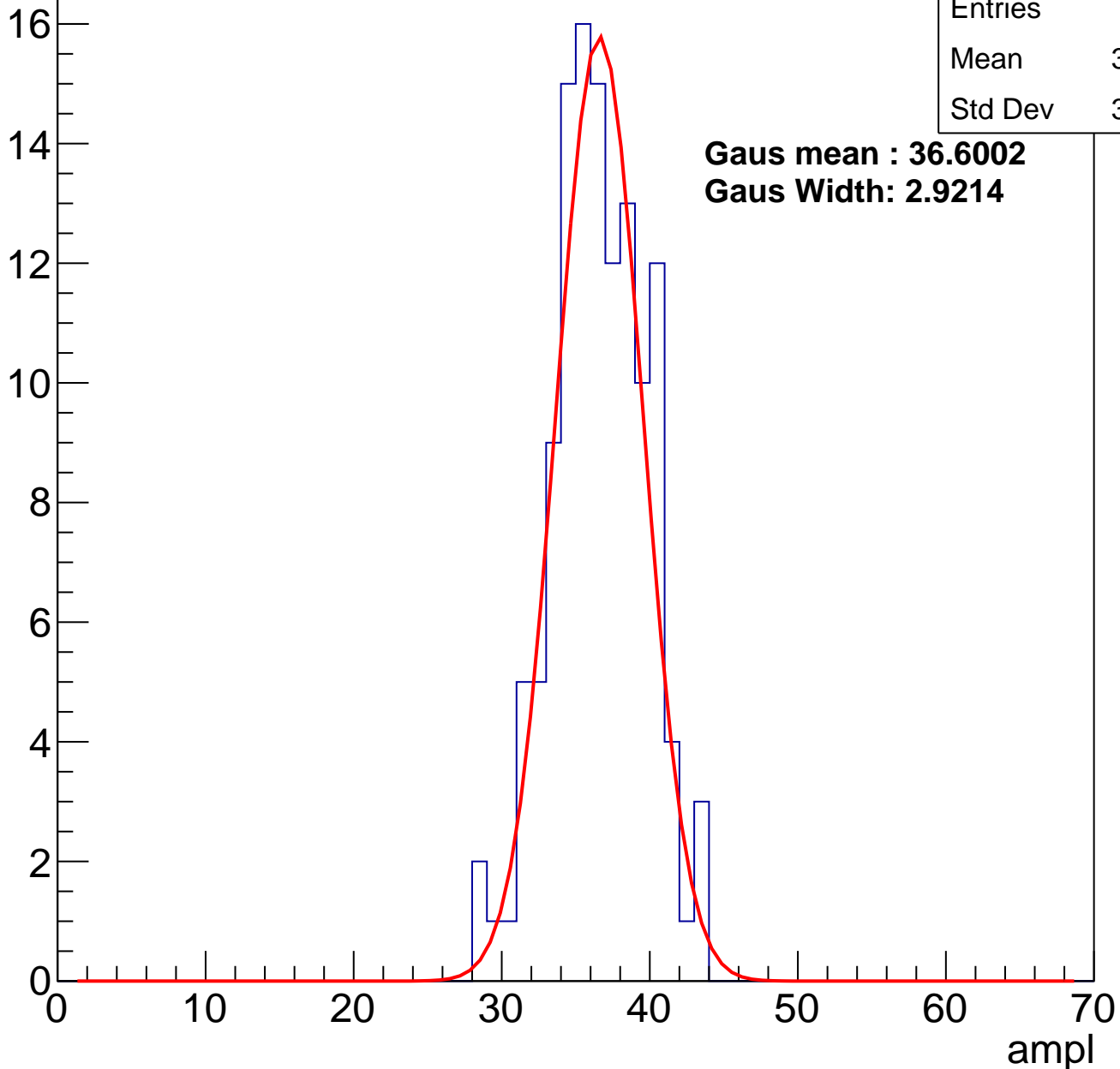
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.13
Std Dev	3.113

Gaus mean : 36.6002

Gaus Width: 2.9214

Entry



B1L001S, U16-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

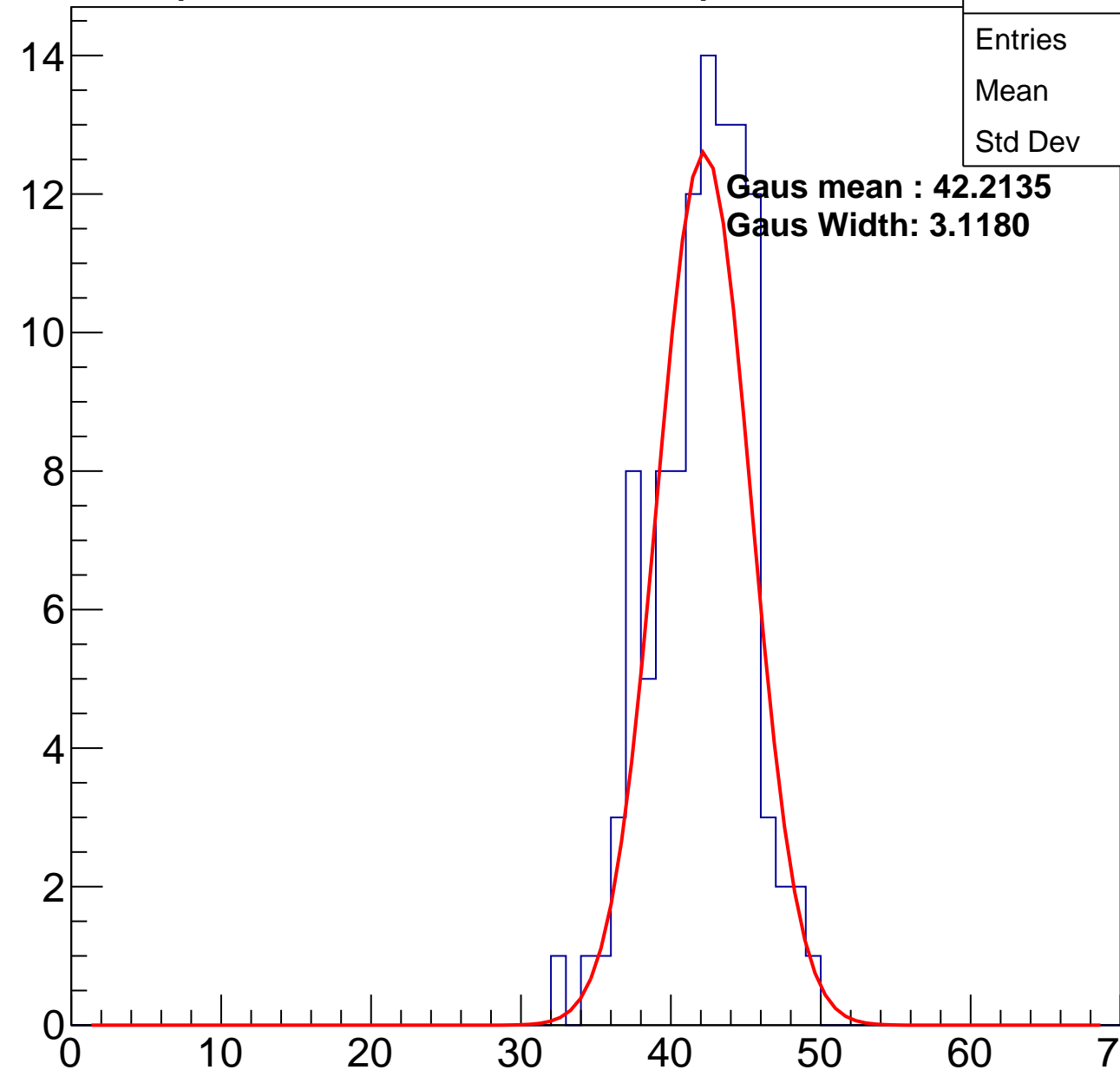
Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	41.64
Std Dev	3.199

Gaus mean : 42.2135
Gaus Width: 3.1180

0 10 20 30 40 50 60 70
ampl



B1L001S, U16-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

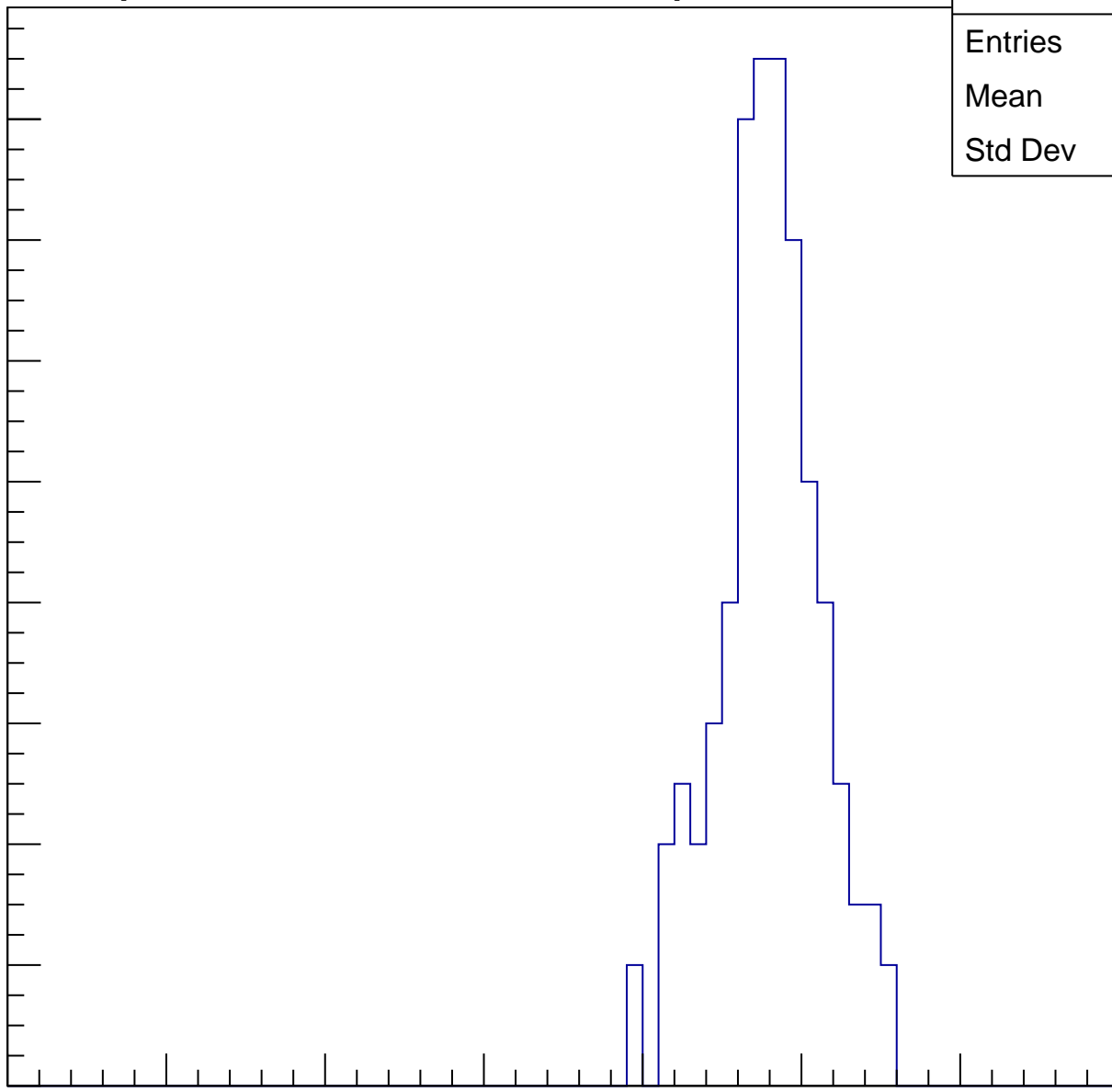
Entries	124
Mean	47.45
Std Dev	3.283

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch80, adc4

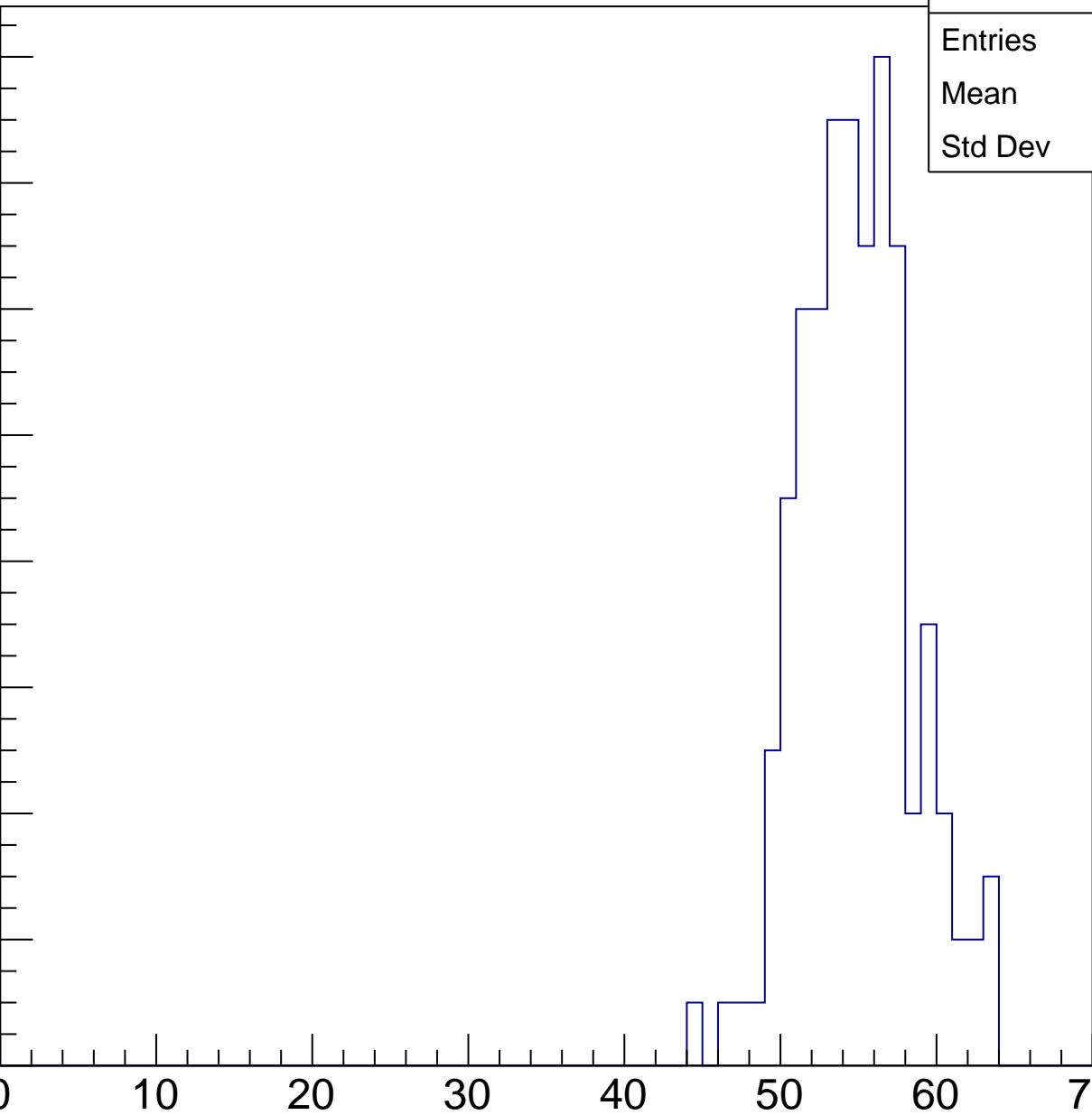
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	54.36
Std Dev	3.554

ampl

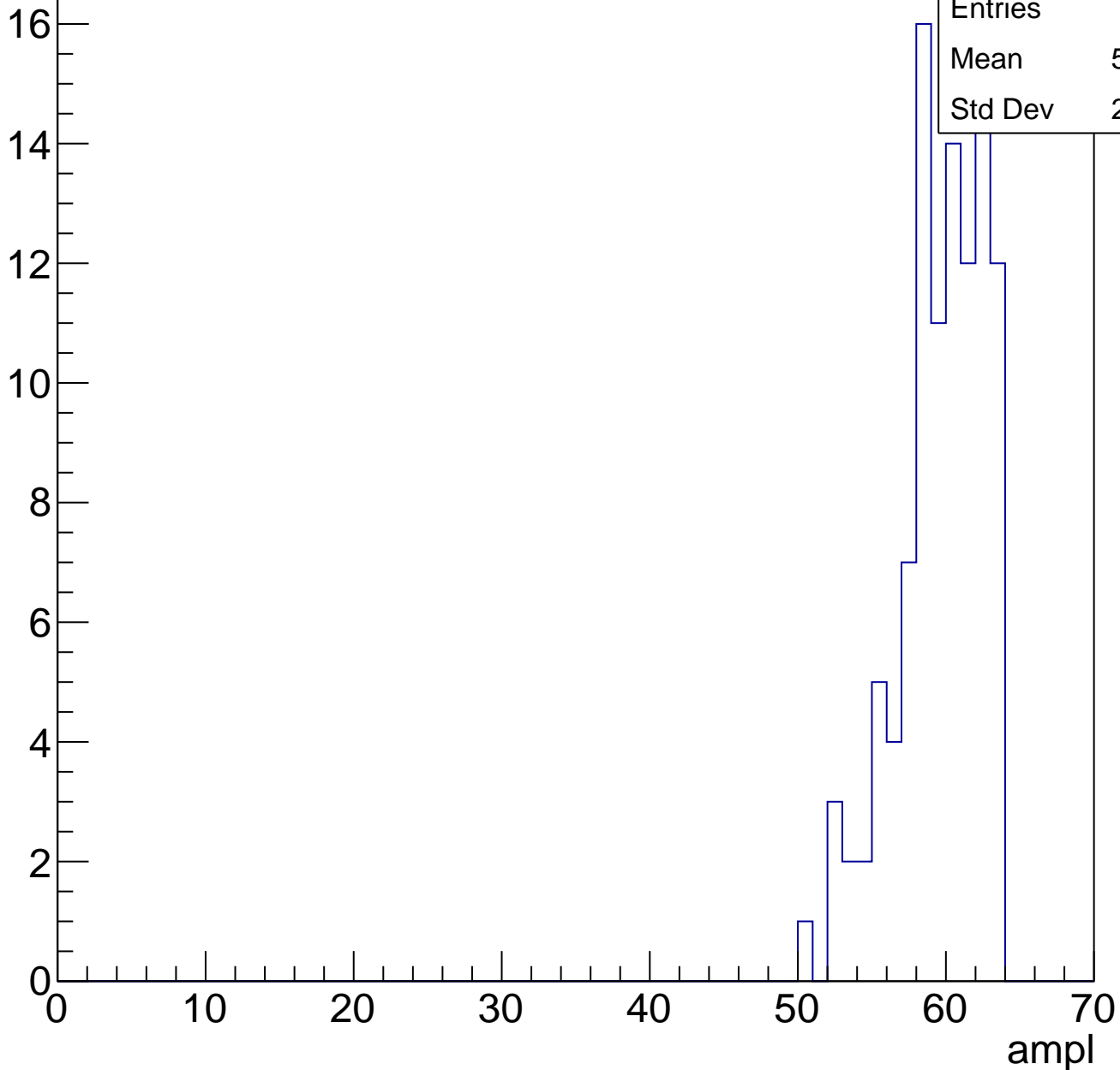


B1L001S, U16-ch80, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	59.16
Std Dev	2.936

Entry

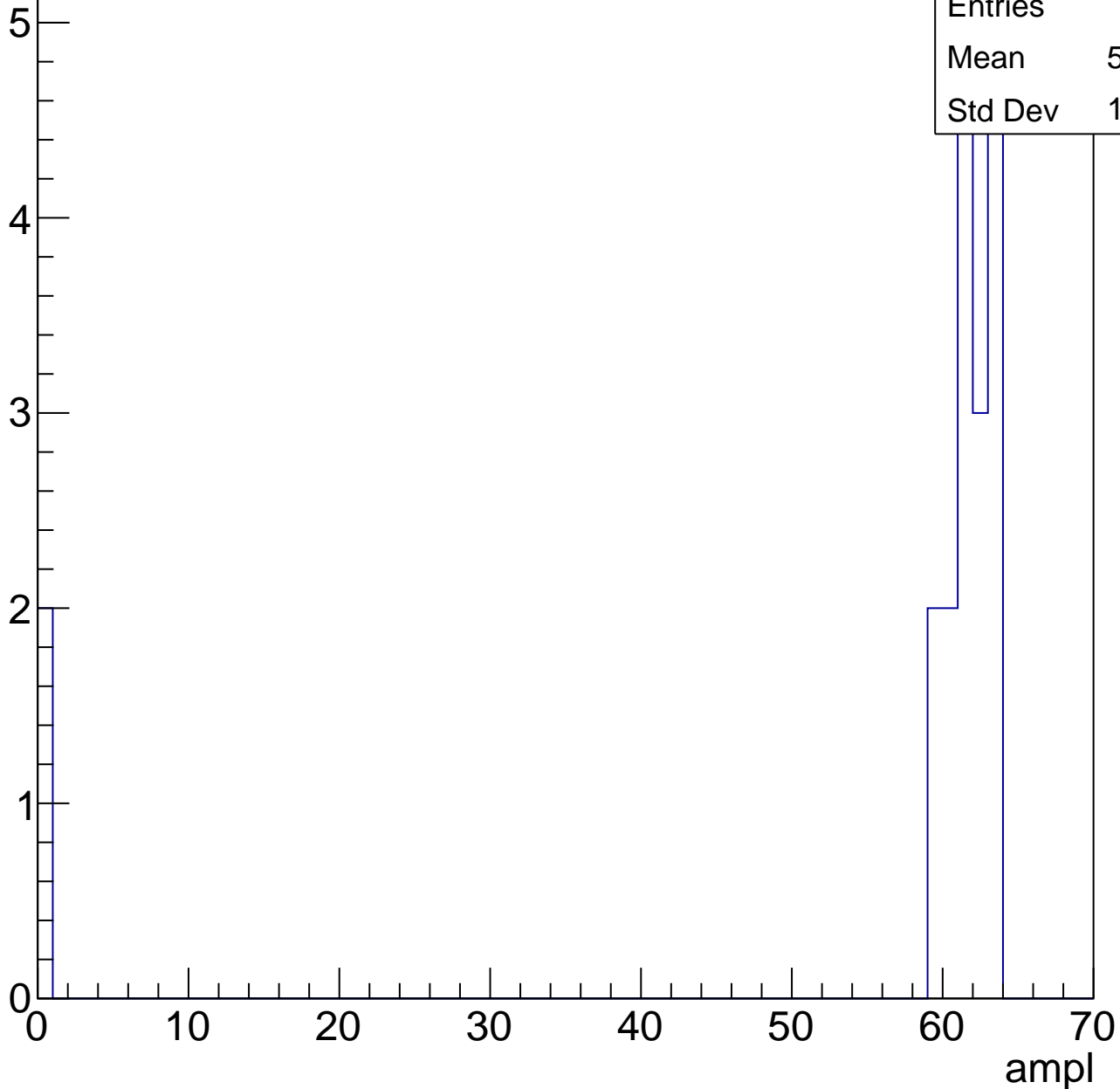


B1L001S, U16-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

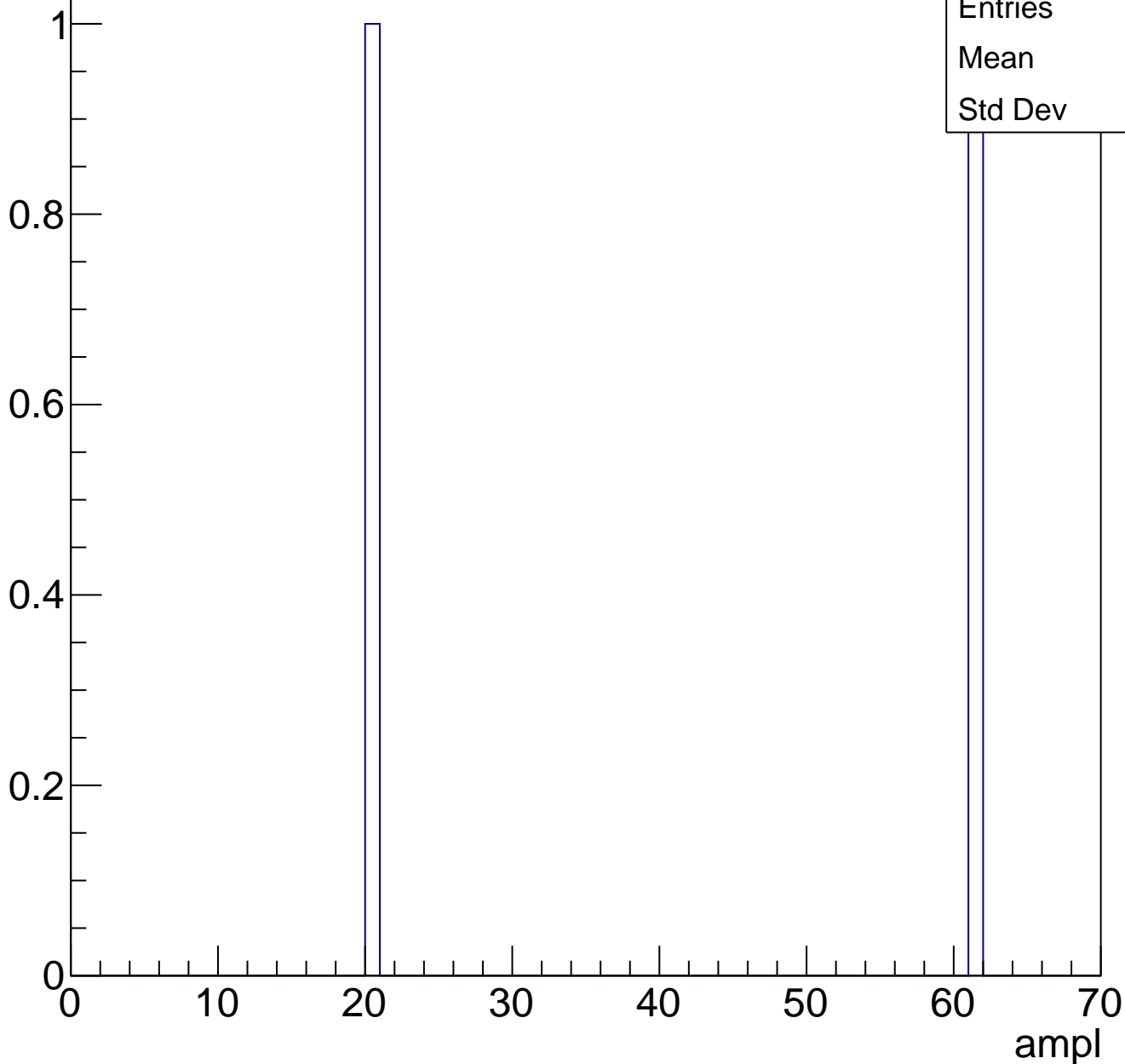
Entries	19
Mean	54.95
Std Dev	18.89



B1L001S, U16-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch81, adc0

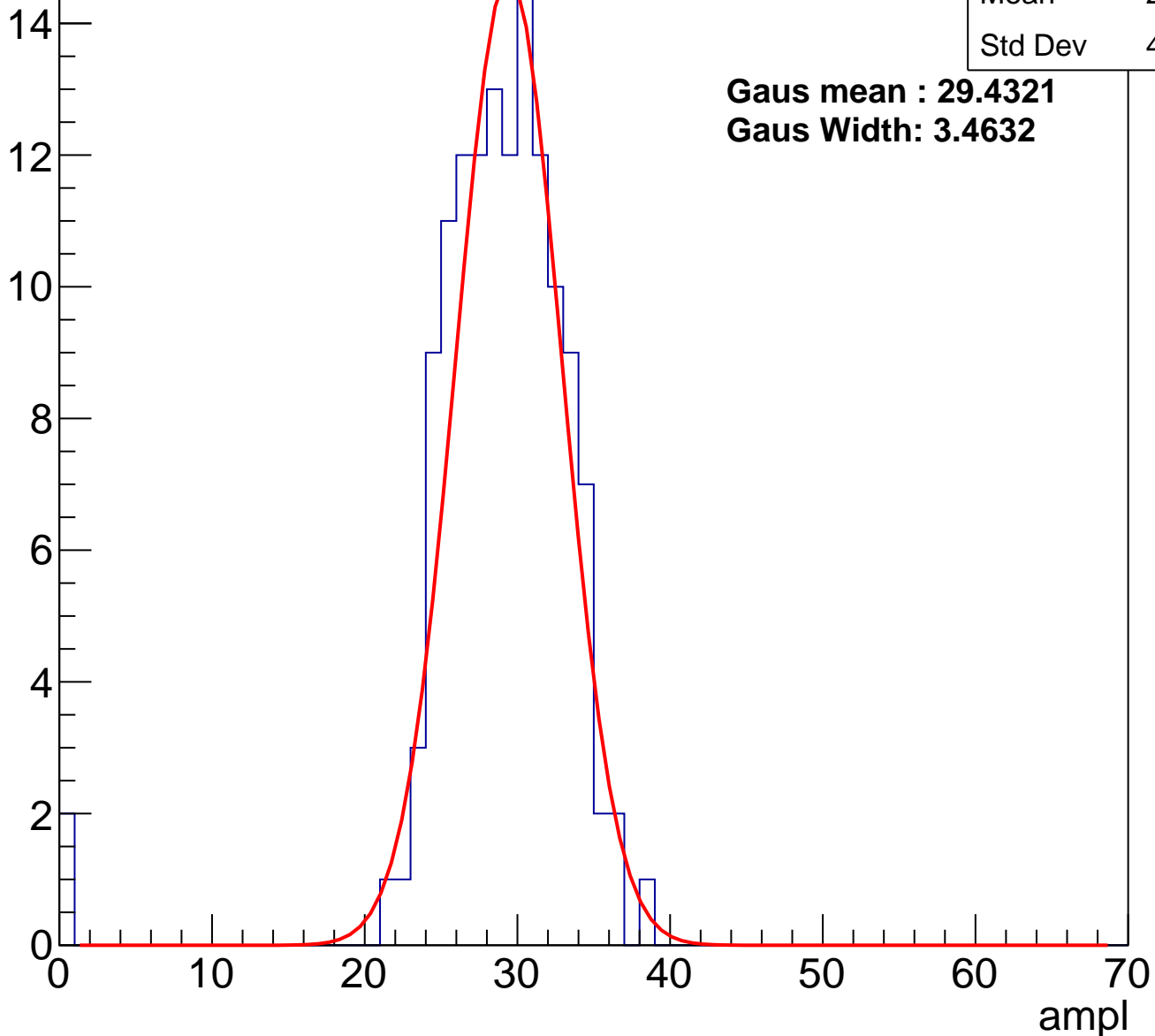
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.42
Std Dev	4.849

Gaus mean : 29.4321

Gaus Width: 3.4632

Entry



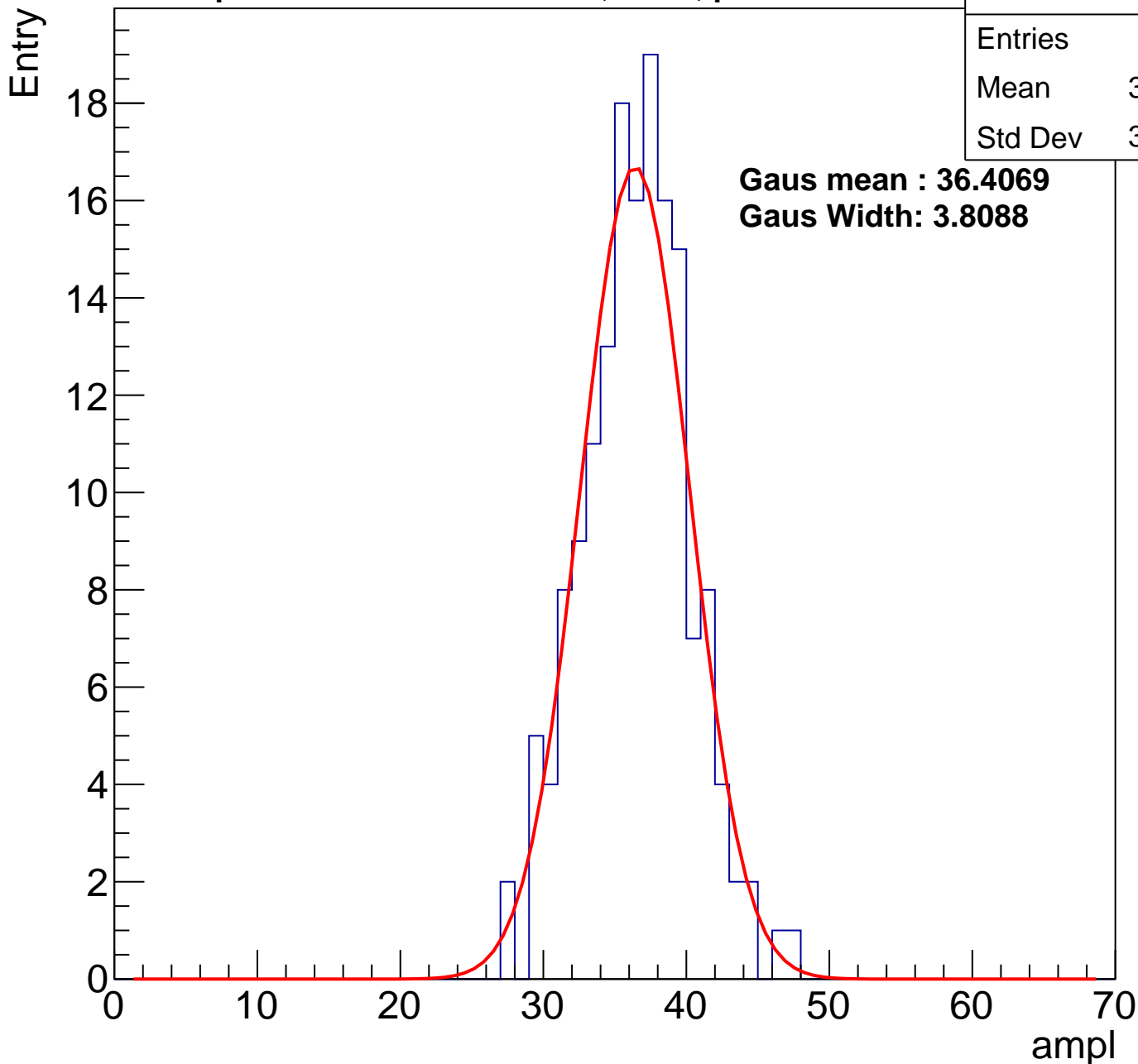
B1L001S, U16-ch81, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	36.06
Std Dev	3.684

Gaus mean : 36.4069

Gaus Width: 3.8088



B1L001S, U16-ch81, adc2

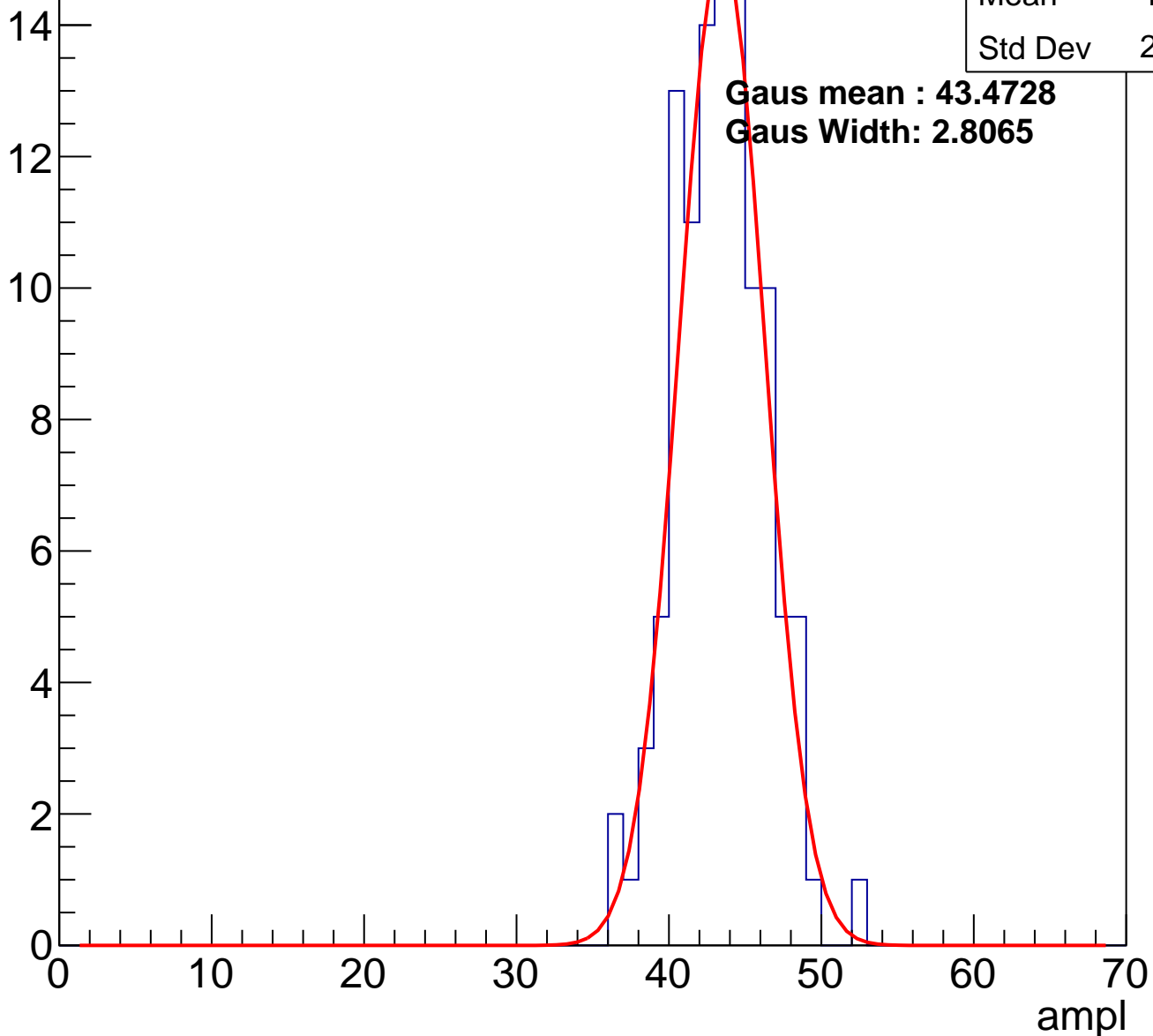
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	42.95
Std Dev	2.896

Gaus mean : 43.4728

Gaus Width: 2.8065

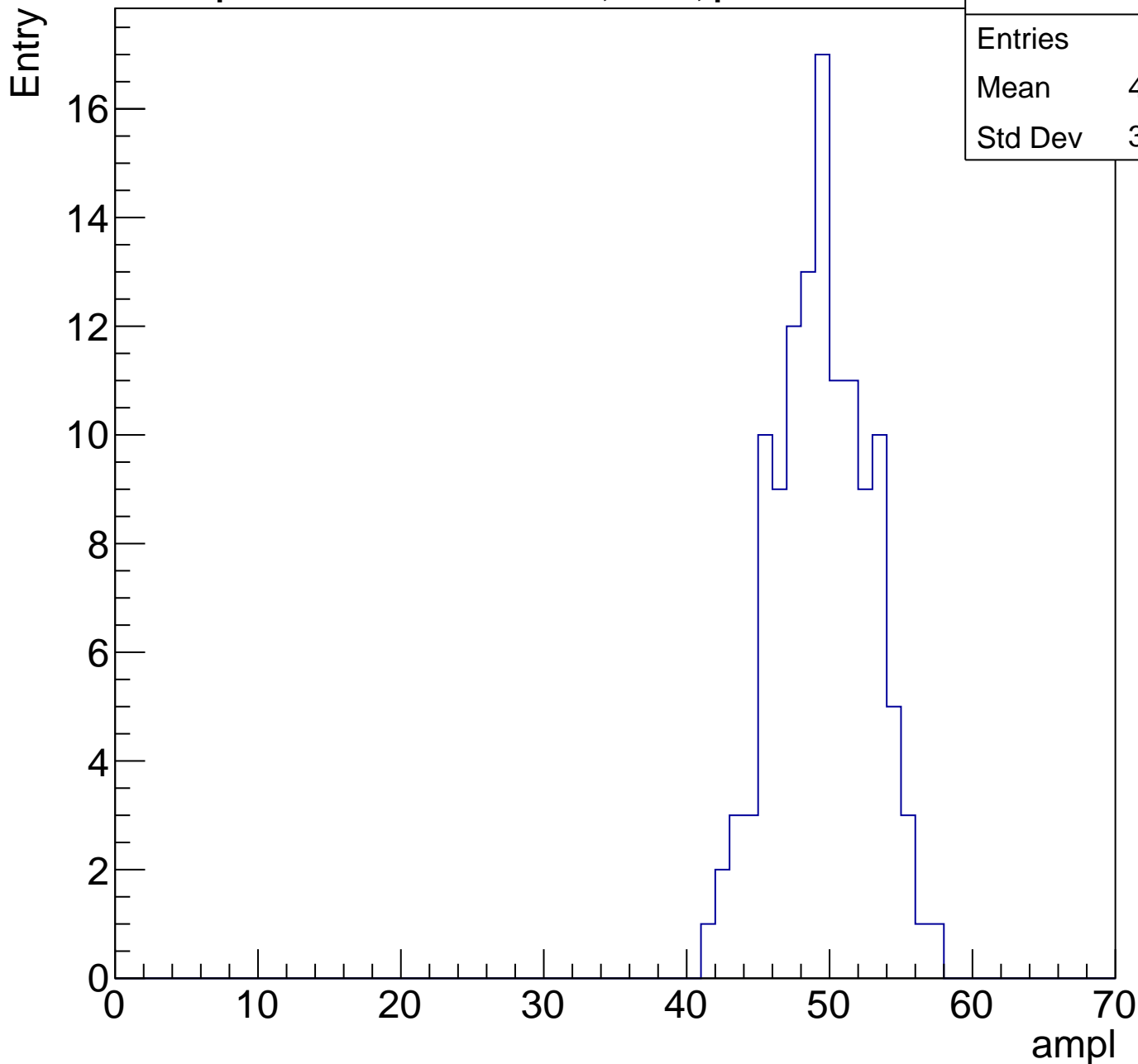
Entry



B1L001S, U16-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

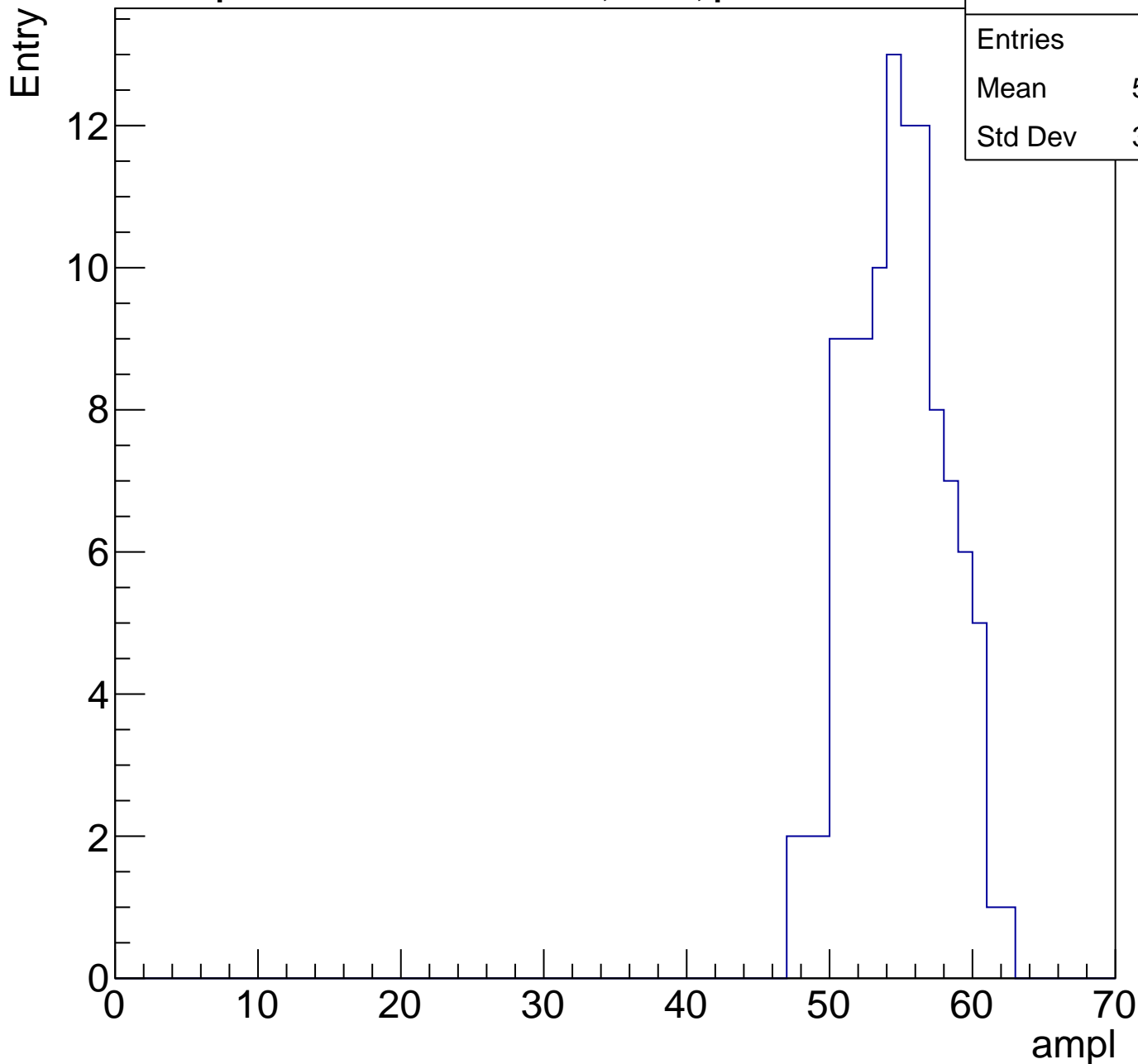
Entries	121
Mean	48.99
Std Dev	3.264



B1L001S, U16-ch81, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	54.33
Std Dev	3.286



B1L001S, U16-ch81, adc5

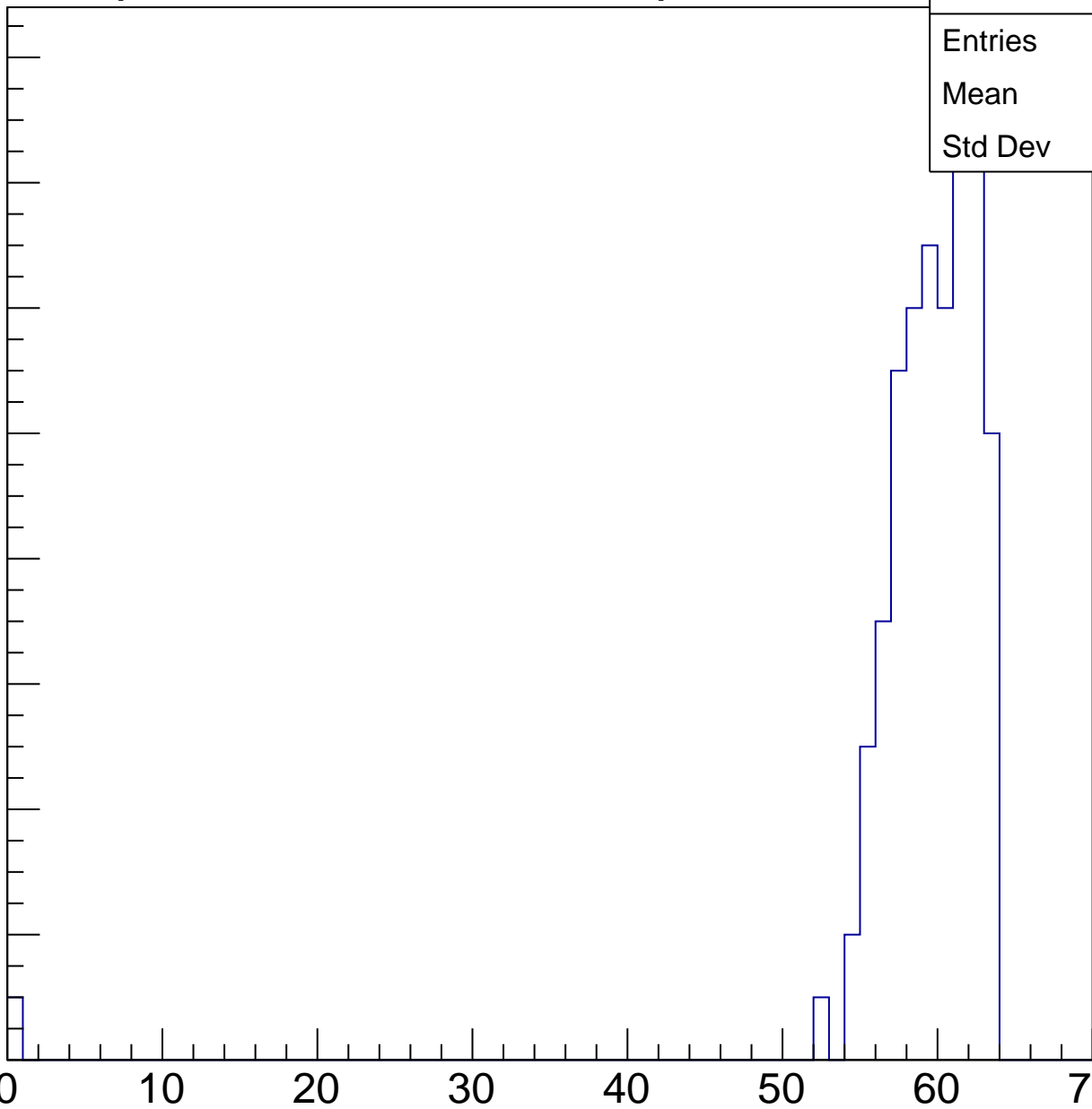
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.8
Std Dev	6.287

ampl

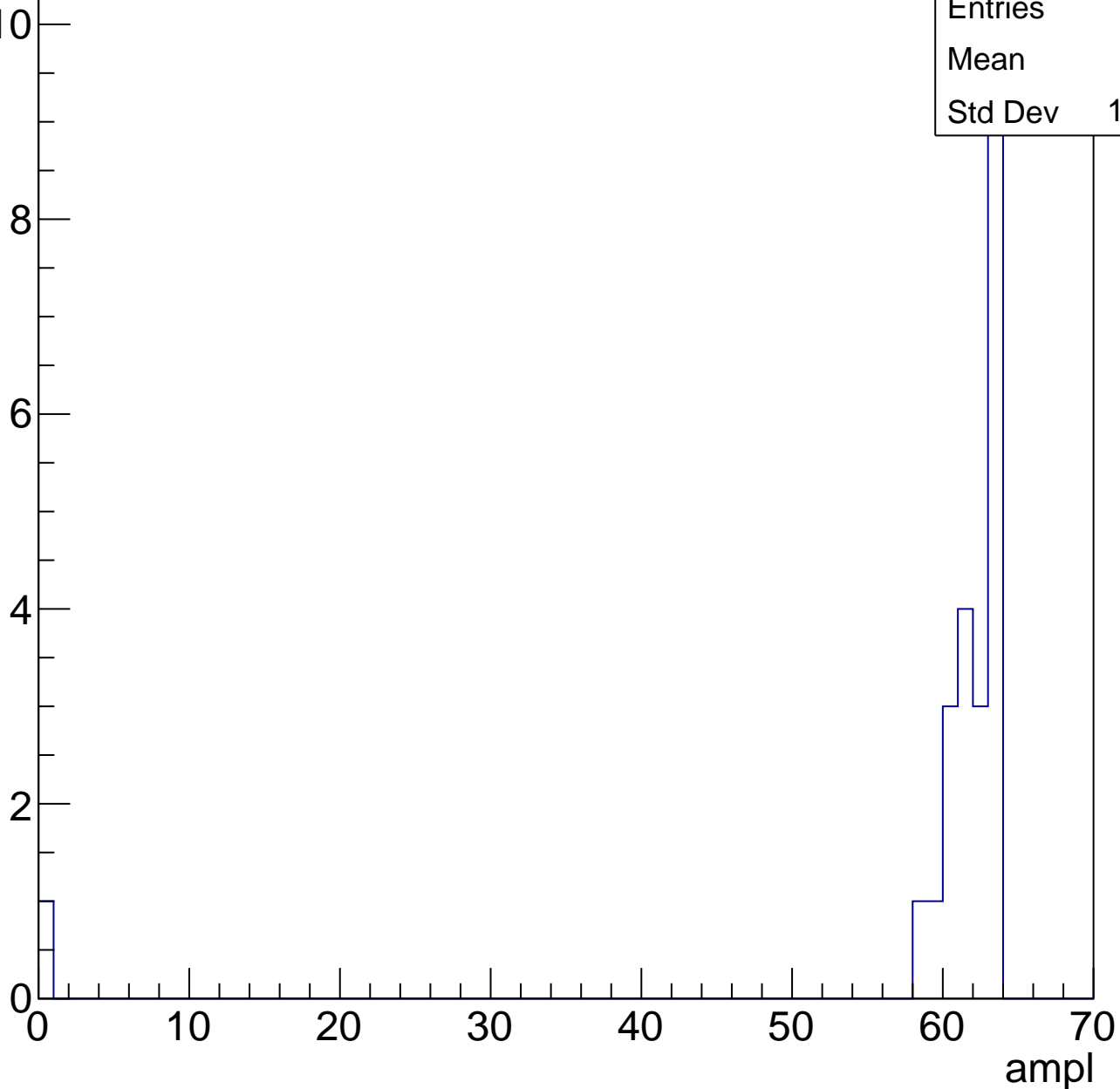


B1L001S, U16-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	59
Std Dev	12.66



B1L001S, U16-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch82, adc0

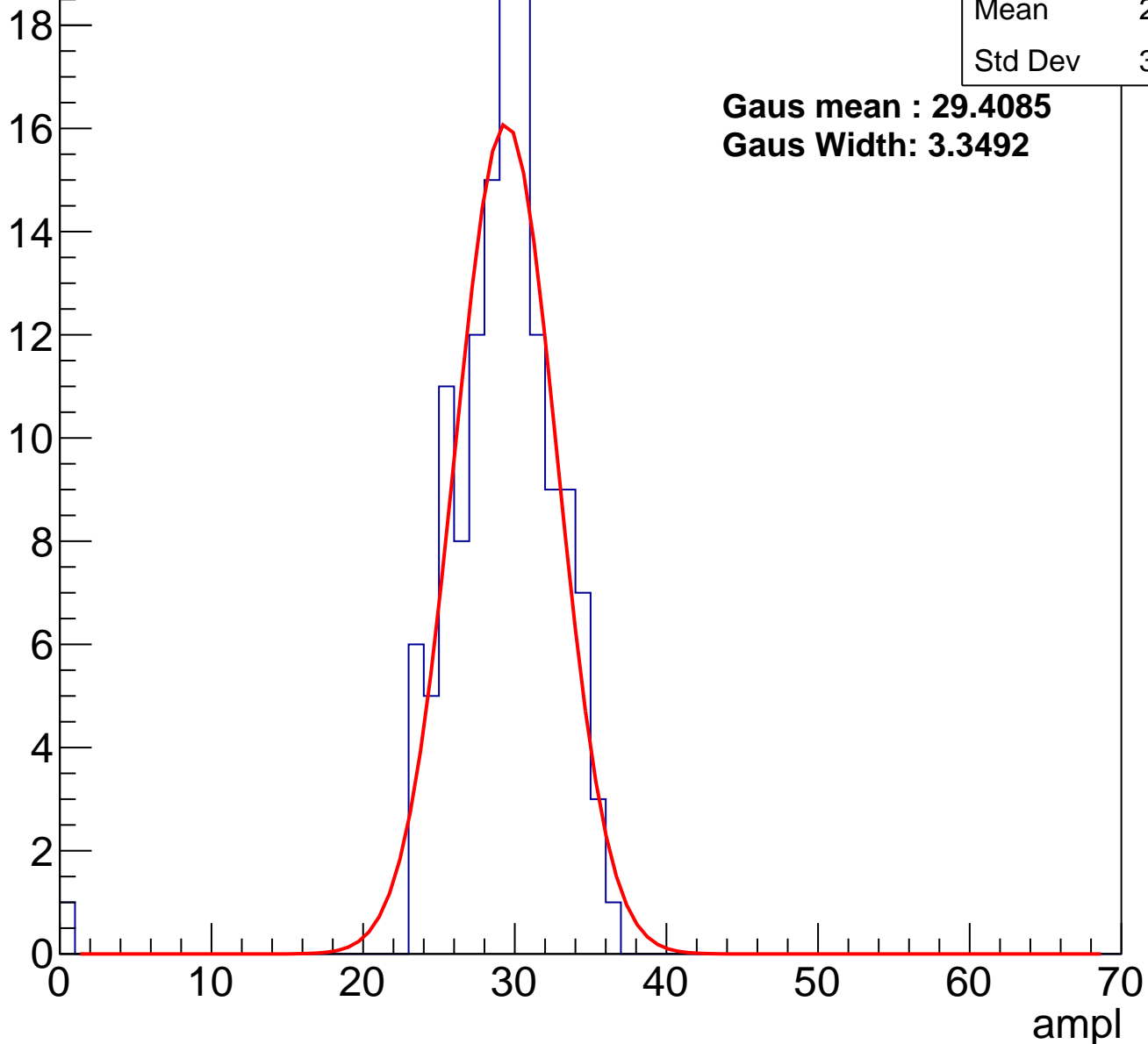
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.77
Std Dev	3.924

Gaus mean : 29.4085

Gaus Width: 3.3492

Entry

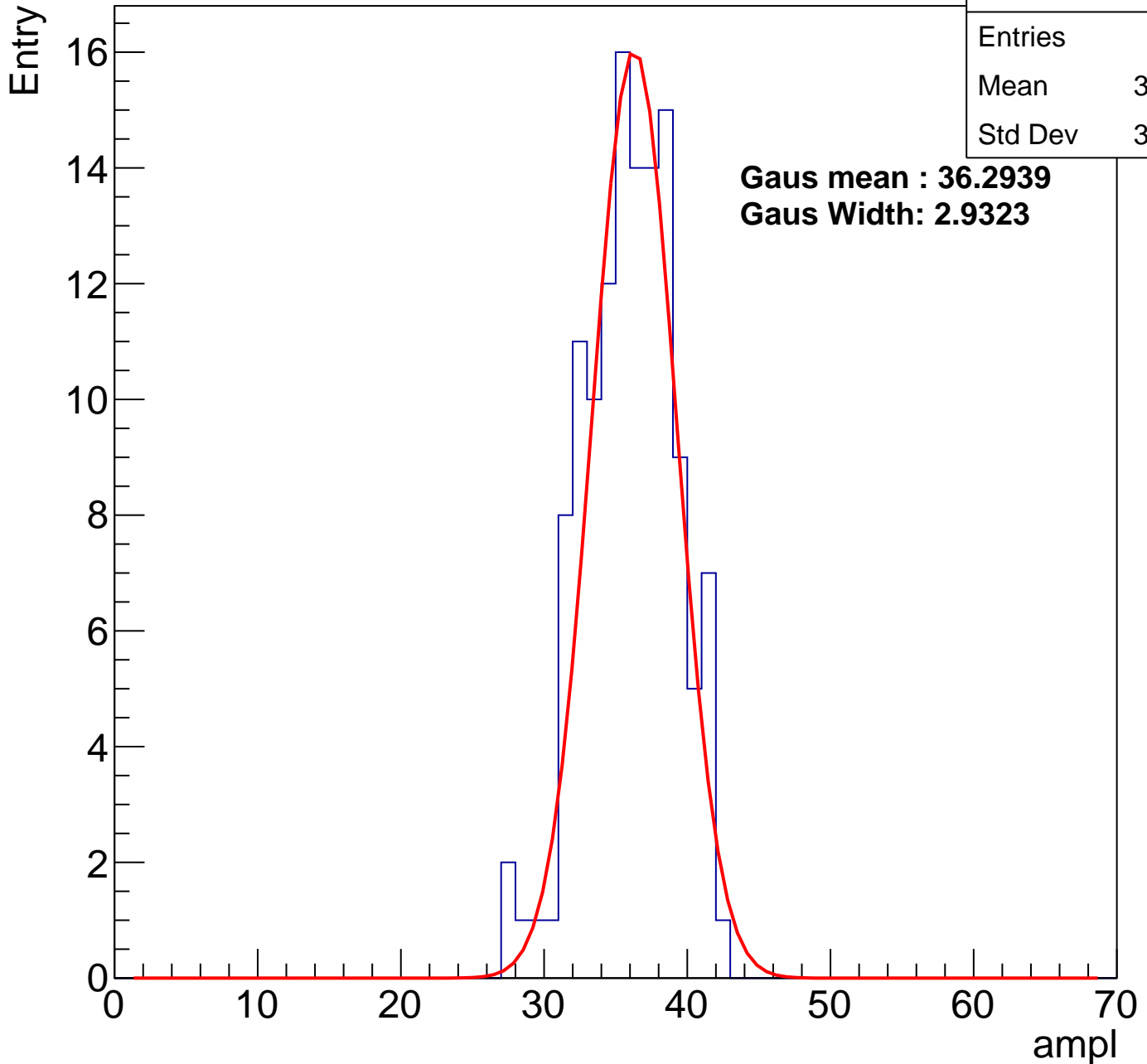


B1L001S, U16-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	35.52
Std Dev	3.147

Gaus mean : 36.2939
Gaus Width: 2.9323



B1L001S, U16-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	41.5
Std Dev	3.207

Gaus mean : 42.2811

Gaus Width: 3.4375

Entry

14

12

10

8

6

4

2

0

0

10

20

30

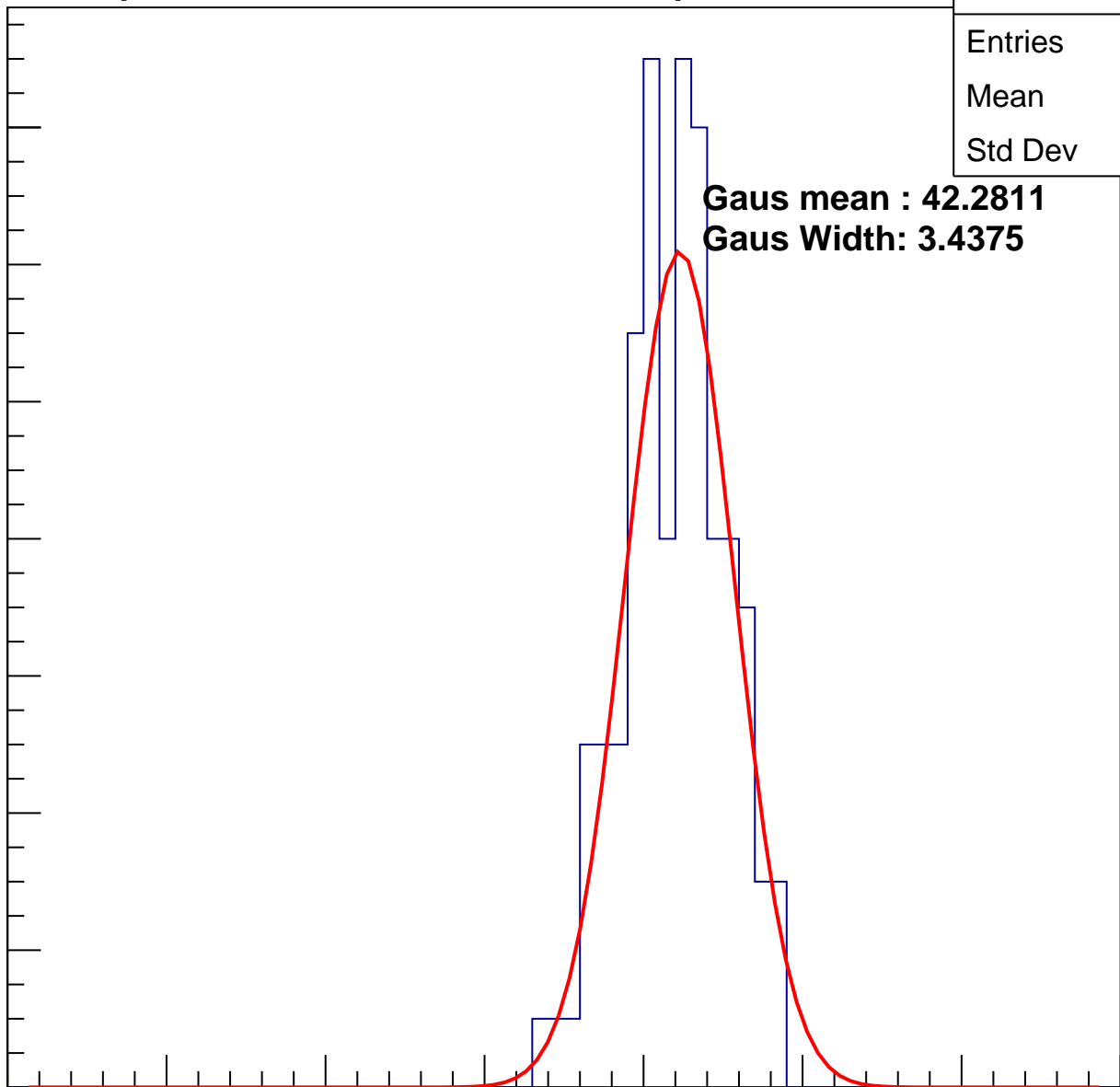
40

50

60

70

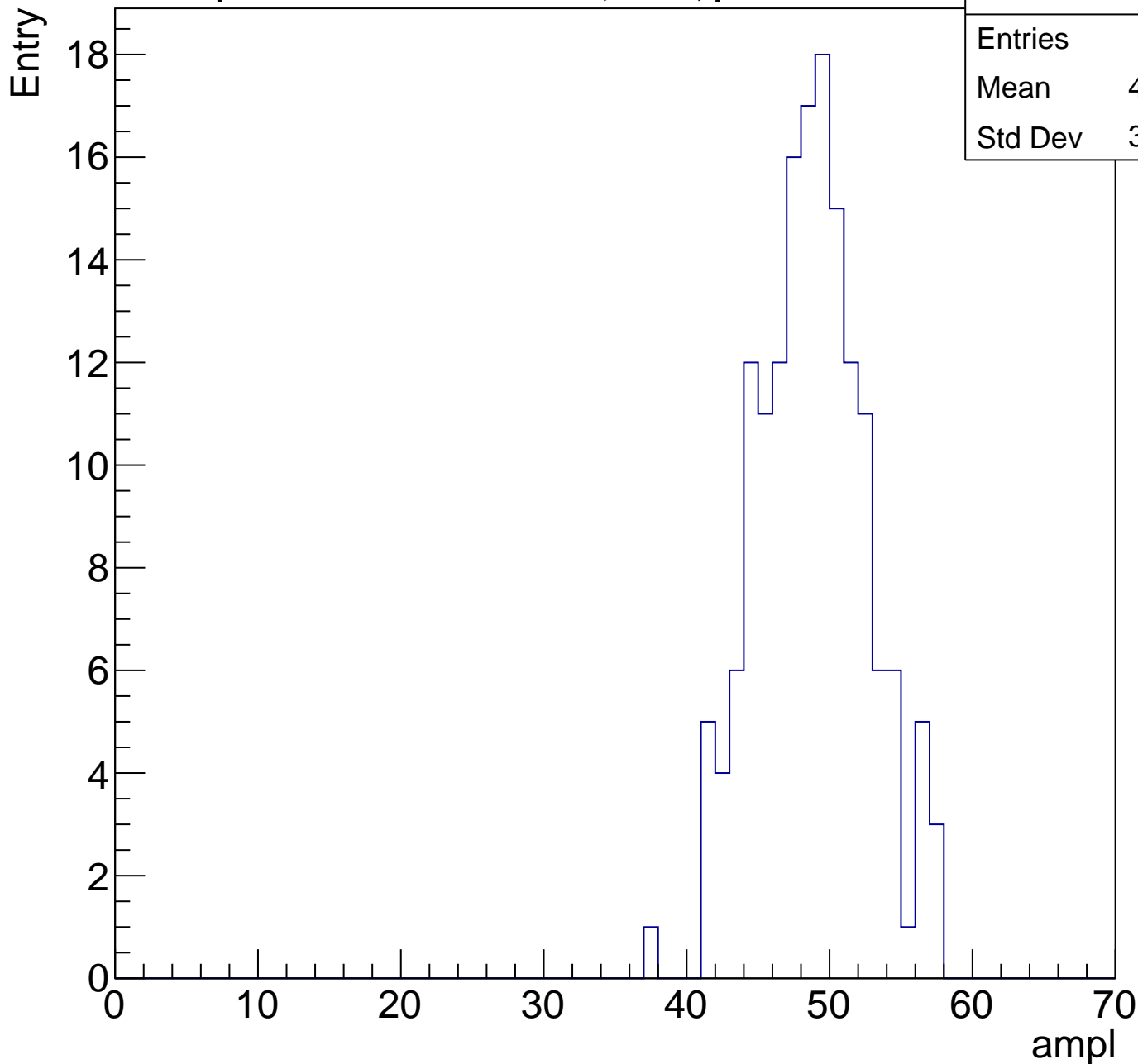
ampl



B1L001S, U16-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	48.29
Std Dev	3.813



B1L001S, U16-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	54.36
Std Dev	3.354

Entry

12

10

8

6

4

2

0

0

10

20

30

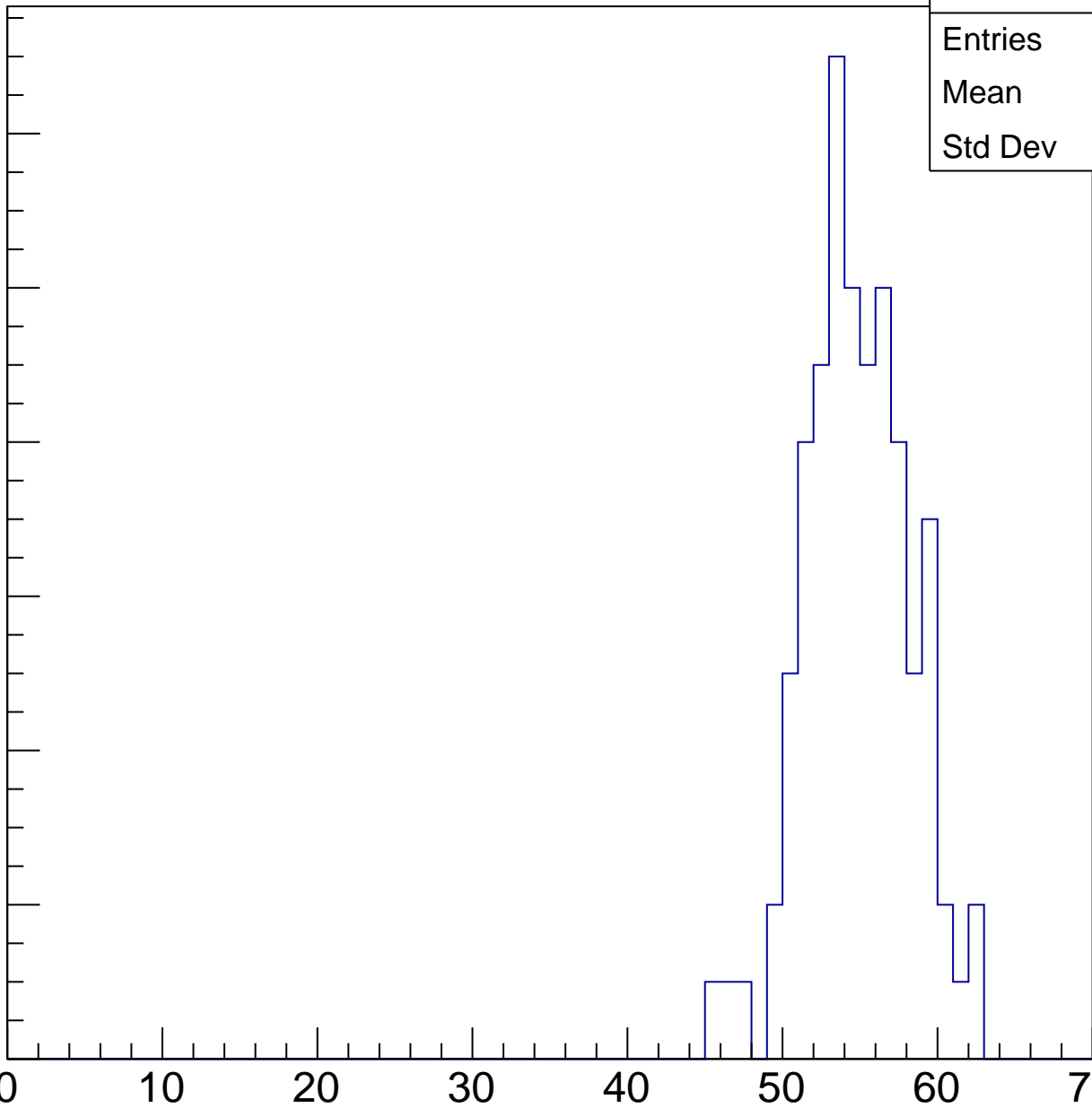
40

50

60

70

ampl



B1L001S, U16-ch82, adc5

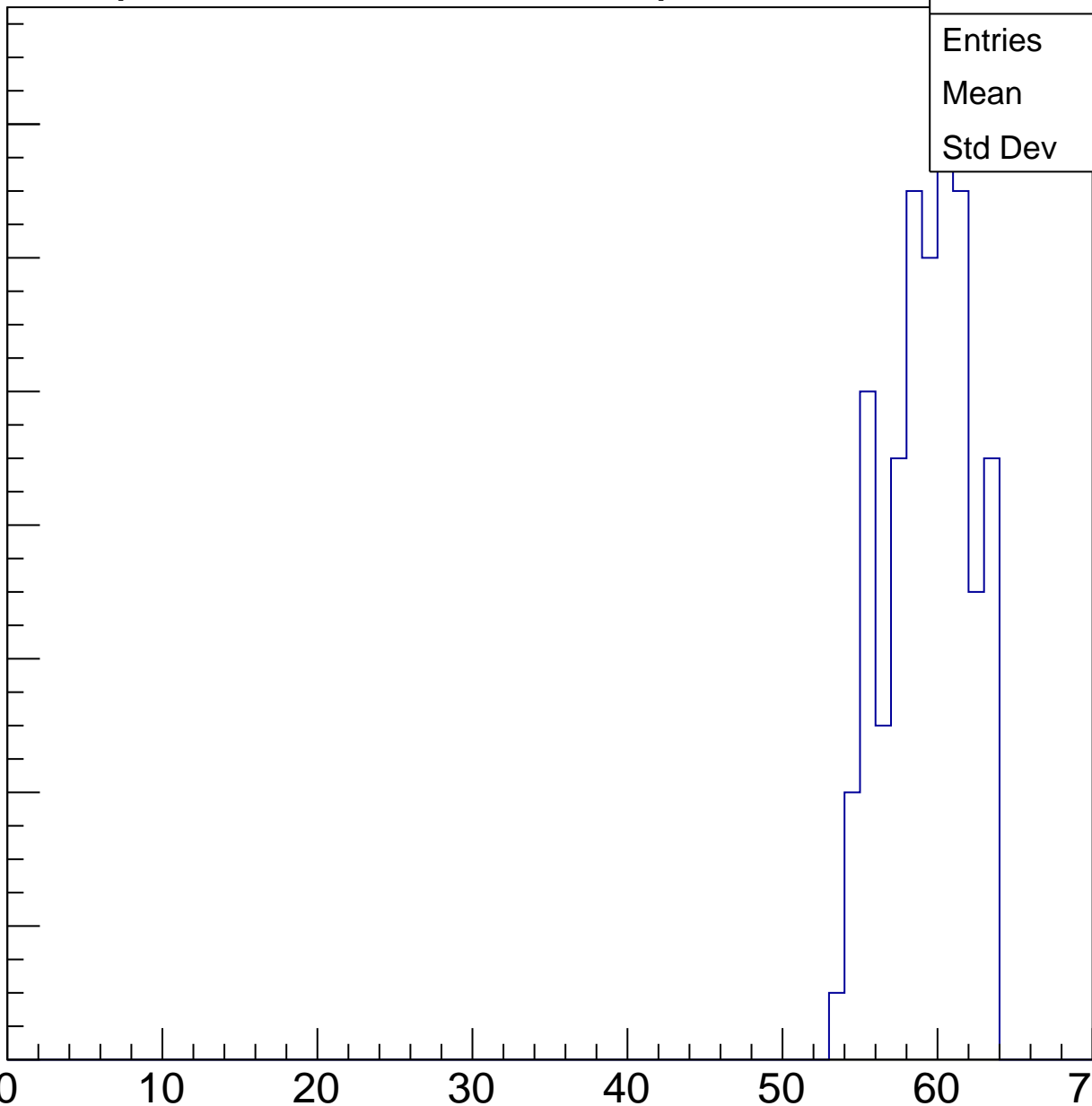
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	58.86
Std Dev	2.599

ampl



B1L001S, U16-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	34
Mean	57.97
Std Dev	14.56

Entry

10

8

6

4

2

0

0

10

20

30

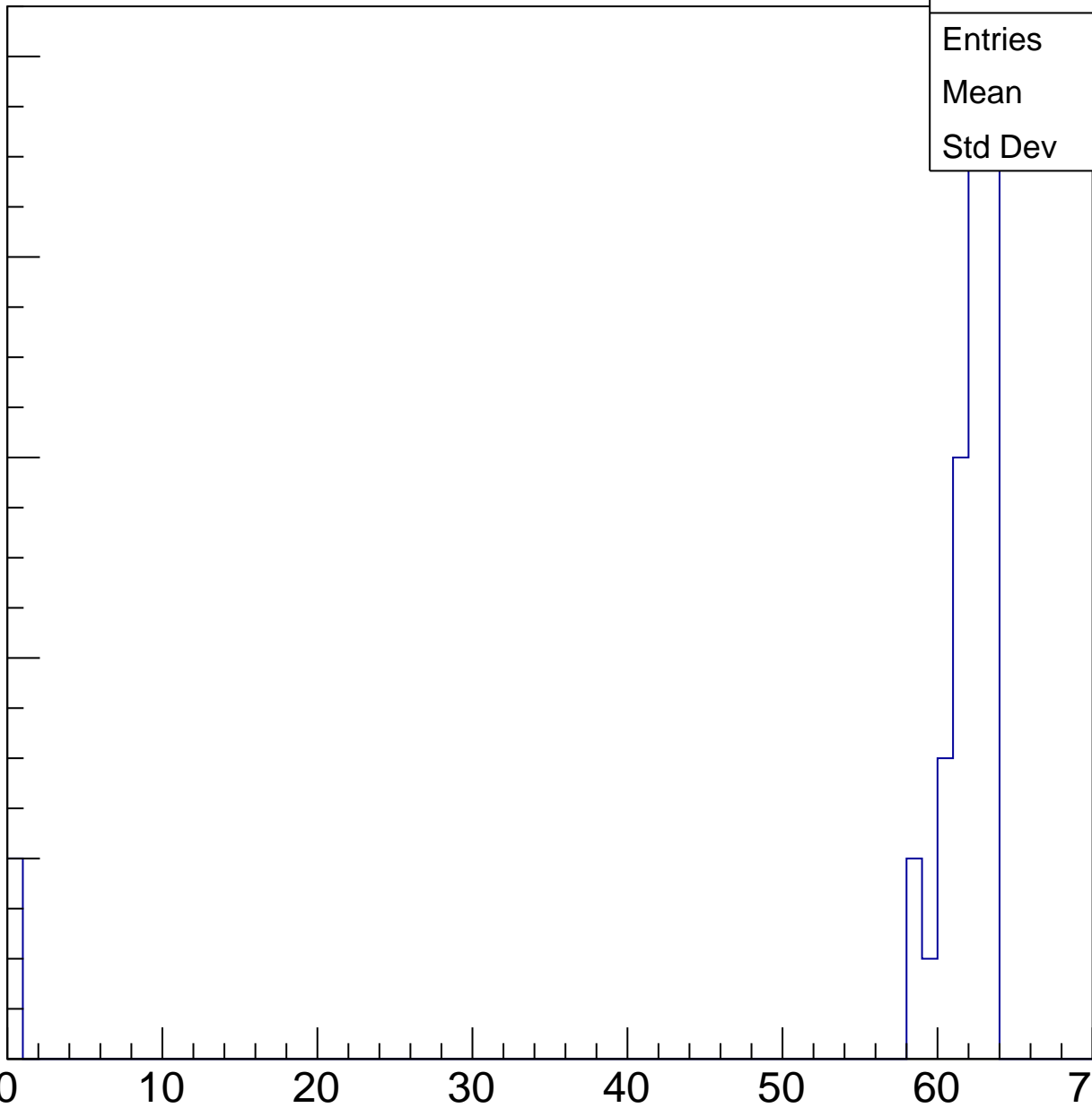
40

50

60

ampl

70



B1L001S, U16-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	49.33
Std Dev	18.62

ampl

0 10 20 30 40 50 60 70

B1L001S, U16-ch83, adc0

calib_packv5_042523_0143.root, FC#2, port C2

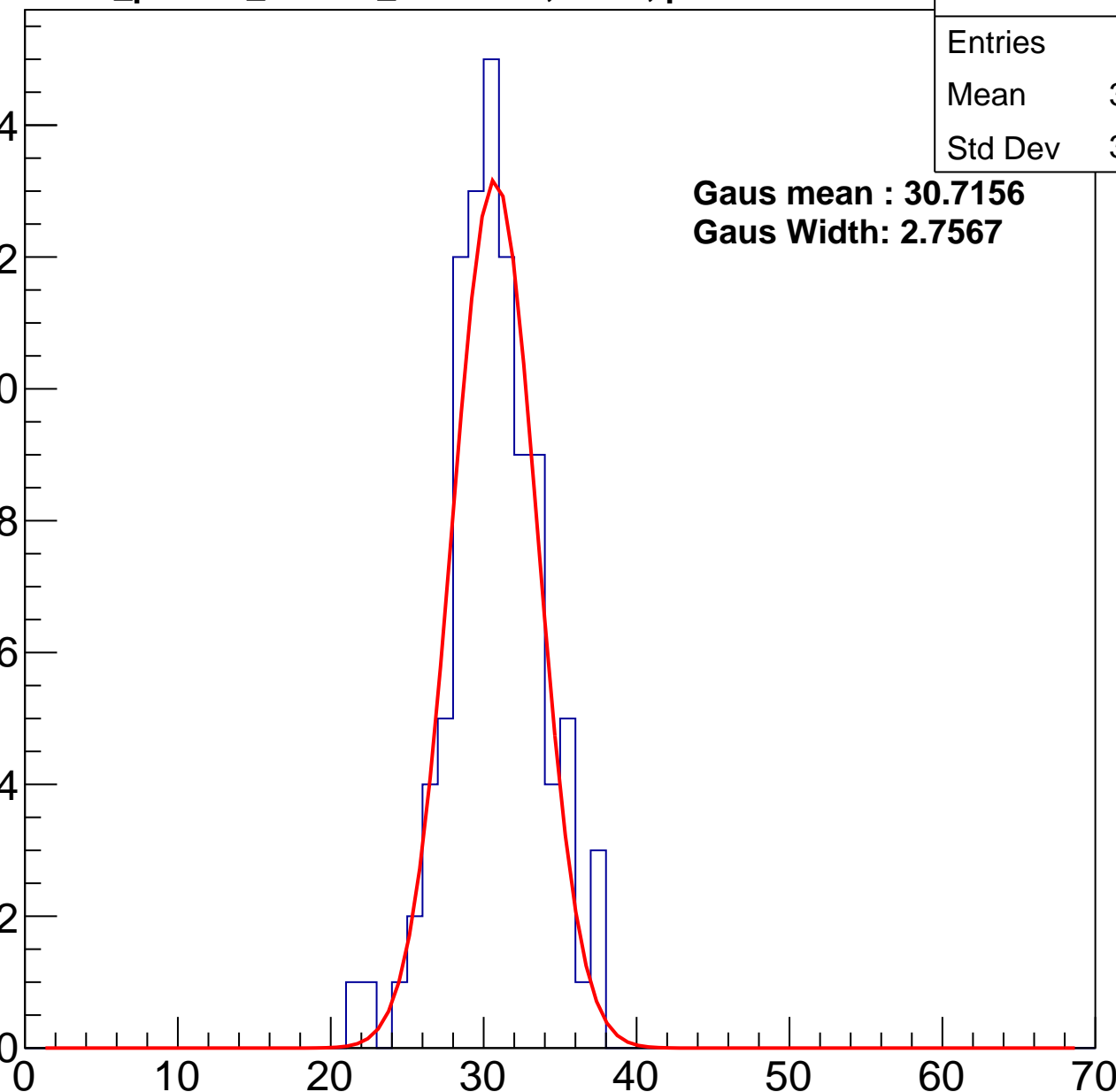
Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	30.25
Std Dev	3.039

Gaus mean : 30.7156

Gaus Width: 2.7567



ampl

B1L001S, U16-ch83, adc1

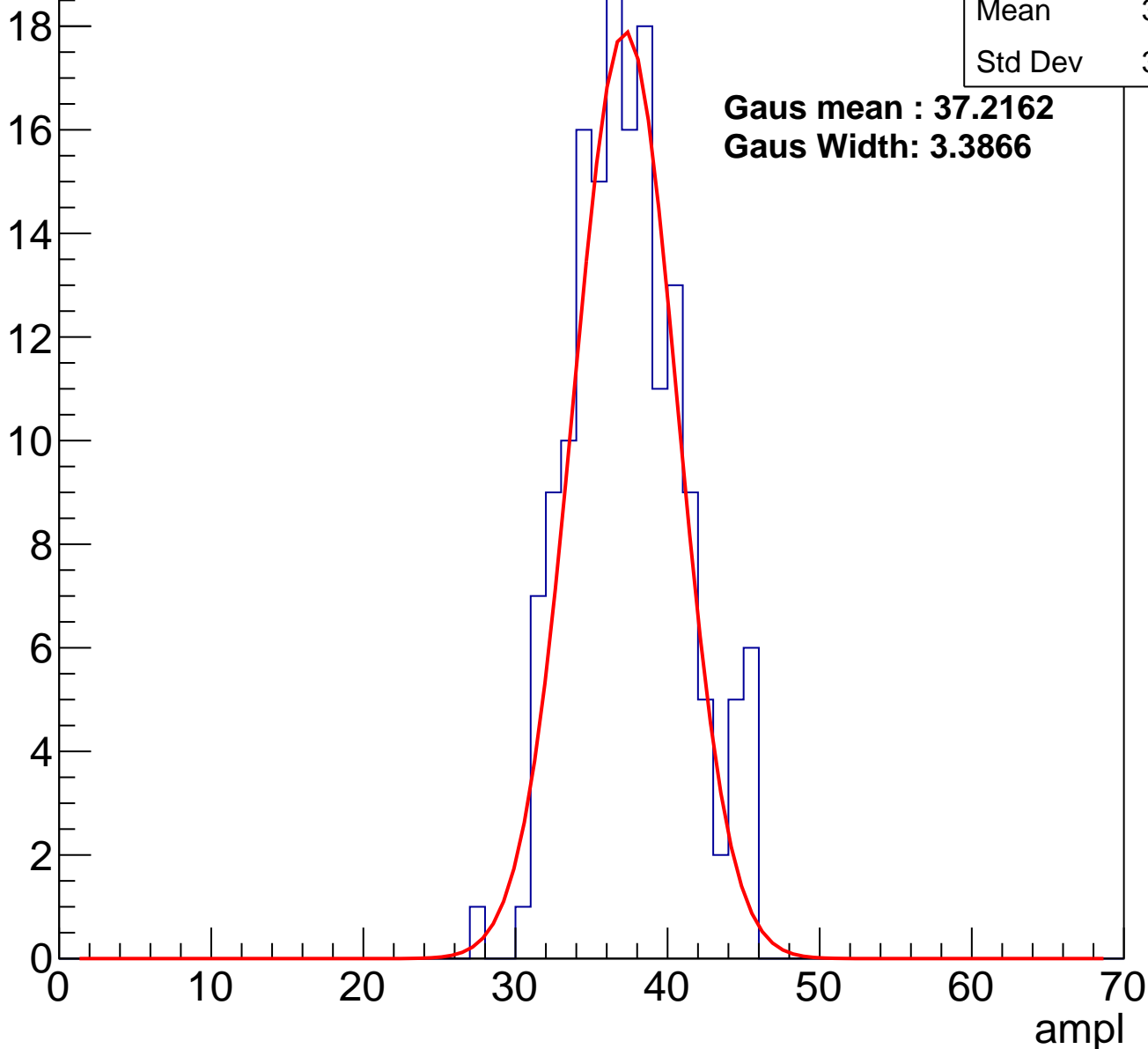
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	36.96
Std Dev	3.638

Gaus mean : 37.2162

Gaus Width: 3.3866

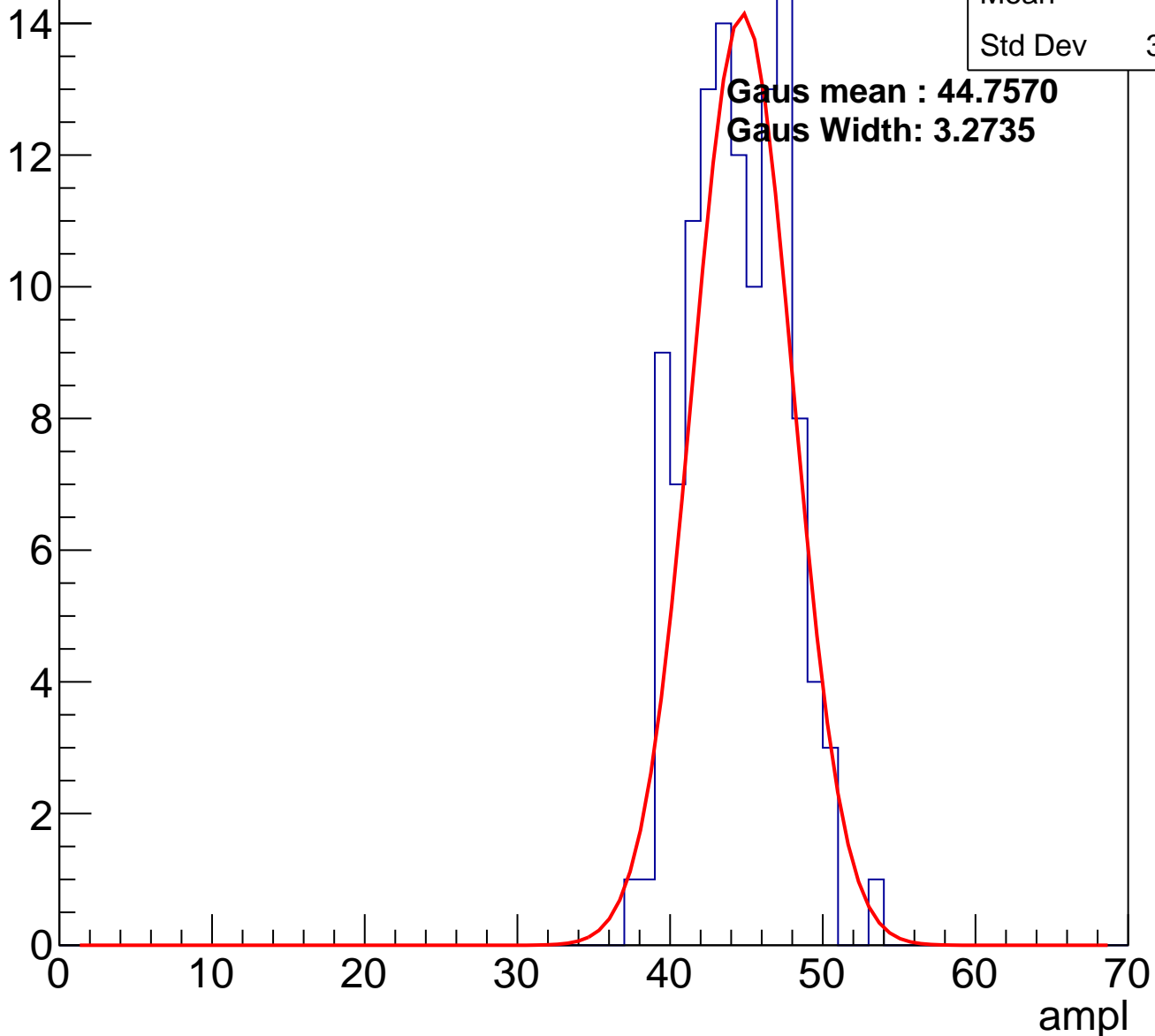
Entry



B1L001S, U16-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

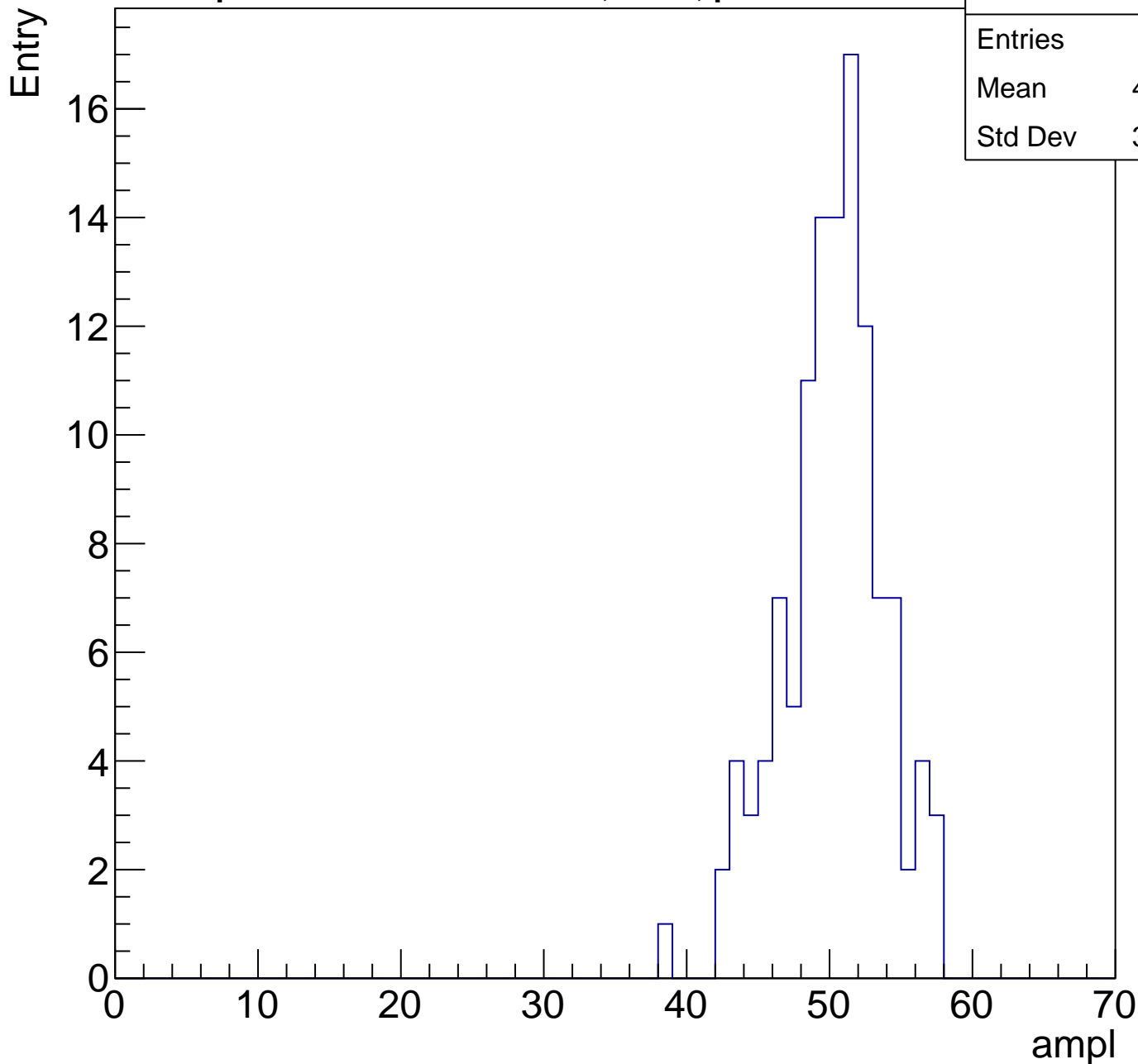
Entry



B1L001S, U16-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

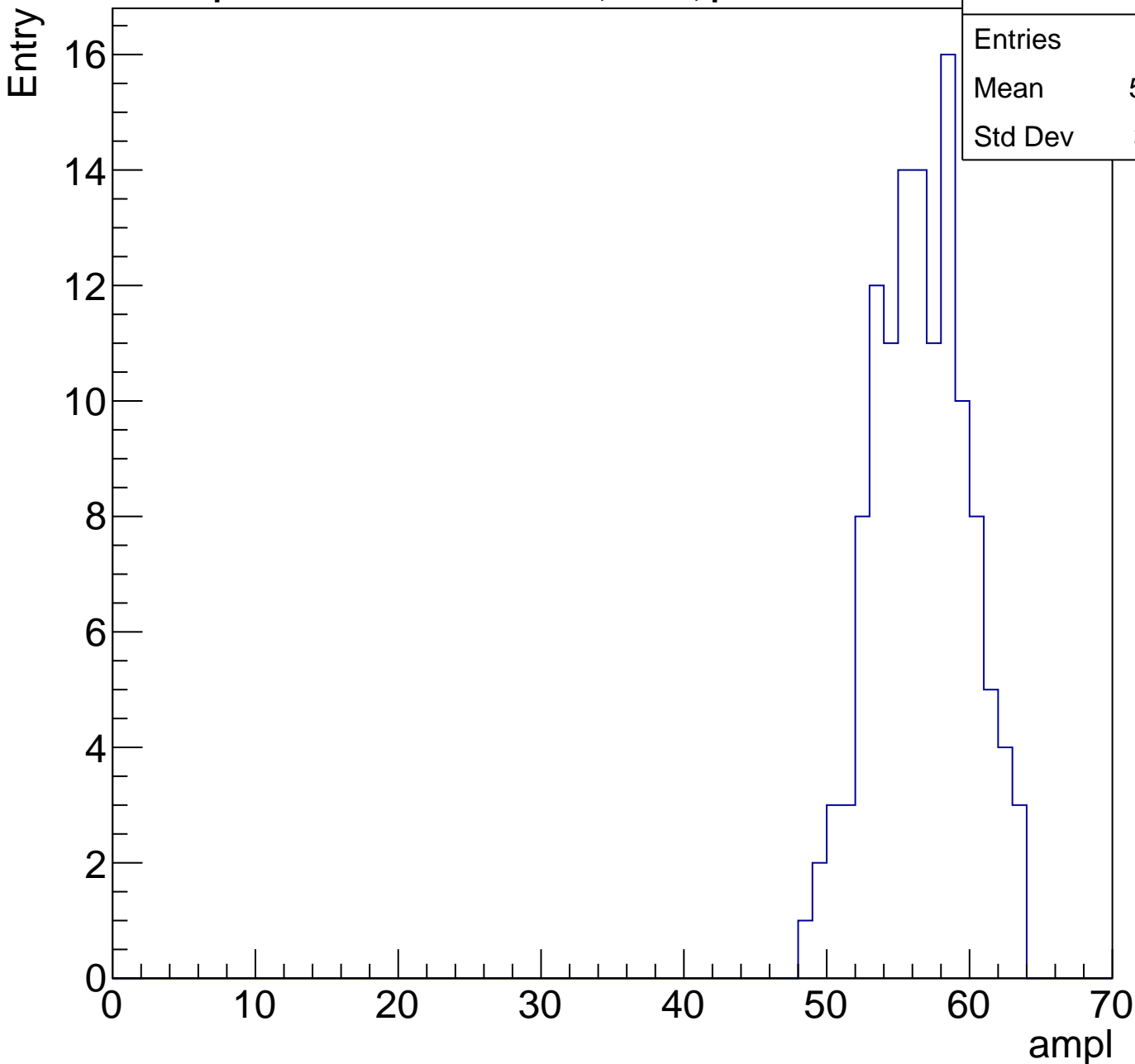
Entries	117
Mean	49.76
Std Dev	3.539



B1L001S, U16-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	56.13
Std Dev	3.281



B1L001S, U16-ch83, adc5

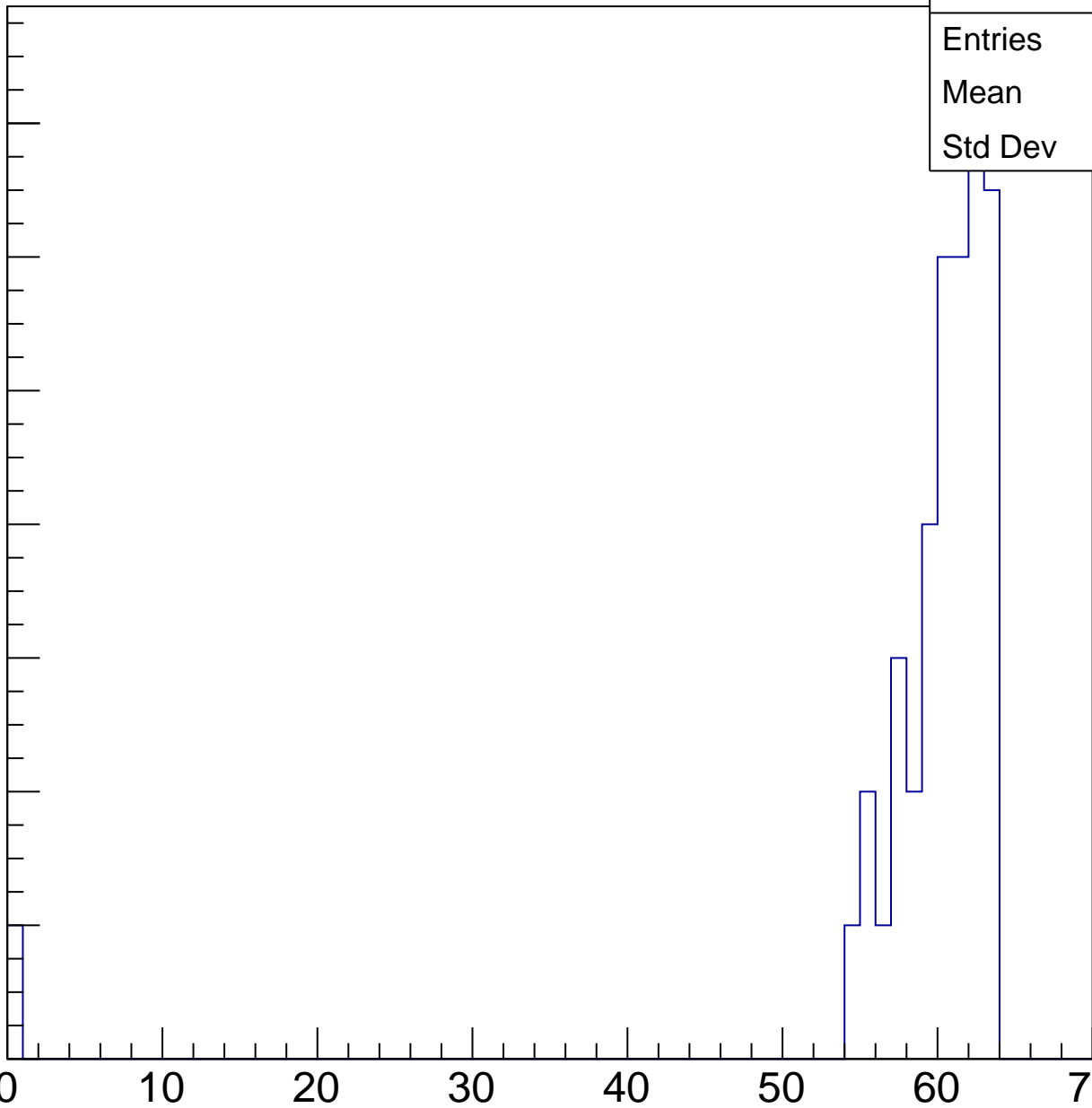
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.59
Std Dev	9.69

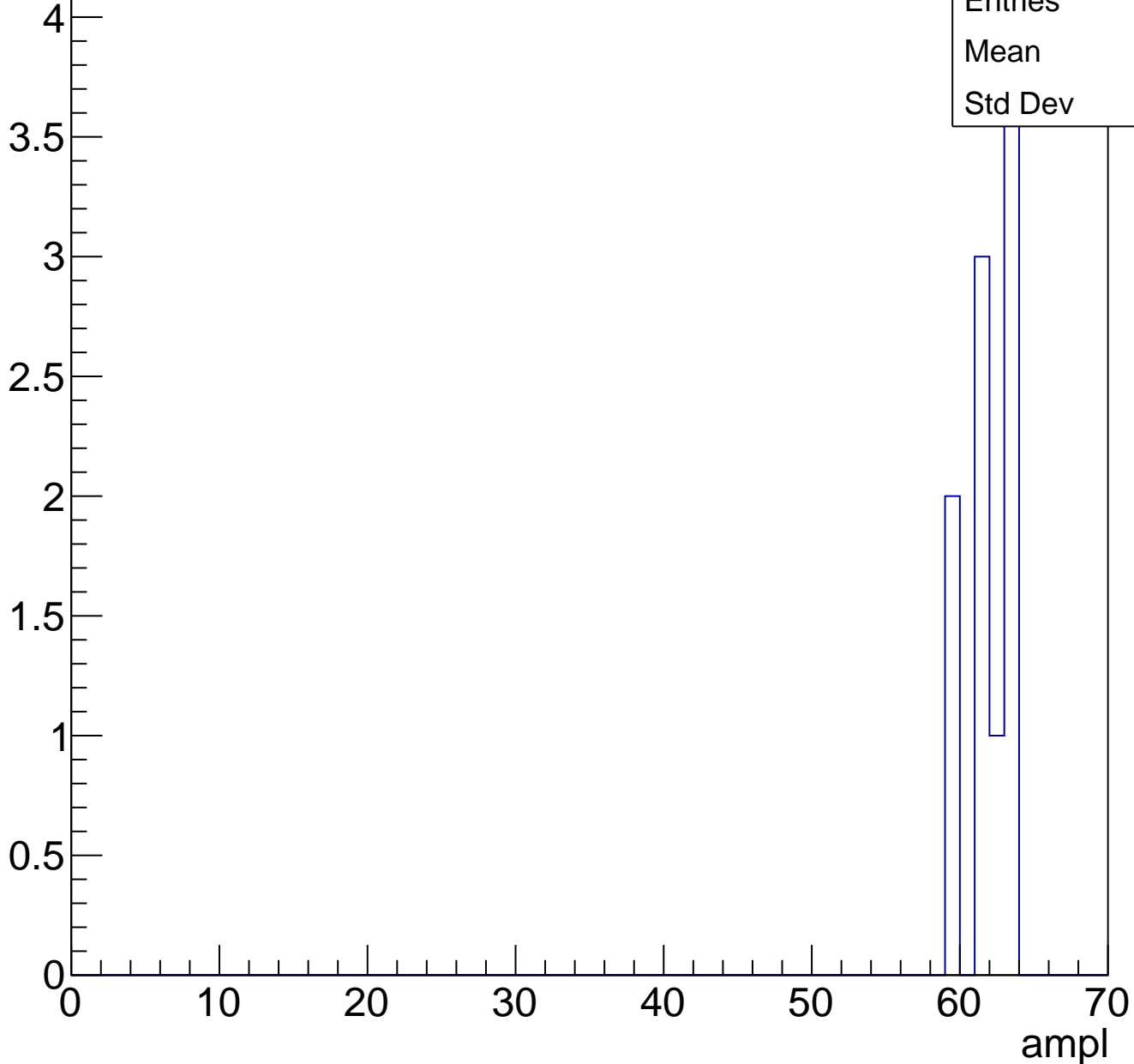
ampl



B1L001S, U16-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

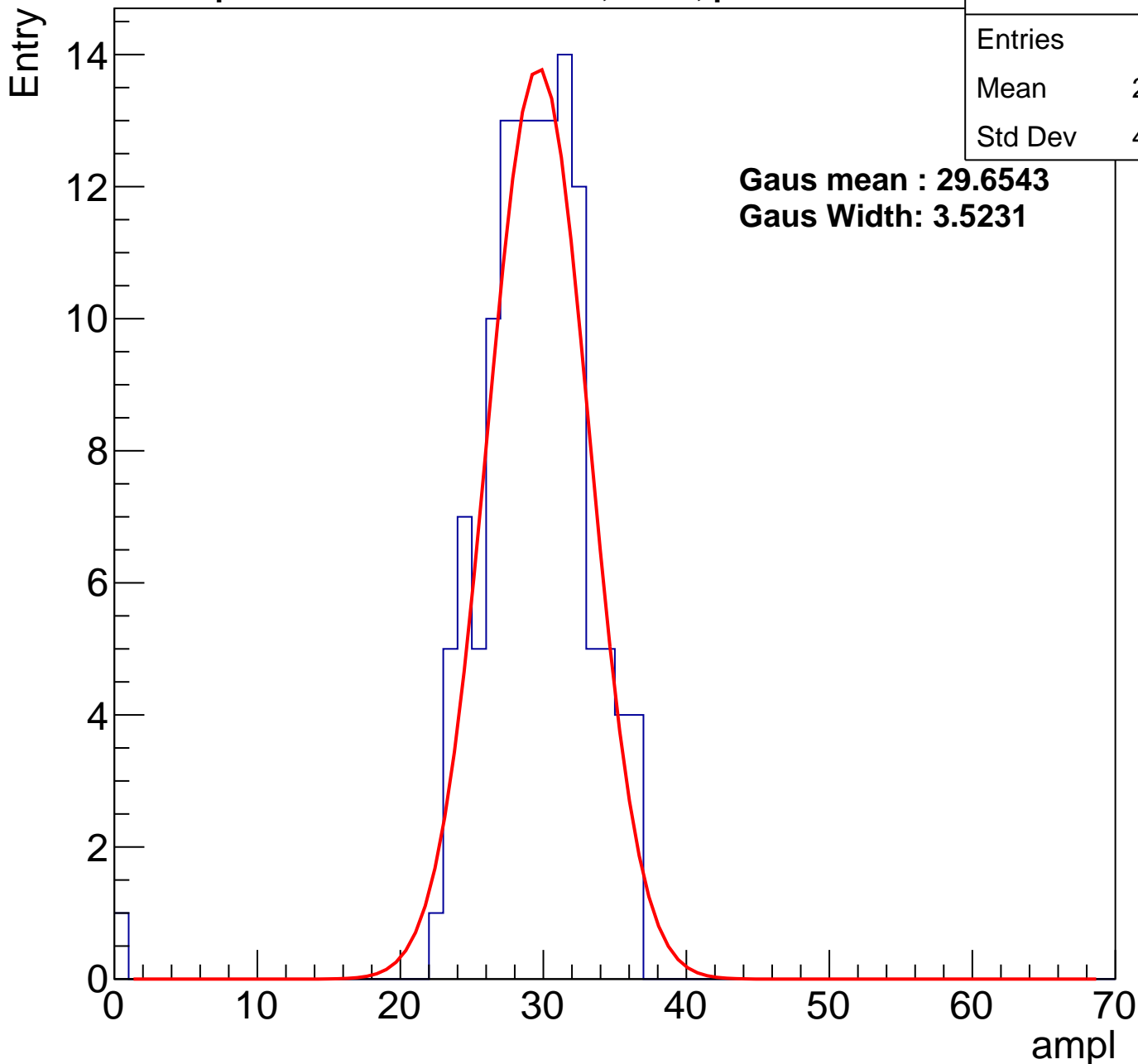
B1L001S, U16-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	28.87
Std Dev	4.195

Gaus mean : 29.6543

Gaus Width: 3.5231

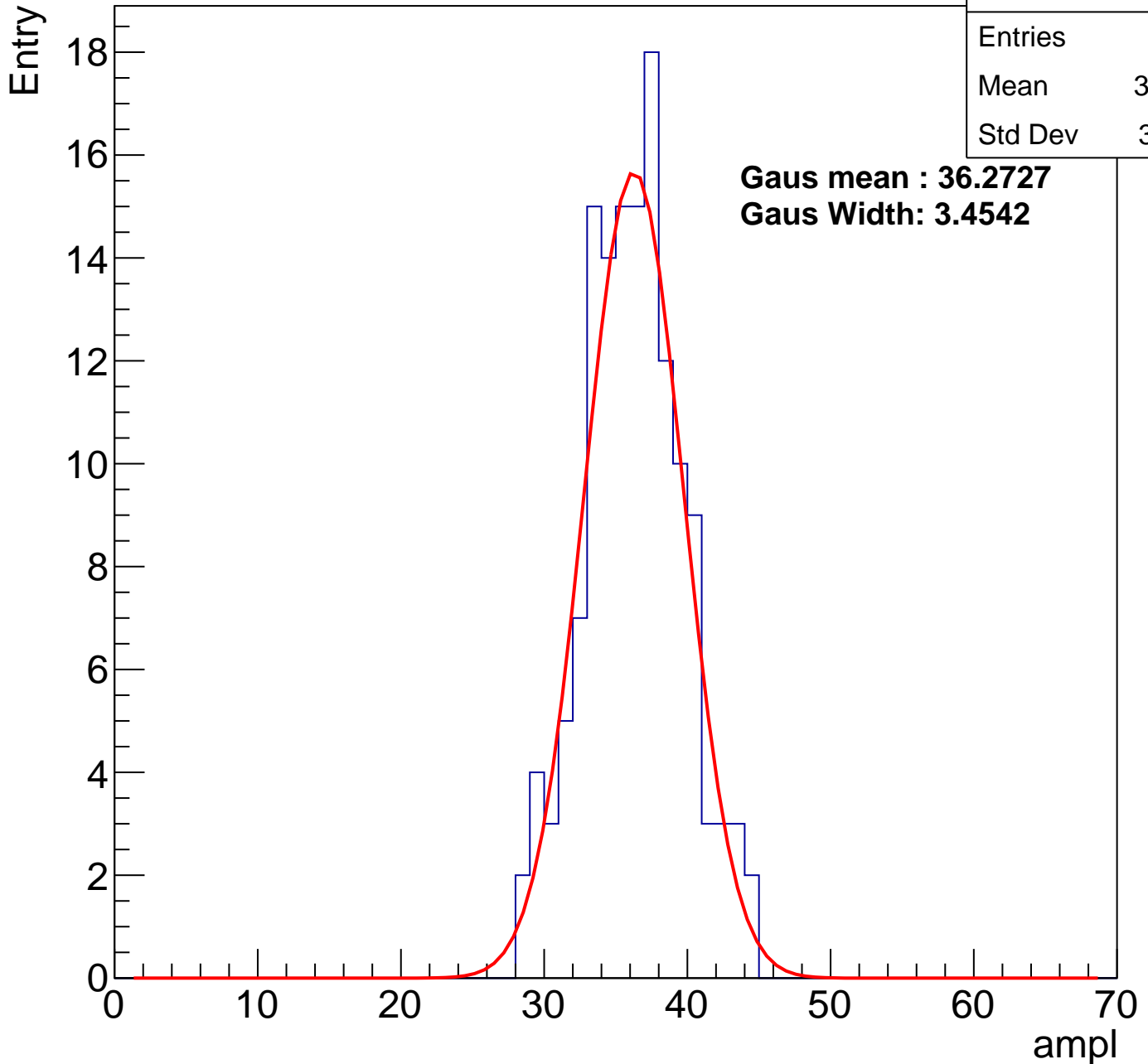


B1L001S, U16-ch84, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.82
Std Dev	3.411

Gaus mean : 36.2727
Gaus Width: 3.4542



B1L001S, U16-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

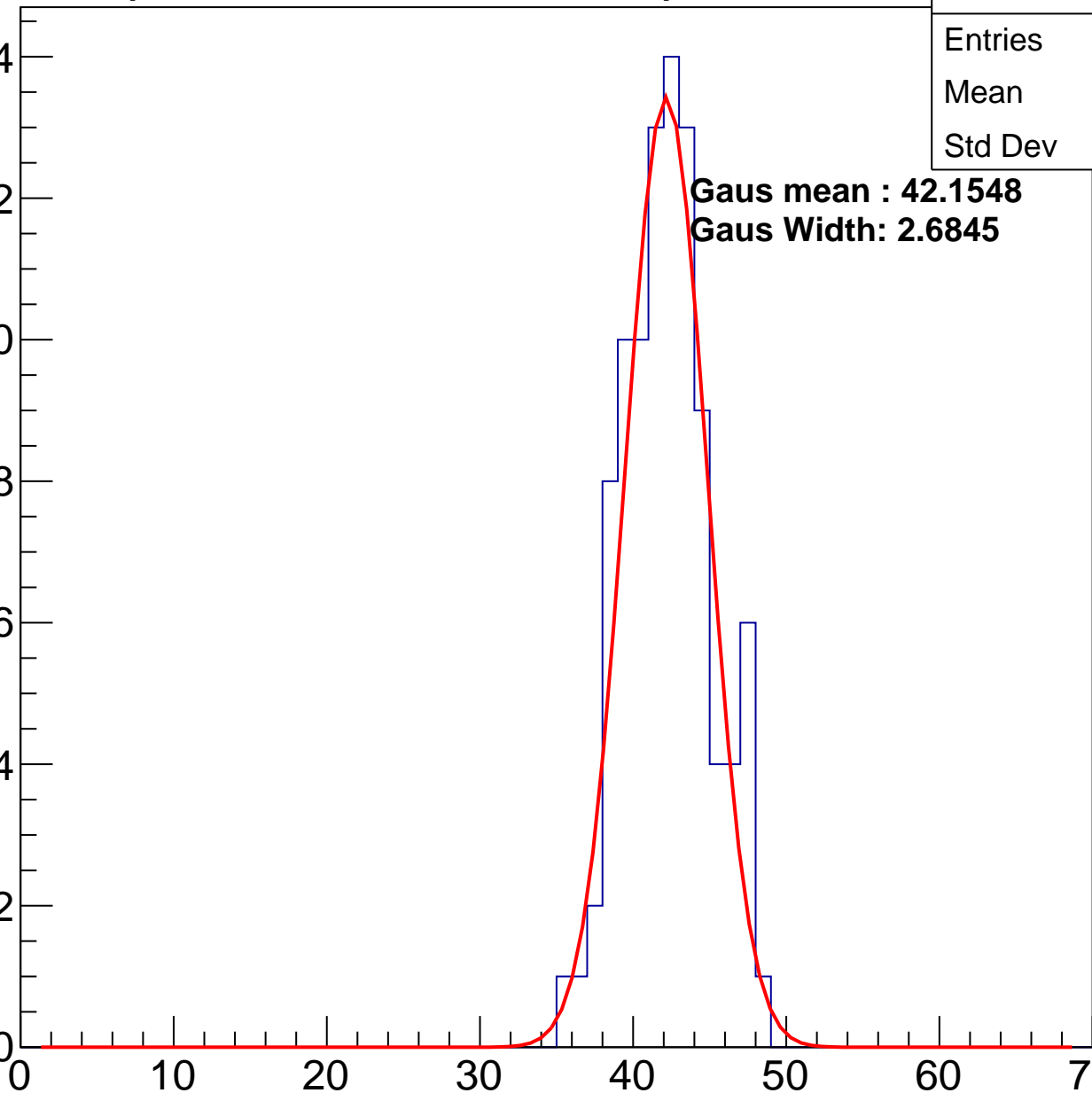
14
12
10
8
6
4
2
0

Entries	96
Mean	41.76
Std Dev	2.768

Gaus mean : 42.1548

Gaus Width: 2.6845

ampl

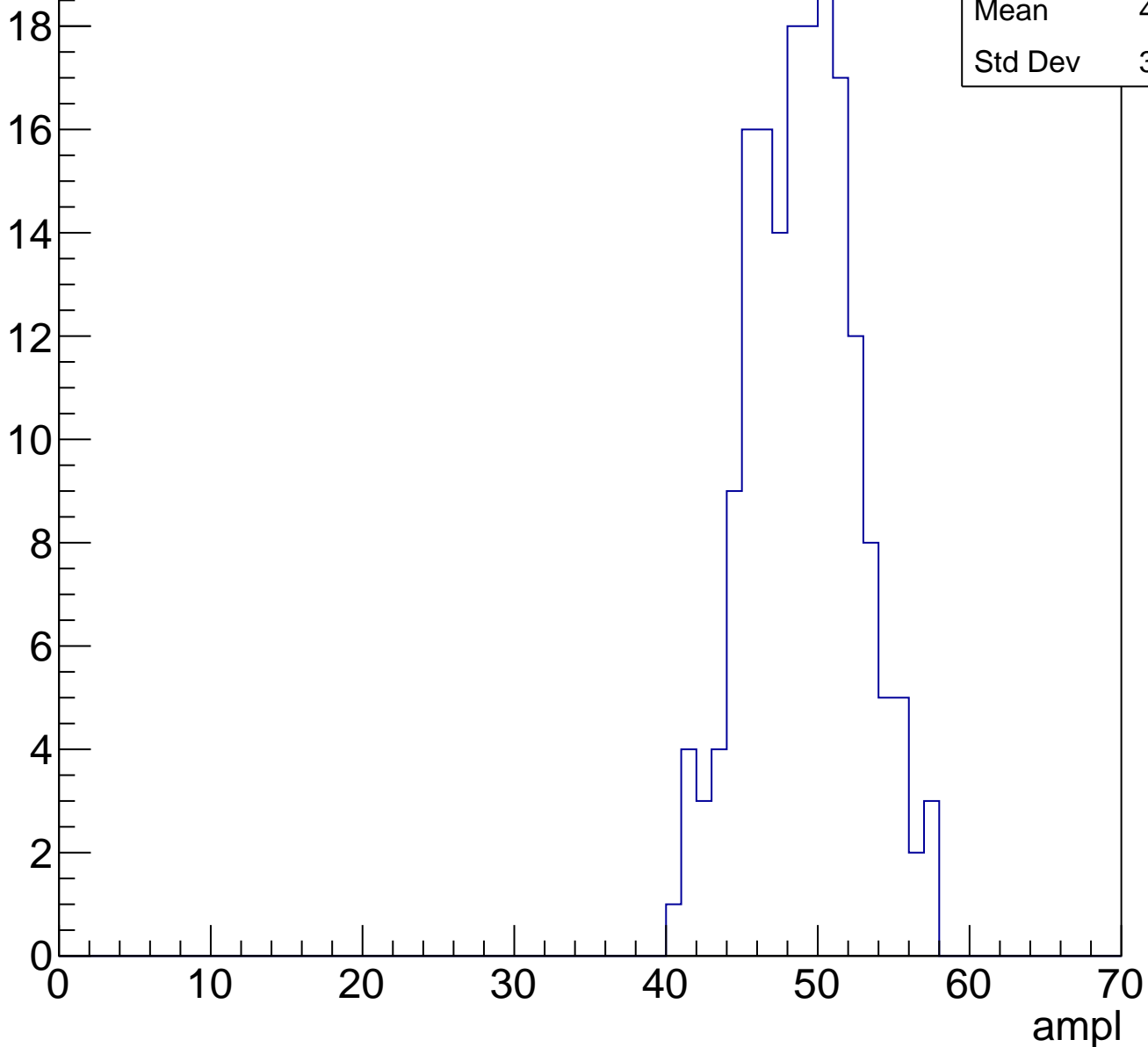


B1L001S, U16-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	48.57
Std Dev	3.566

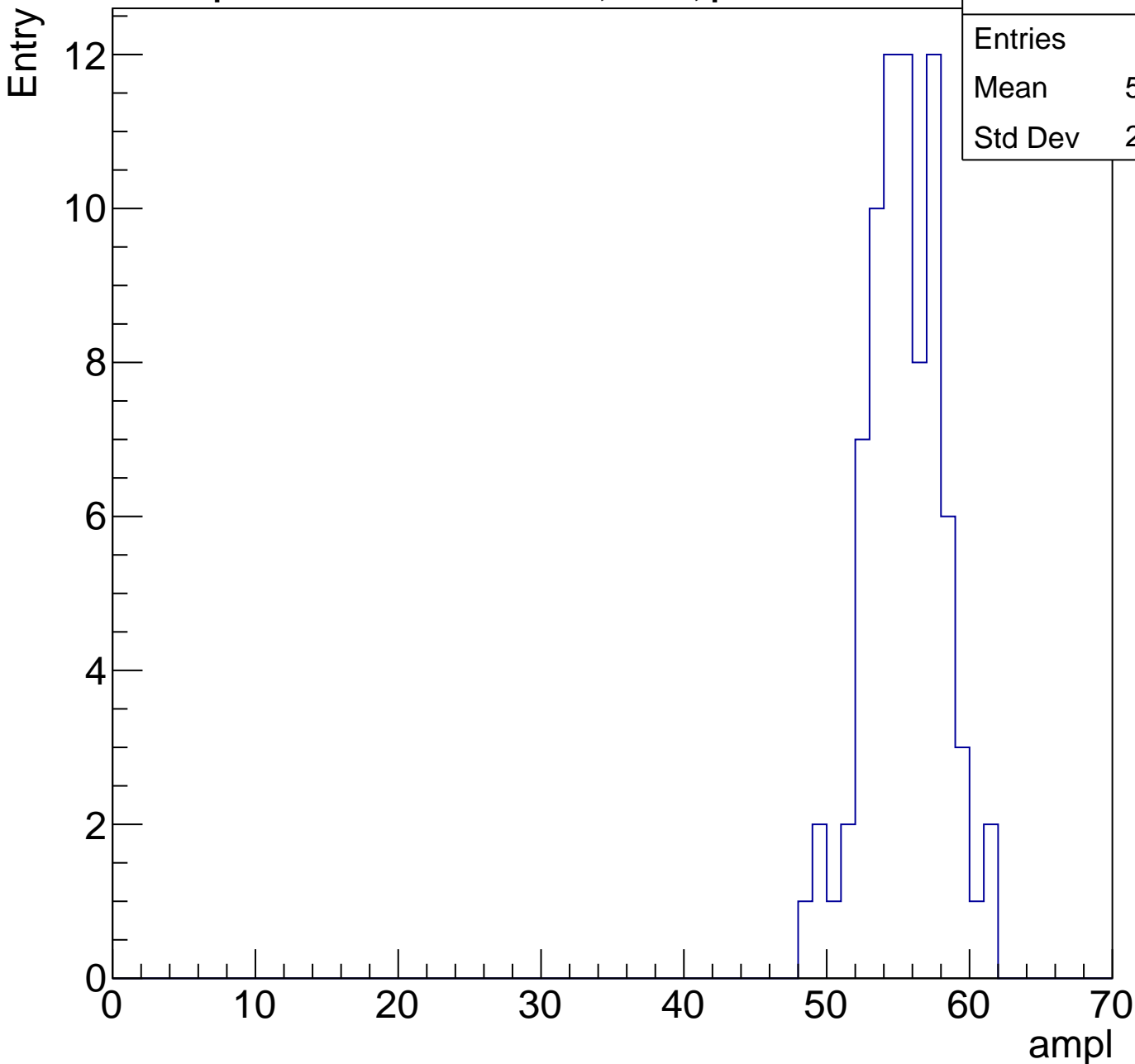
Entry



B1L001S, U16-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	54.92
Std Dev	2.633



B1L001S, U16-ch84, adc5

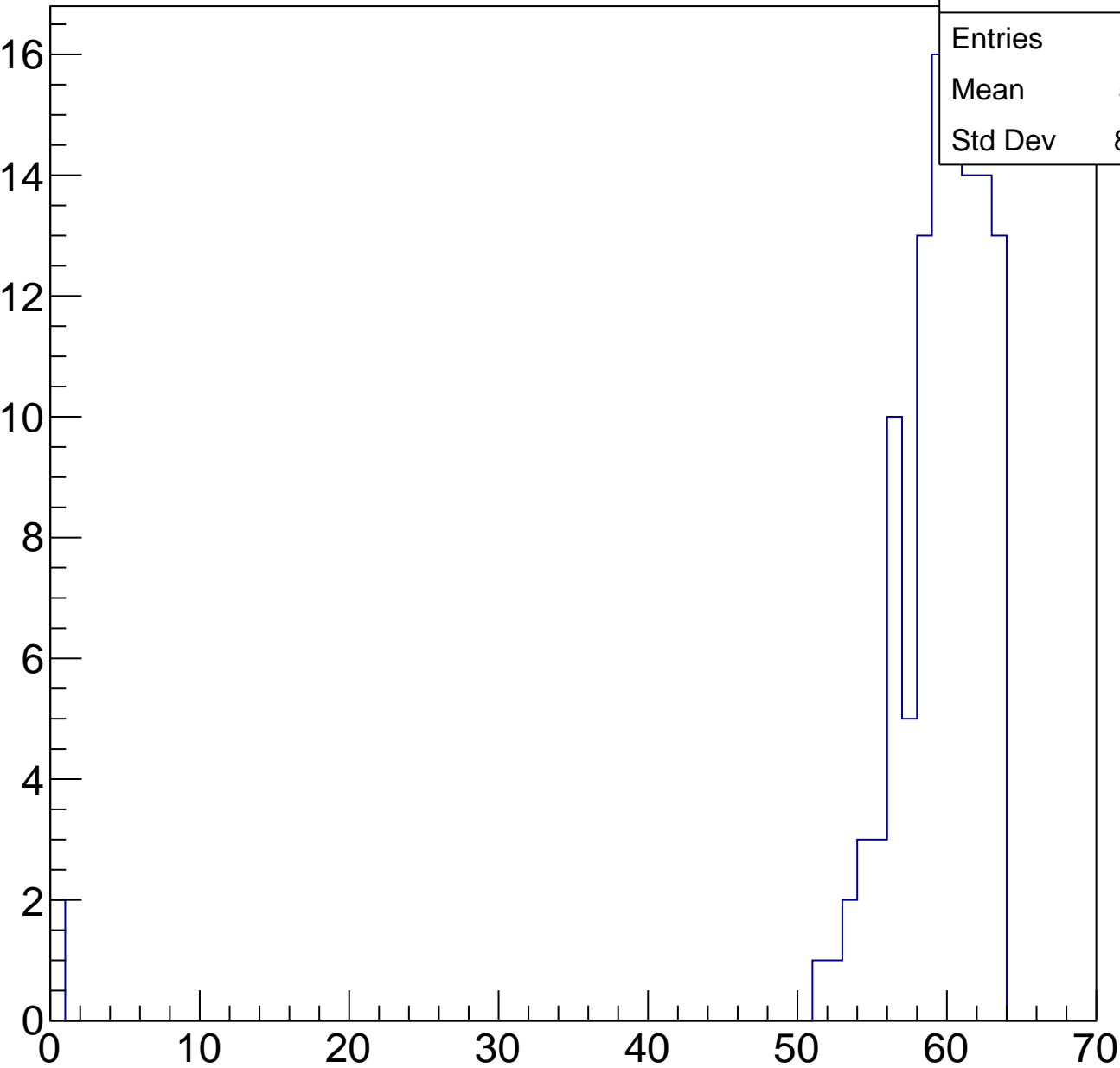
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.21
Std Dev	8.307

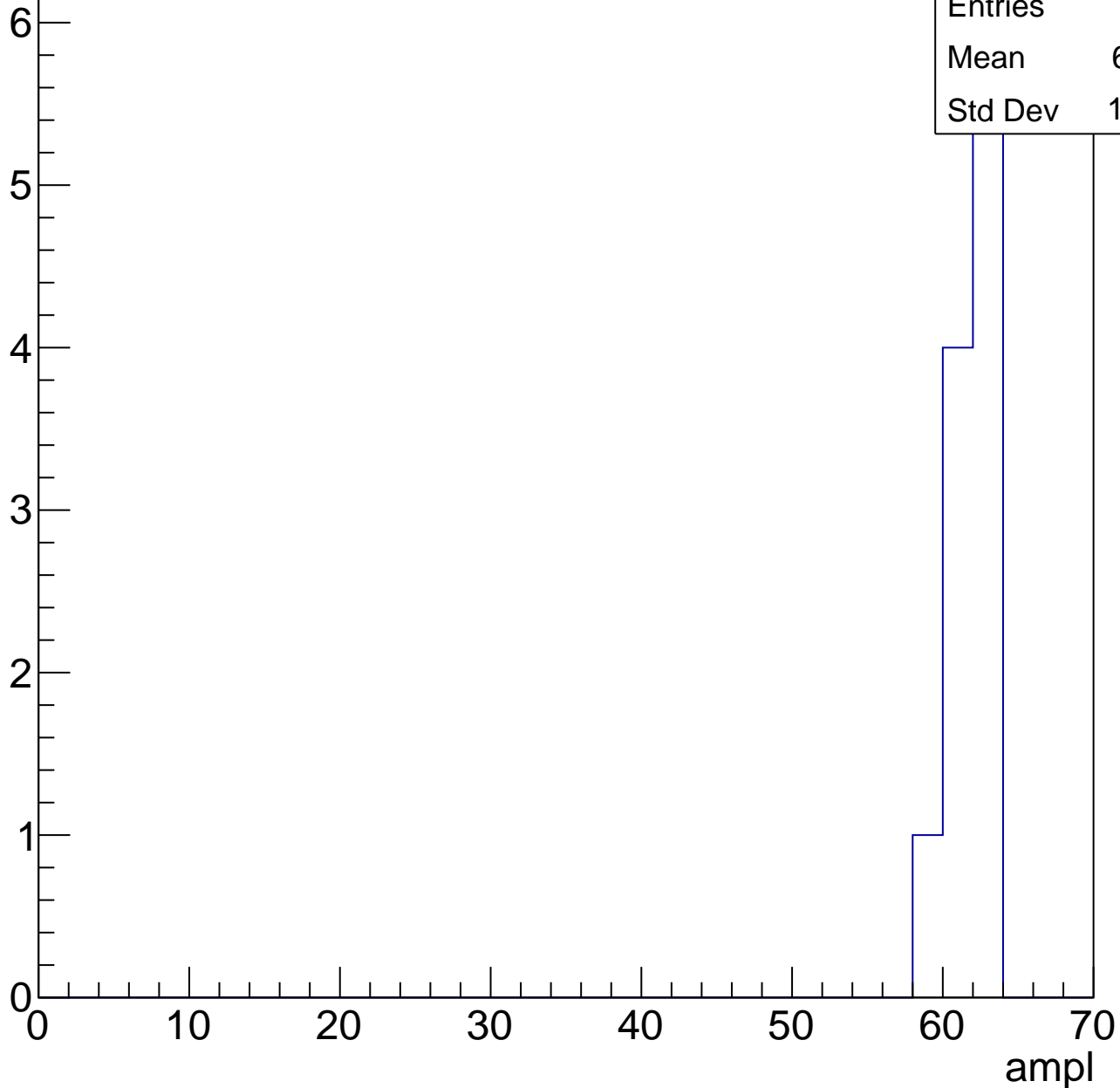
ampl



B1L001S, U16-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

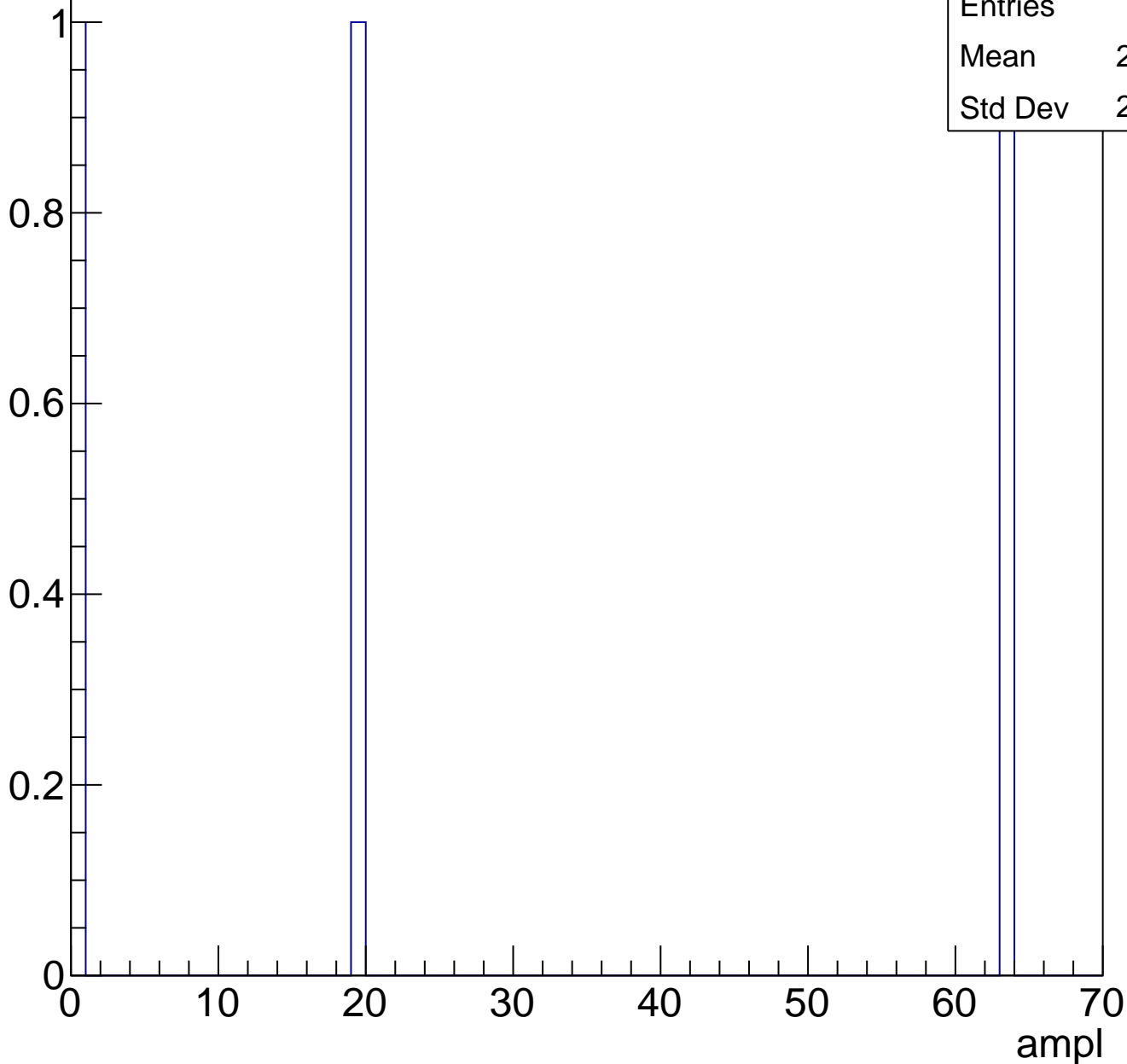
Entry



B1L001S, U16-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch85, adc0

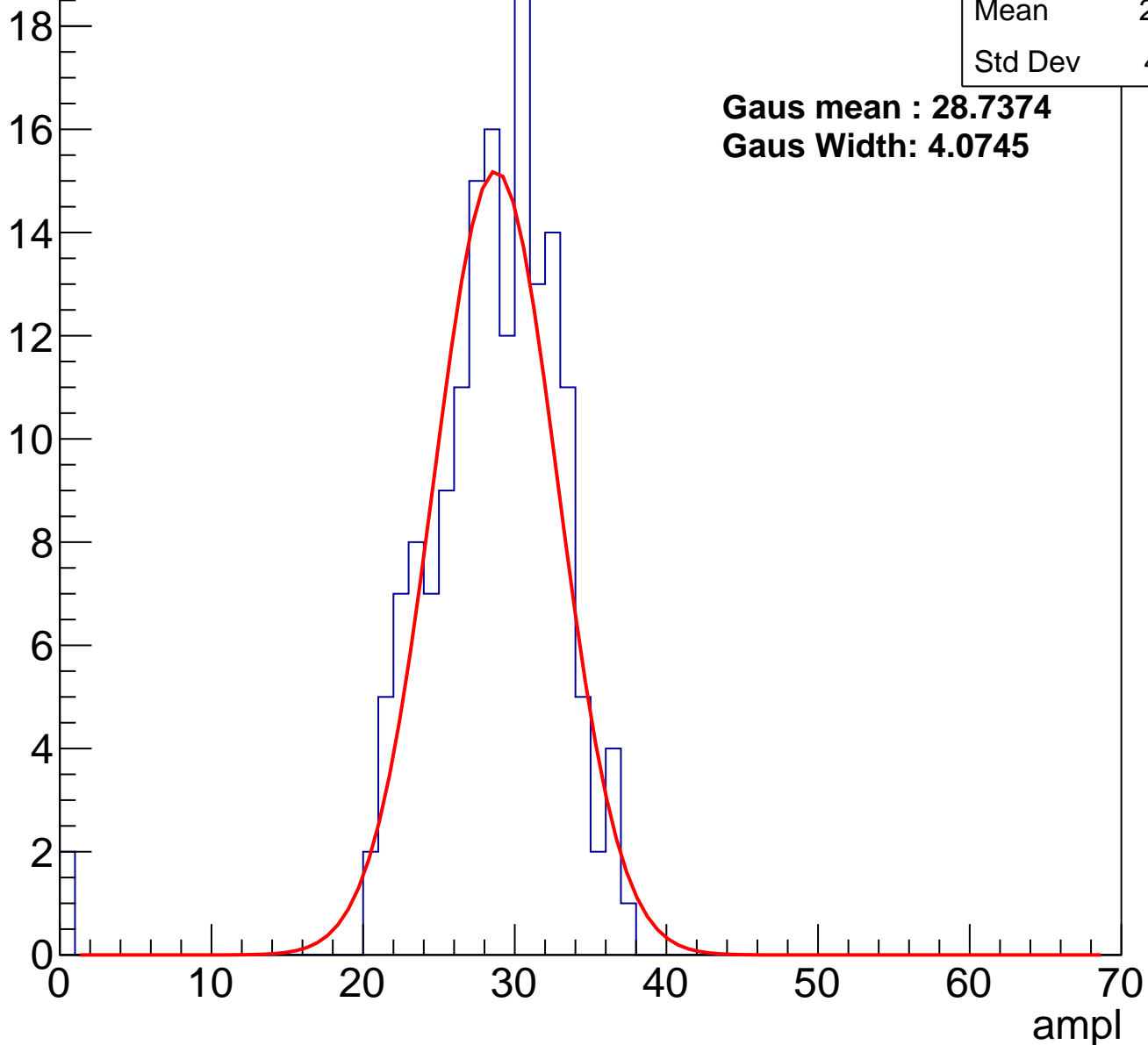
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	28.02
Std Dev	4.931

Gaus mean : 28.7374

Gaus Width: 4.0745

Entry



B1L001S, U16-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	34.99
Std Dev	3.295

Gaus mean : 35.8028

Gaus Width: 2.7630

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

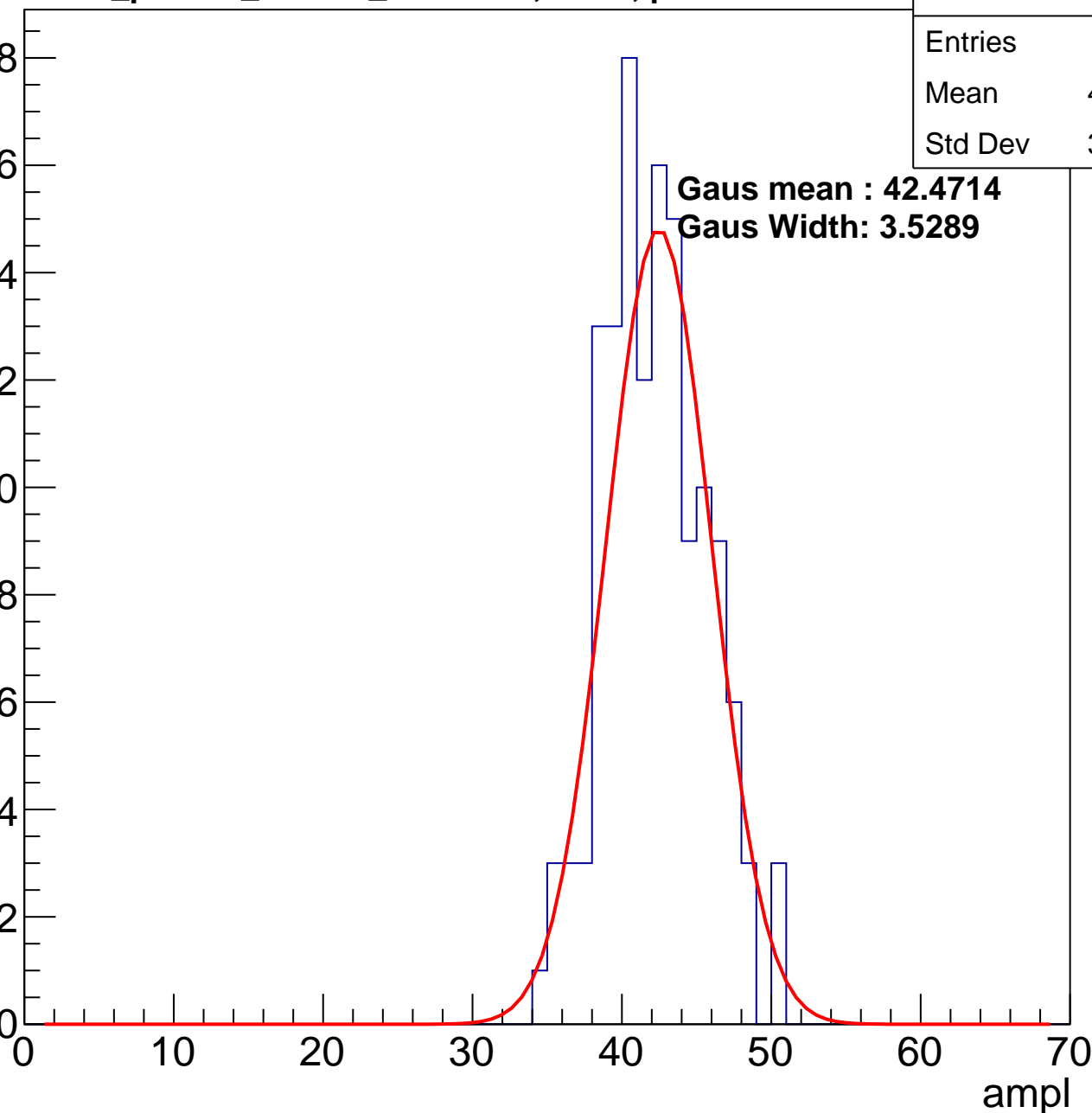
Entry

18
16
14
12
10
8
6
4
2
0

Entries	137
Mean	41.78
Std Dev	3.343

Gaus mean : 42.4714

Gaus Width: 3.5289



B1L001S, U16-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	47.91
Std Dev	3.449

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

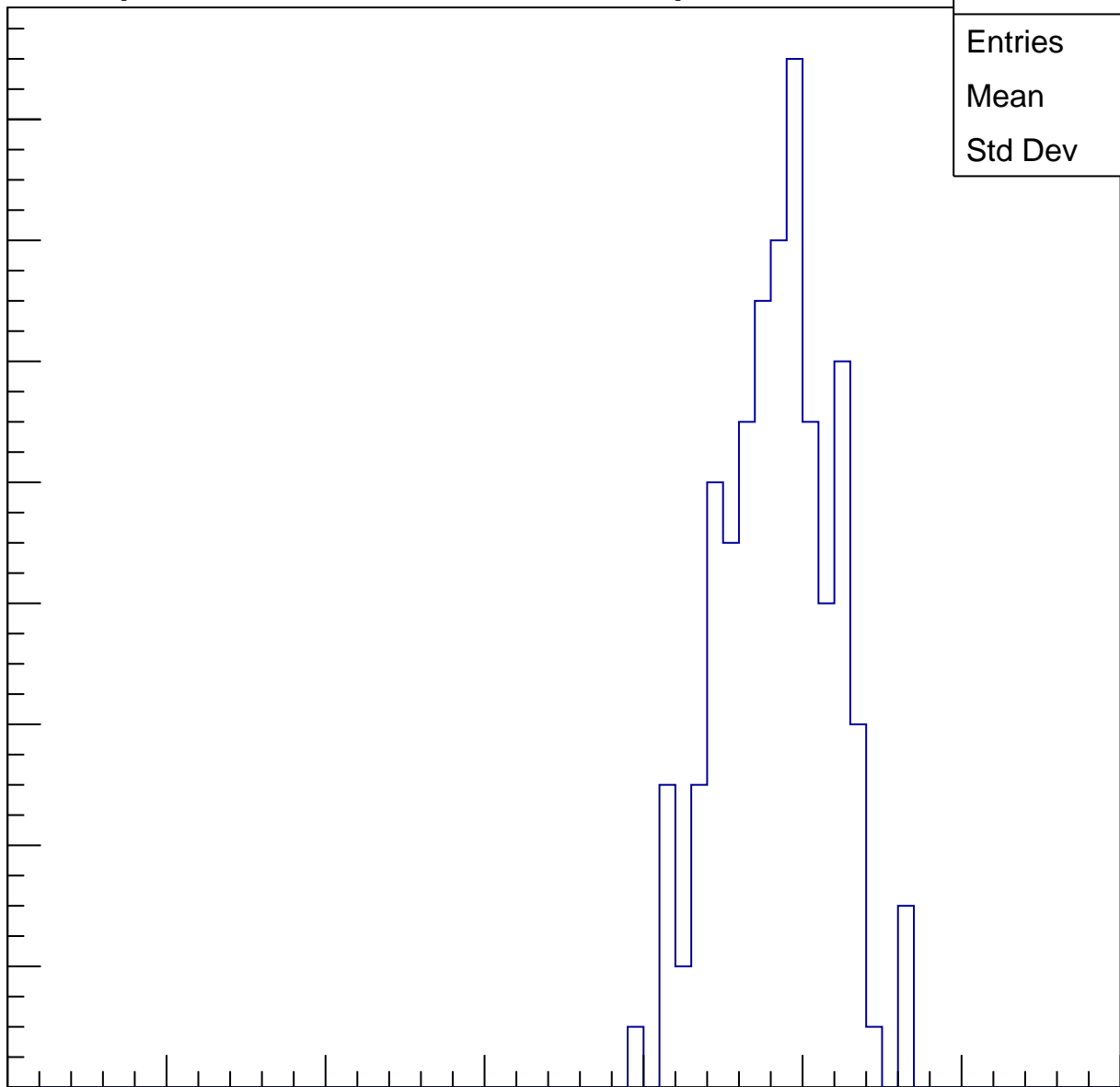
40

50

60

70

ampl



B1L001S, U16-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

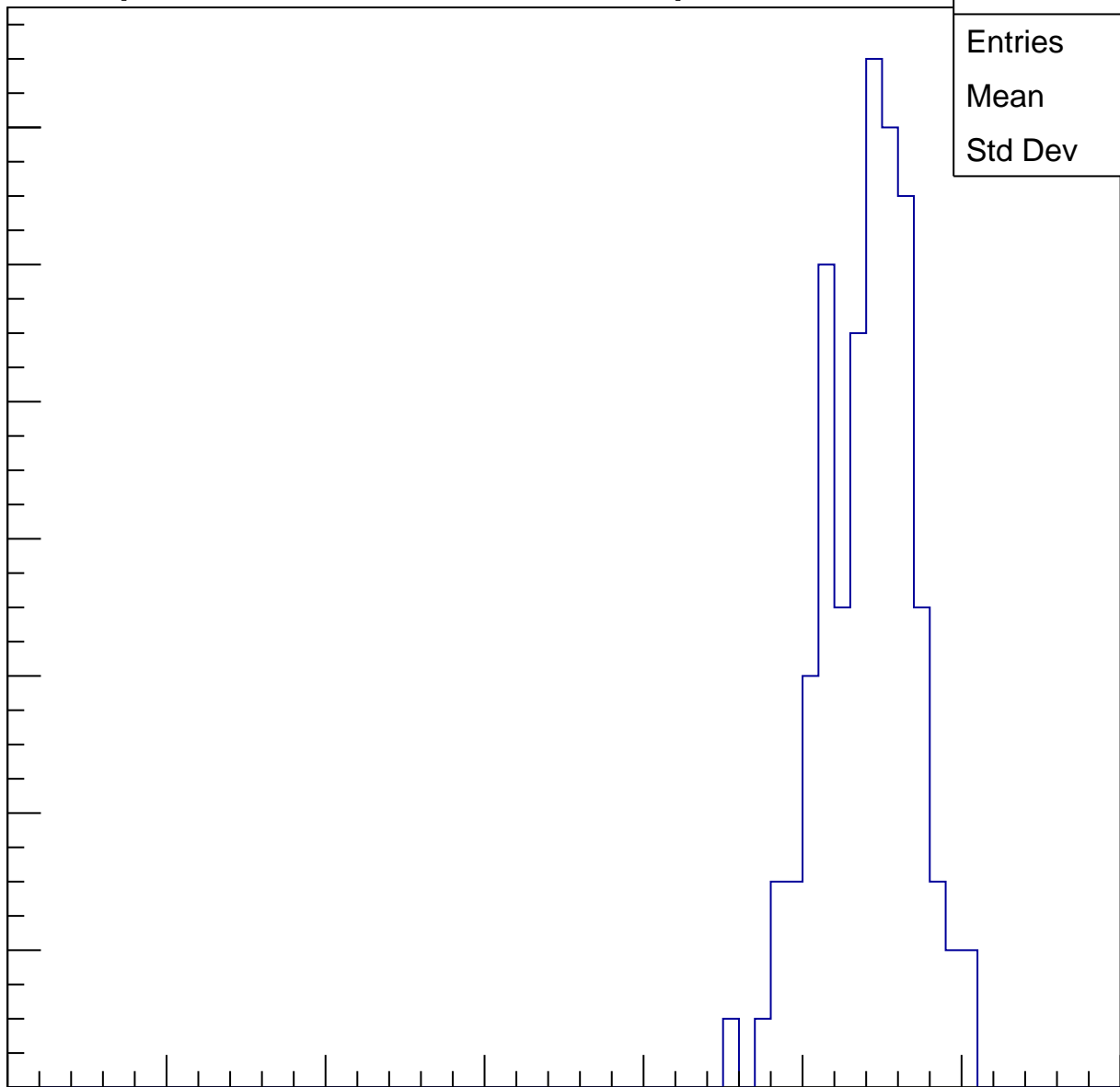
Entries	100
Mean	53.61
Std Dev	2.898

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch85, adc5

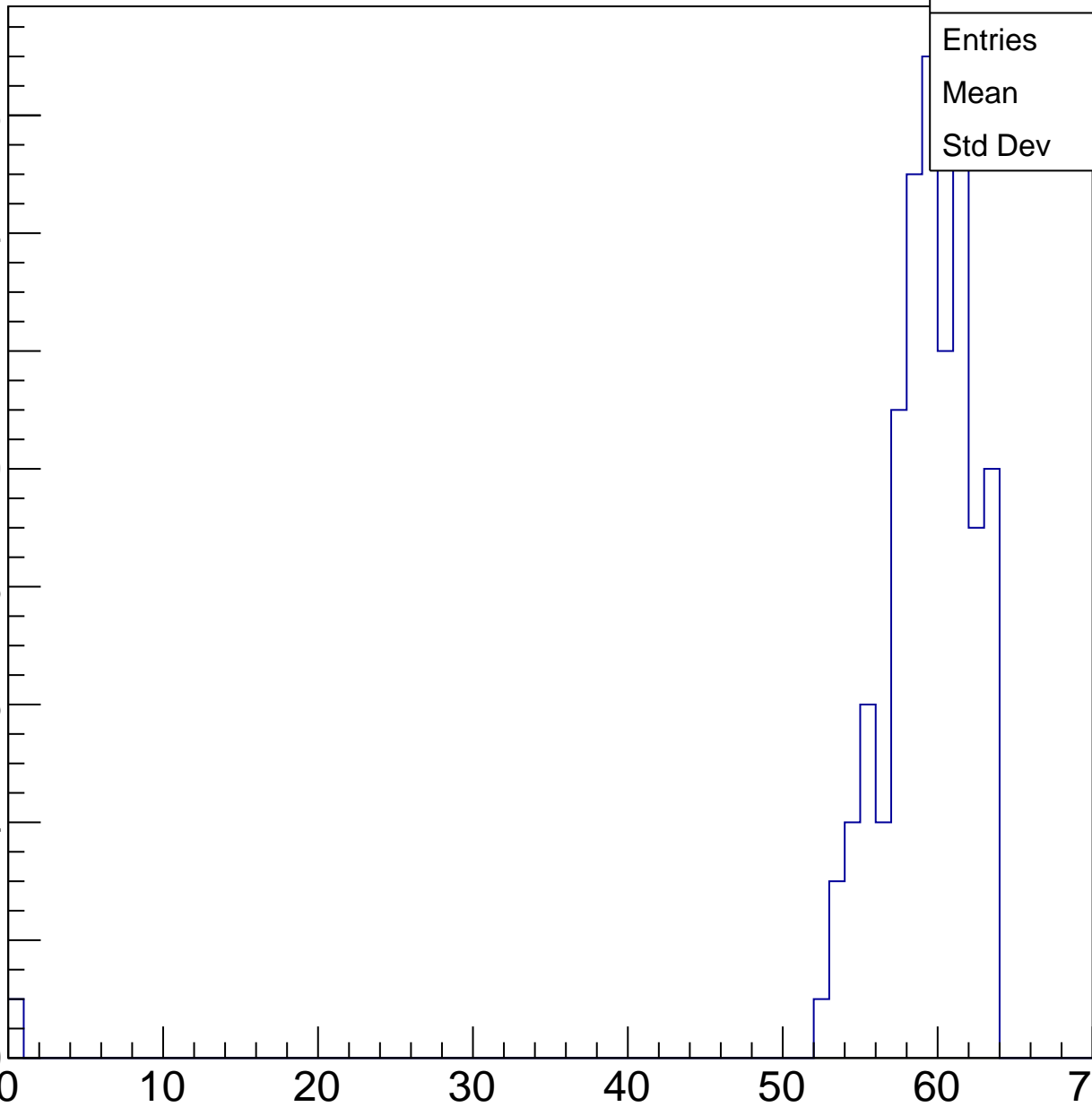
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.39
Std Dev	6.212

ampl



B1L001S, U16-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

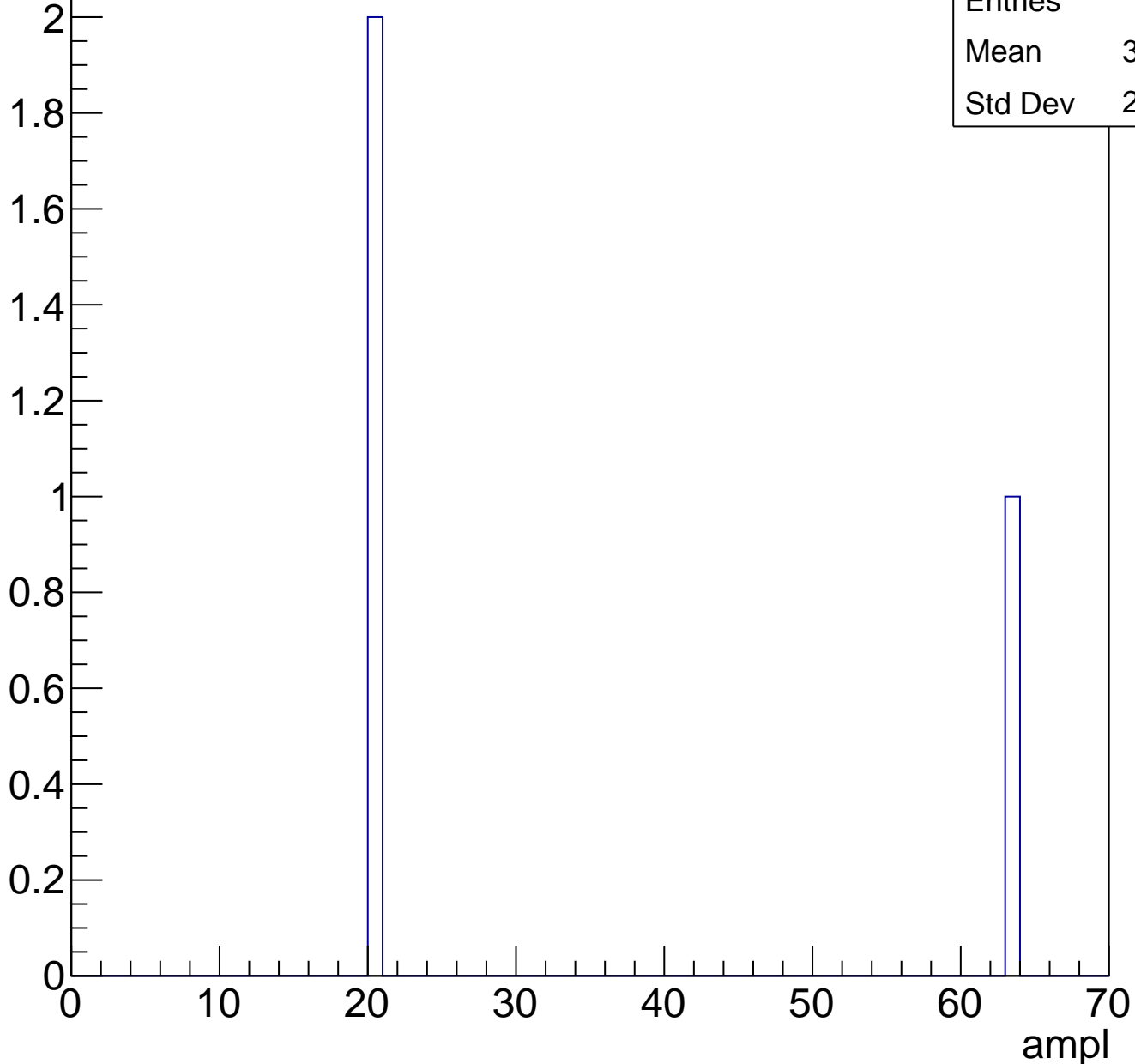
Entries	36
Mean	59.5
Std Dev	10.2

0

B1L001S, U16-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	34.33
Std Dev	20.27

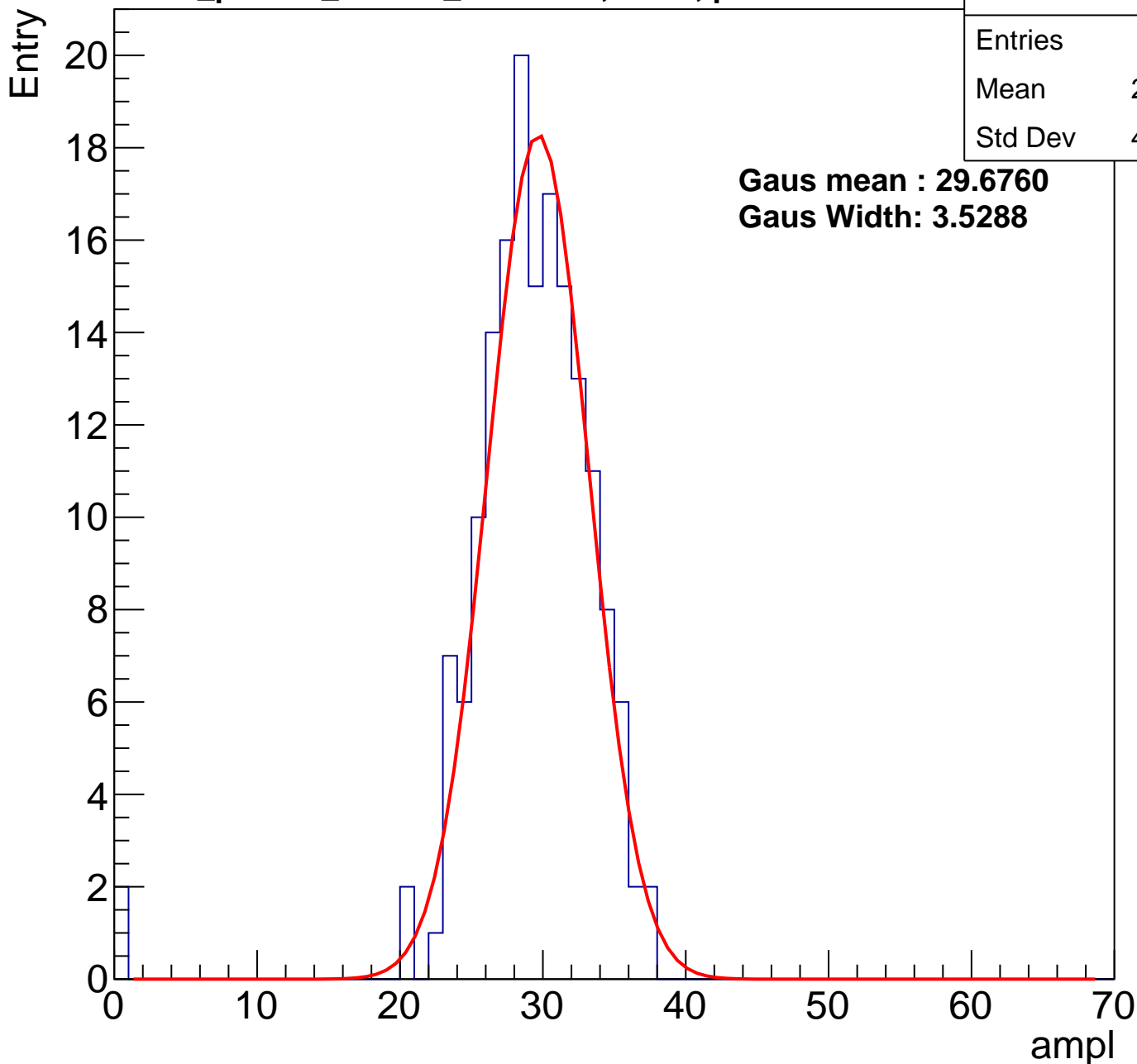
B1L001S, U16-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	28.68
Std Dev	4.682

Gaus mean : 29.6760

Gaus Width: 3.5288



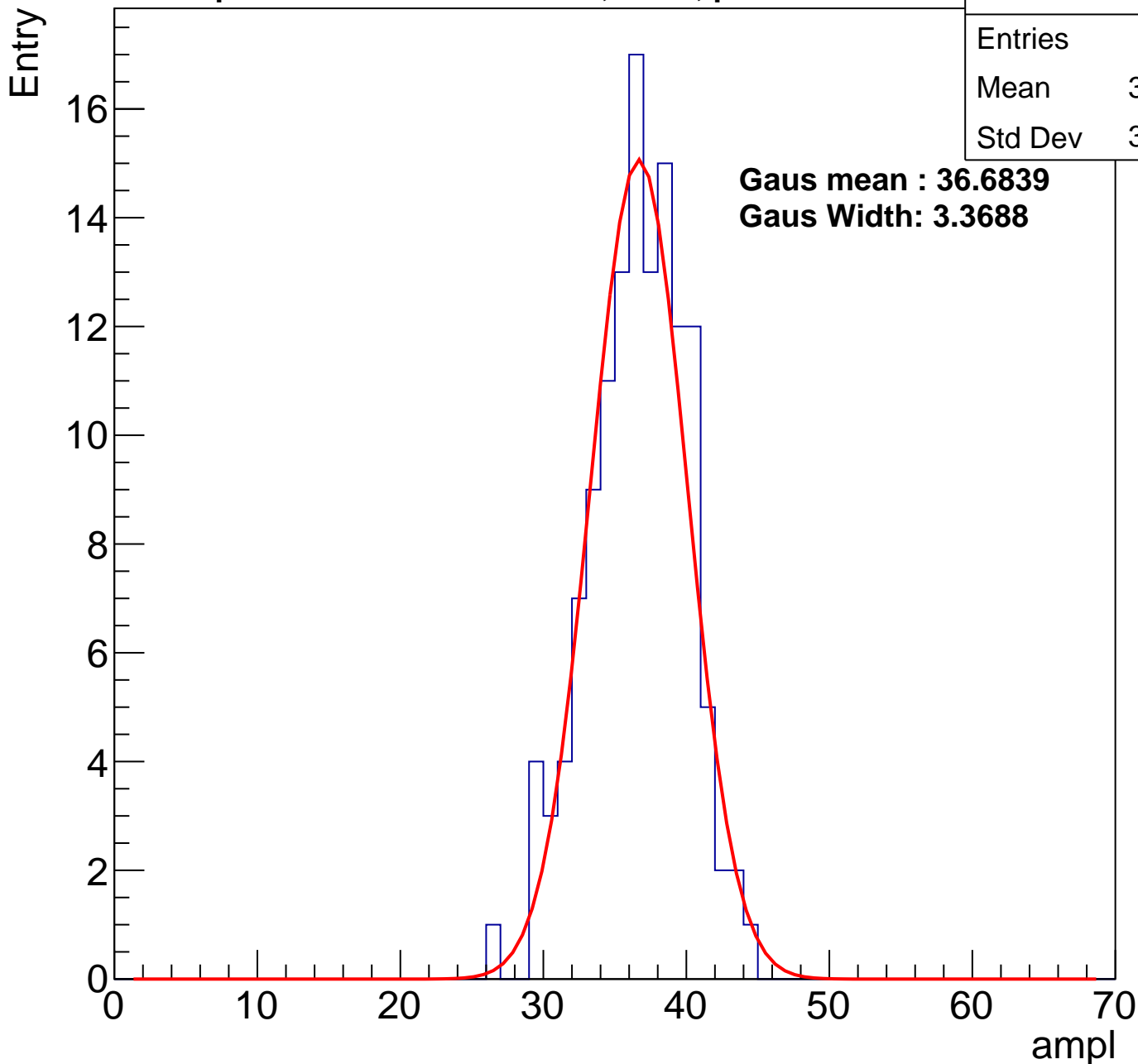
B1L001S, U16-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.15
Std Dev	3.365

Gaus mean : 36.6839

Gaus Width: 3.3688



B1L001S, U16-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

109

Mean

42.48

Std Dev

3.216

Gaus mean : 43.1890

Gaus Width: 3.4132

0

10

20

30

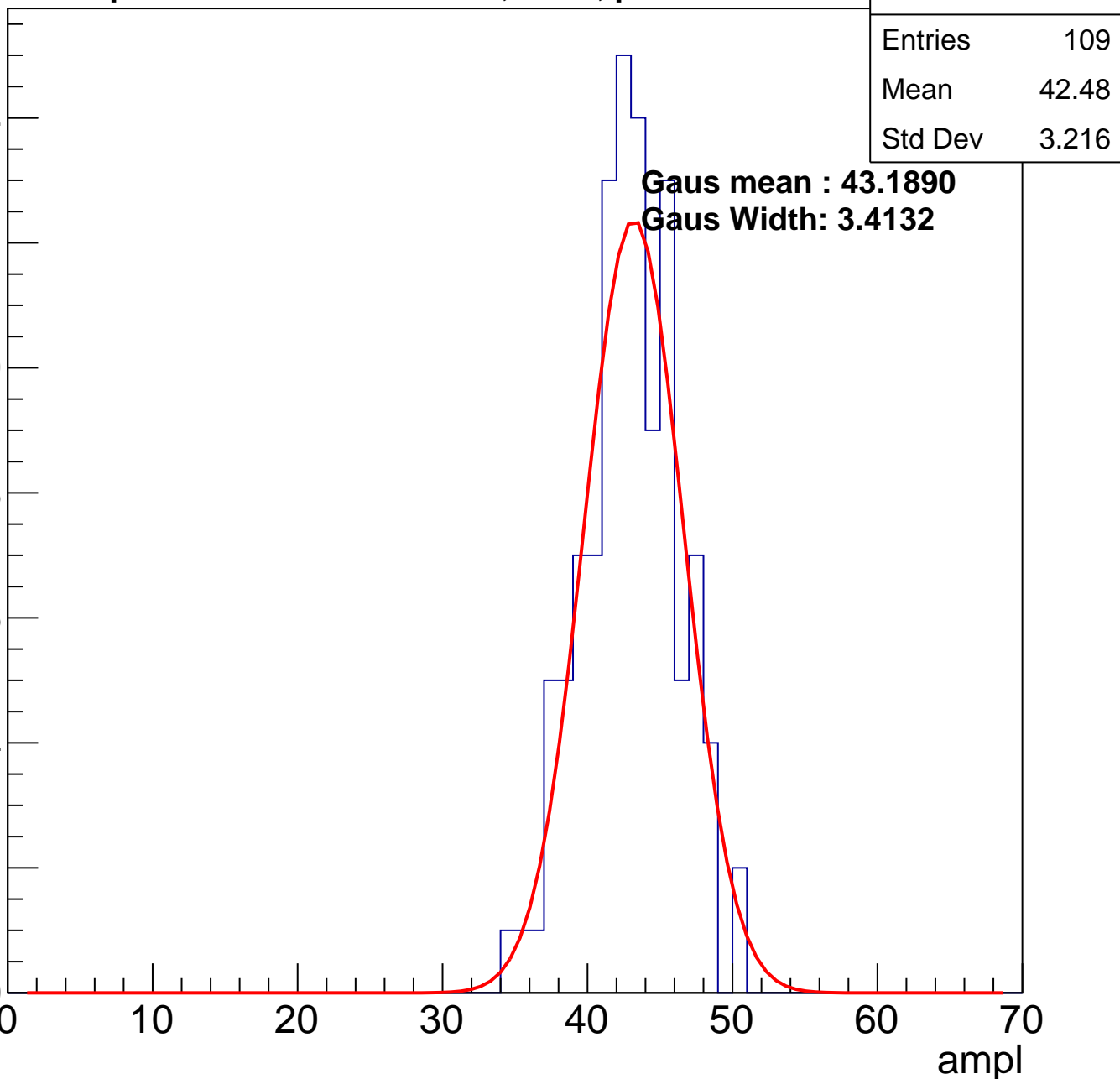
40

50

60

70

ampl



B1L001S, U16-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	48.14
Std Dev	3.626

Entry

12

10

8

6

4

2

0

0

10

20

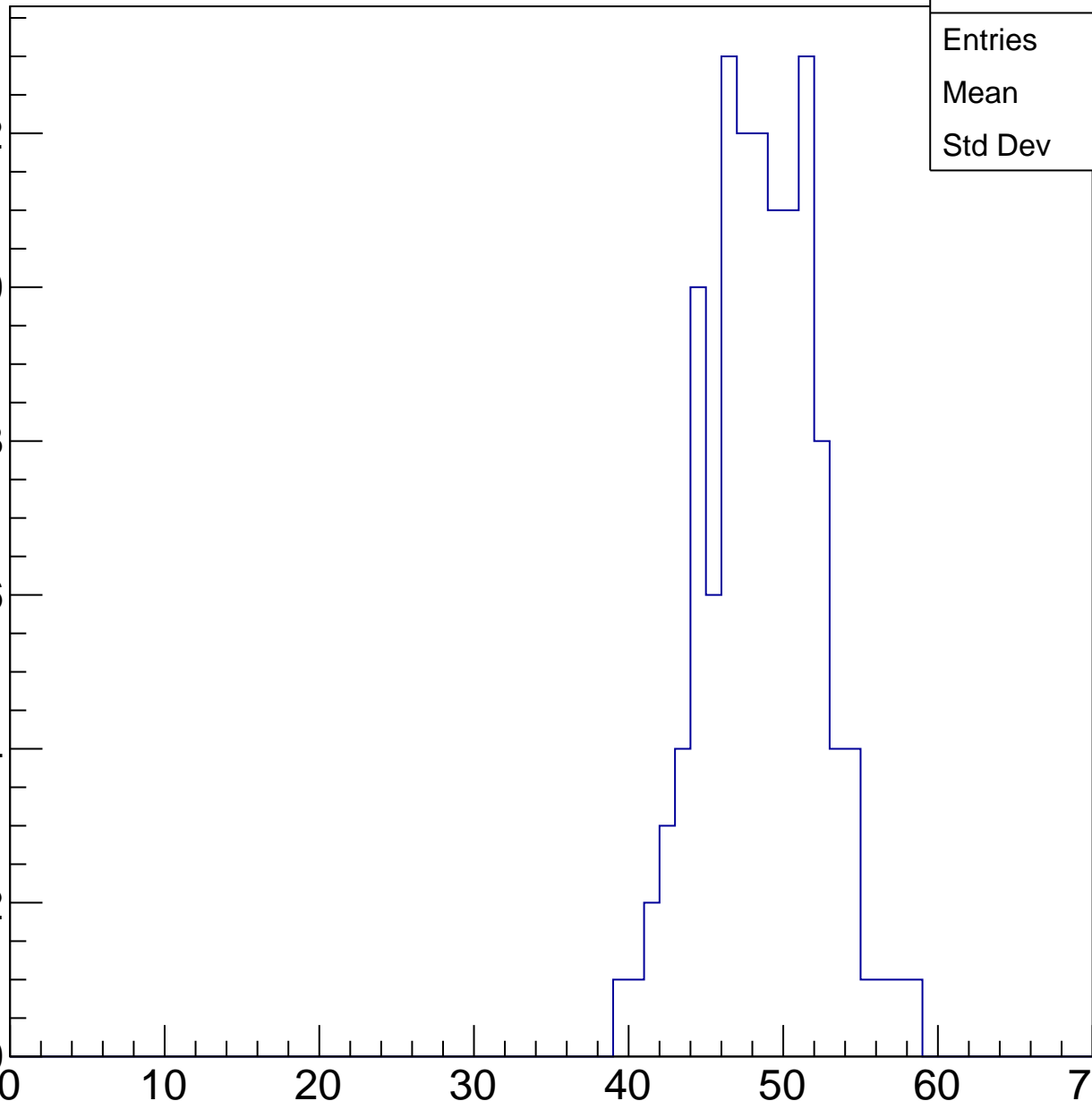
30

40

50

60

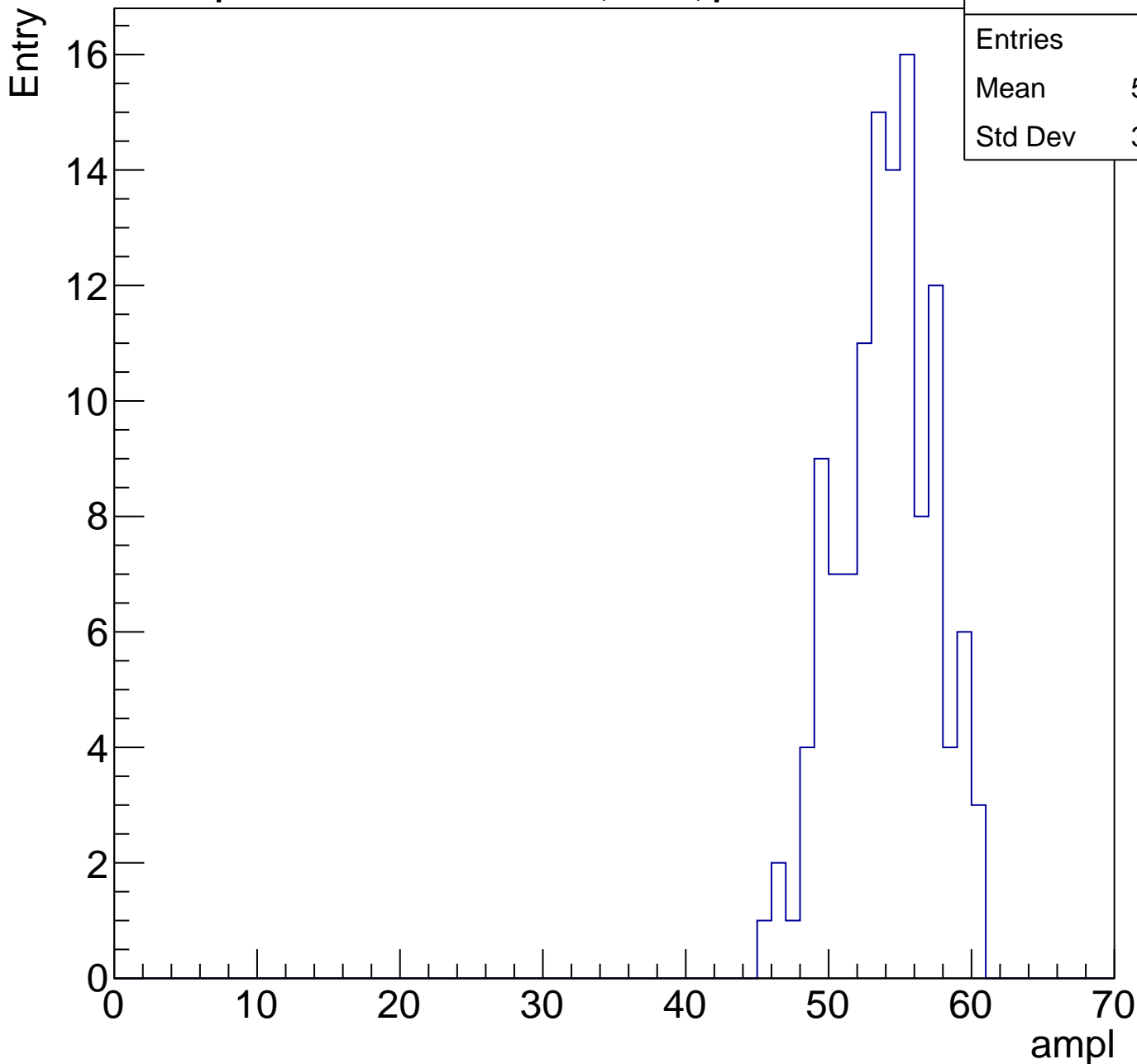
ampl



B1L001S, U16-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

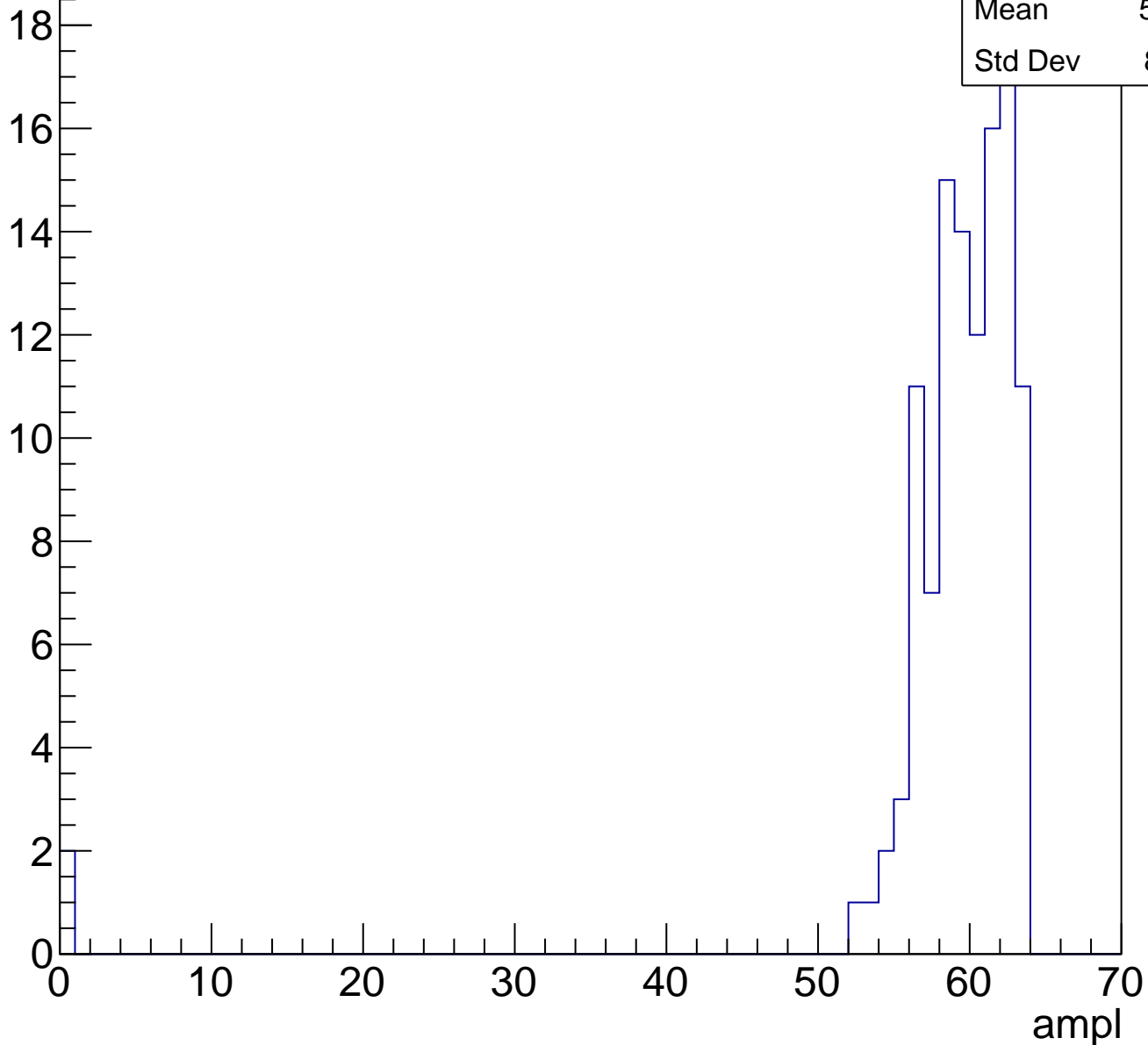
Entries	120
Mean	53.54
Std Dev	3.309



B1L001S, U16-ch86, adc5

calib_packv5_042523_0143.root, FC#2, port C2

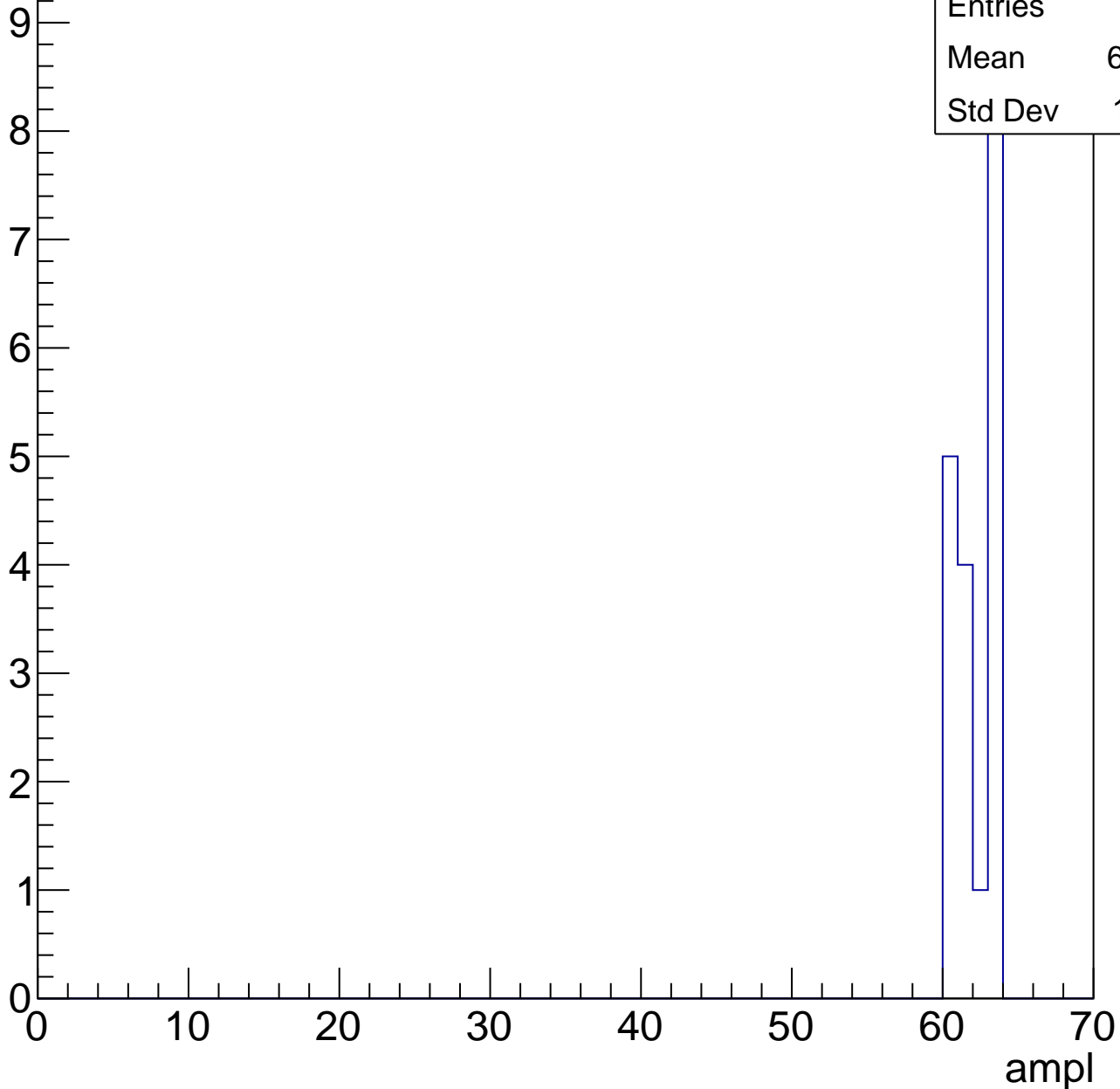
Entry



B1L001S, U16-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

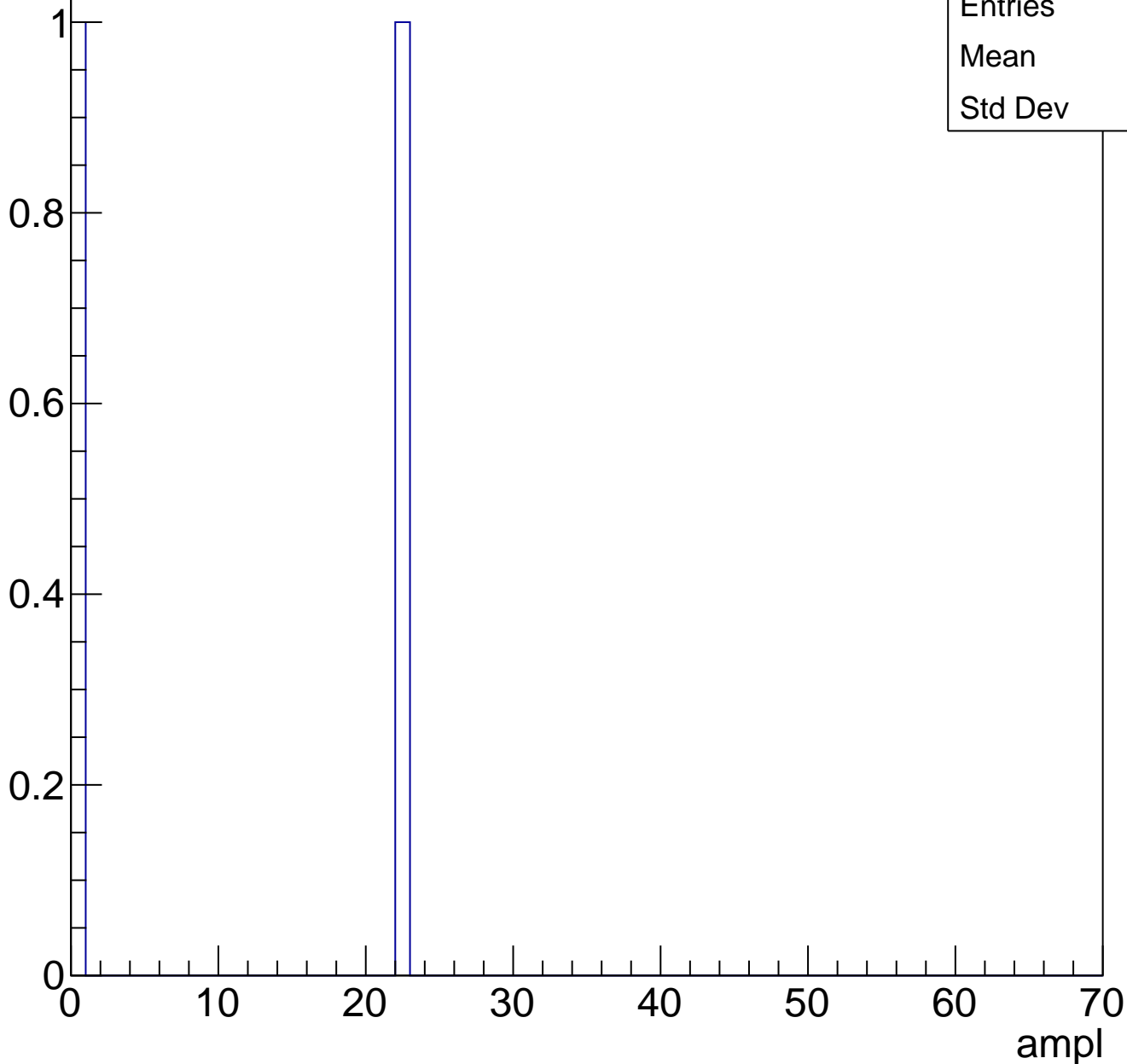
Entry



B1L001S, U16-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



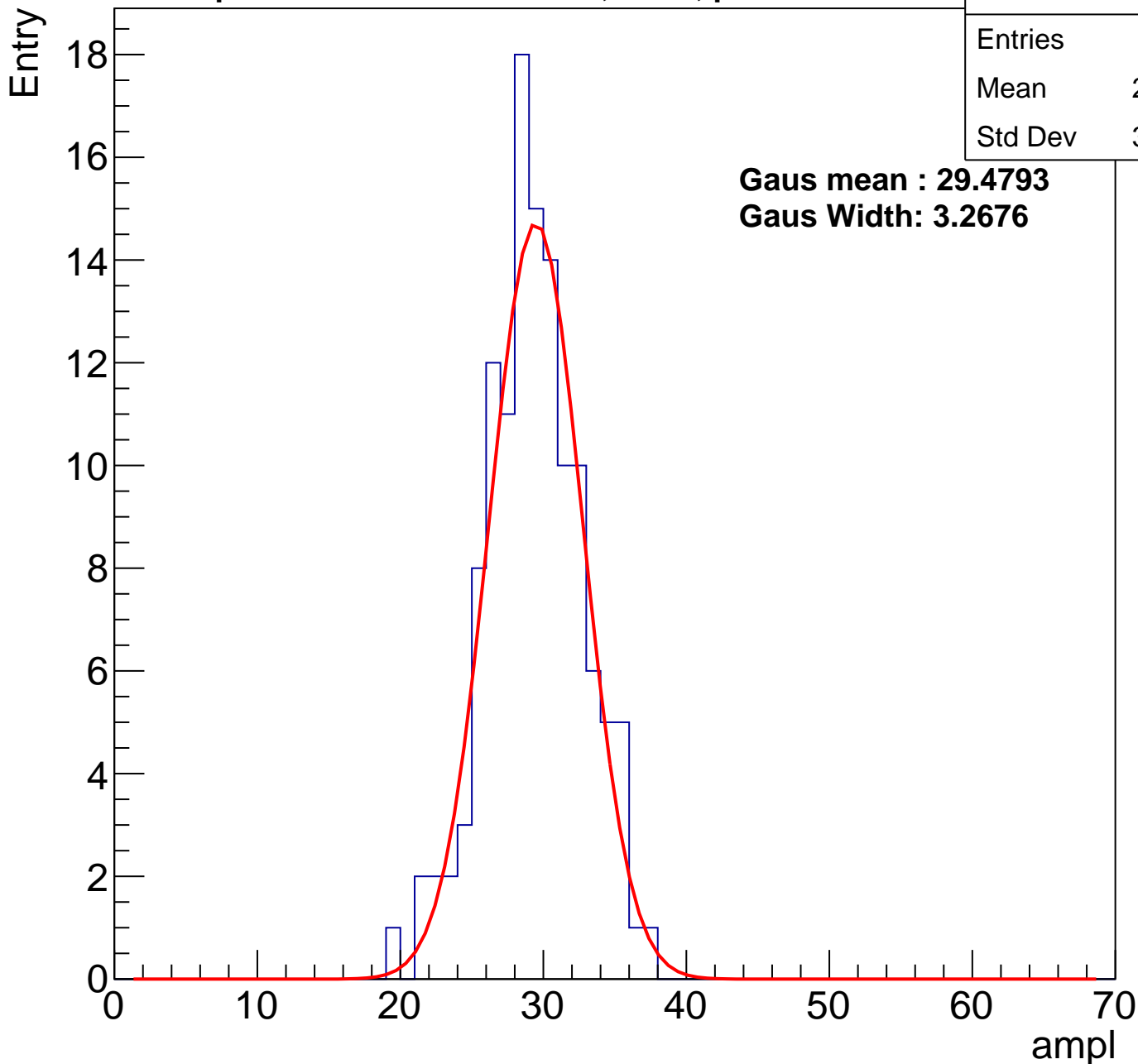
B1L001S, U16-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	28.87
Std Dev	3.365

Gaus mean : 29.4793

Gaus Width: 3.2676

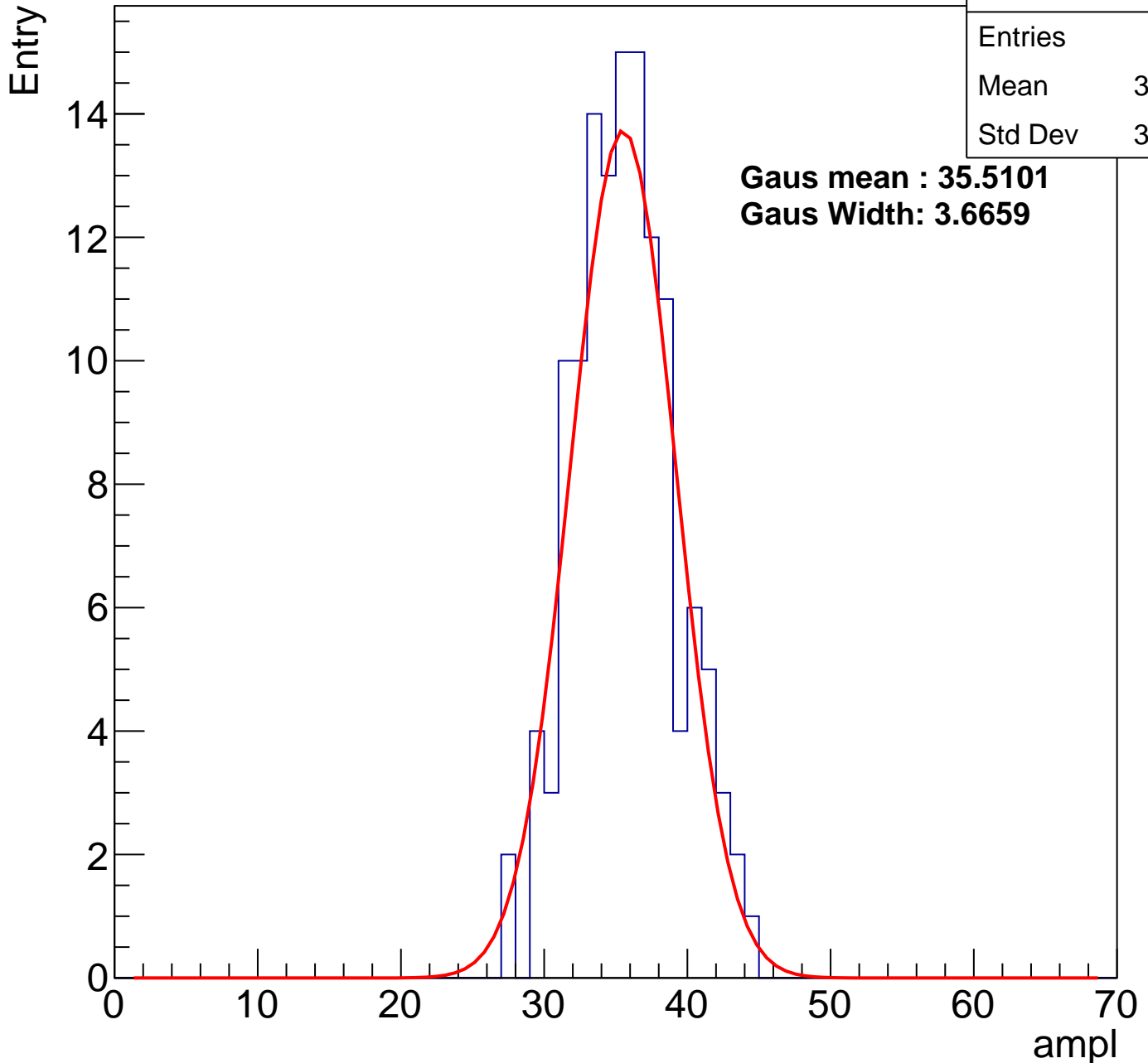


B1L001S, U16-ch87, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	35.22
Std Dev	3.477

Gaus mean : 35.5101
Gaus Width: 3.6659



B1L001S, U16-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	42.18
Std Dev	3.48

Gaus mean : 42.4477

Gaus Width: 3.8715

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

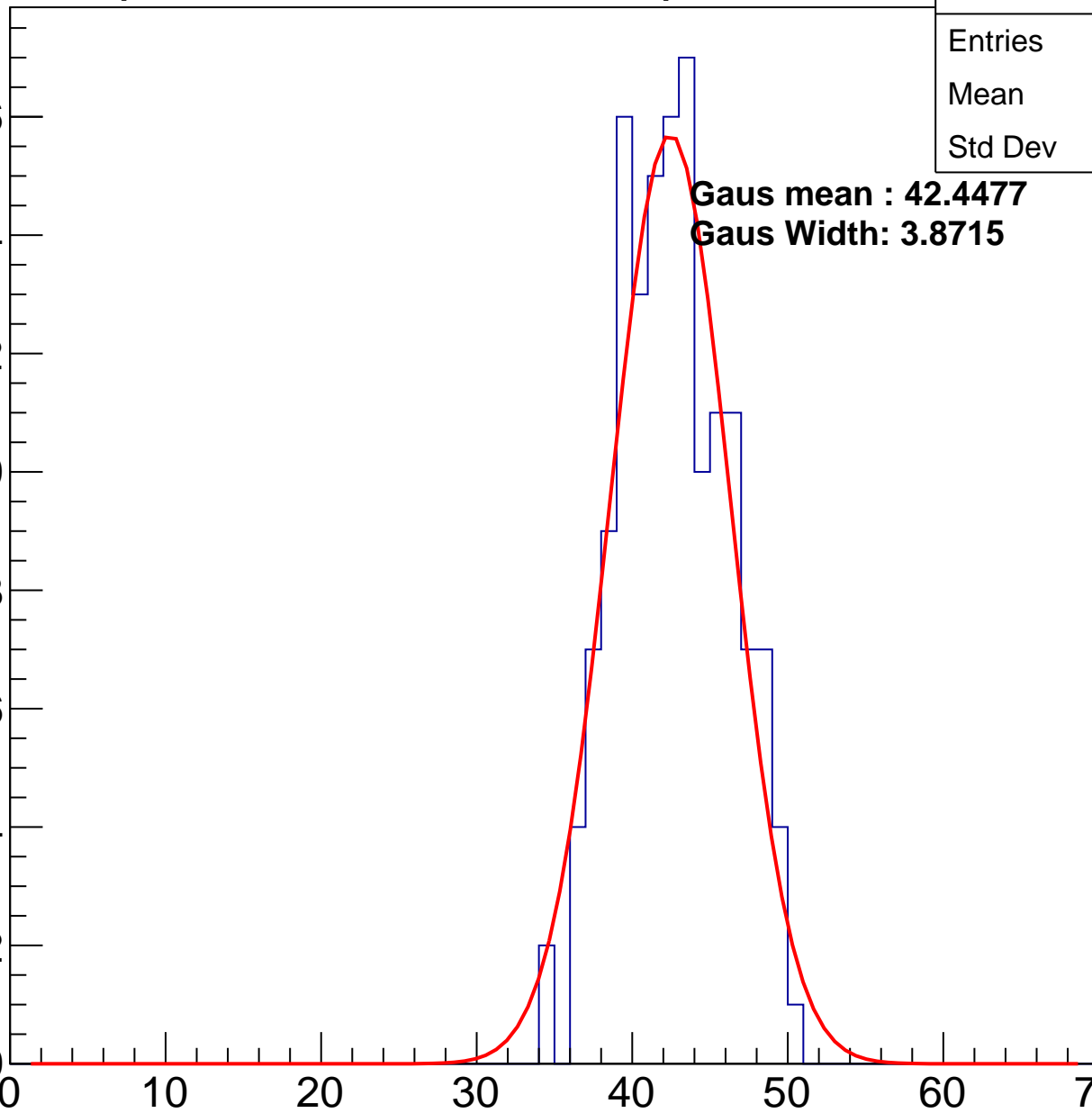
40

50

60

70

ampl



B1L001S, U16-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

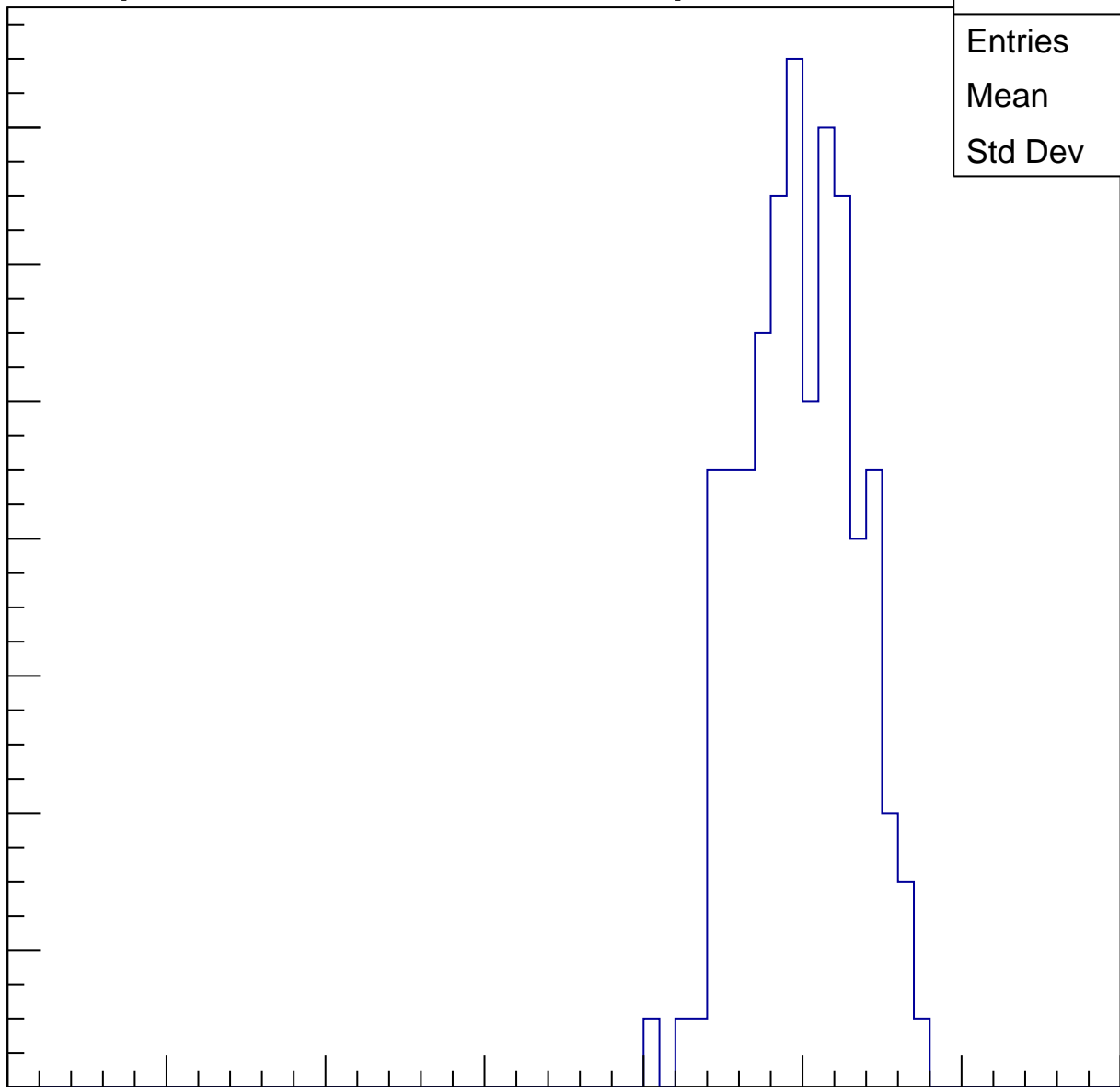
Entries	131
Mean	49.32
Std Dev	3.44

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

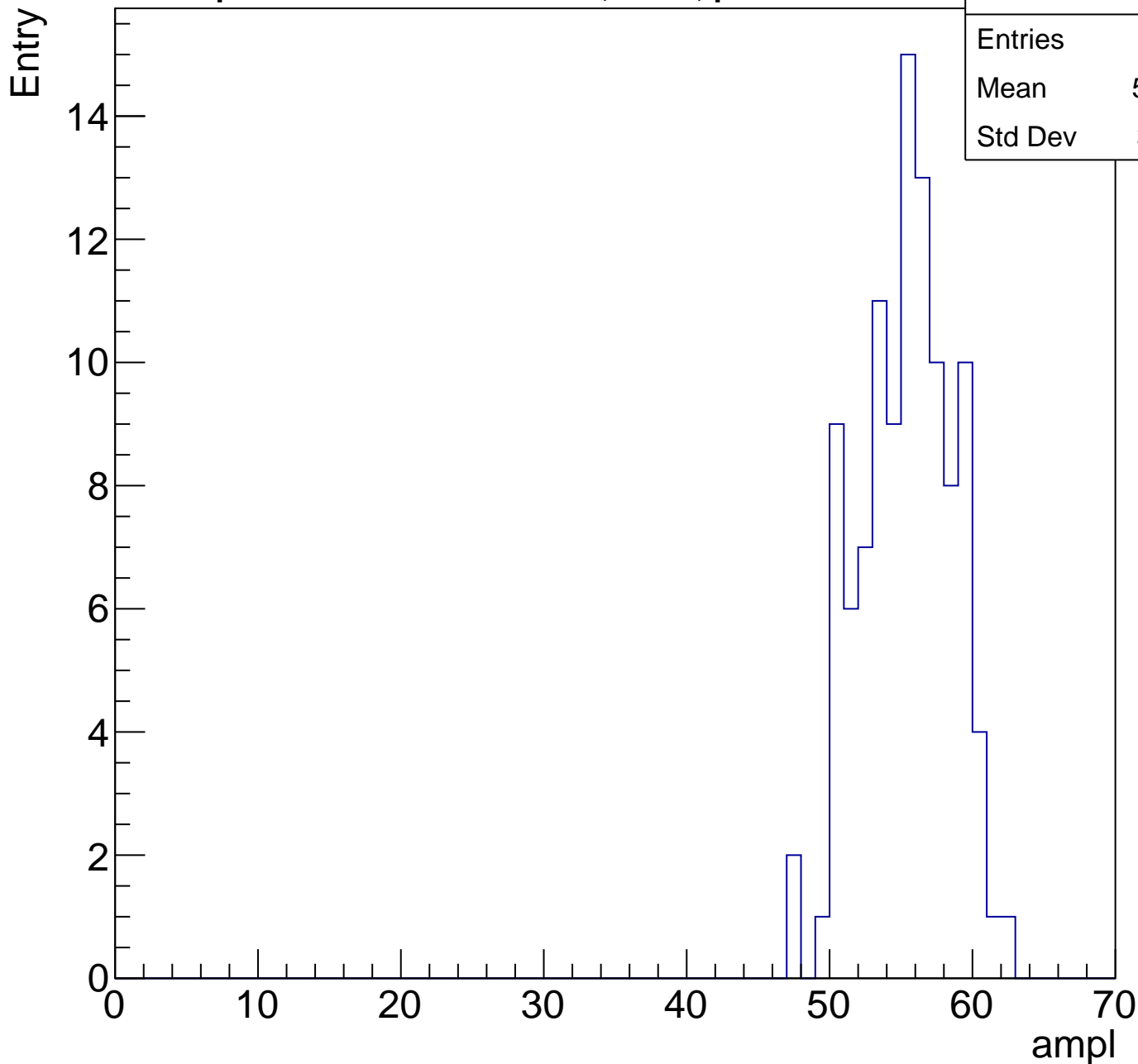
ampl



B1L001S, U16-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	54.88
Std Dev	3.161



B1L001S, U16-ch87, adc5

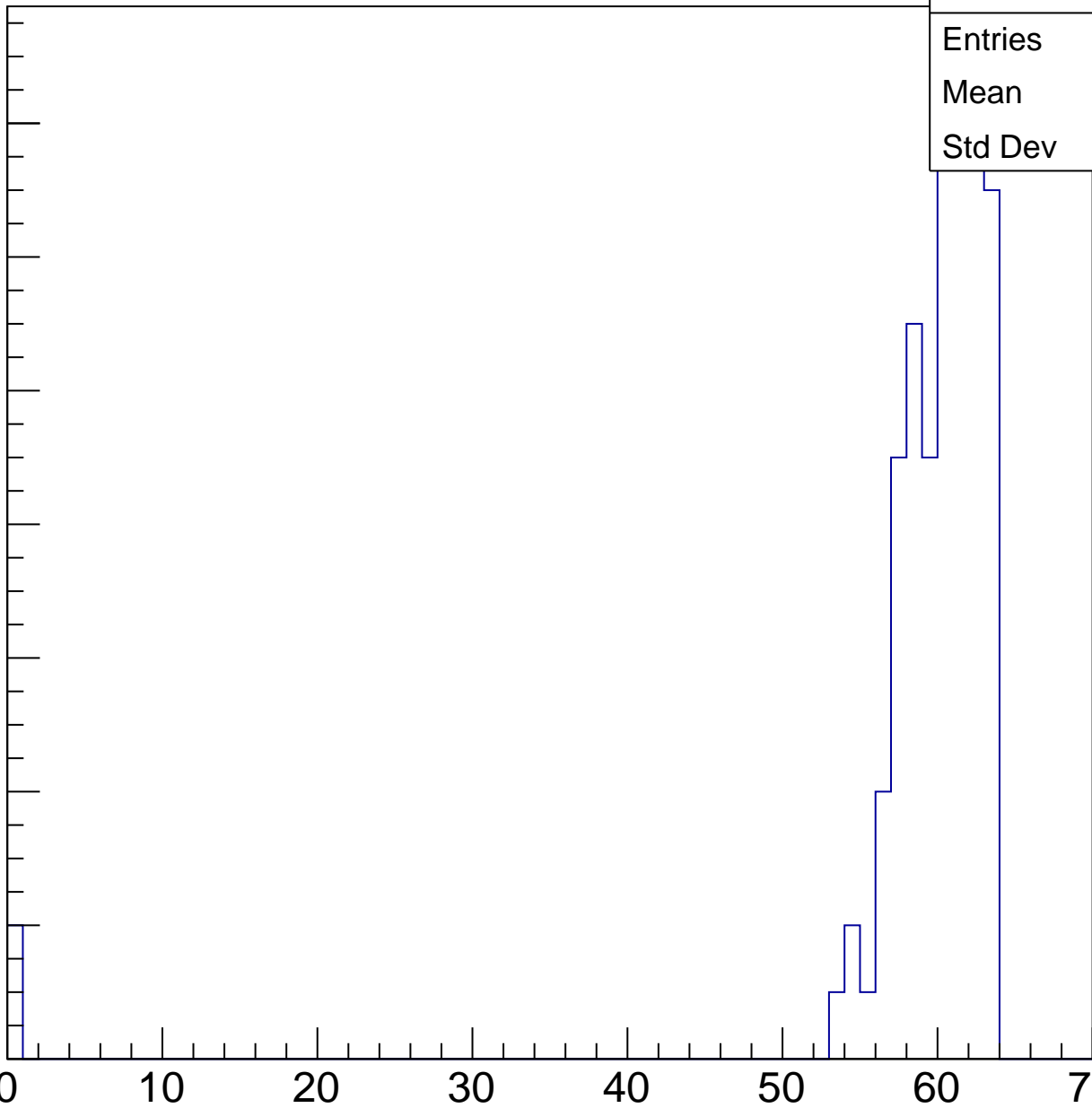
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.56
Std Dev	8.905

ampl

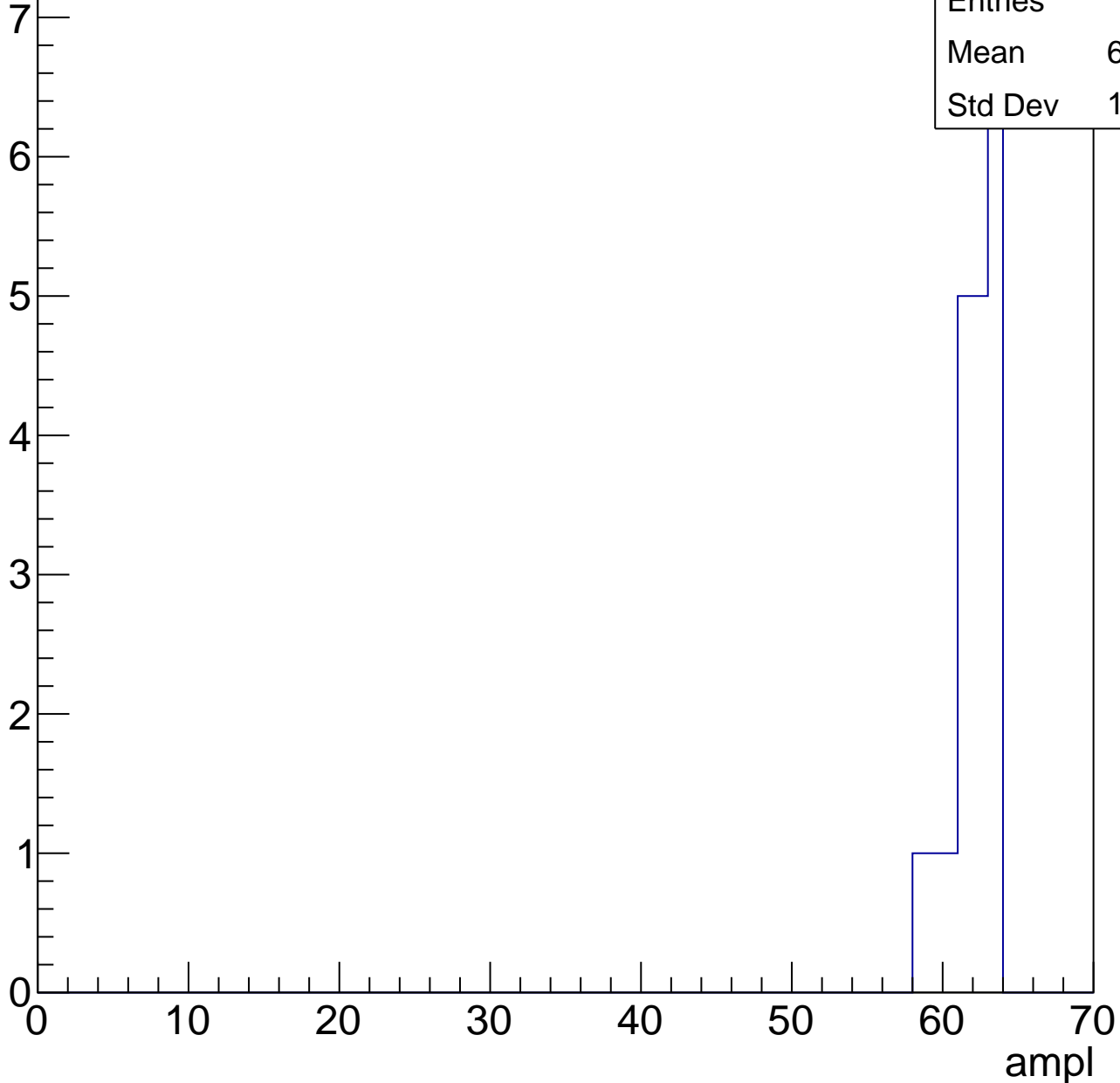


B1L001S, U16-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	61.65
Std Dev	1.388



B1L001S, U16-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch88, adc0

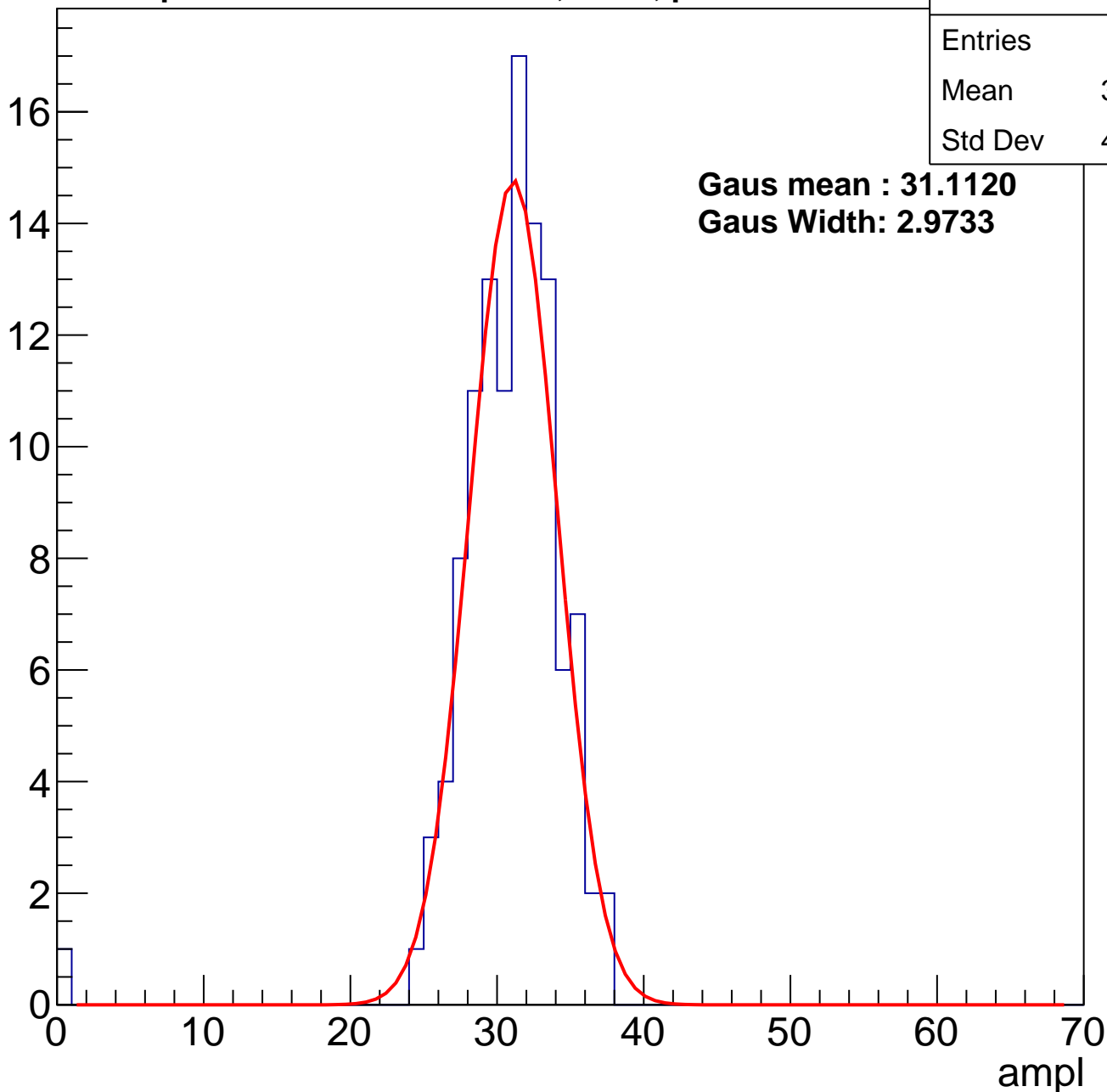
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	30.38
Std Dev	4.012

Gaus mean : 31.1120

Gaus Width: 2.9733

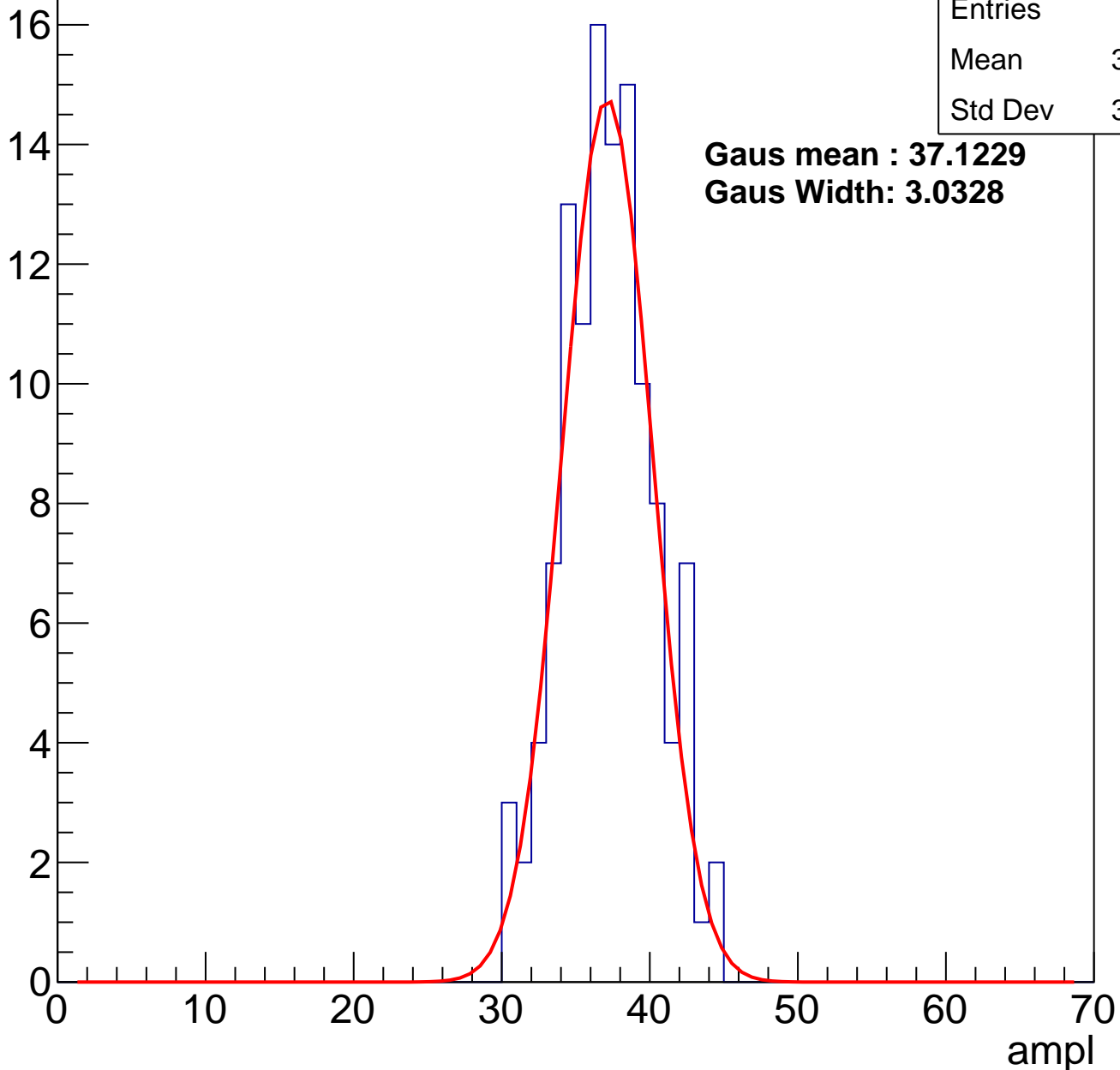
Entry



B1L001S, U16-ch88, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

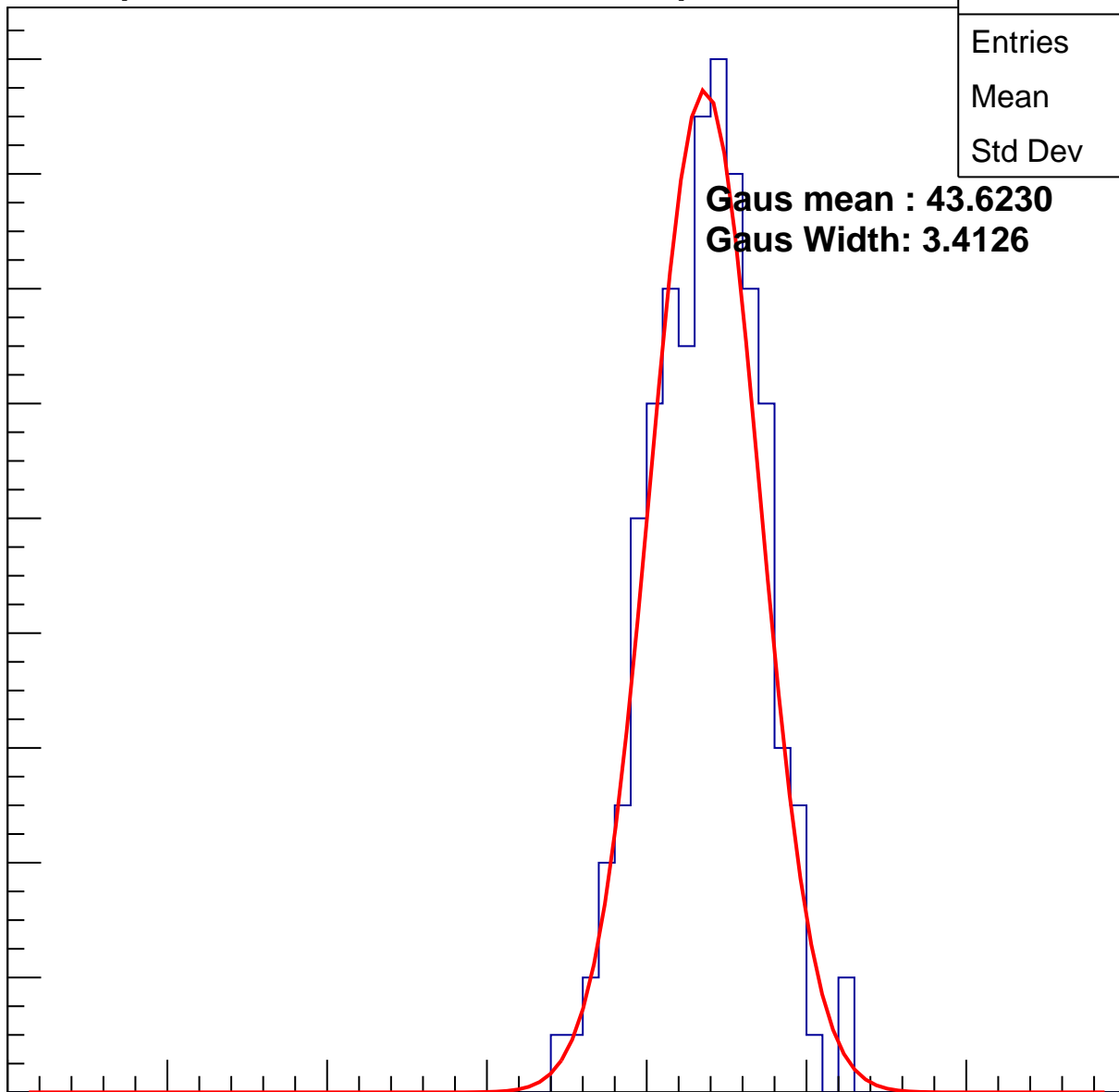
Entries	153
Mean	43.18
Std Dev	3.416

Gaus mean : 43.6230

Gaus Width: 3.4126

0 10 20 30 40 50 60 70

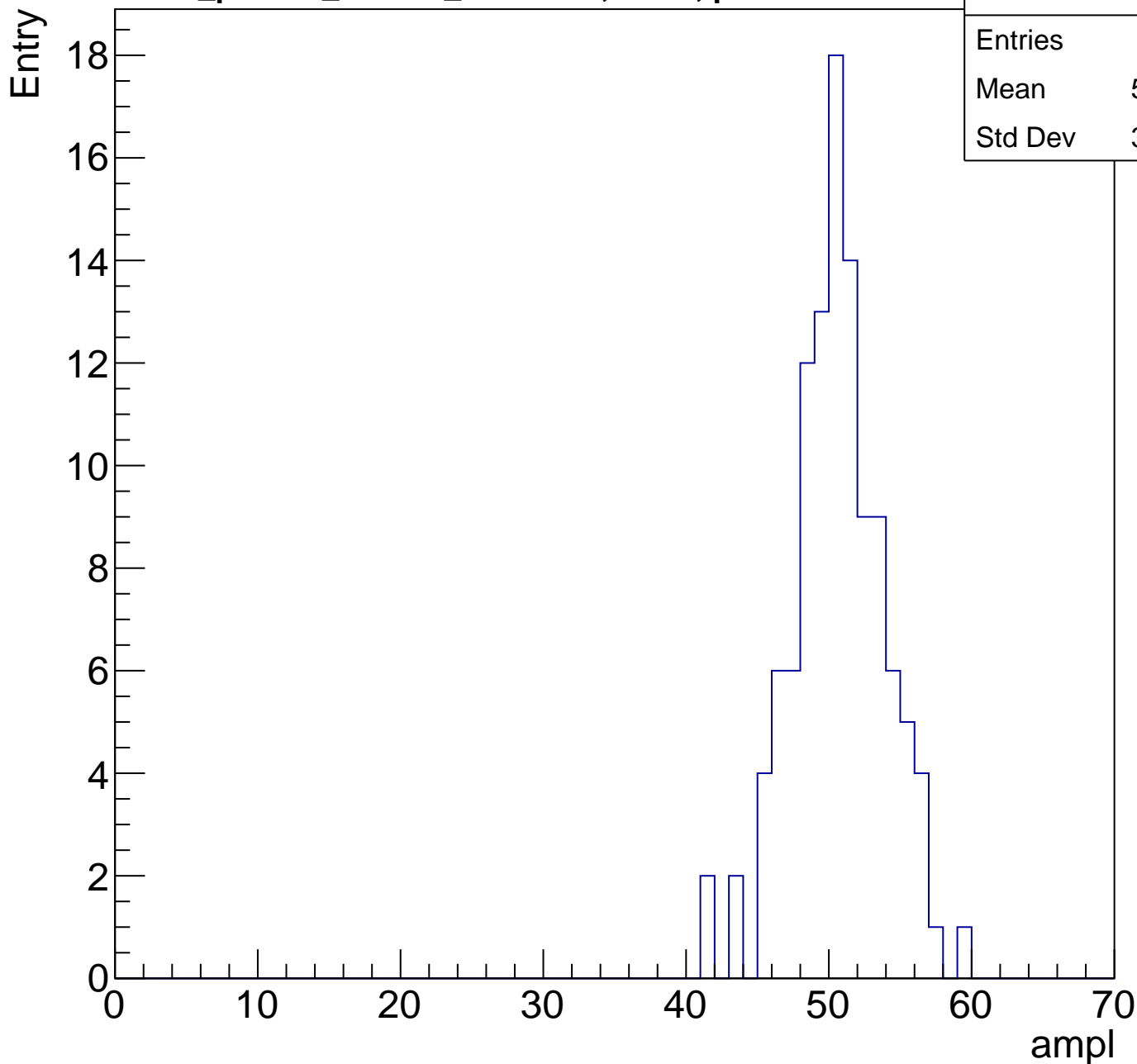
ampl



B1L001S, U16-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	50.15
Std Dev	3.266



B1L001S, U16-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

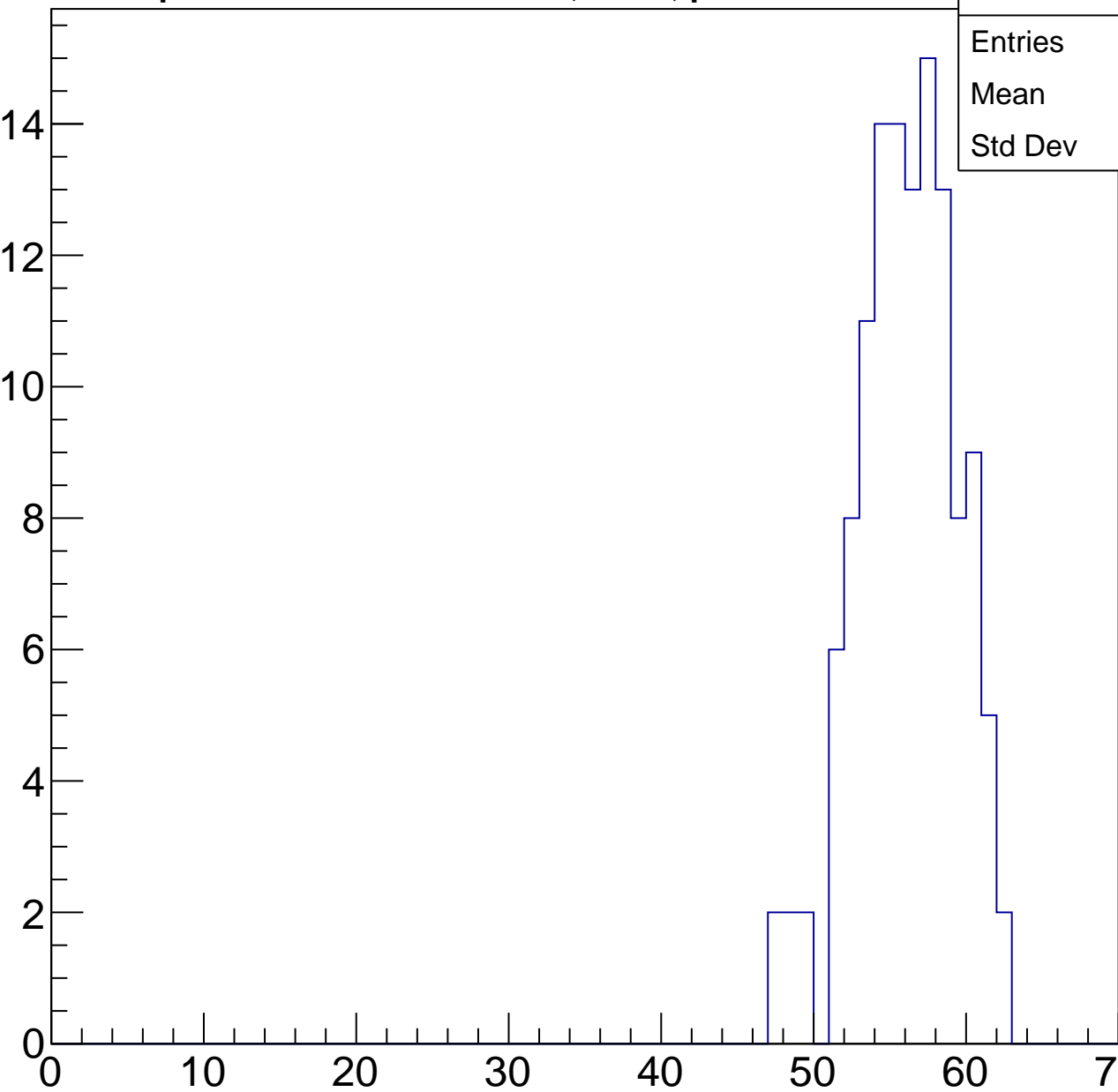
Entries	124
Mean	55.62
Std Dev	3.234

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch88, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	76
Mean	59.32
Std Dev	7.306

ampl

0

10

20

30

40

50

60

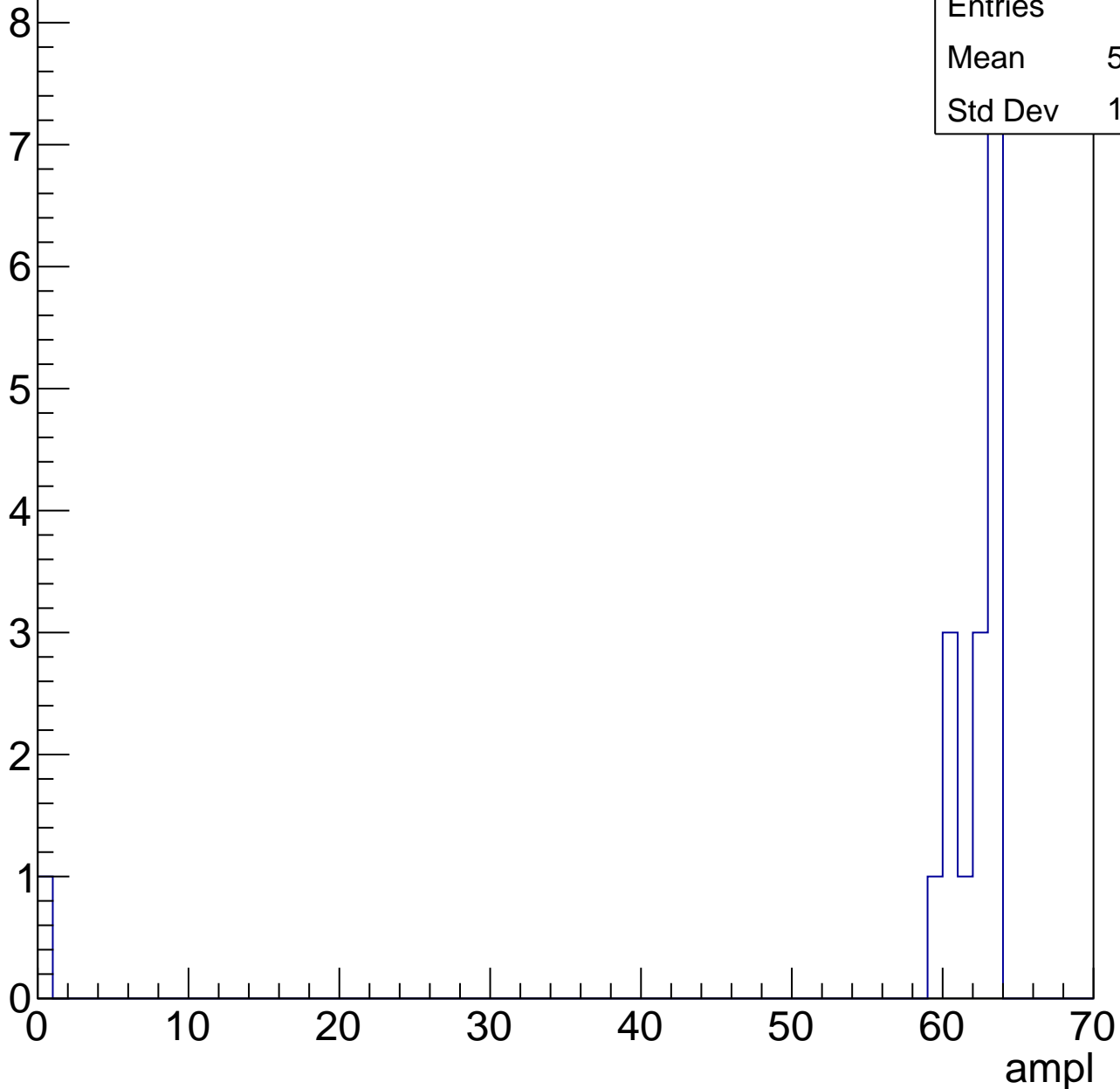
70

B1L001S, U16-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

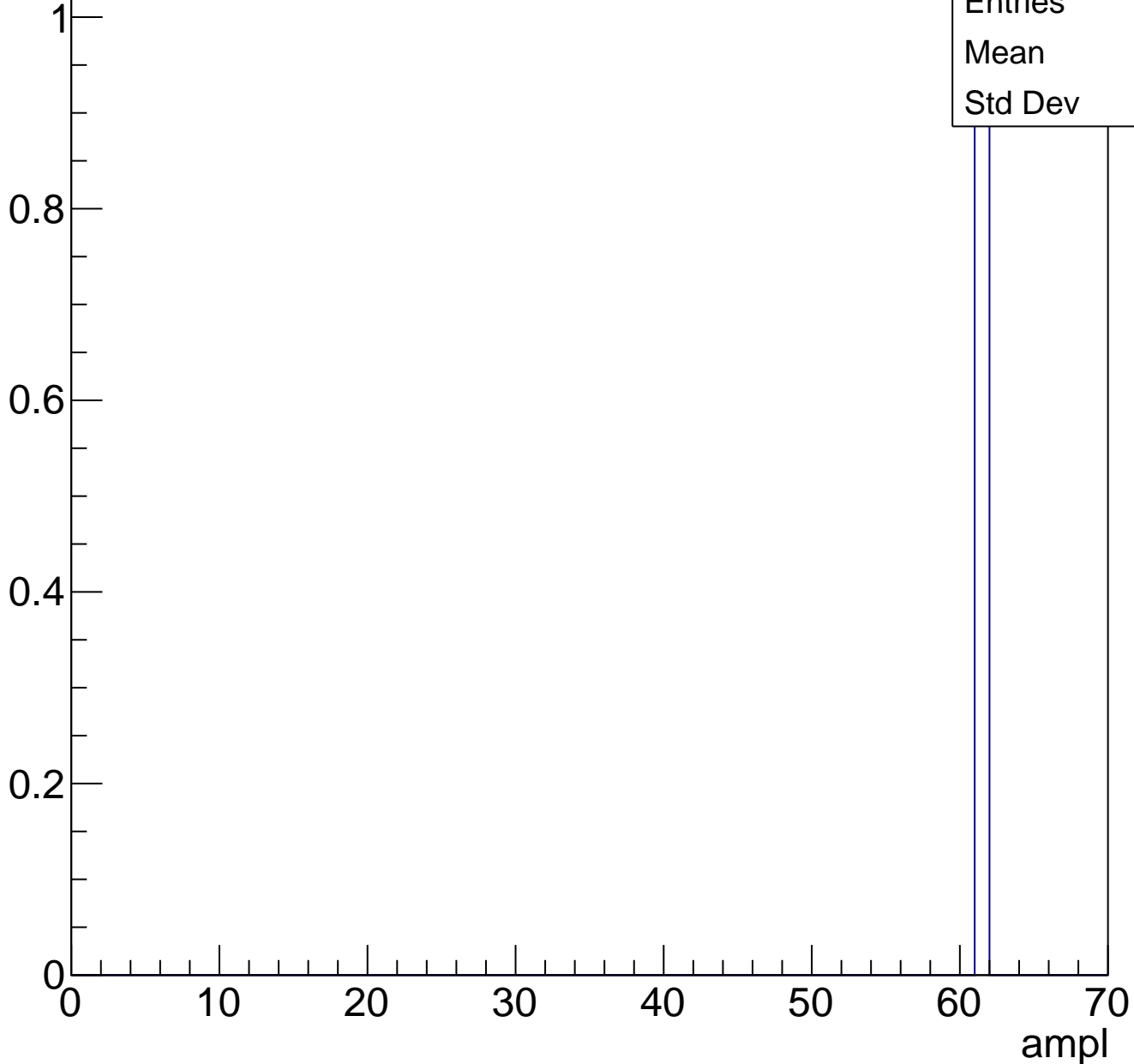
Entries	17
Mean	58.24
Std Dev	14.62



B1L001S, U16-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

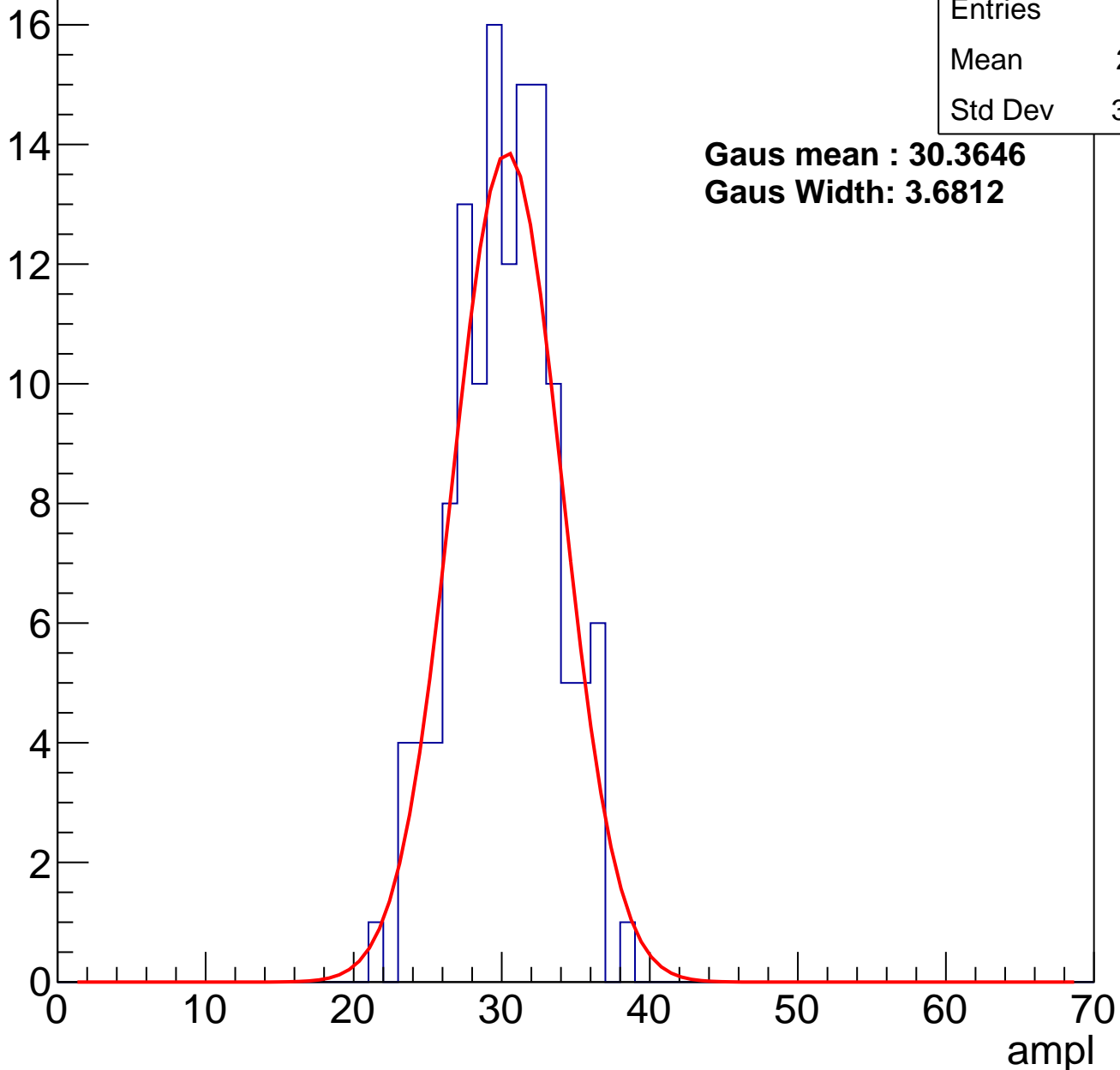
Entry



B1L001S, U16-ch89, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	129
Mean	29.81
Std Dev	3.373

B1L001S, U16-ch89, adc1

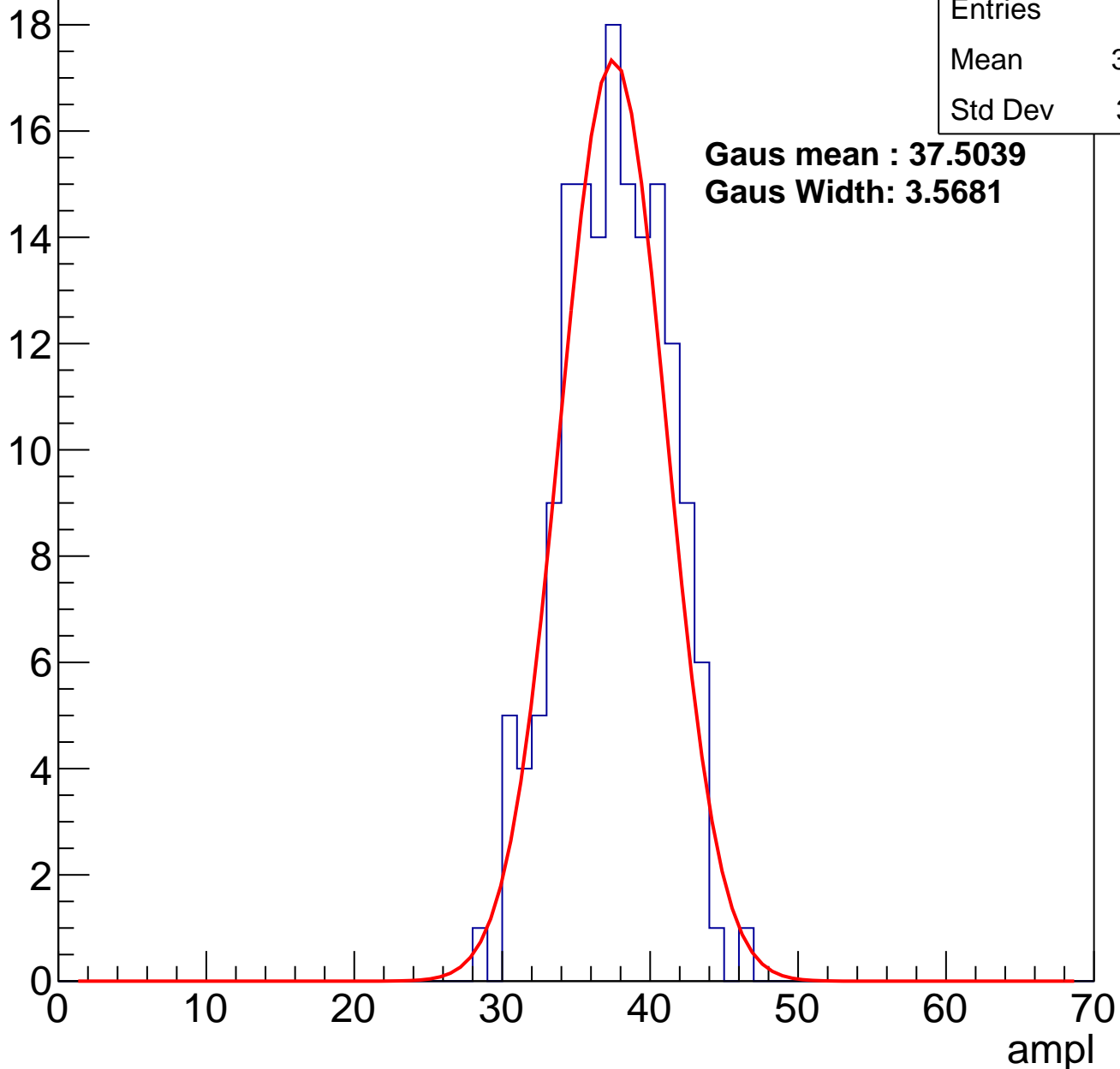
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.09
Std Dev	3.471

Gaus mean : 37.5039

Gaus Width: 3.5681

Entry



B1L001S, U16-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	43.91
Std Dev	3.023

Gaus mean : 44.3430

Gaus Width: 3.0826

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

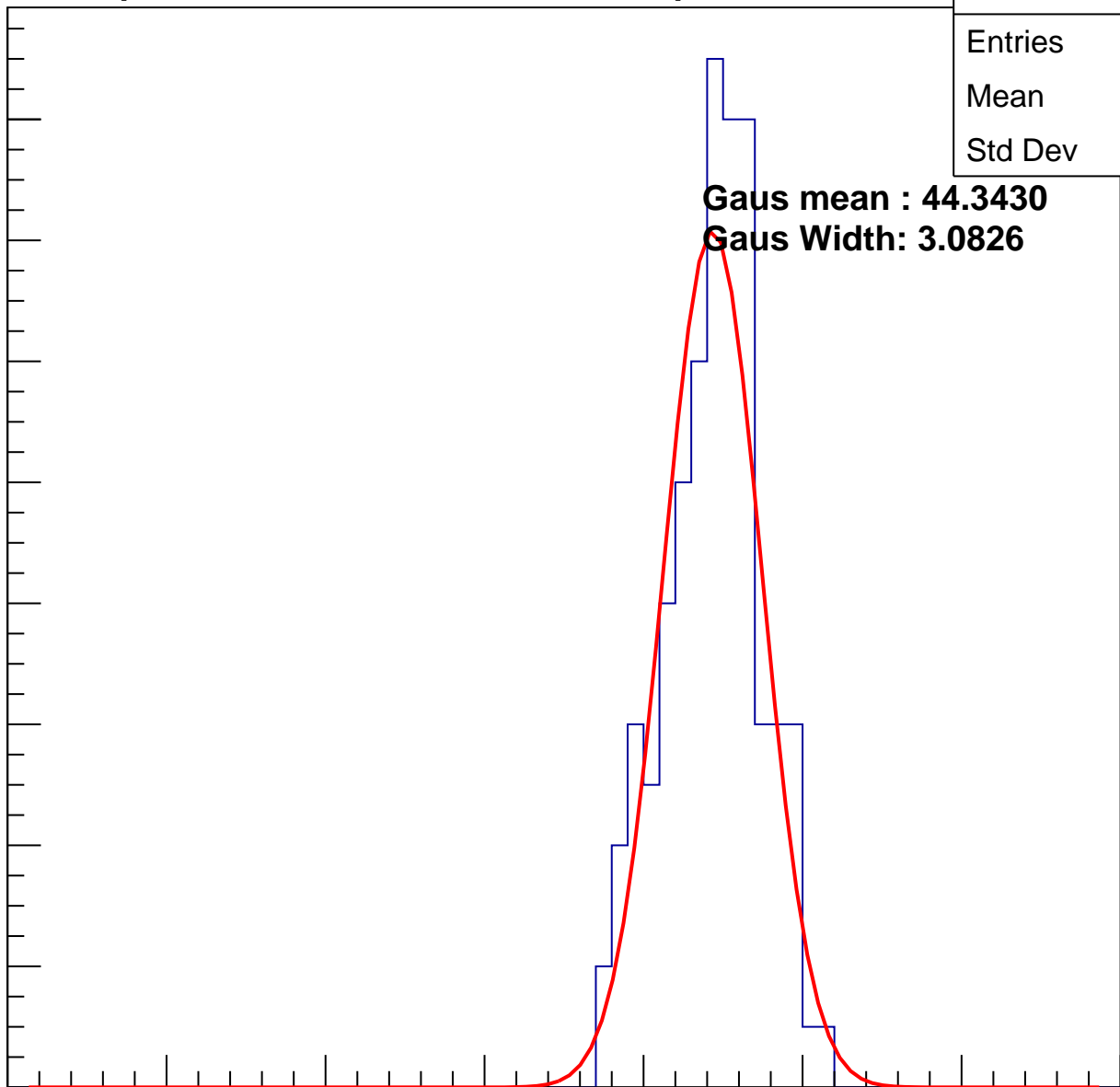
40

50

60

70

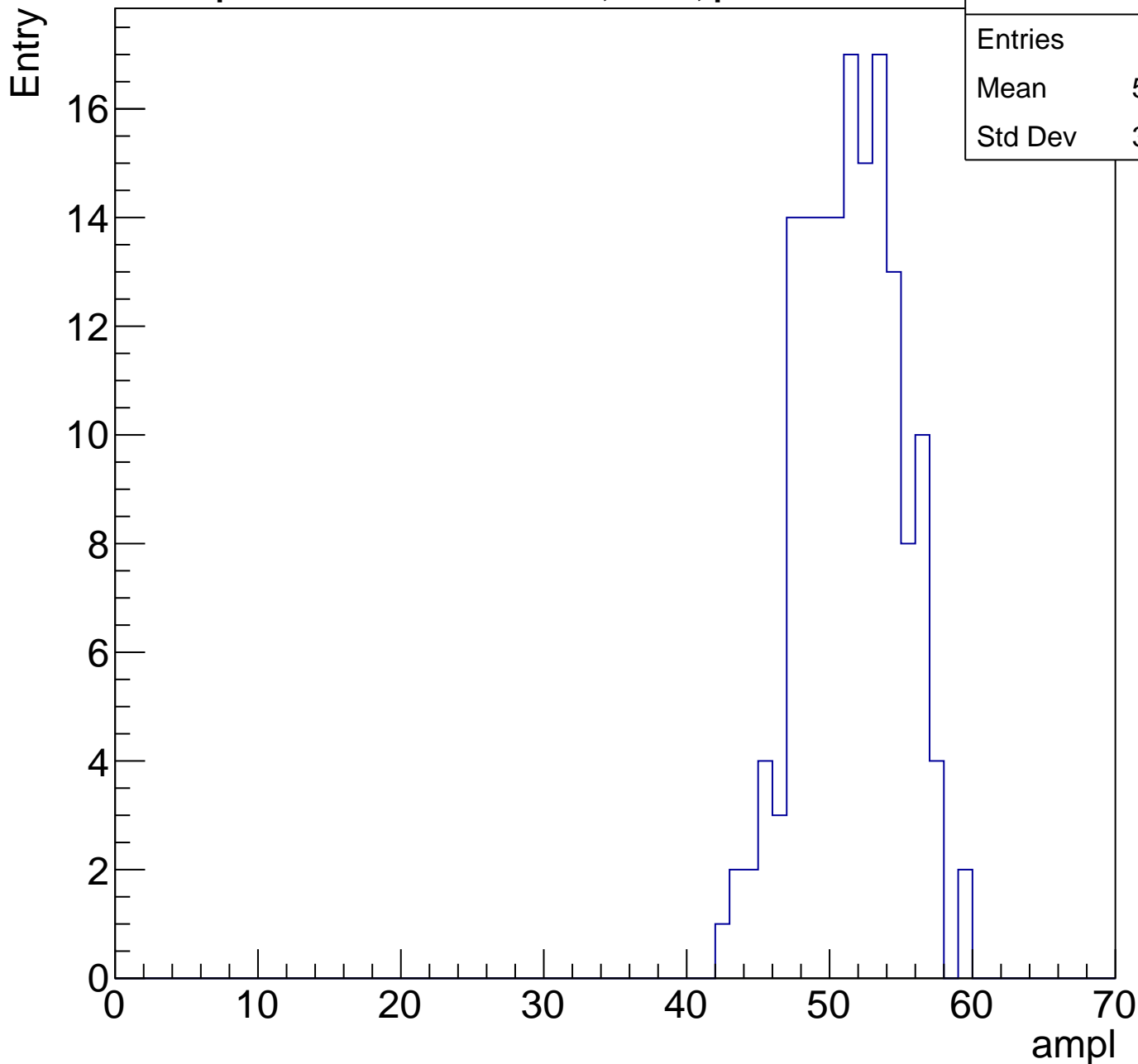
ampl



B1L001S, U16-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	50.95
Std Dev	3.415



B1L001S, U16-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	76
Mean	56.14
Std Dev	2.963

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B1L001S, U16-ch89, adc5

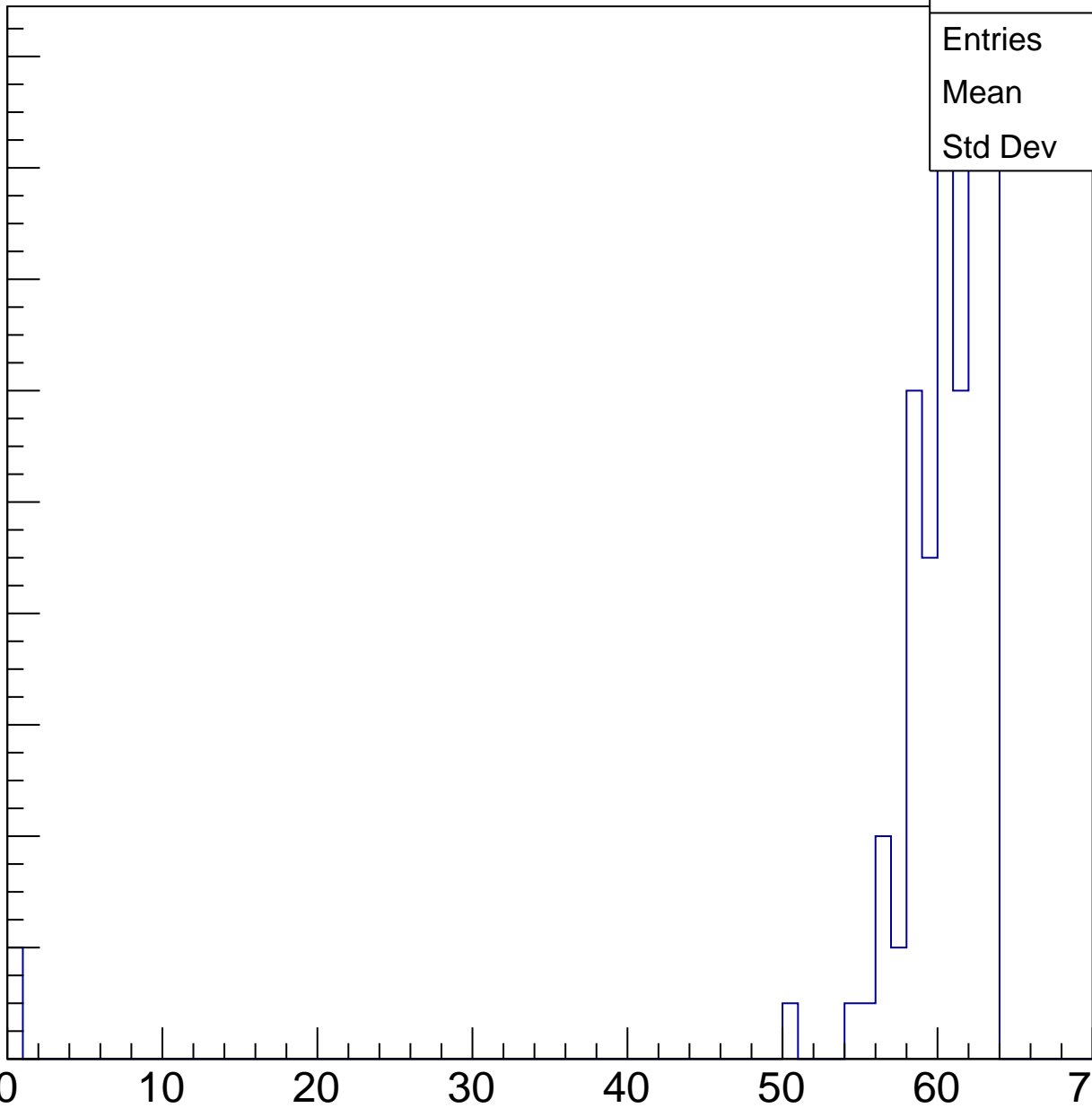
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.04
Std Dev	8.839

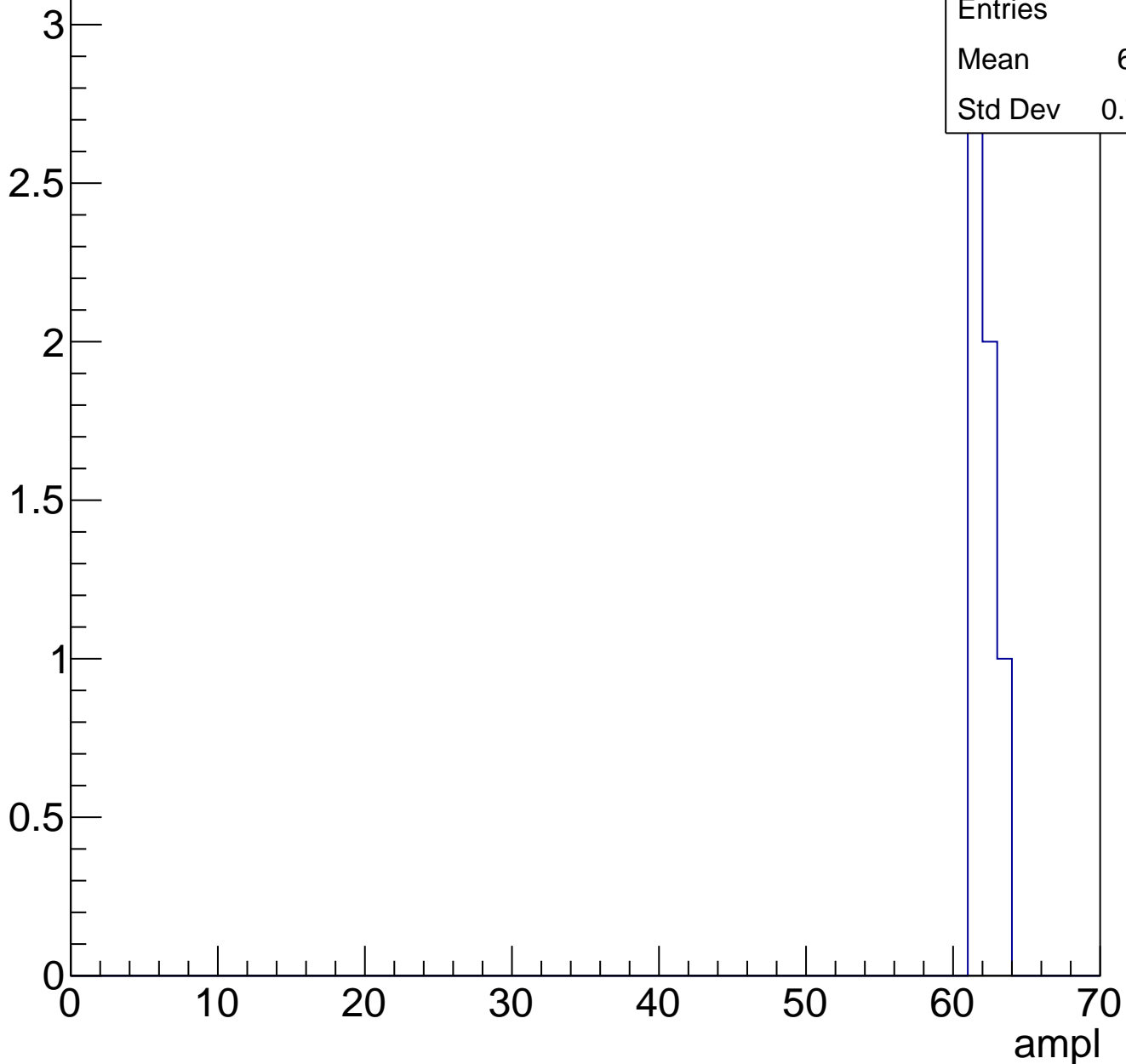
ampl



B1L001S, U16-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U16-ch90, adc0

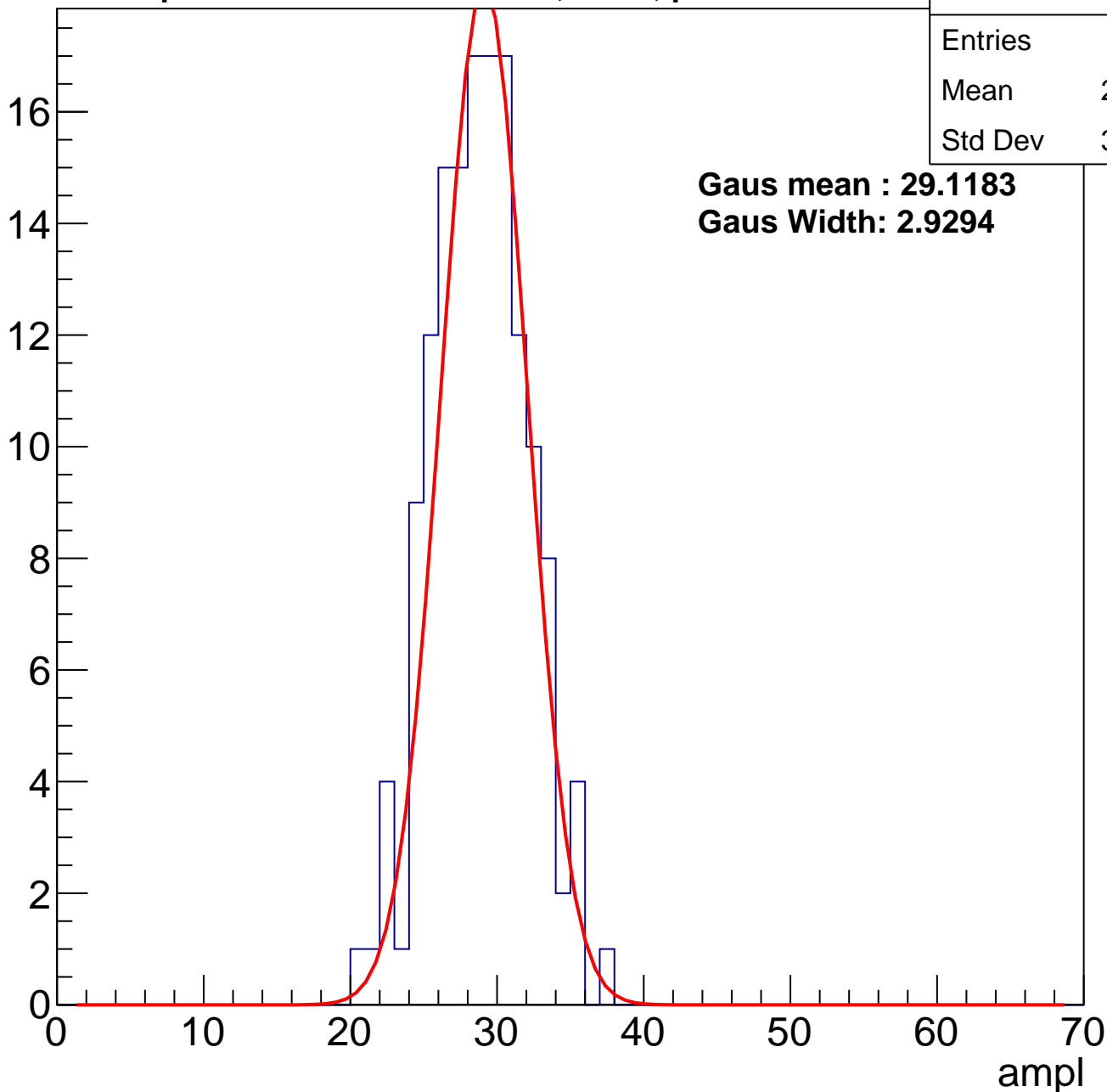
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	28.38
Std Dev	3.195

Gaus mean : 29.1183

Gaus Width: 2.9294

Entry

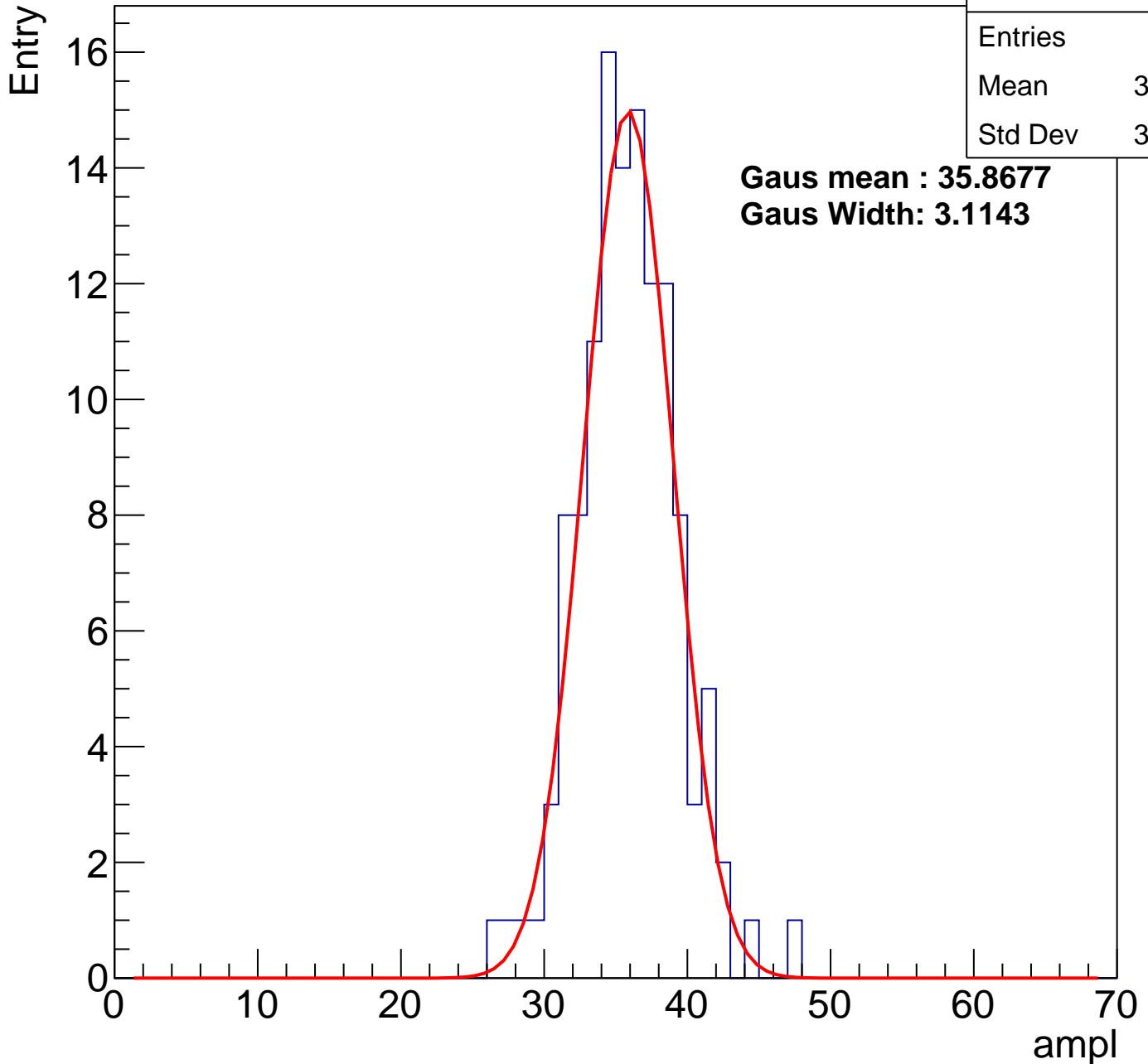


B1L001S, U16-ch90, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.39
Std Dev	3.402

Gaus mean : 35.8677
Gaus Width: 3.1143



B1L001S, U16-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	41.08
Std Dev	3.331

Gaus mean : 41.5340

Gaus Width: 3.2247

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

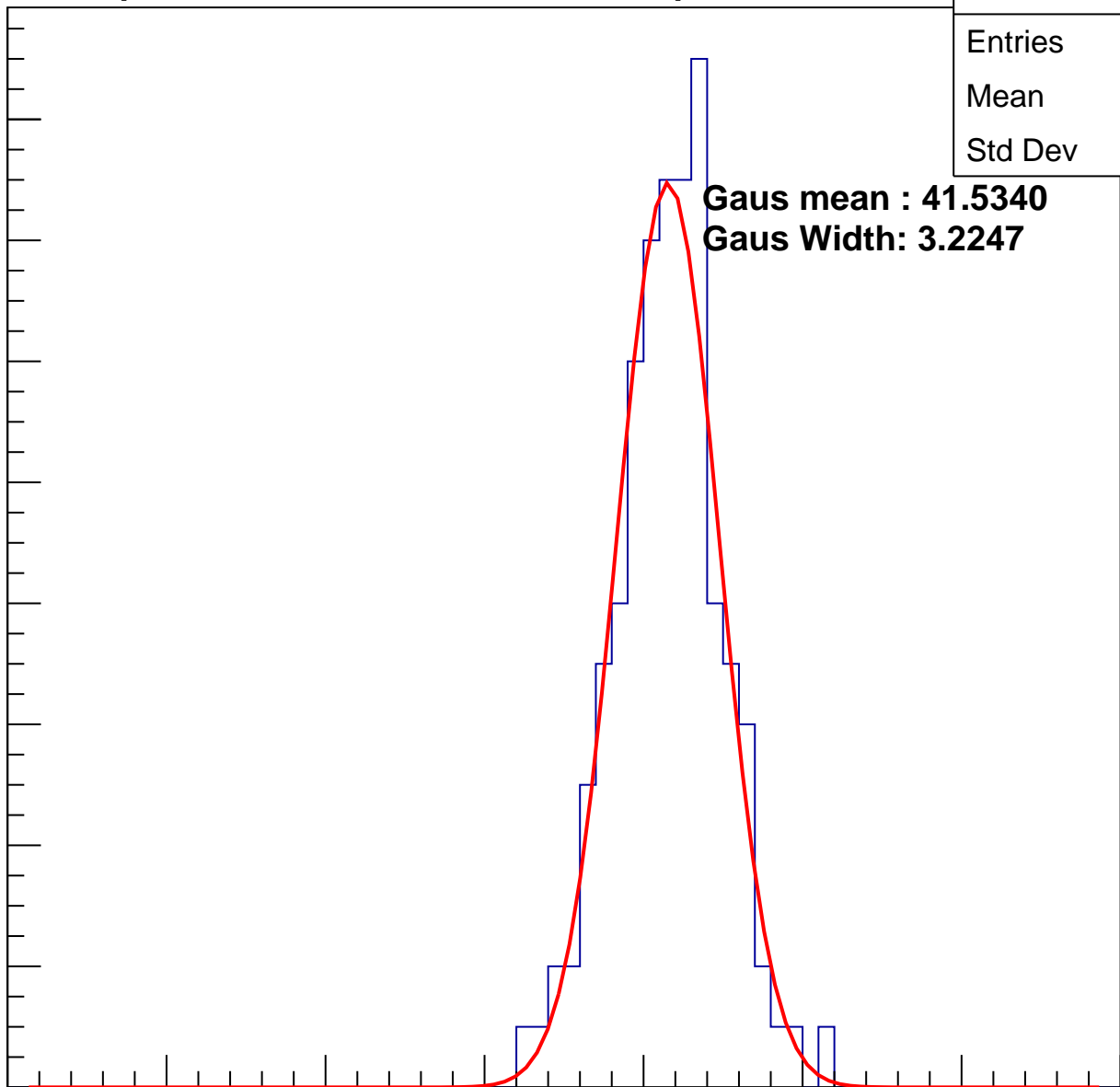
40

50

60

70

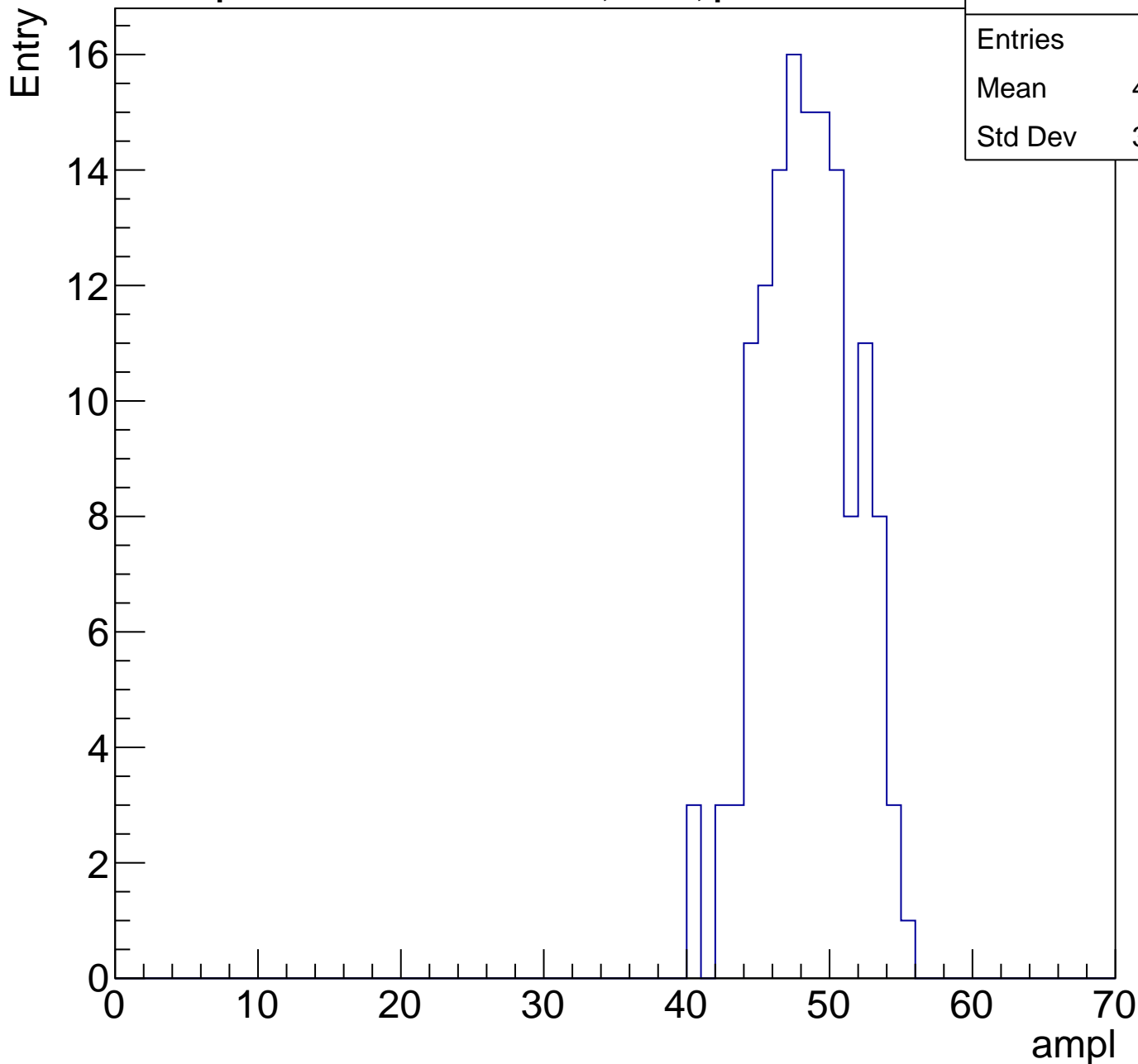
ampl



B1L001S, U16-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

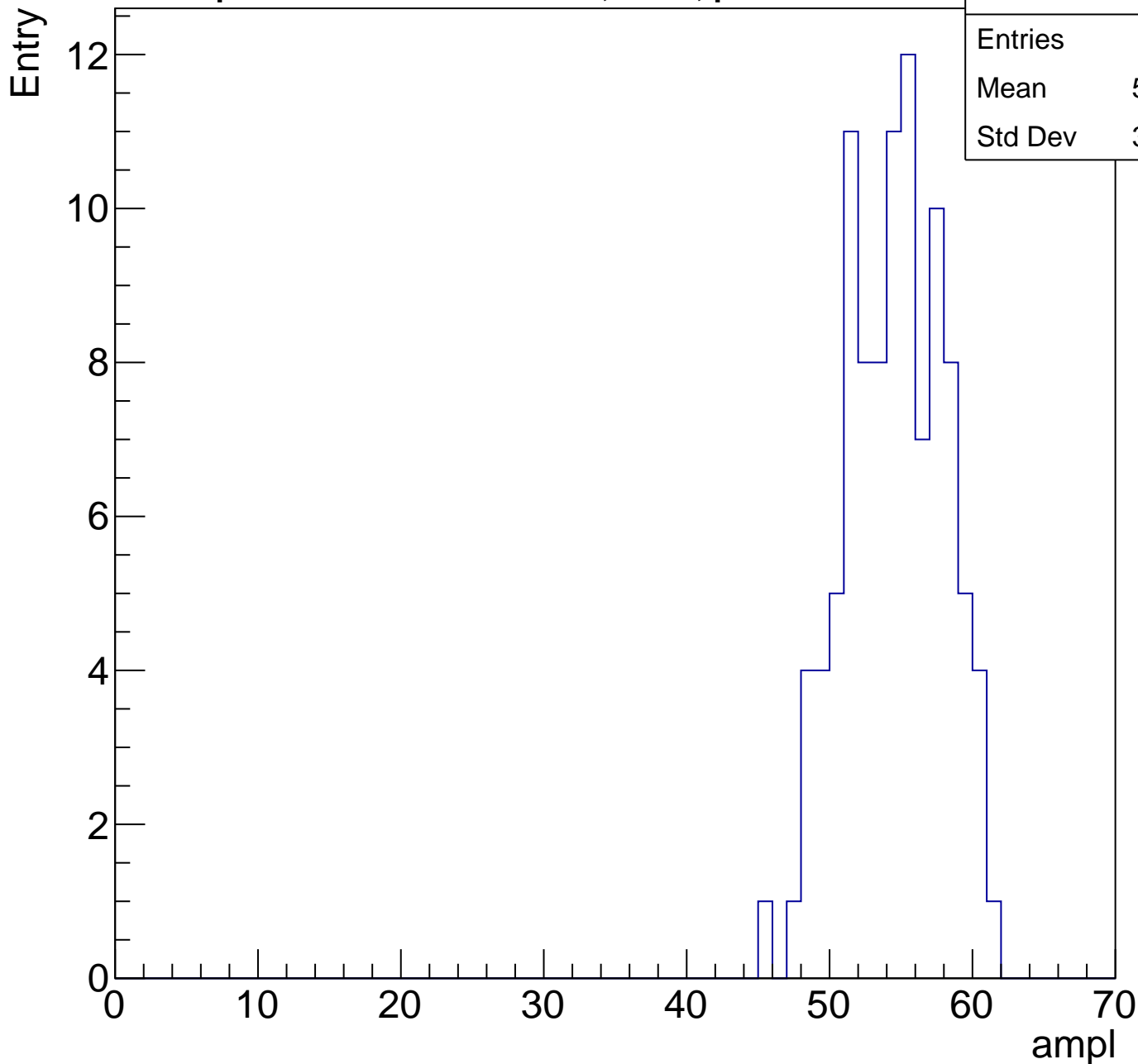
Entries	137
Mean	47.96
Std Dev	3.204



B1L001S, U16-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	54.07
Std Dev	3.418



B1L001S, U16-ch90, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	56.99
Std Dev	8.601

Entry

12

10

8

6

4

2

0

0

10

20

30

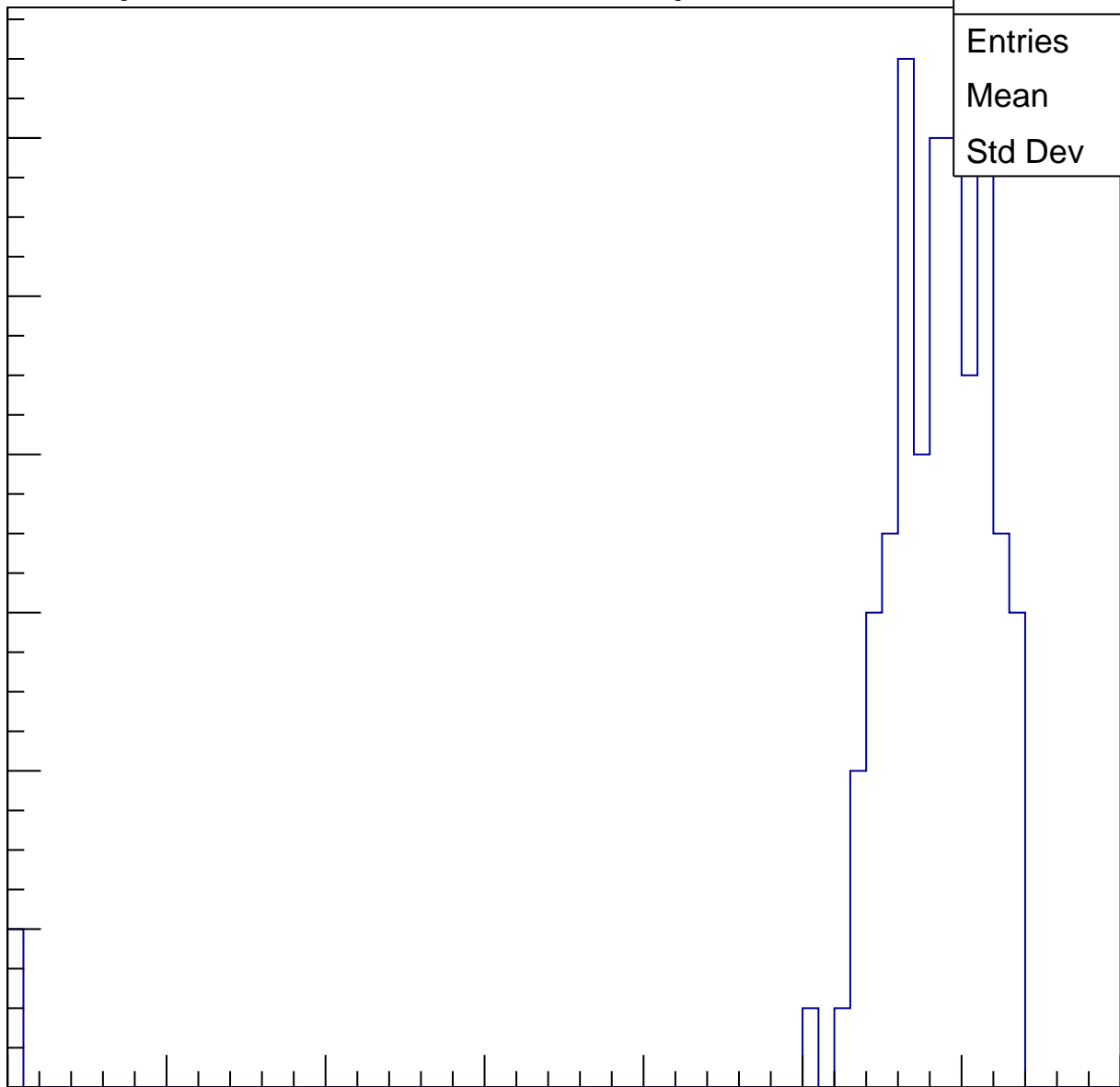
40

50

60

70

ampl



B1L001S, U16-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

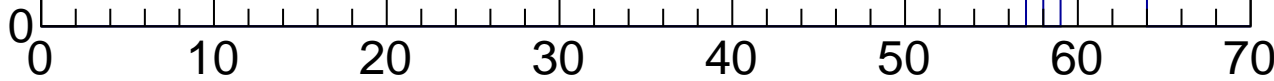
Entry

14
12
10
8
6
4
2
0

Entries	45
Mean	61.44
Std Dev	1.571

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch91, adc0

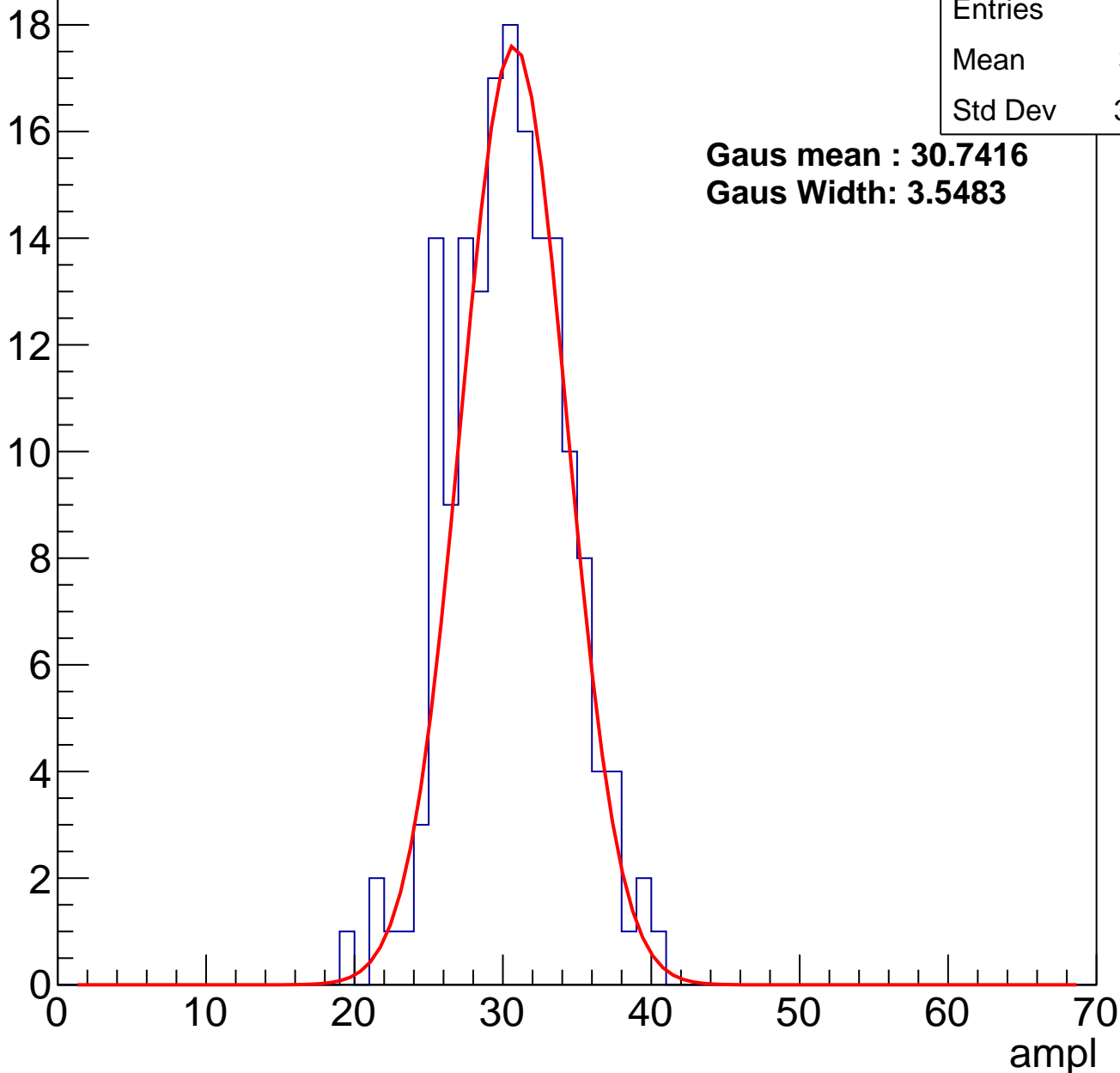
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	30.01
Std Dev	3.804

Gaus mean : 30.7416

Gaus Width: 3.5483

Entry



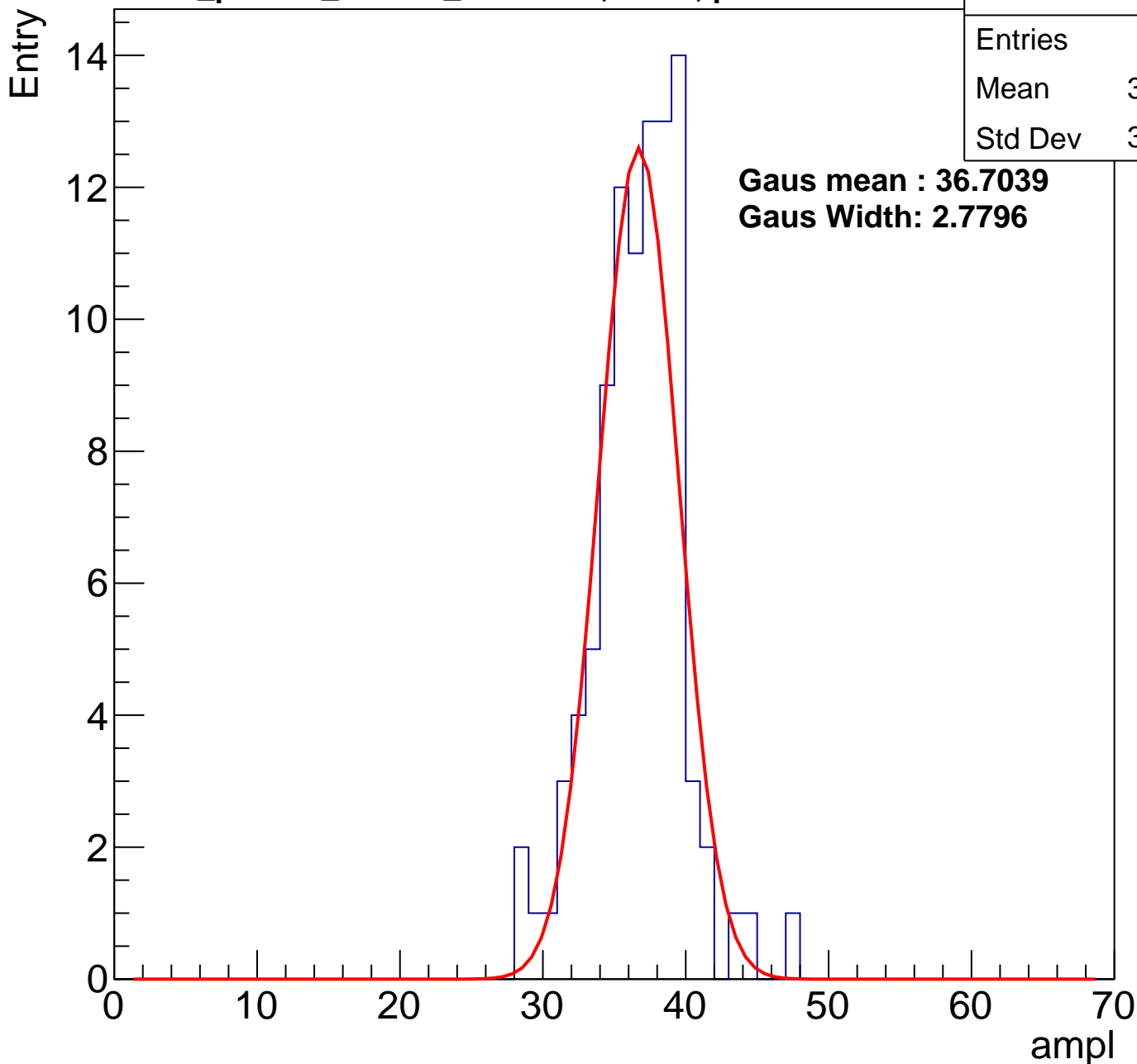
B1L001S, U16-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	36.25
Std Dev	3.166

Gaus mean : 36.7039

Gaus Width: 2.7796

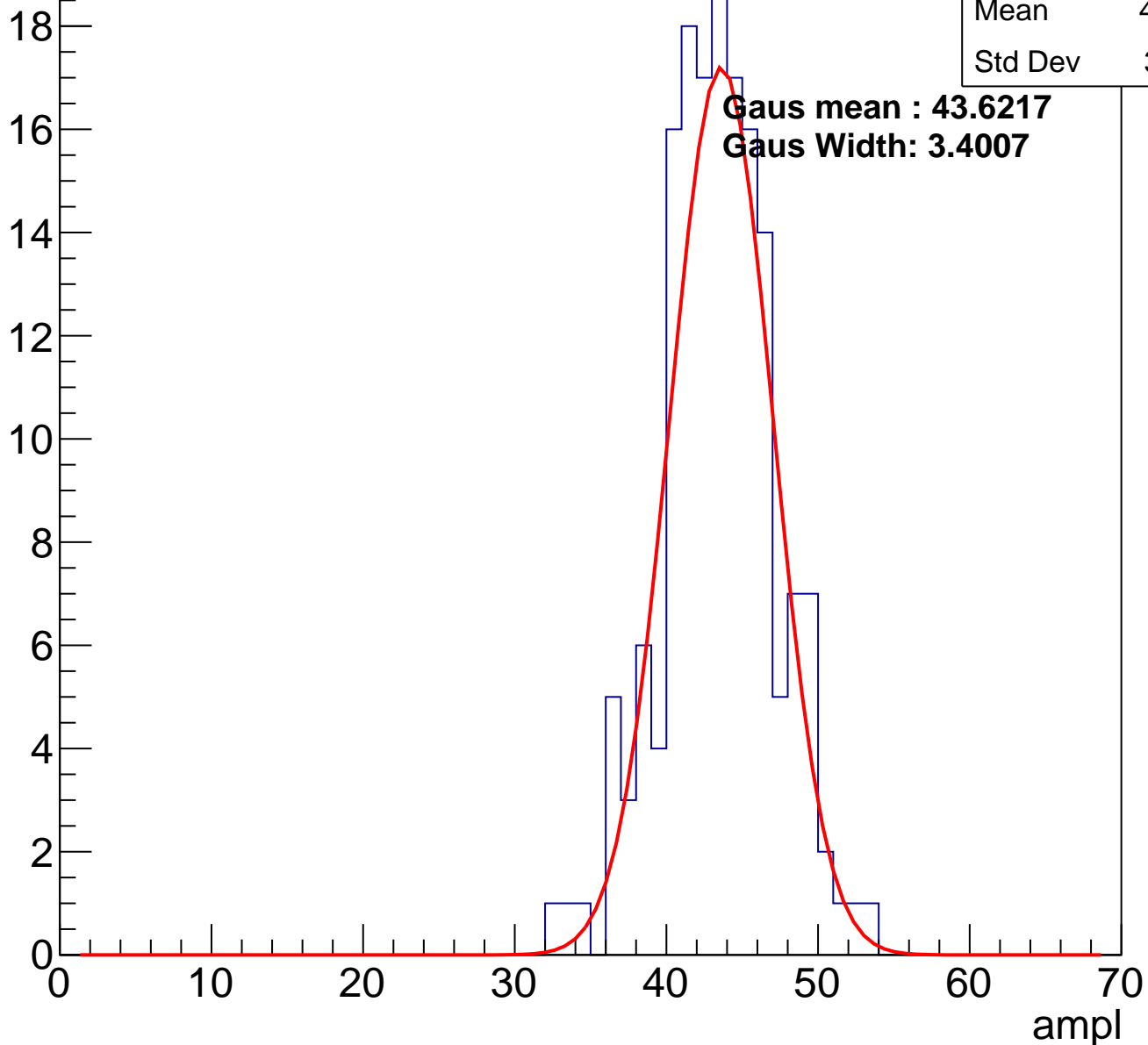


B1L001S, U16-ch91, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	42.99
Std Dev	3.651

Entry



B1L001S, U16-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

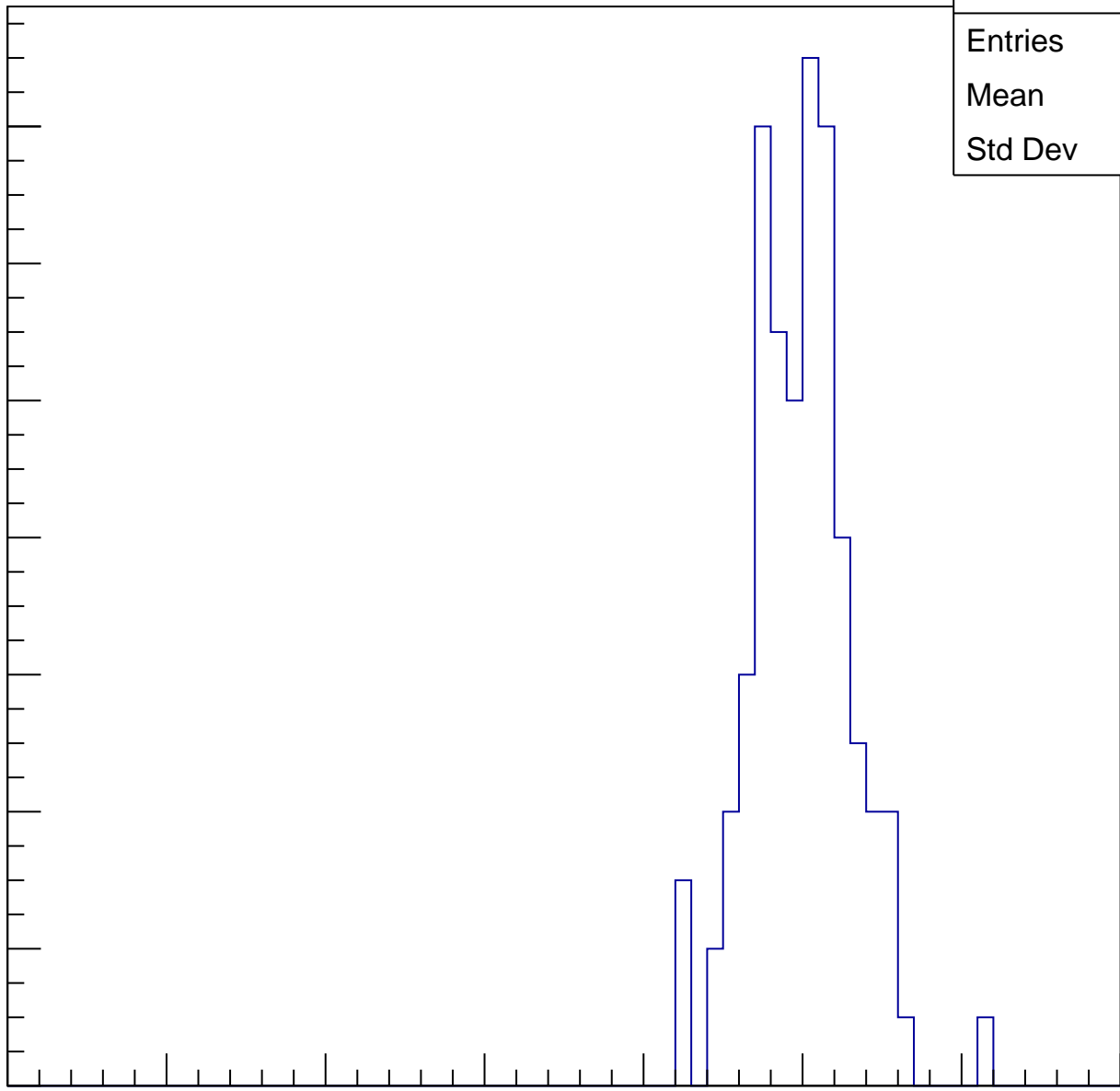
Entries	102
Mean	49.45
Std Dev	3.164

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

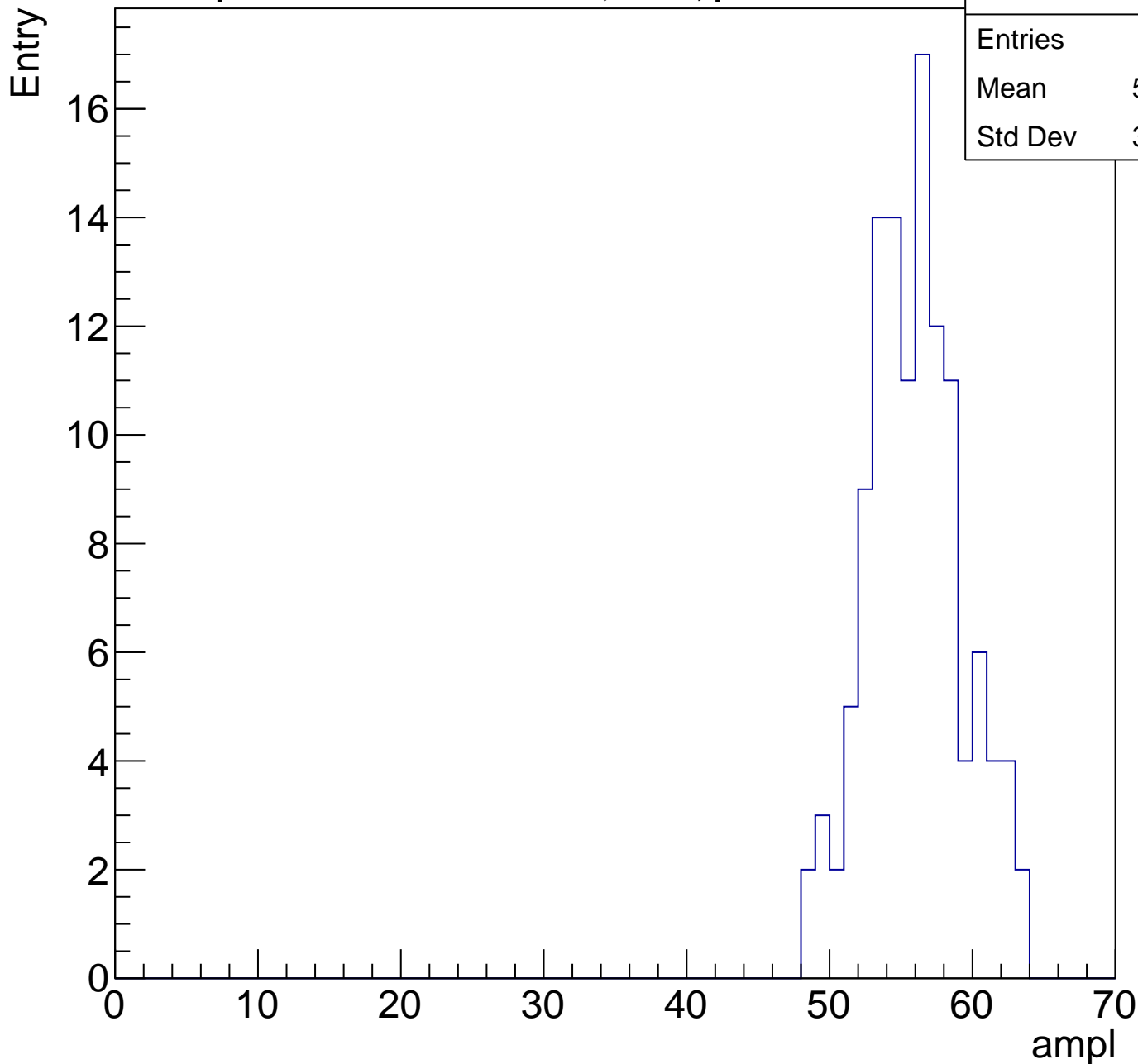
ampl



B1L001S, U16-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	55.48
Std Dev	3.299



B1L001S, U16-ch91, adc5

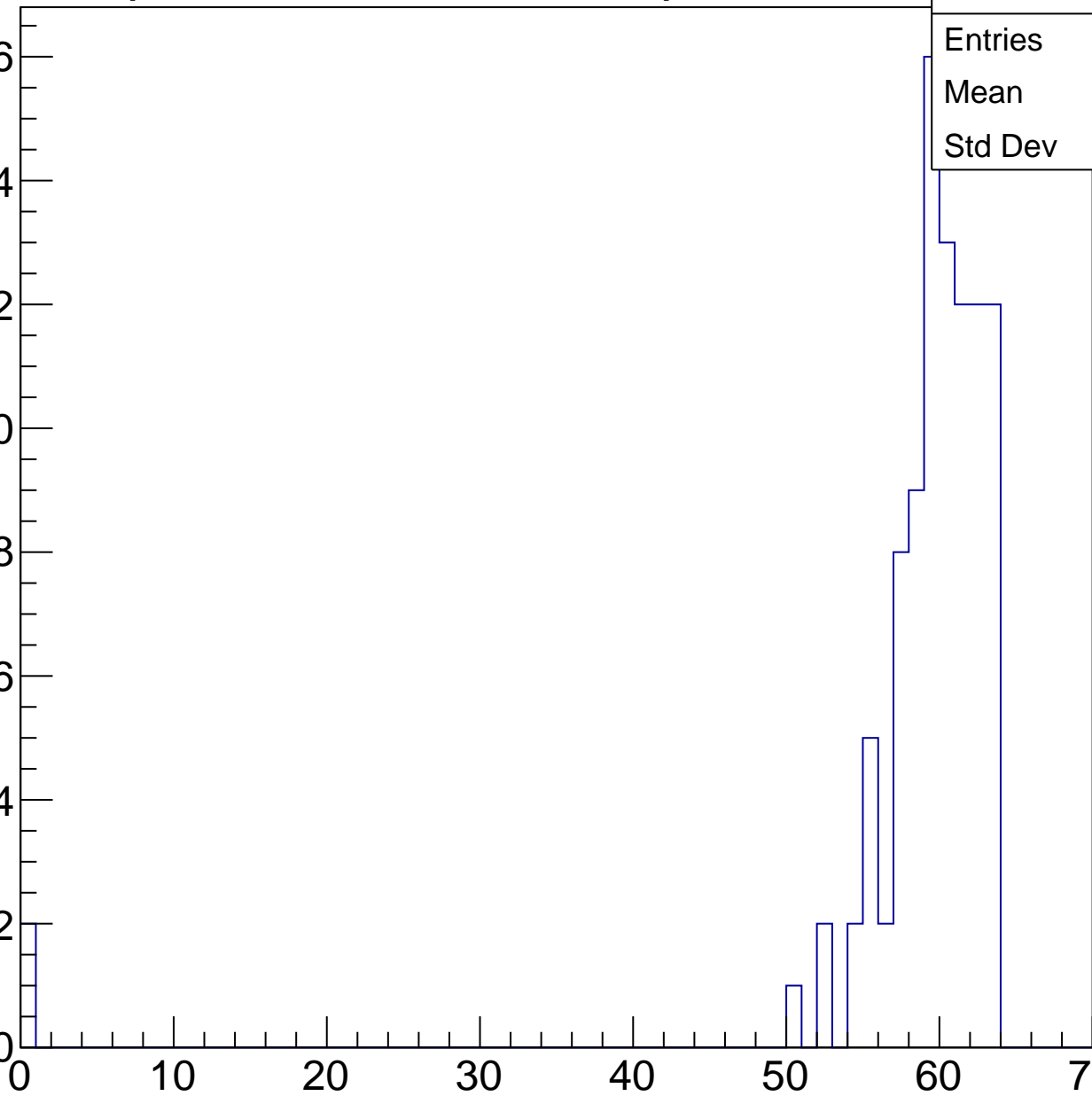
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.16
Std Dev	8.911

ampl

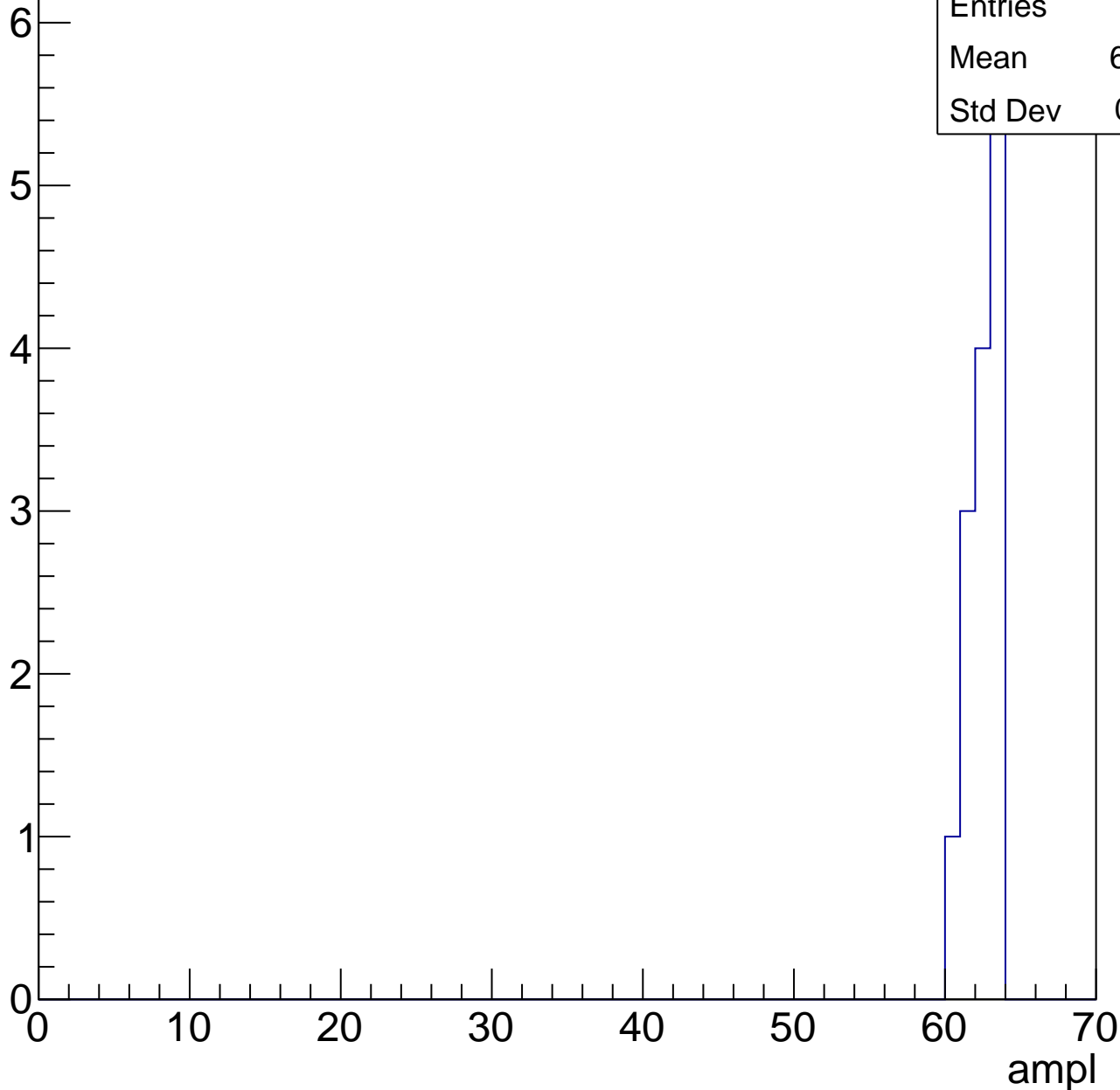


B1L001S, U16-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

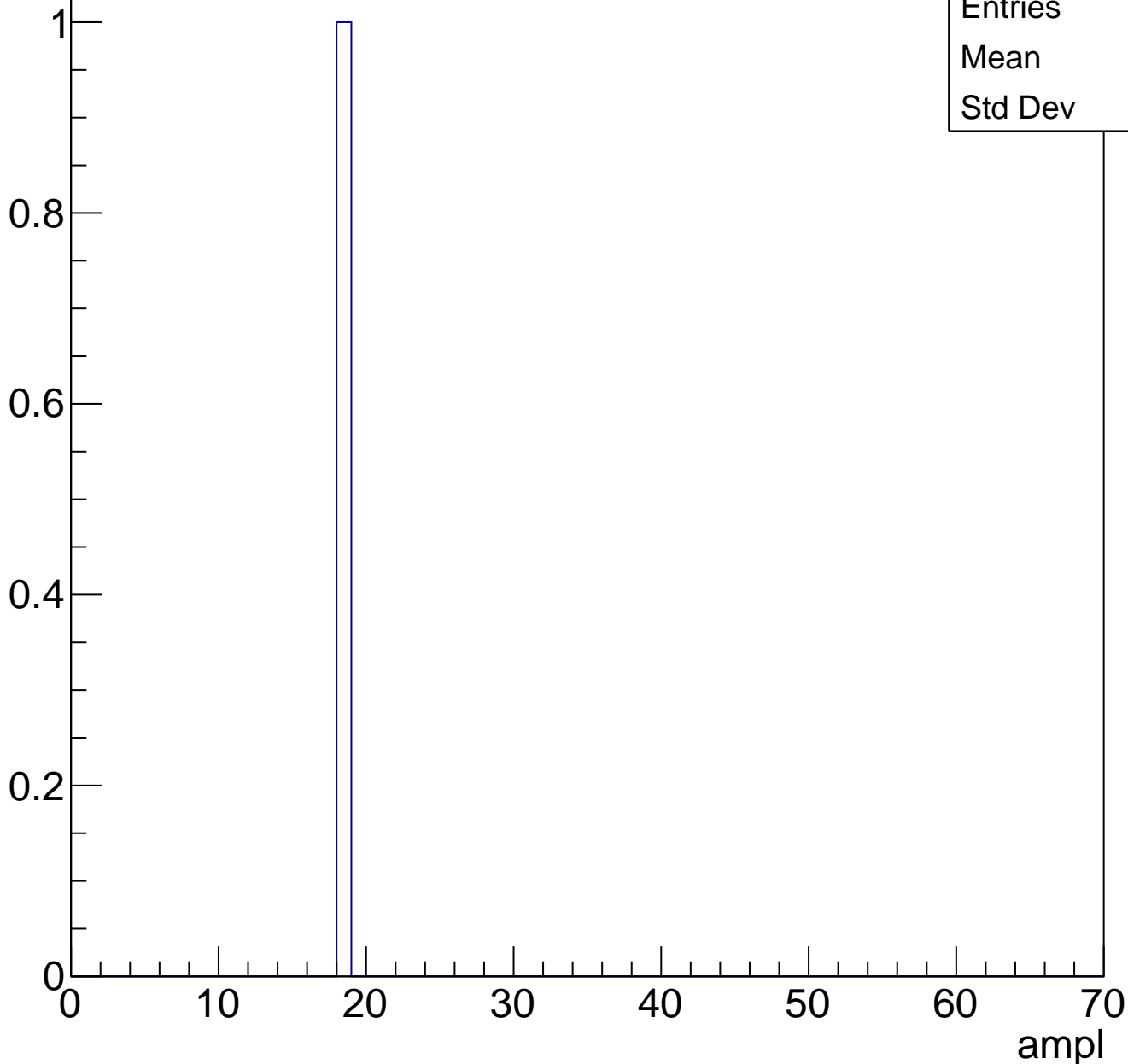
Entries	14
Mean	62.07
Std Dev	0.961



B1L001S, U16-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch92, adc0

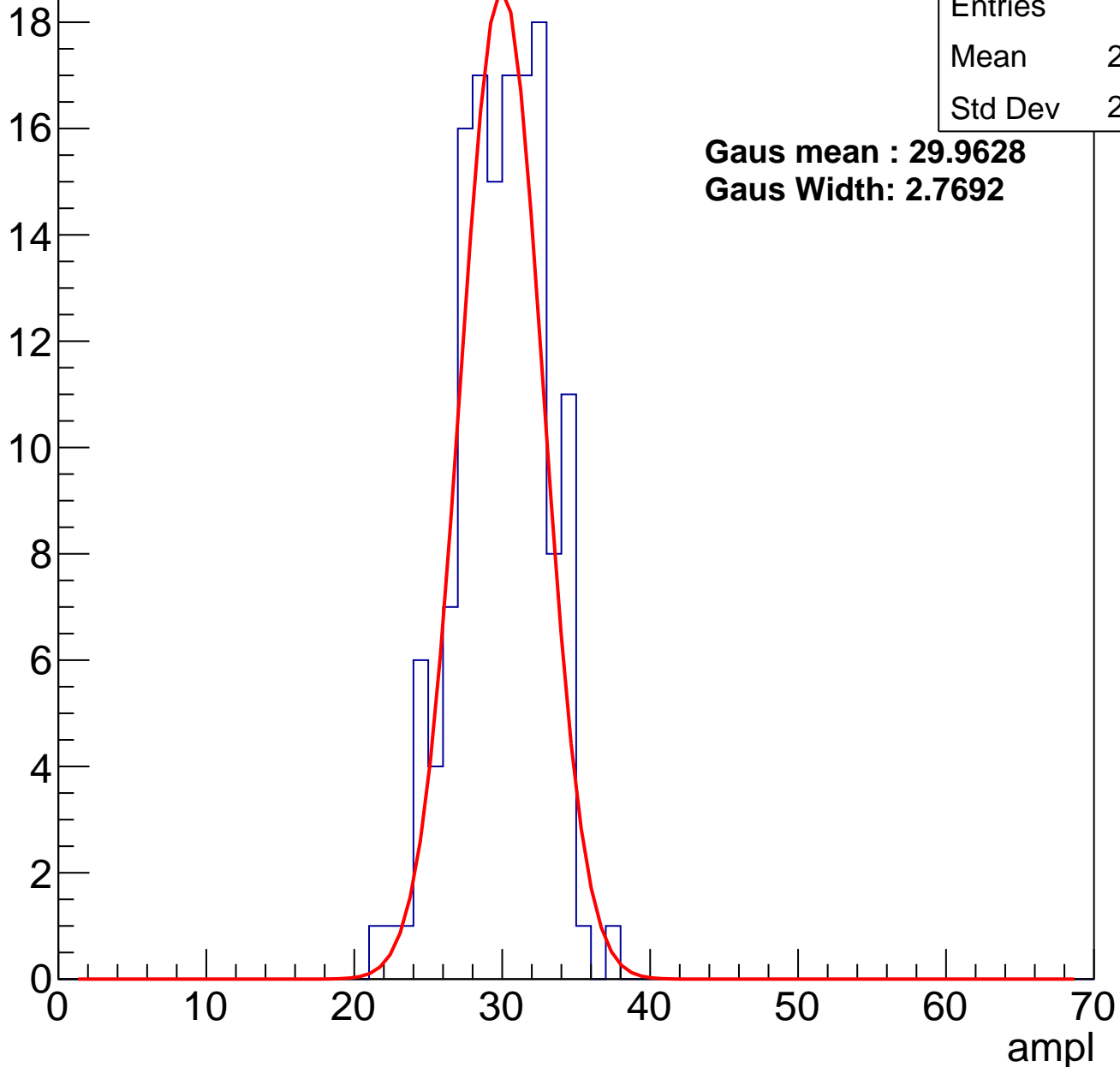
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	29.49
Std Dev	2.945

Gaus mean : 29.9628

Gaus Width: 2.7692

Entry



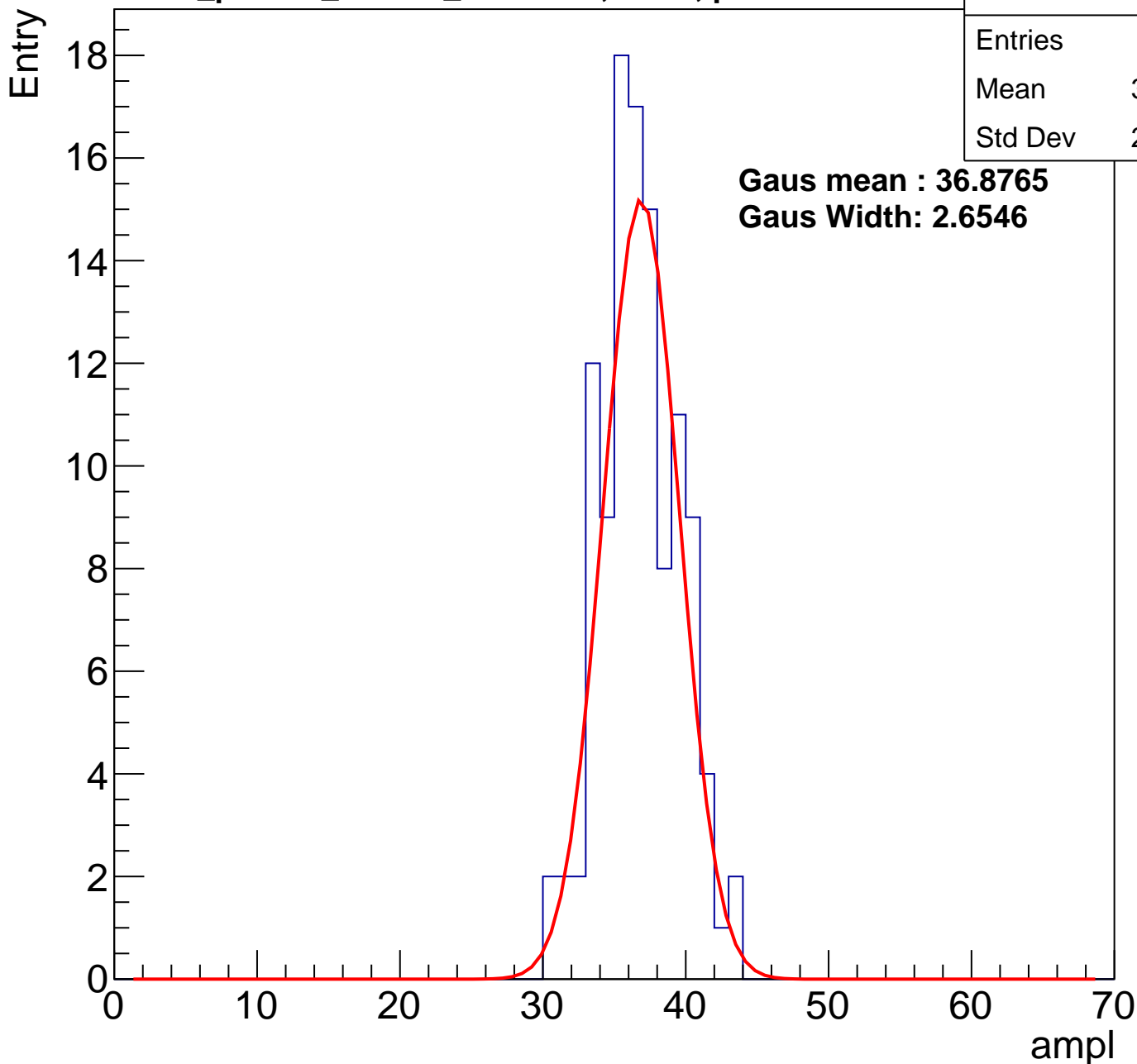
B1L001S, U16-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	36.34
Std Dev	2.718

Gaus mean : 36.8765

Gaus Width: 2.6546



B1L001S, U16-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

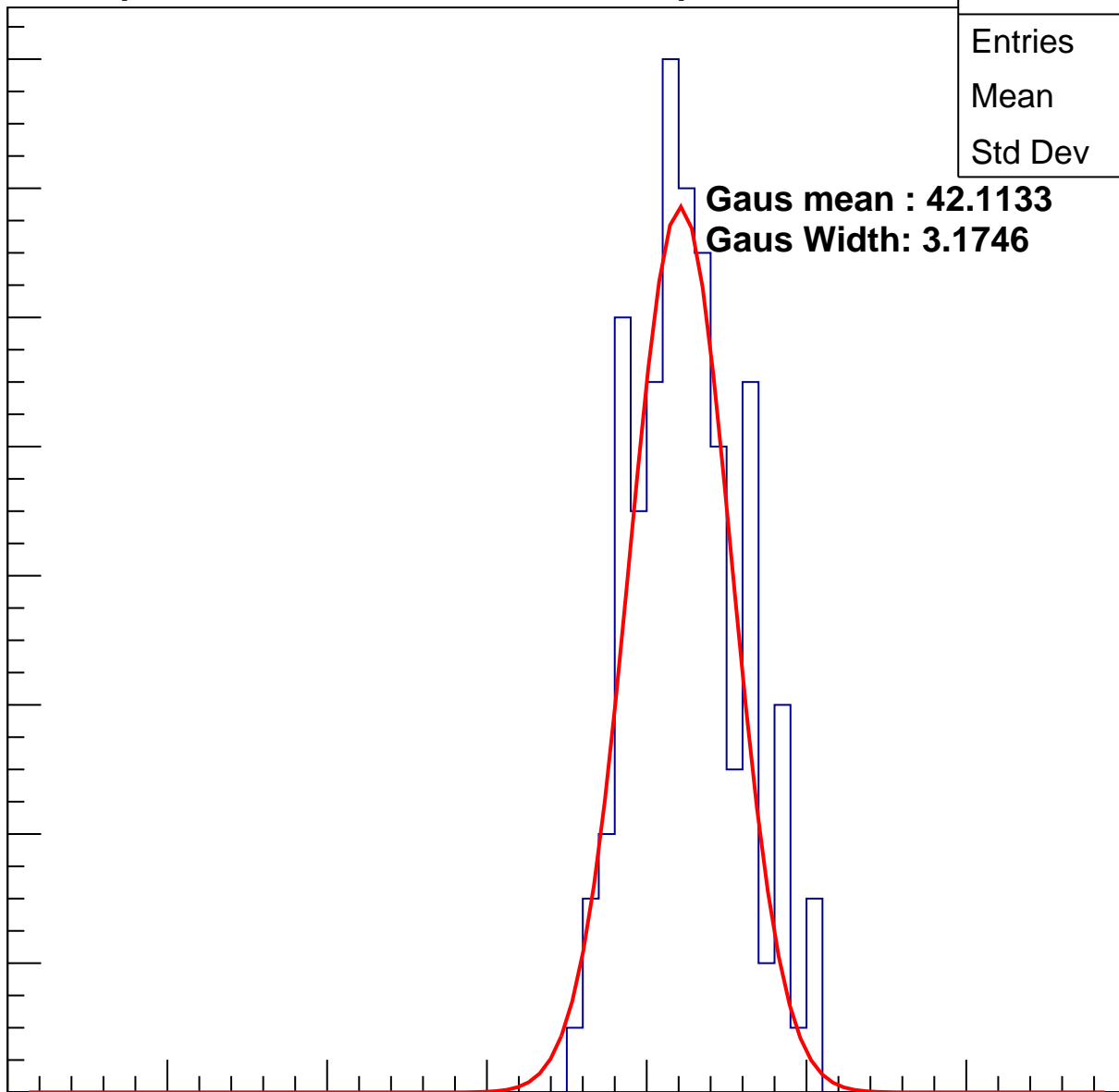
Entries	121
Mean	42.09
Std Dev	3.365

Gaus mean : 42.1133

Gaus Width: 3.1746

0 10 20 30 40 50 60 70

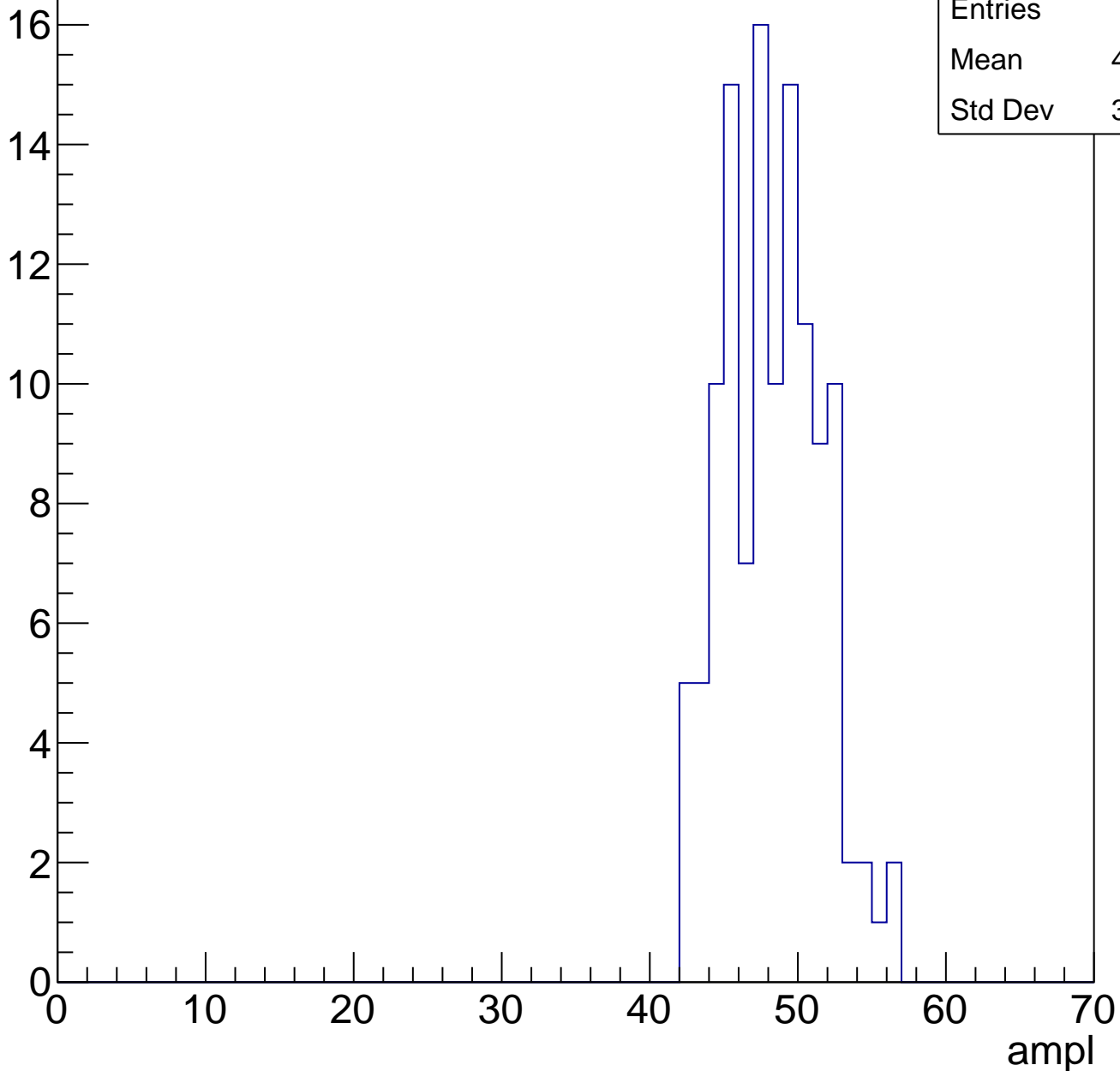
ampl



B1L001S, U16-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

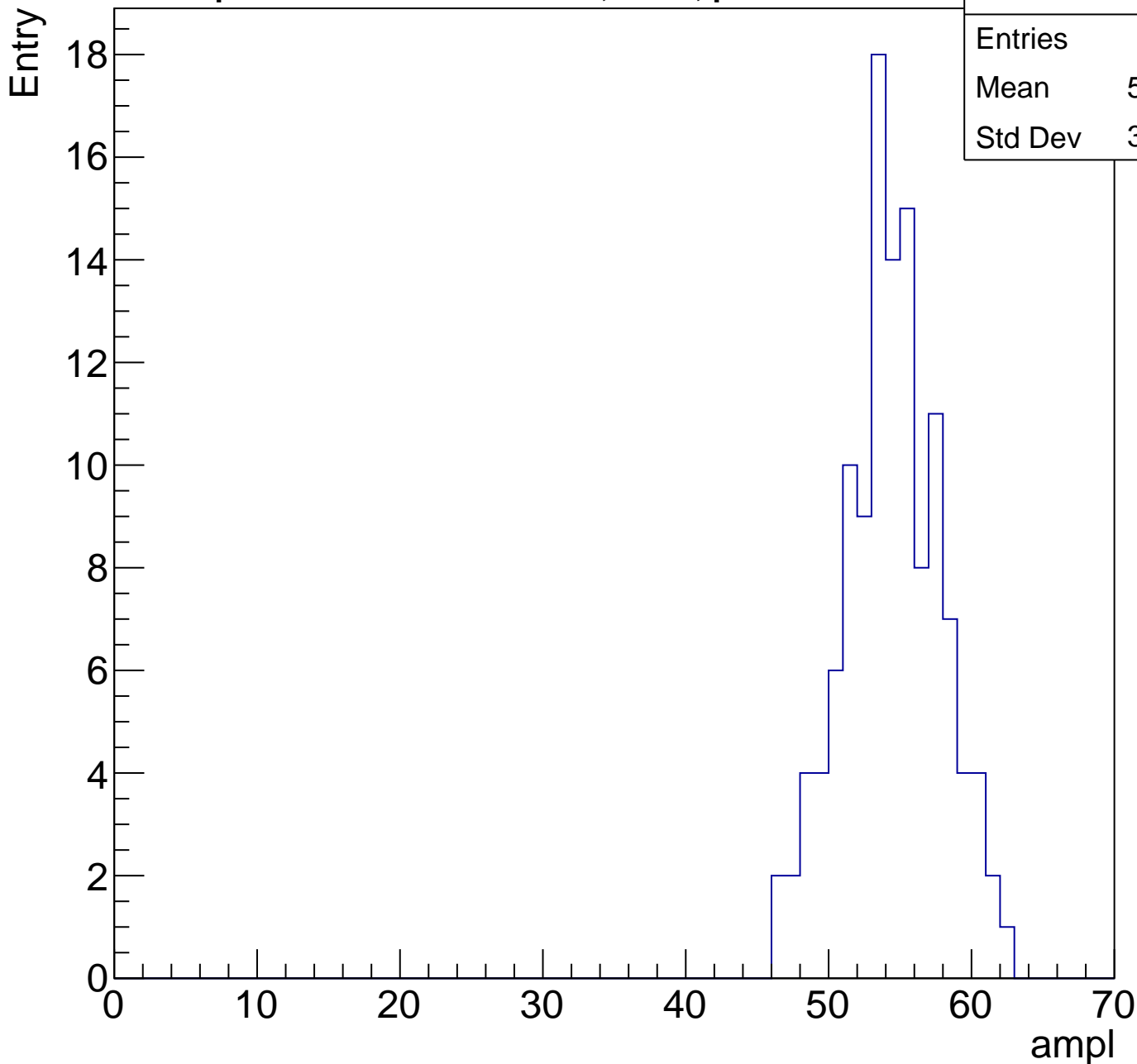
Entry



B1L001S, U16-ch92, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	53.95
Std Dev	3.369



B1L001S, U16-ch92, adc5

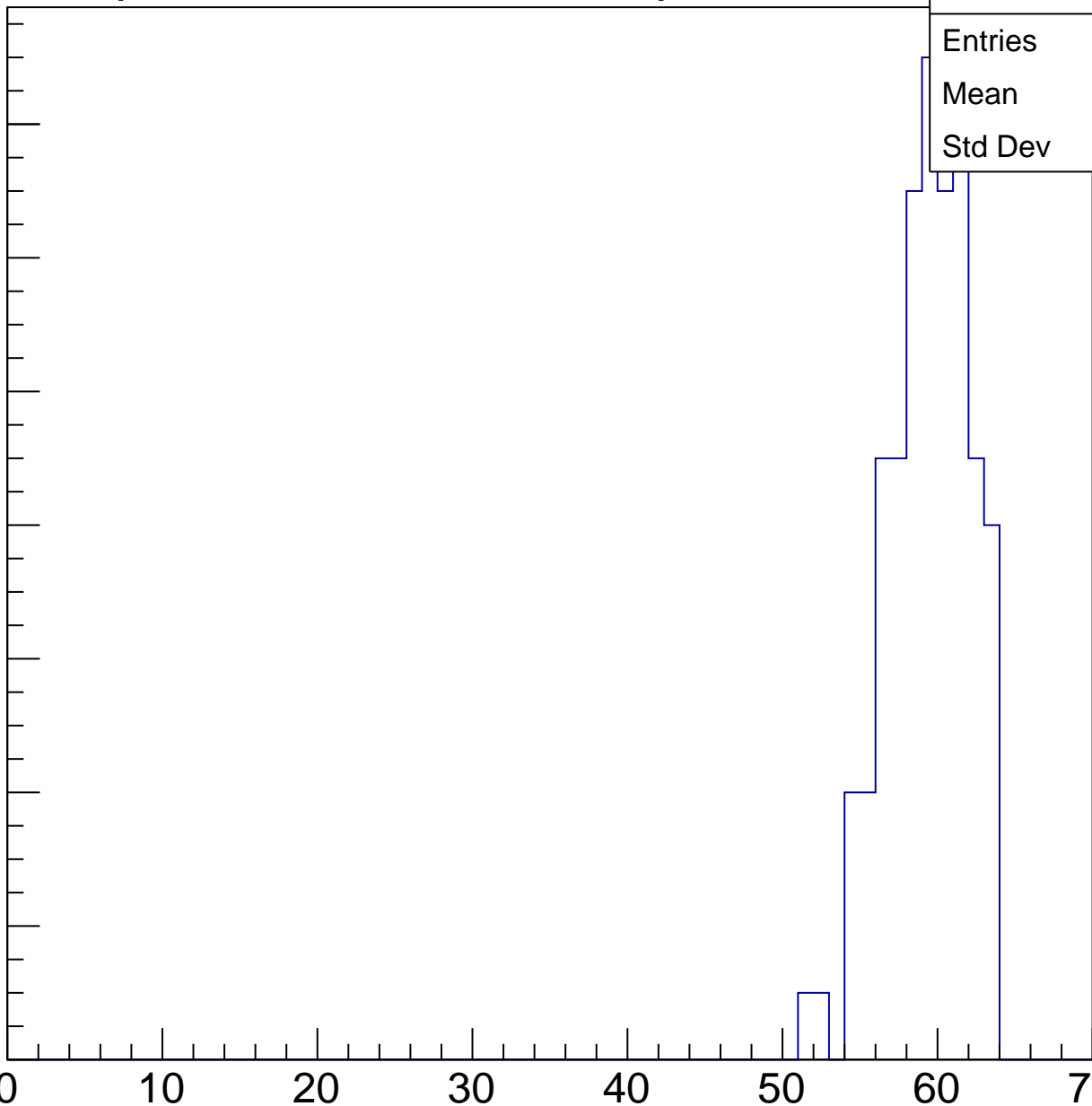
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	58.91
Std Dev	2.616

ampl



B1L001S, U16-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	31
Mean	57.84
Std Dev	15.26

ampl

0

10

20

30

40

50

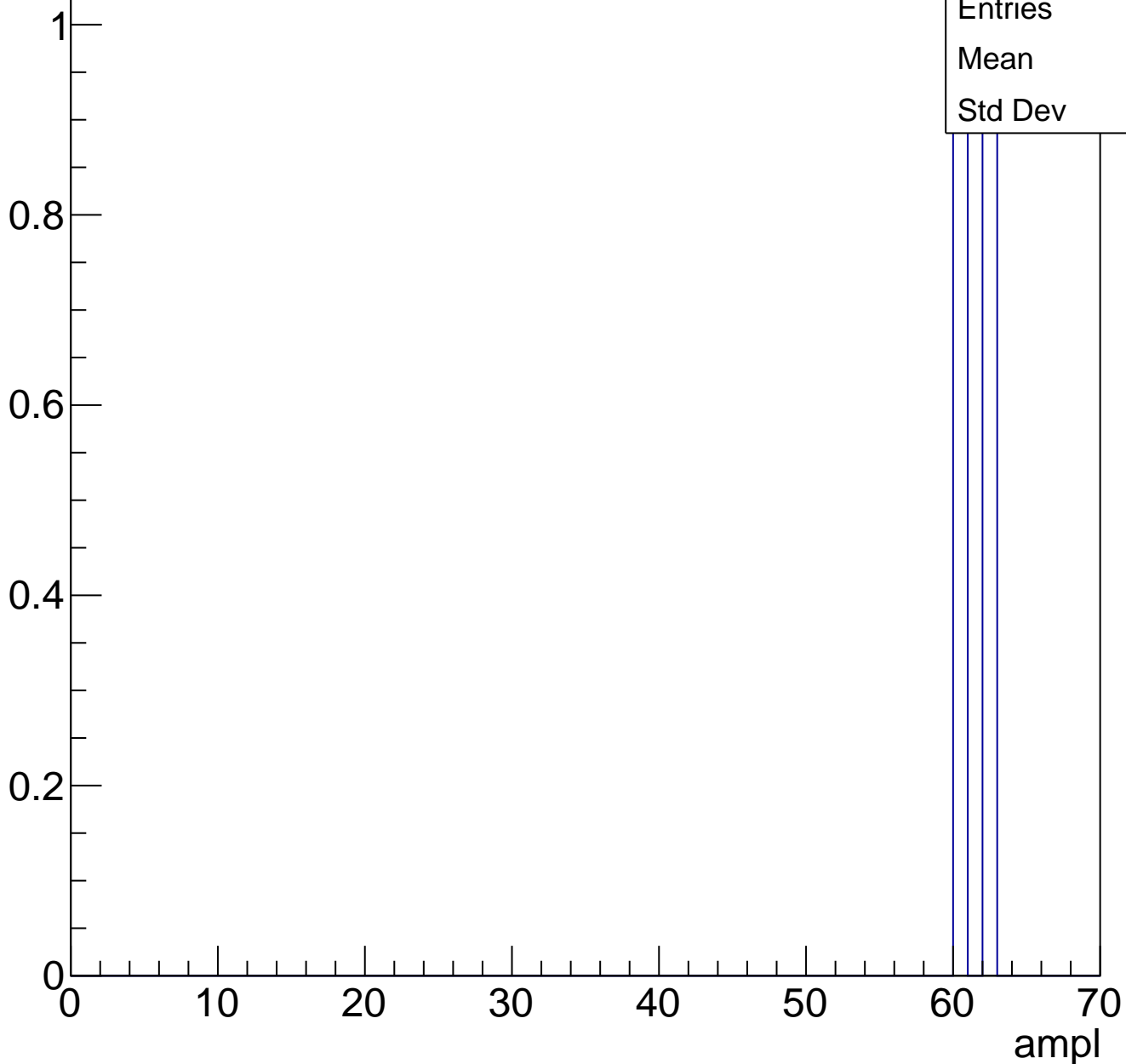
60

70

B1L001S, U16-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch93, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	28.7
Std Dev	3.339

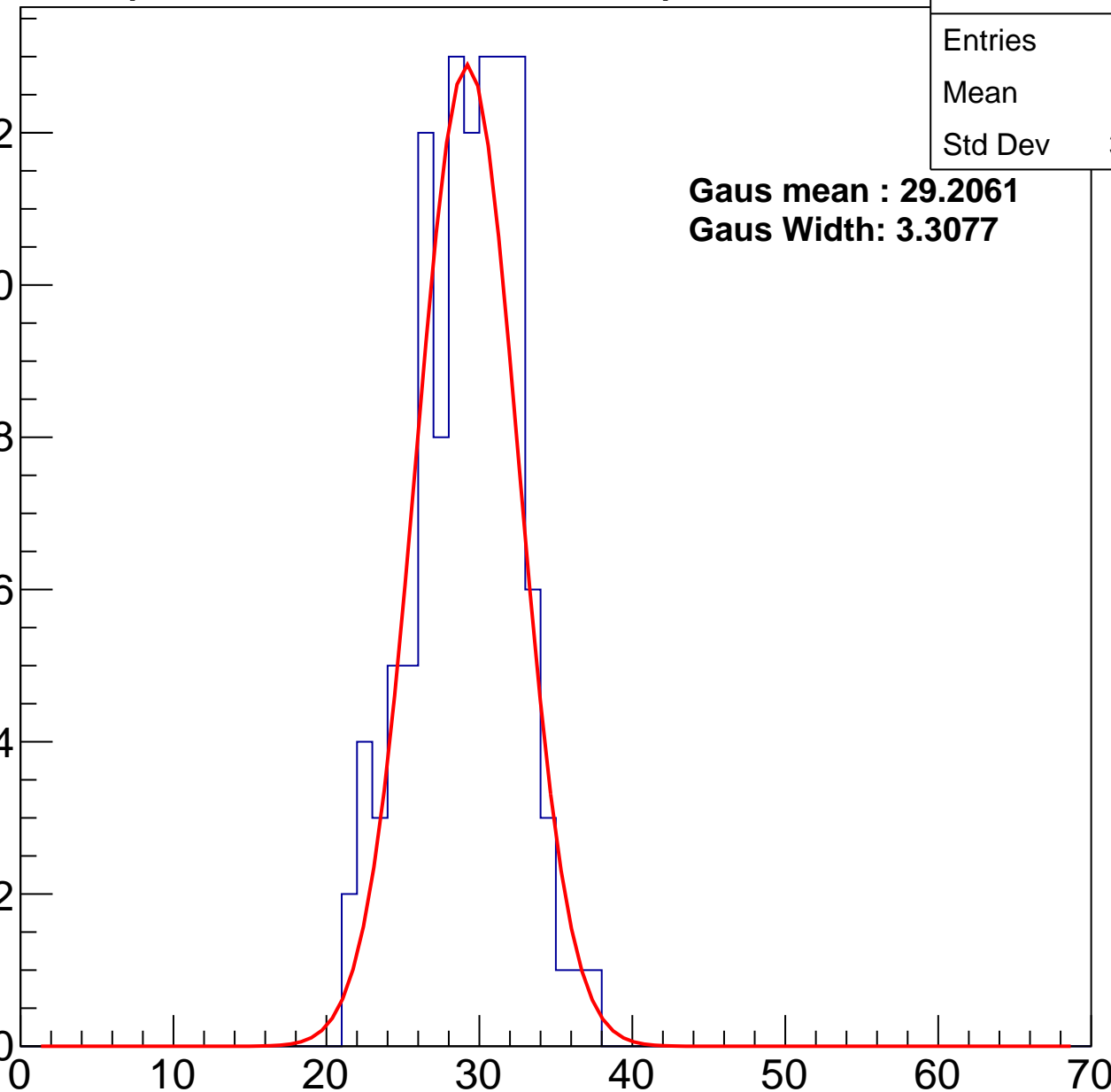
Gaus mean : 29.2061

Gaus Width: 3.3077

Entry

12
10
8
6
4
2
0

ampl



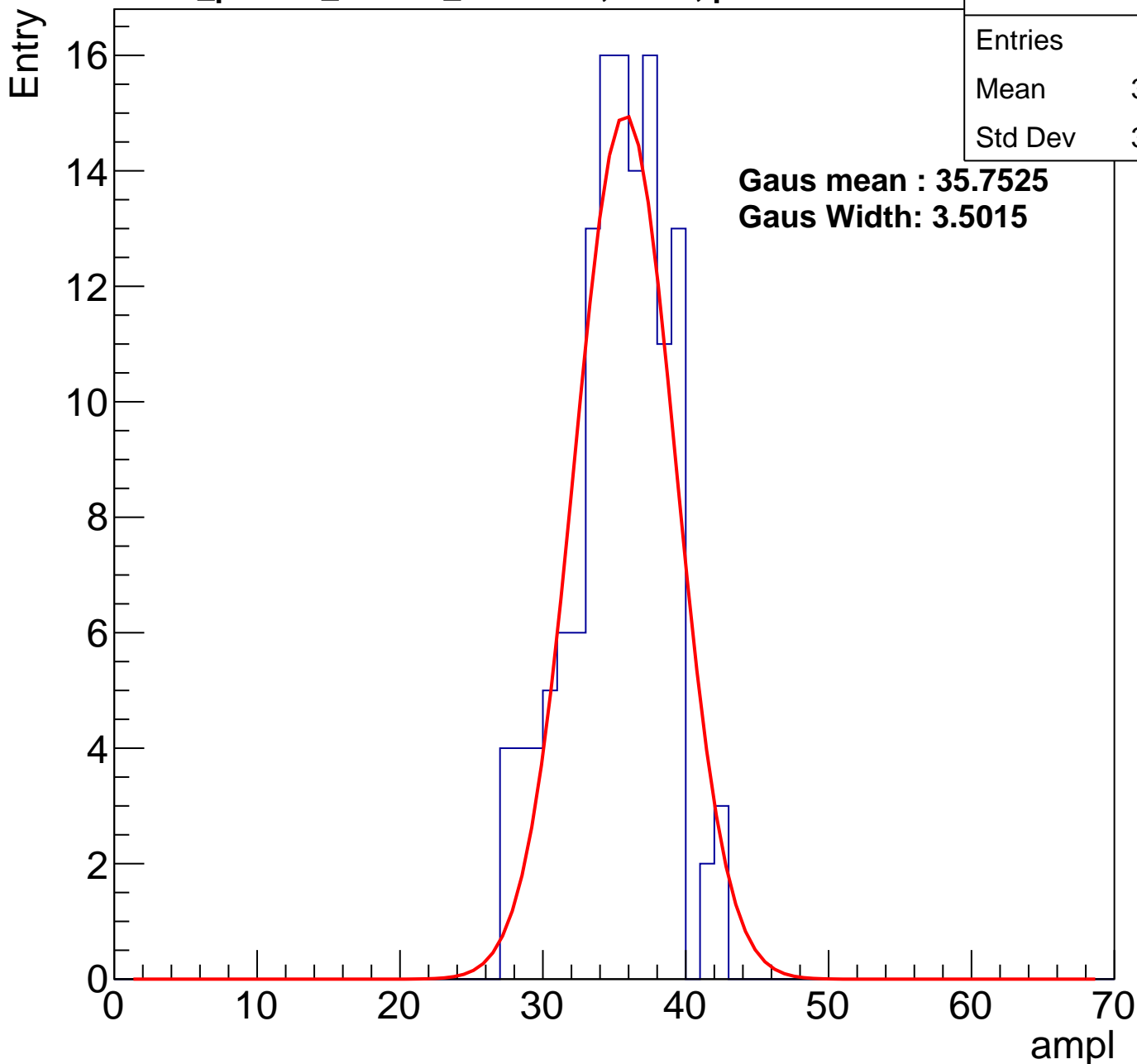
B1L001S, U16-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	34.78
Std Dev	3.399

Gaus mean : 35.7525

Gaus Width: 3.5015



B1L001S, U16-ch93, adc2

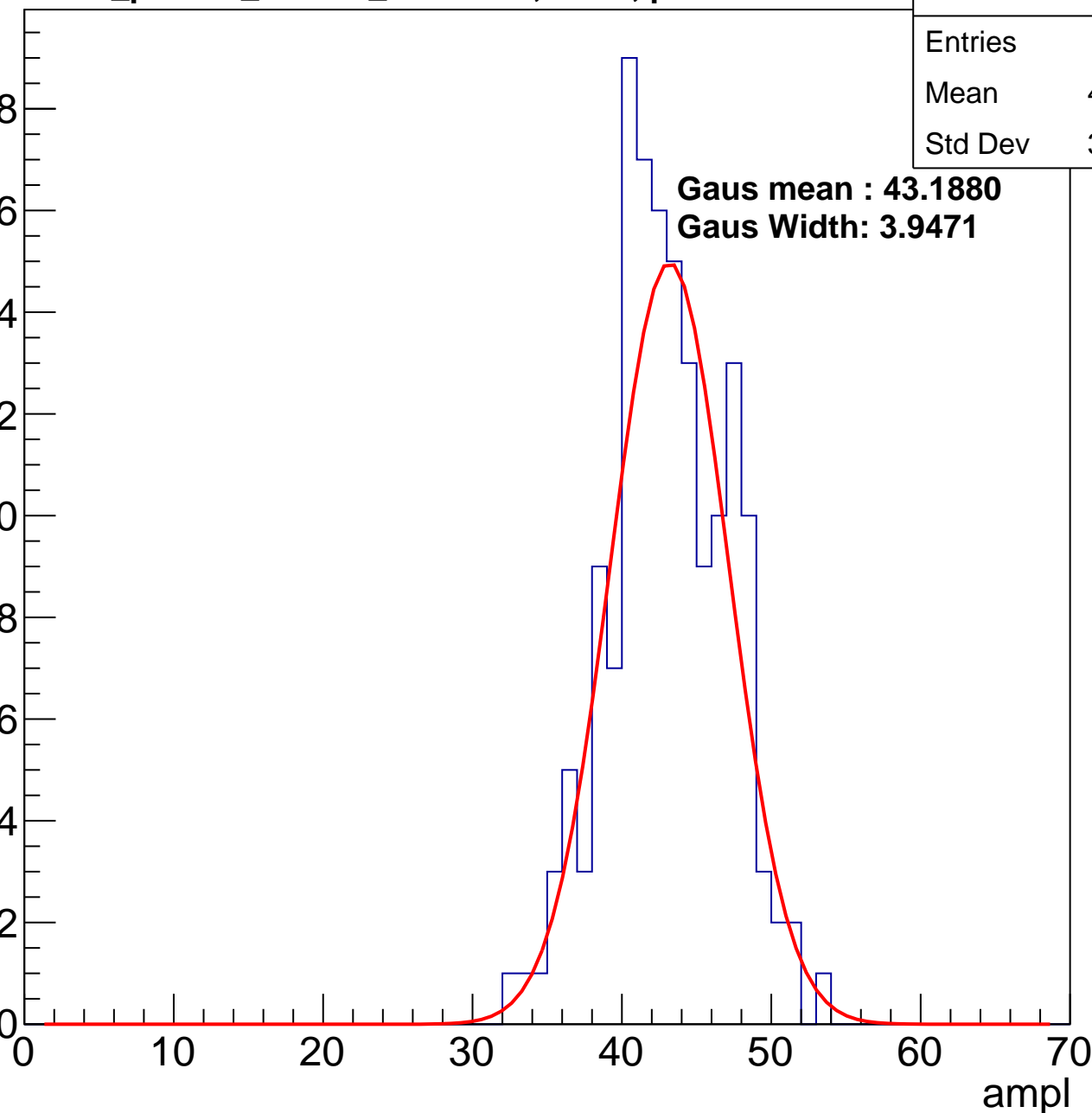
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	160
Mean	42.59
Std Dev	3.907

Gaus mean : 43.1880
Gaus Width: 3.9471

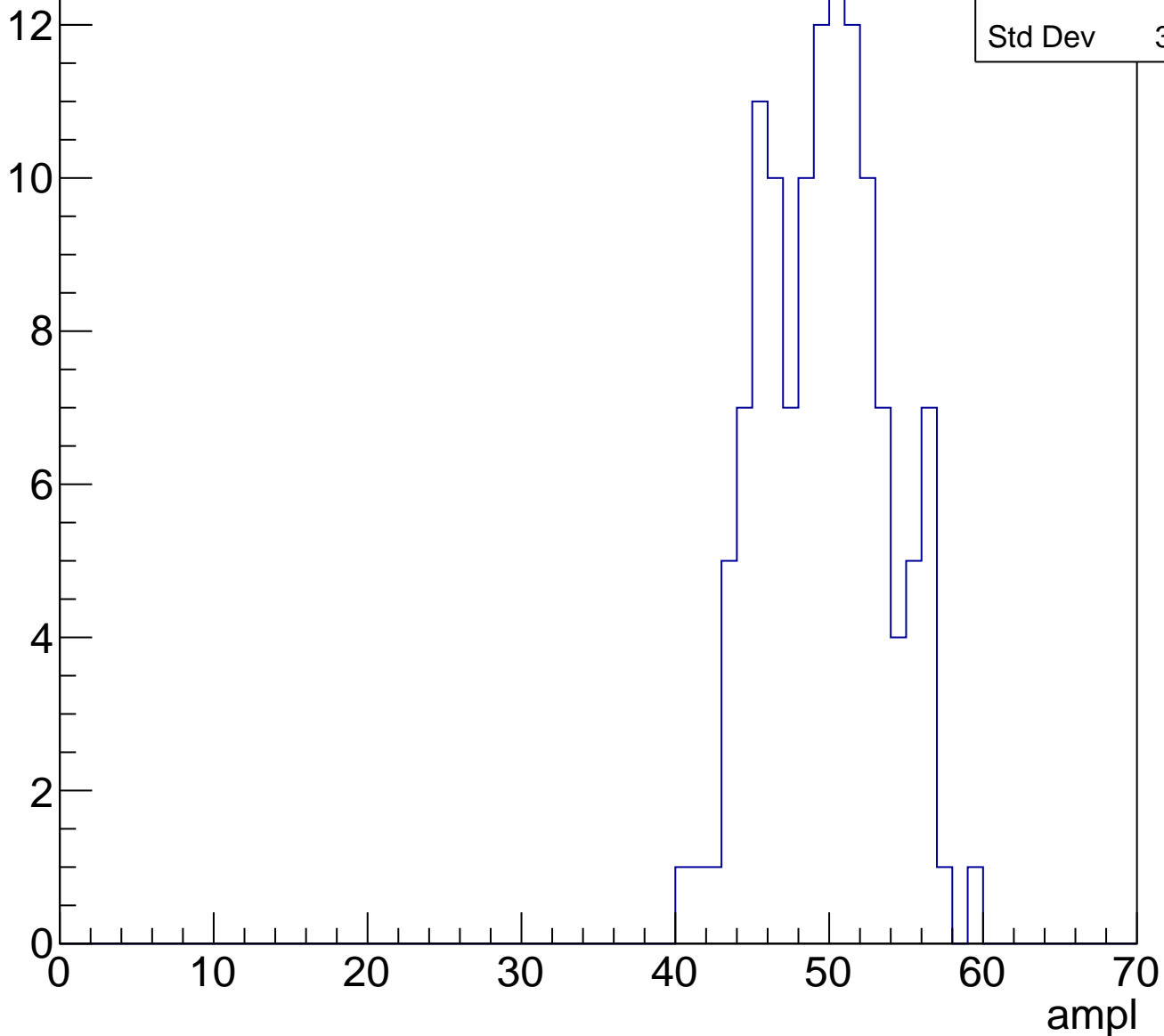


B1L001S, U16-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	49.2
Std Dev	3.897

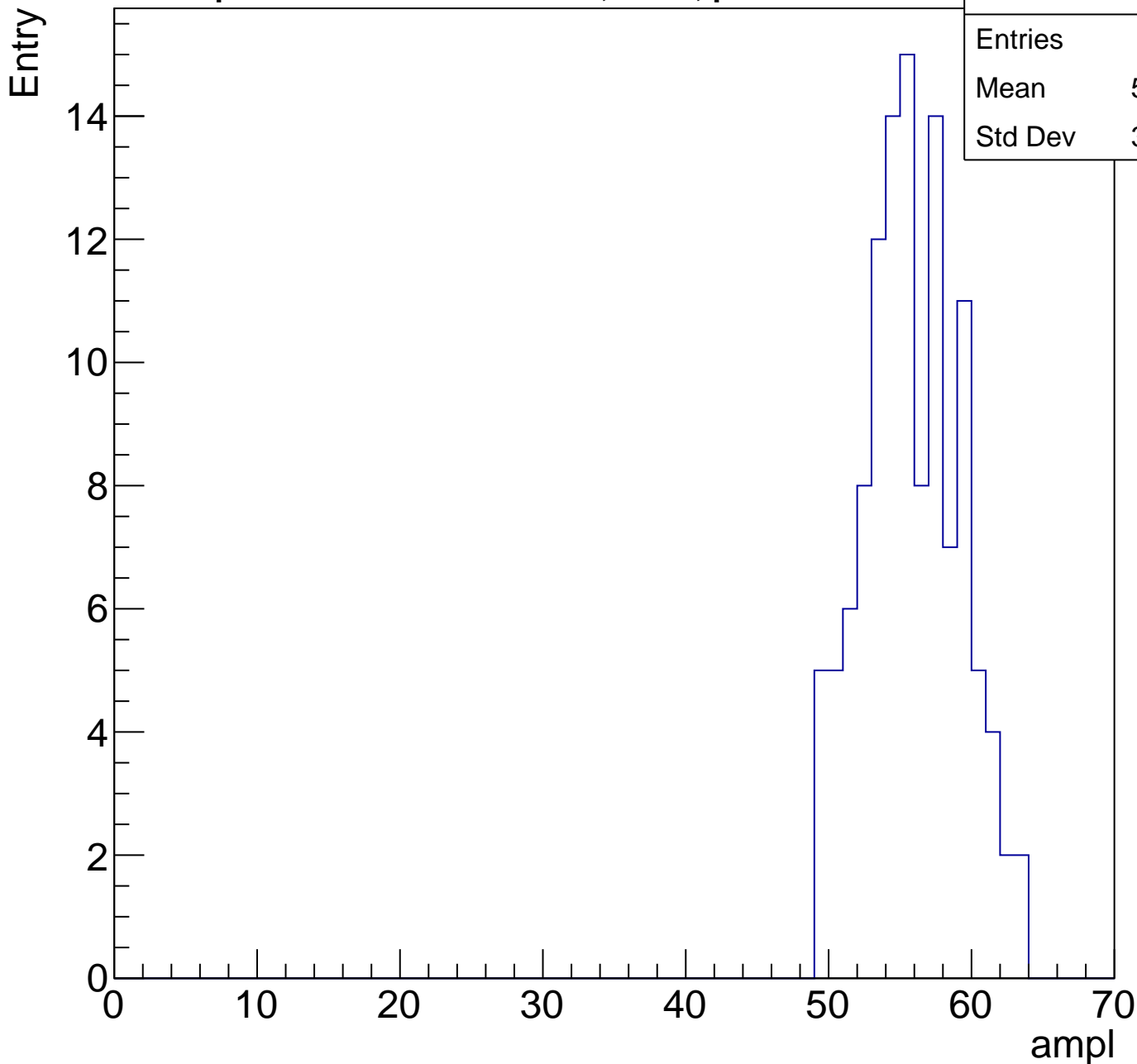
Entry



B1L001S, U16-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	55.33
Std Dev	3.345



B1L001S, U16-ch93, adc5

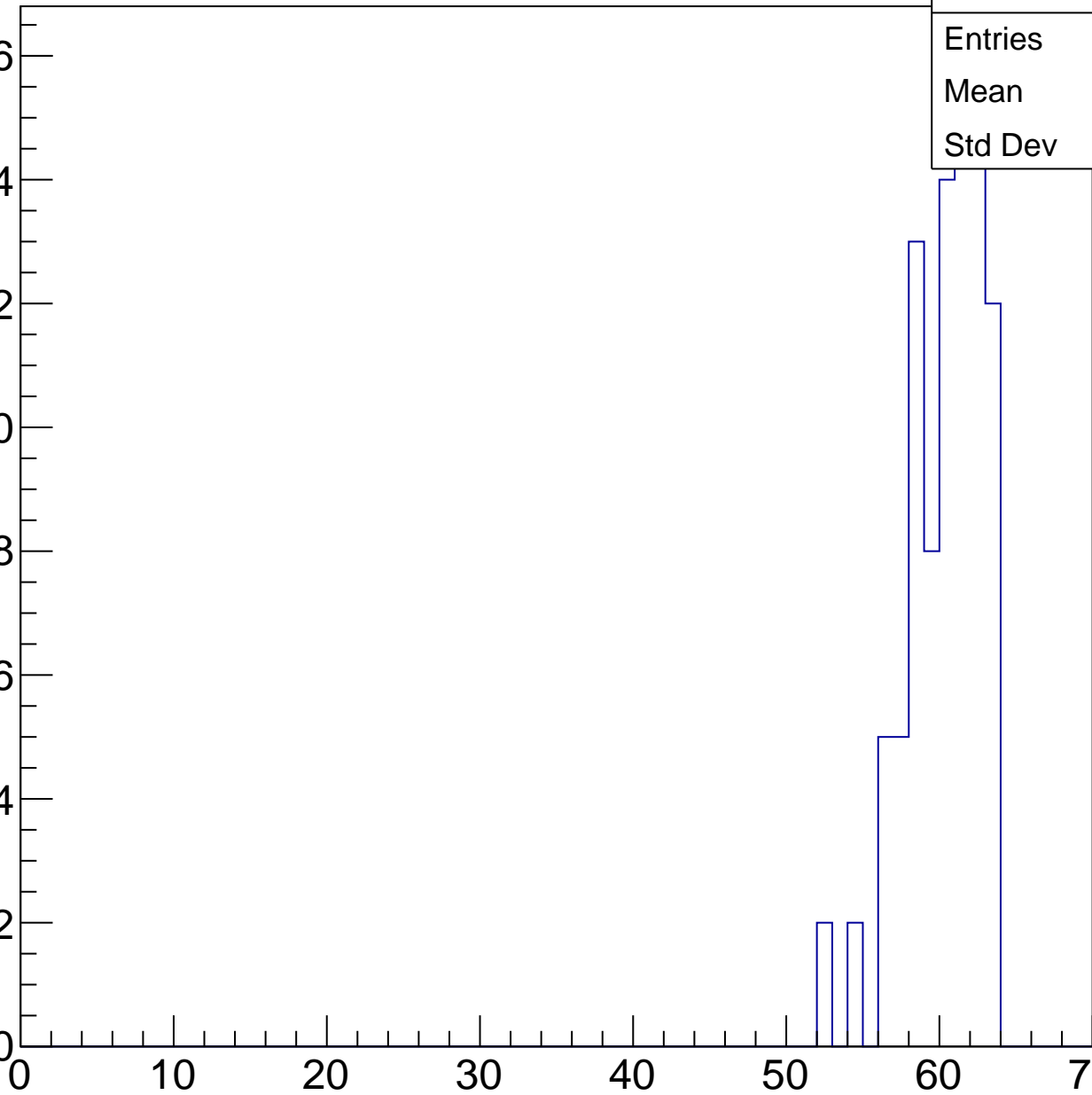
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.84
Std Dev	2.482

ampl

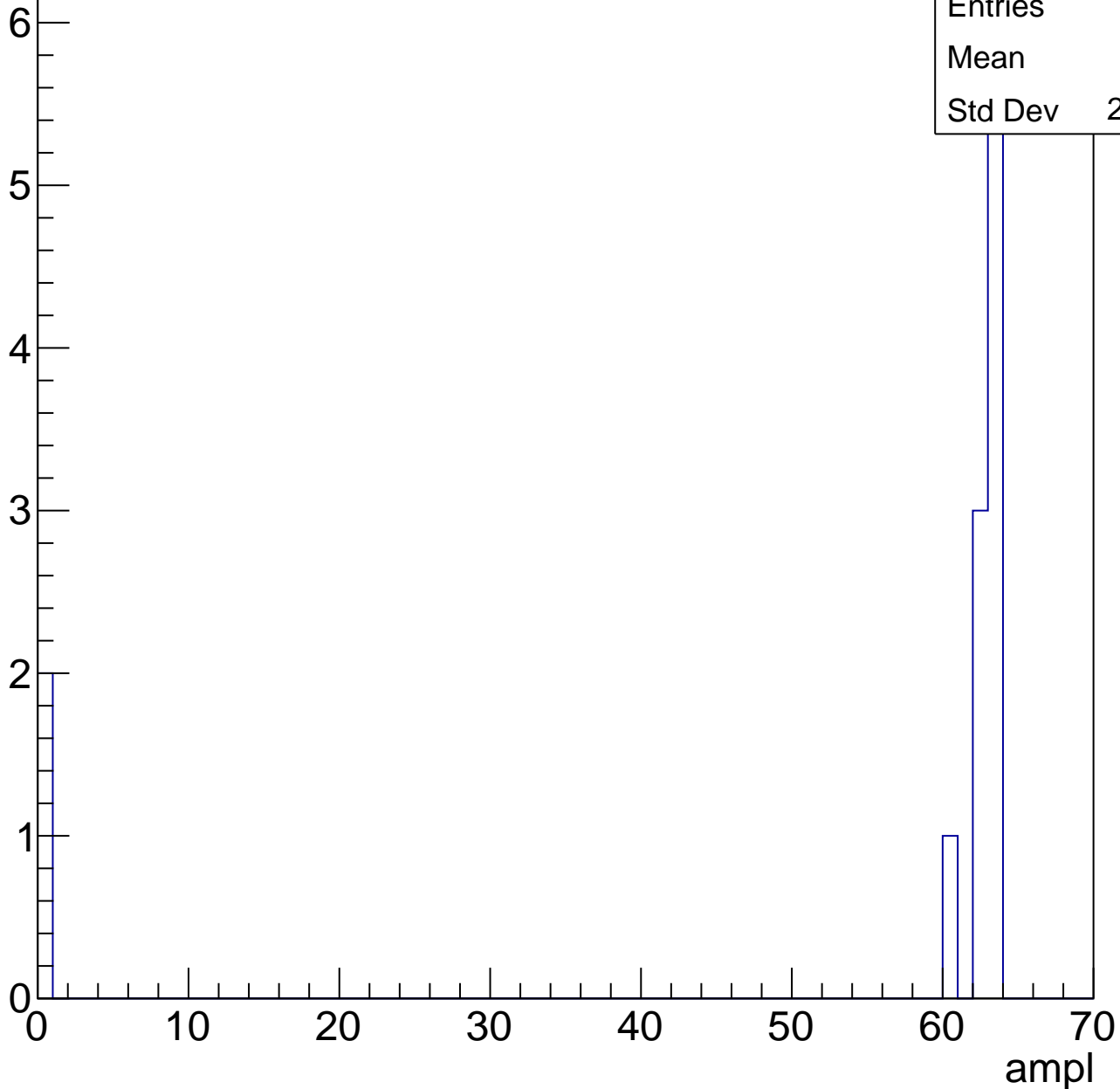


B1L001S, U16-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	52
Std Dev	23.27



B1L001S, U16-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch94, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	29
Std Dev	4.509

Gaus mean : 29.8440

Gaus Width: 3.8591

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

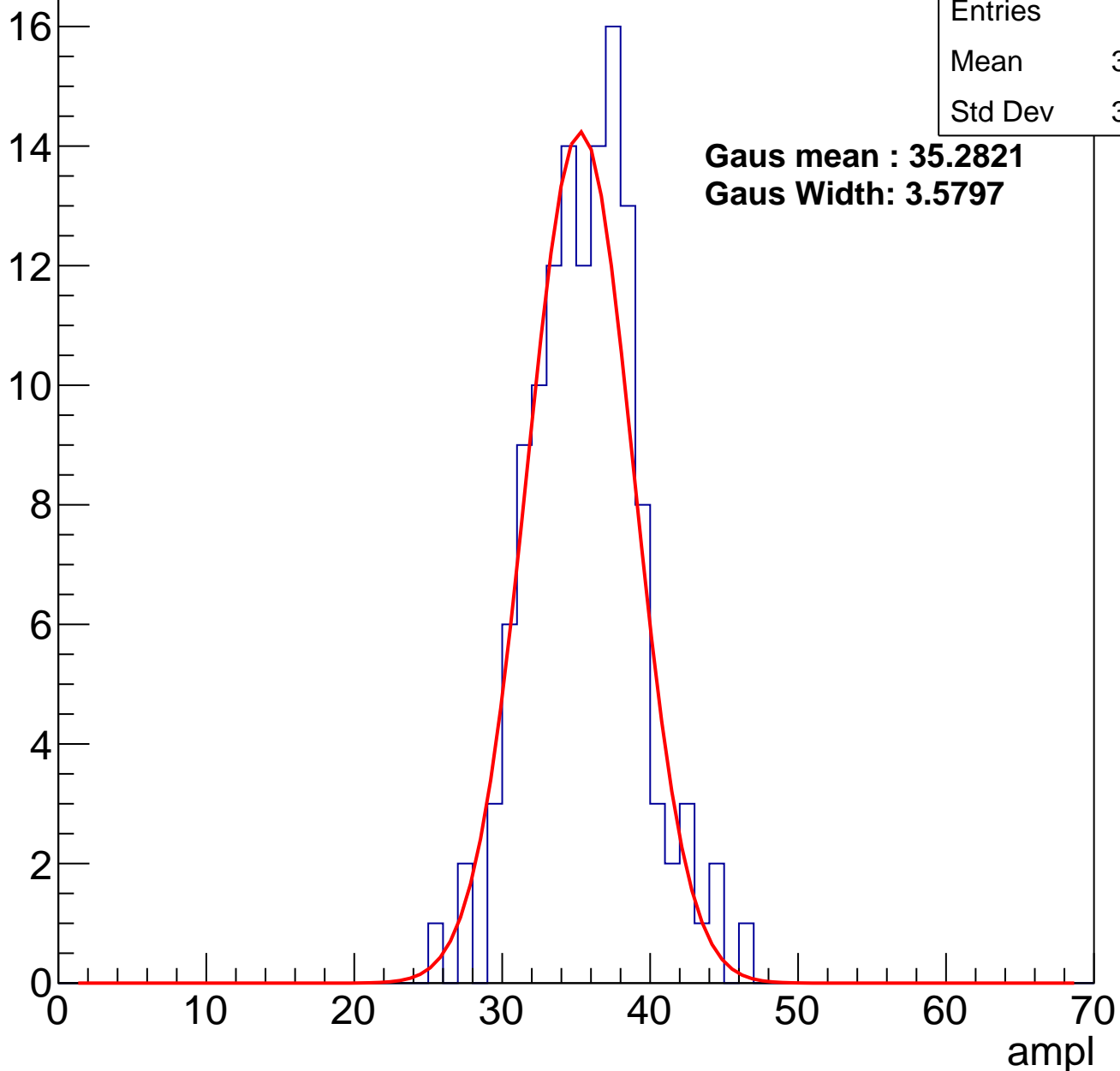
B1L001S, U16-ch94, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	35.18
Std Dev	3.632

Gaus mean : 35.2821
Gaus Width: 3.5797

Entry



B1L001S, U16-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

121

Mean

41.5

Std Dev

2.895

Gaus mean : 42.1177

Gaus Width: 2.9954

0

10

20

30

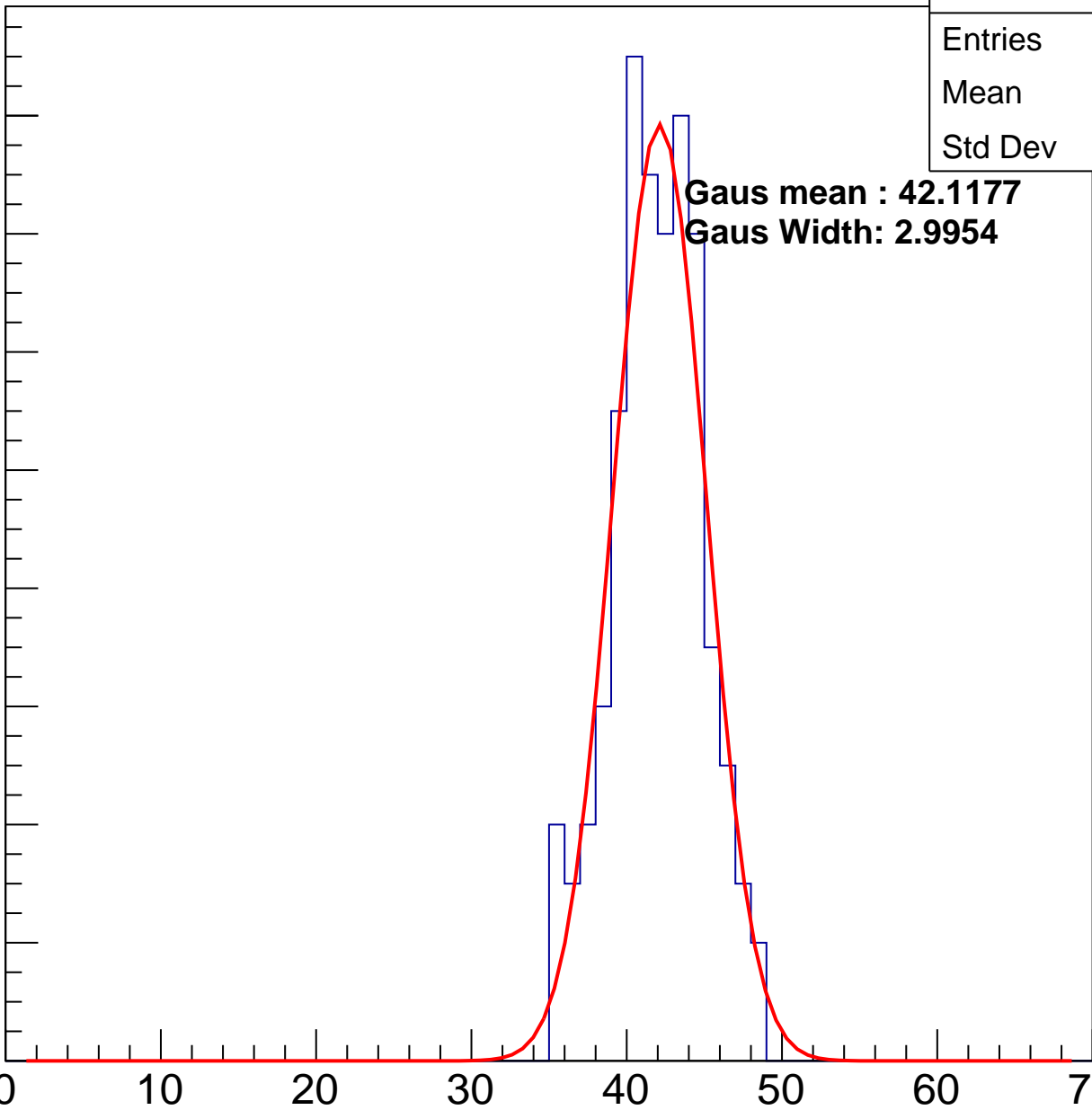
40

50

60

70

ampl



B1L001S, U16-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	48.05
Std Dev	3.148

Entry

14

12

10

8

6

4

2

0

0

10

20

30

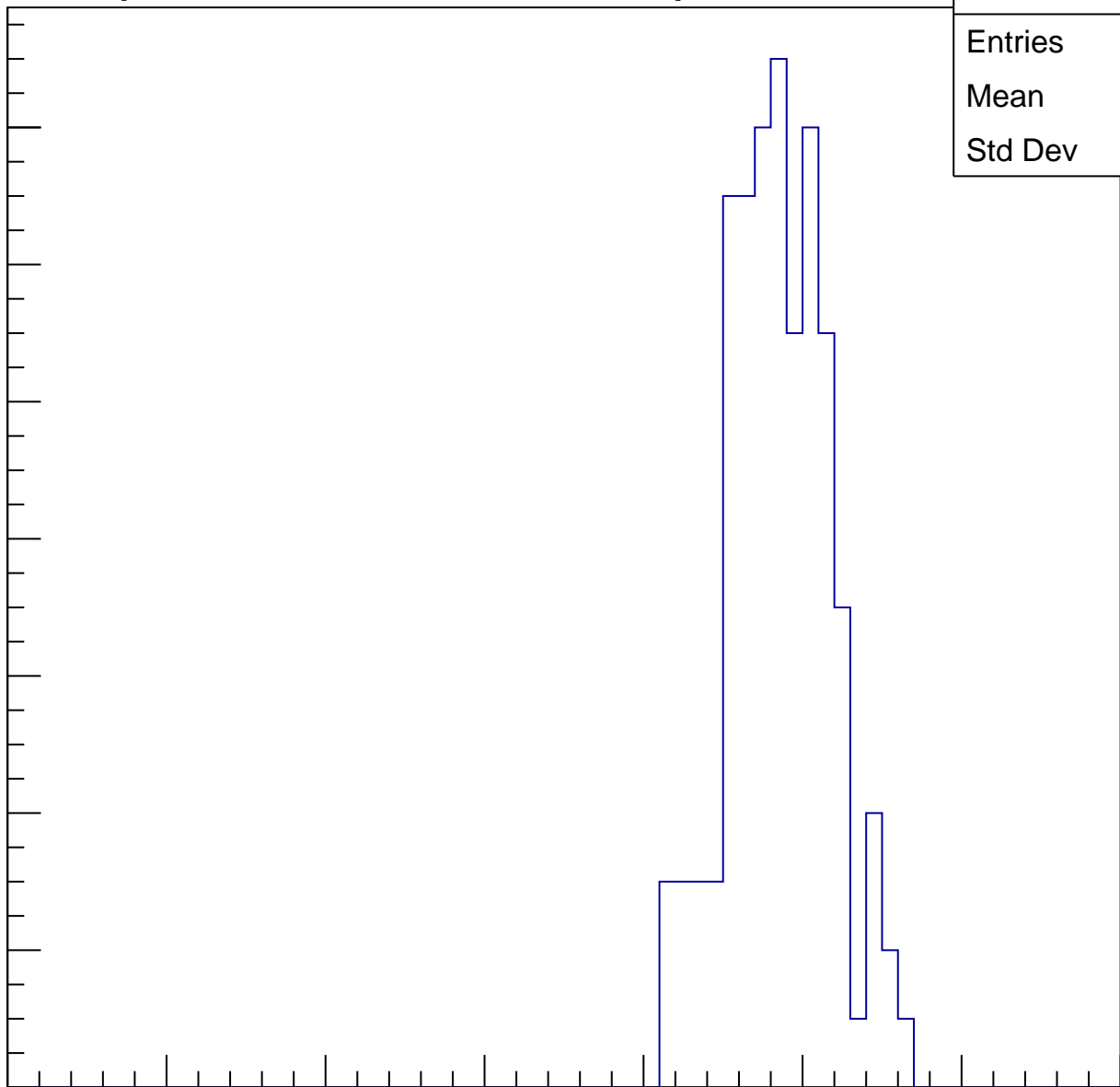
40

50

60

70

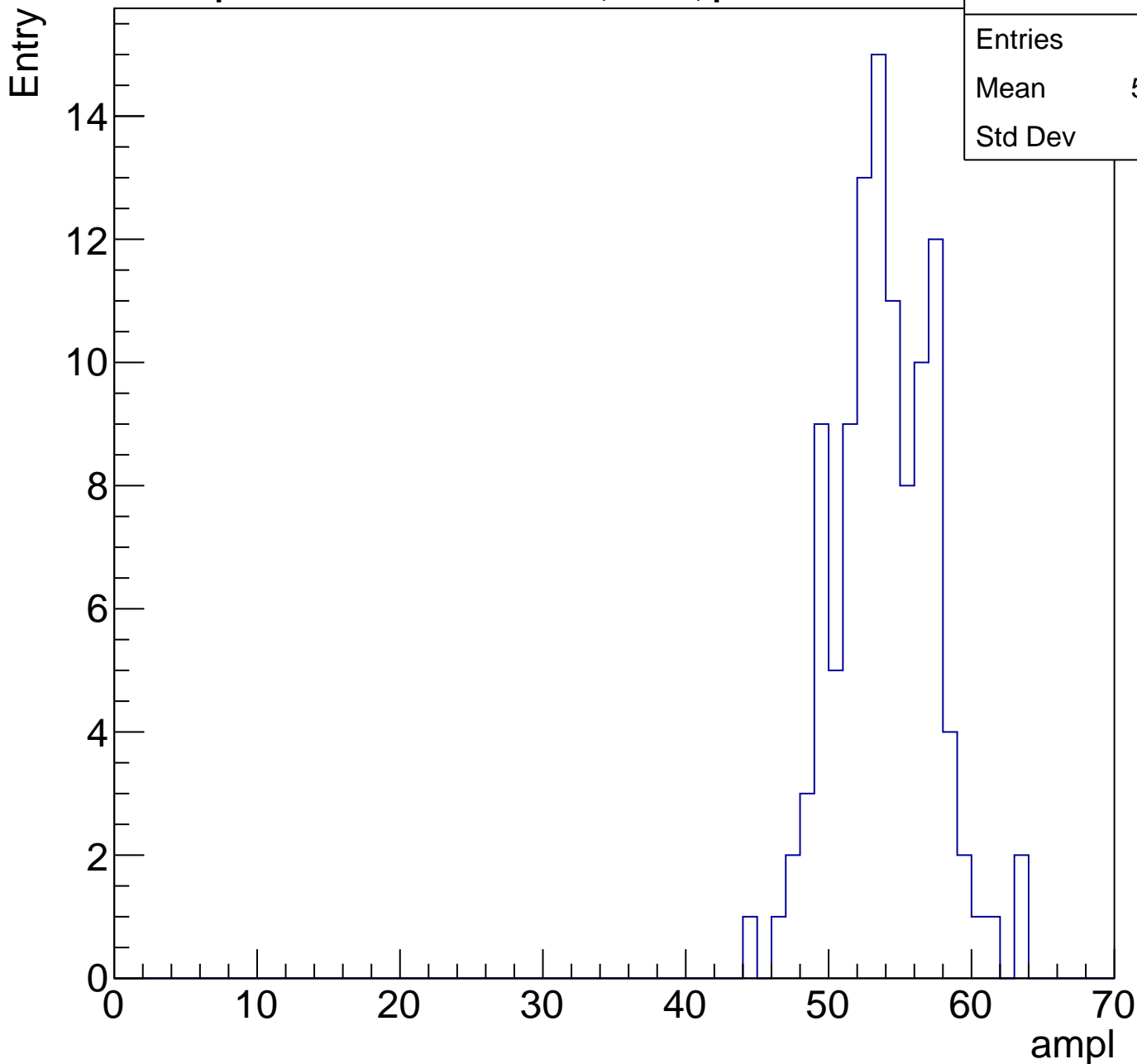
ampl



B1L001S, U16-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	53.43
Std Dev	3.46



B1L001S, U16-ch94, adc5

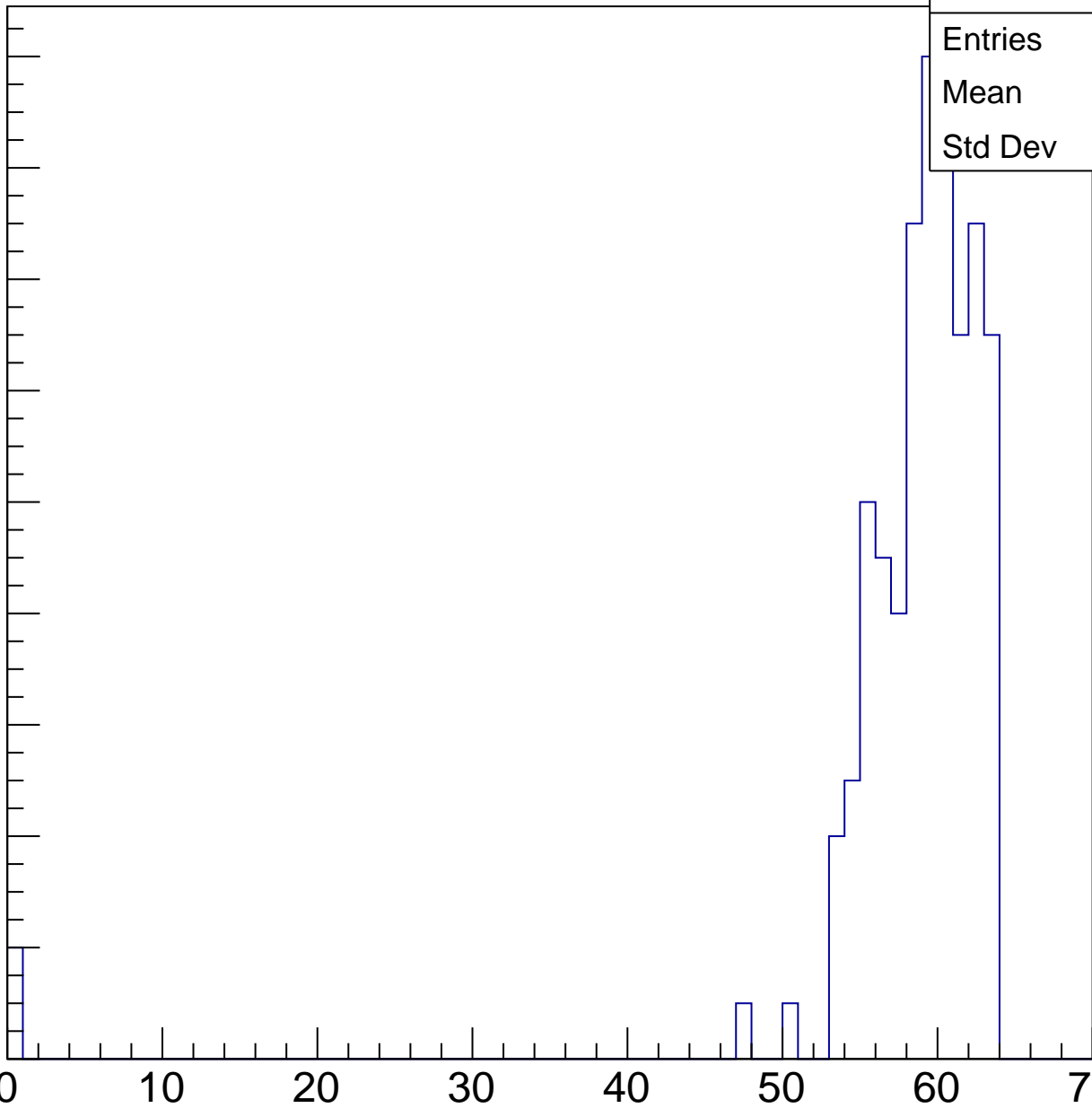
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	131
Mean	57.89
Std Dev	7.811

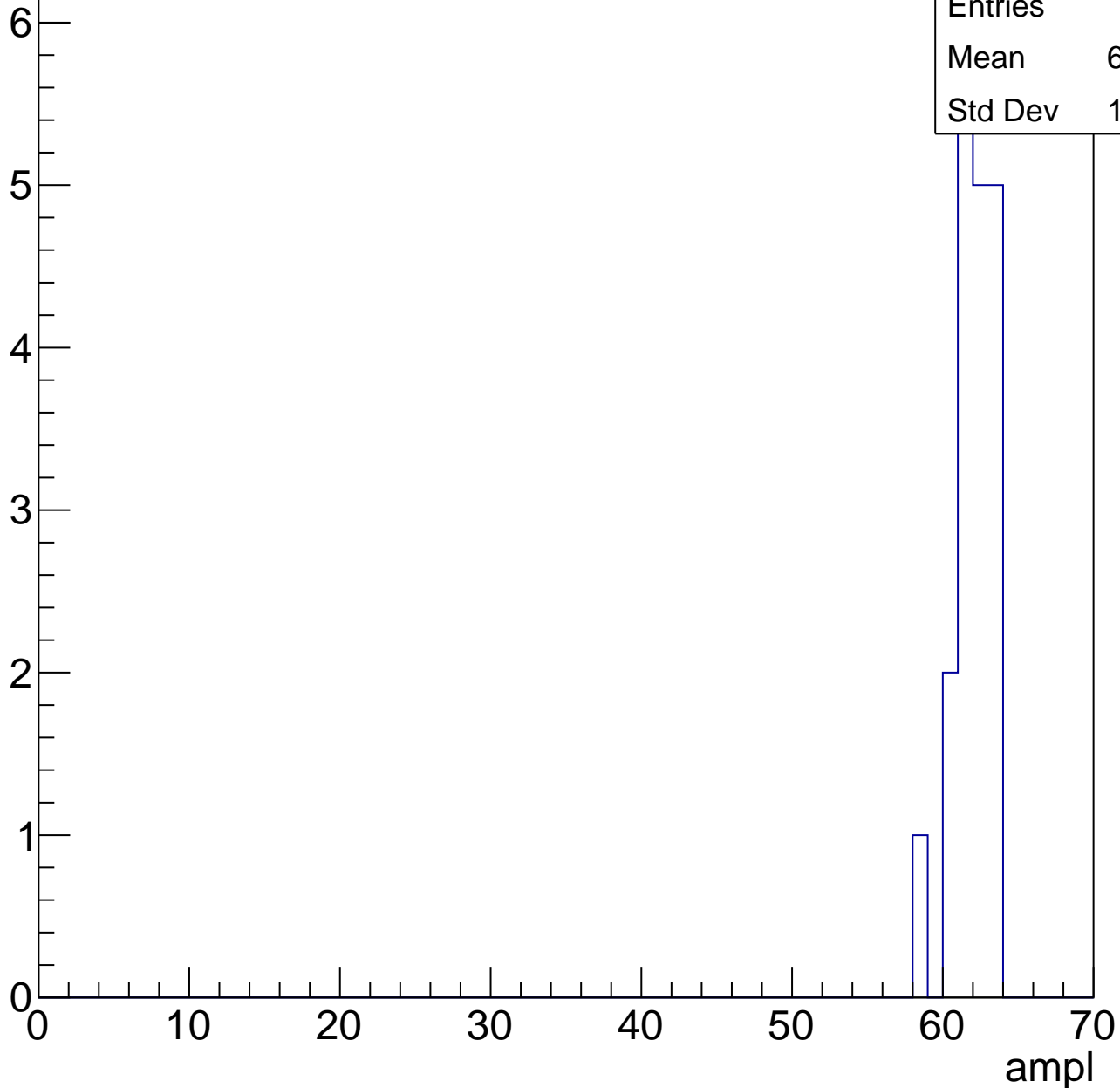
ampl



B1L001S, U16-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L001S, U16-ch95, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	29.08
Std Dev	4.18

Gaus mean : 29.8852

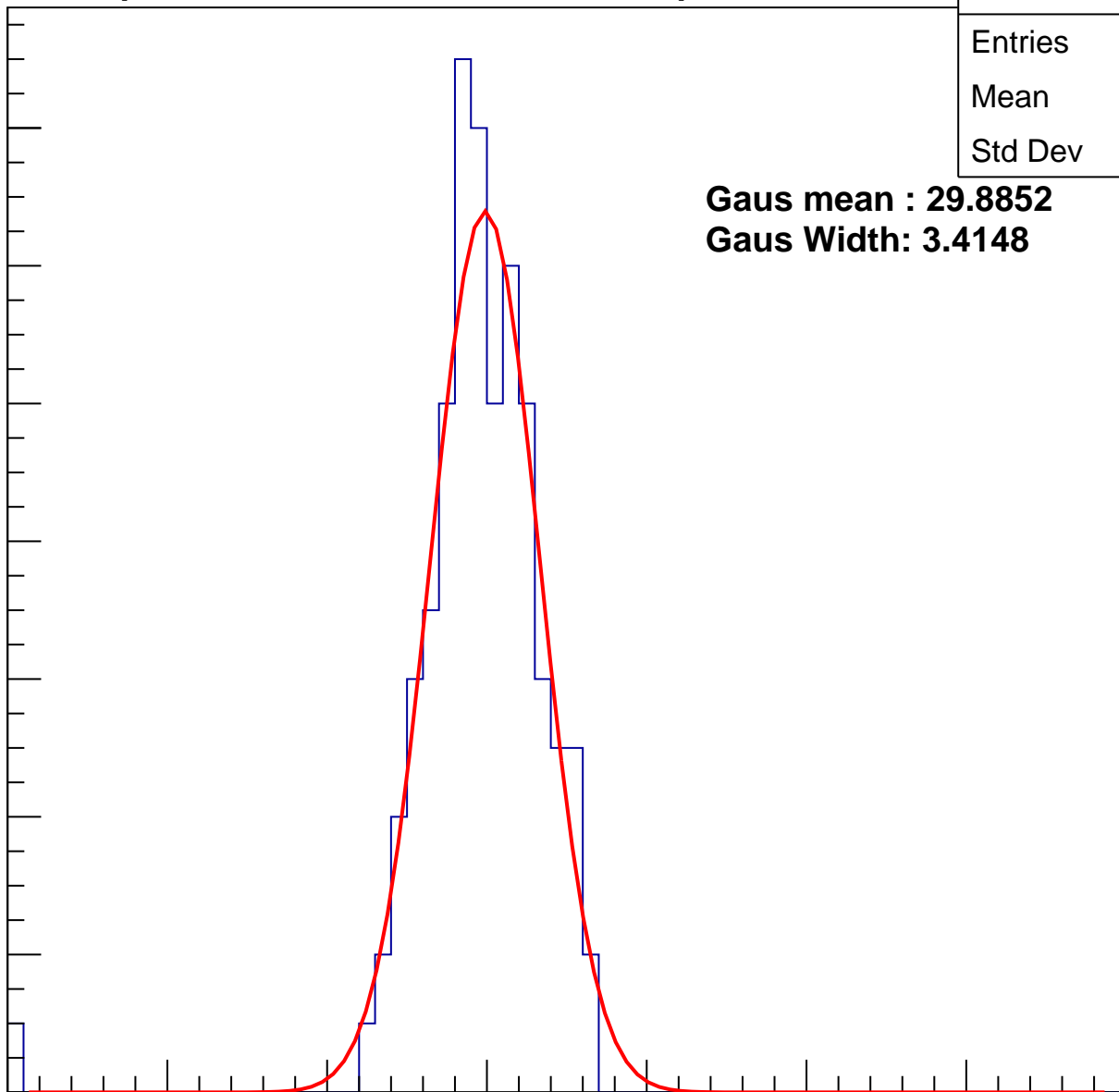
Gaus Width: 3.4148

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

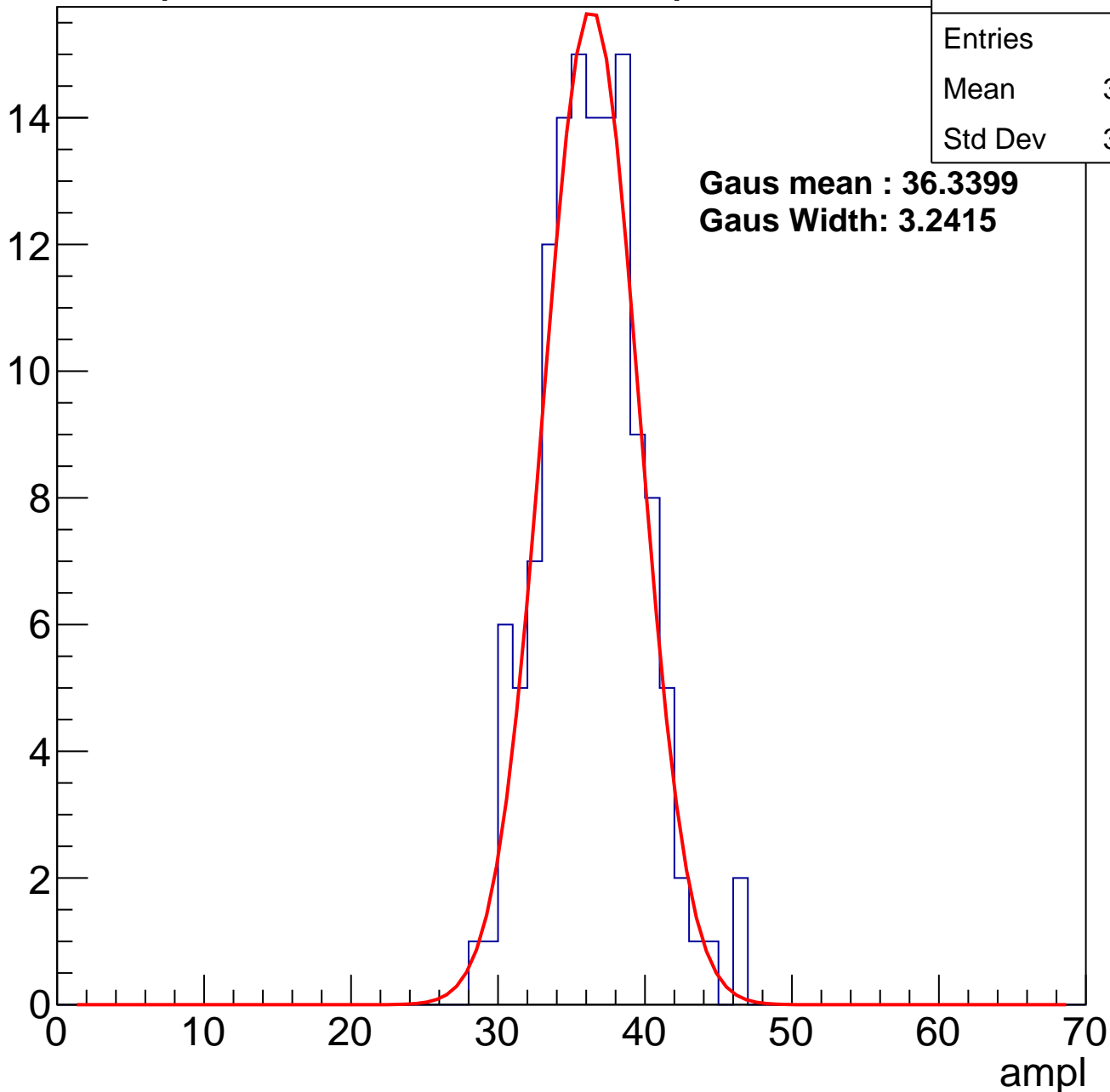
ampl



B1L001S, U16-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

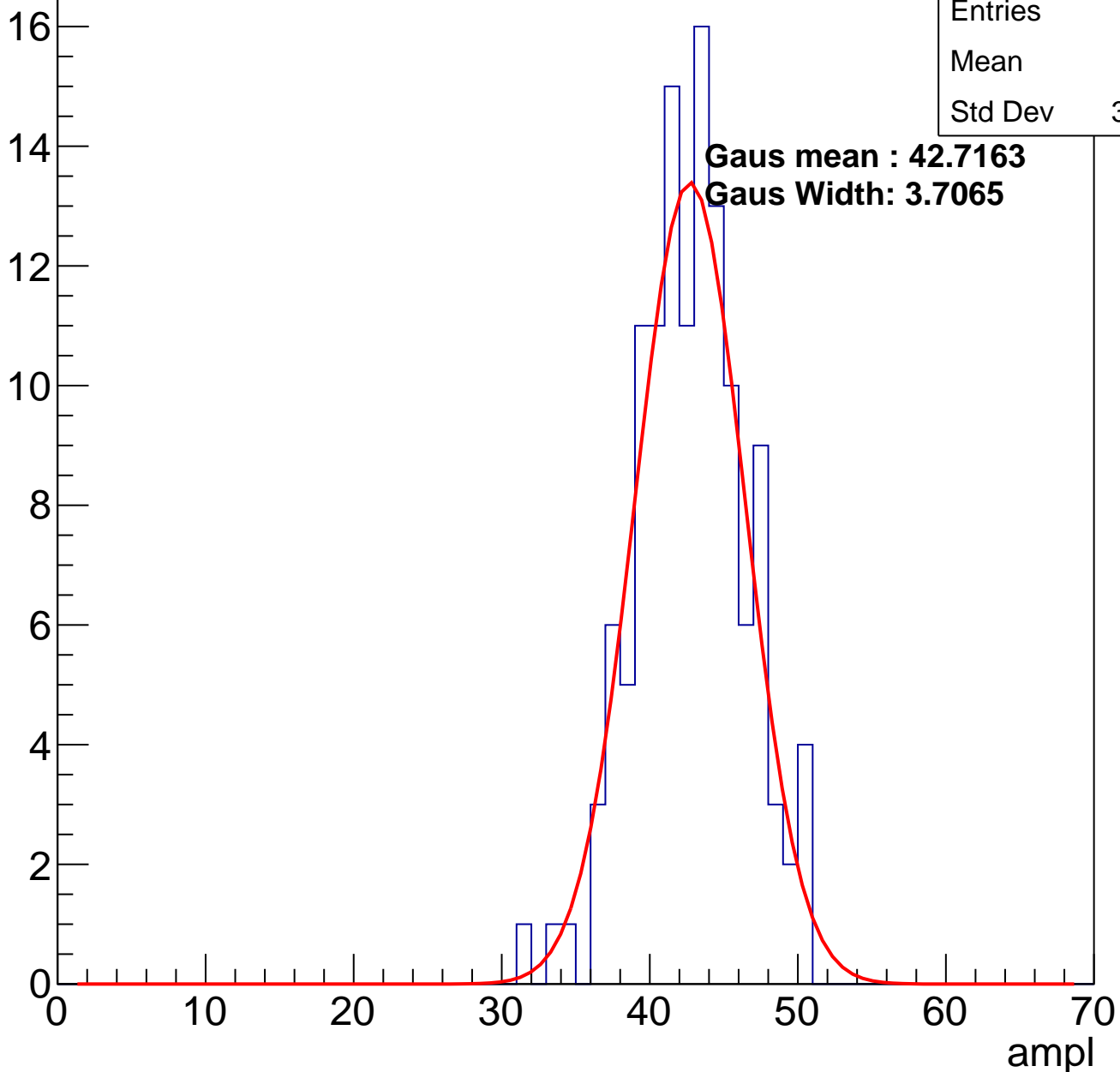
Entry



B1L001S, U16-ch95, adc2

calib_packv5_042523_0143.root, FC#2, port C2

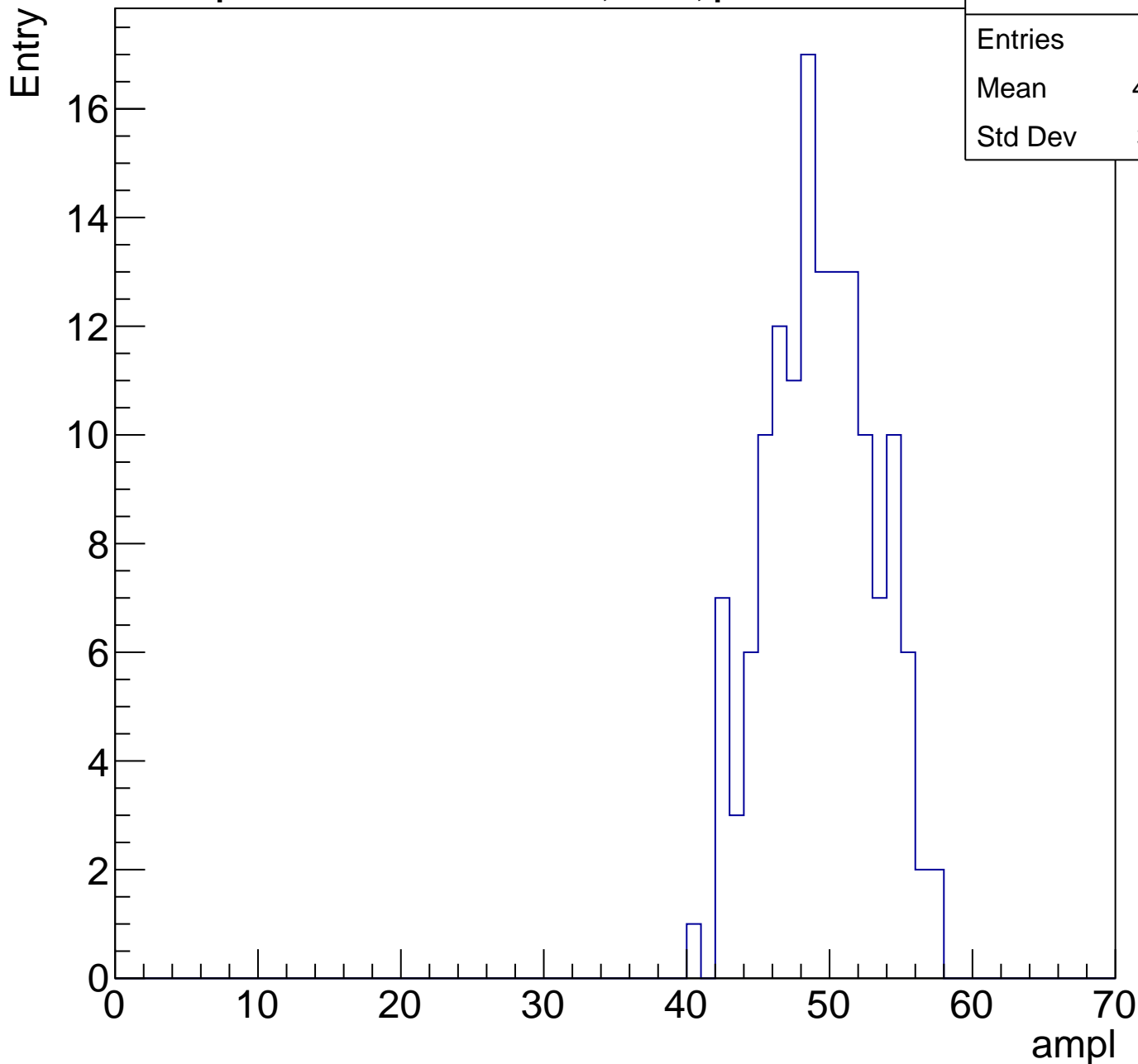
Entry



B1L001S, U16-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	48.94
Std Dev	3.711

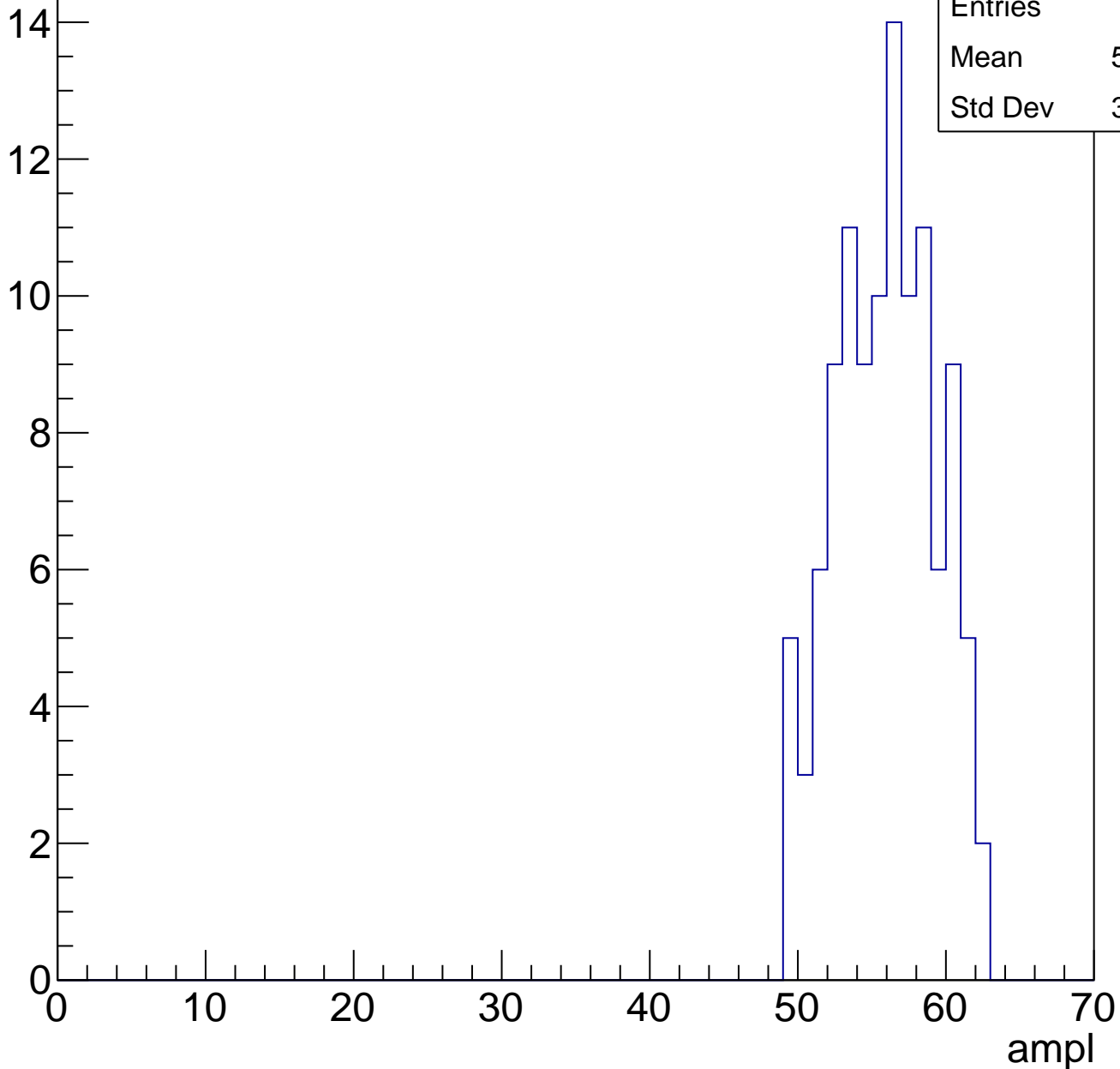


B1L001S, U16-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	55.48
Std Dev	3.319

Entry



B1L001S, U16-ch95, adc5

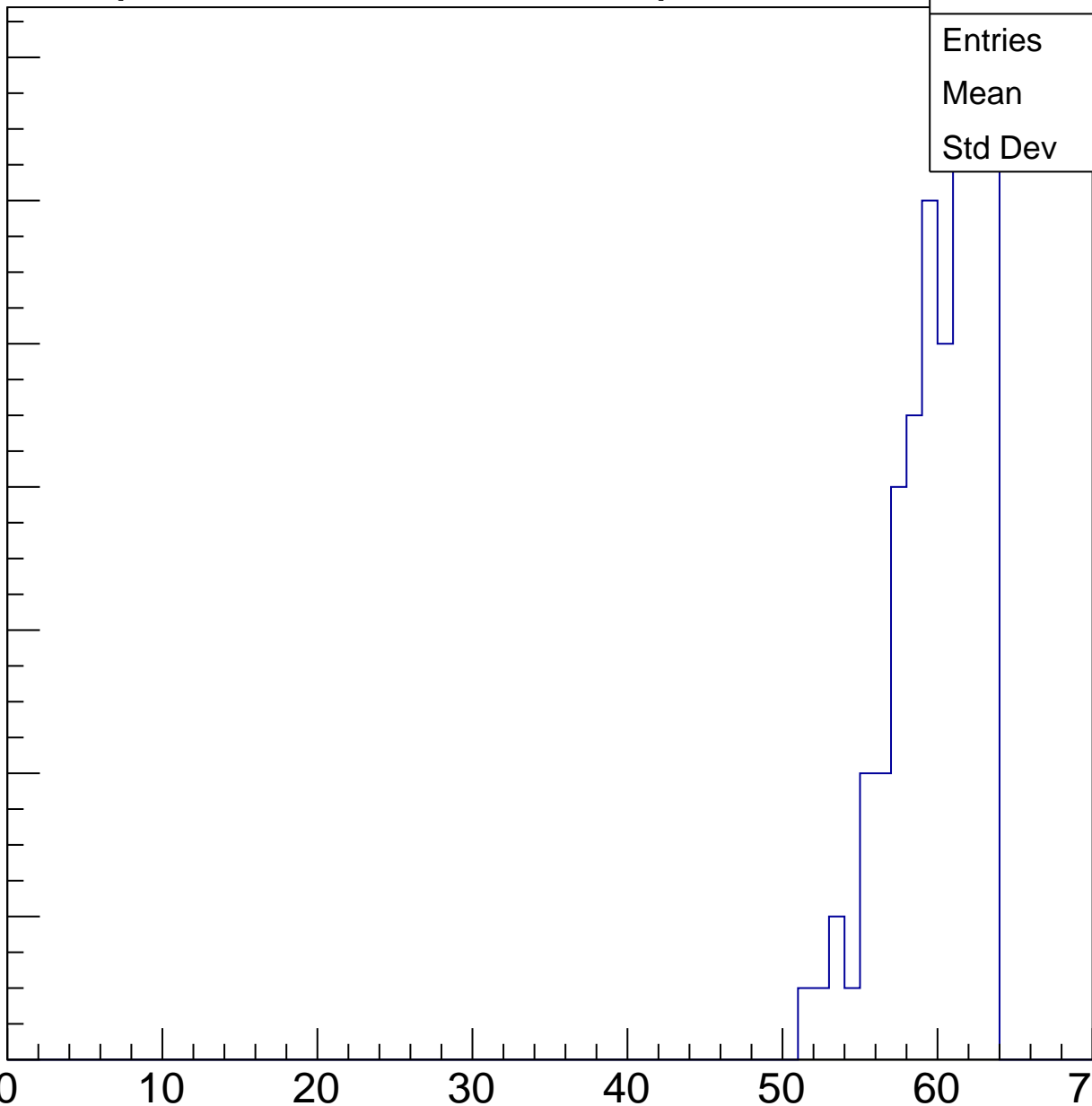
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	59.52
Std Dev	2.808

ampl

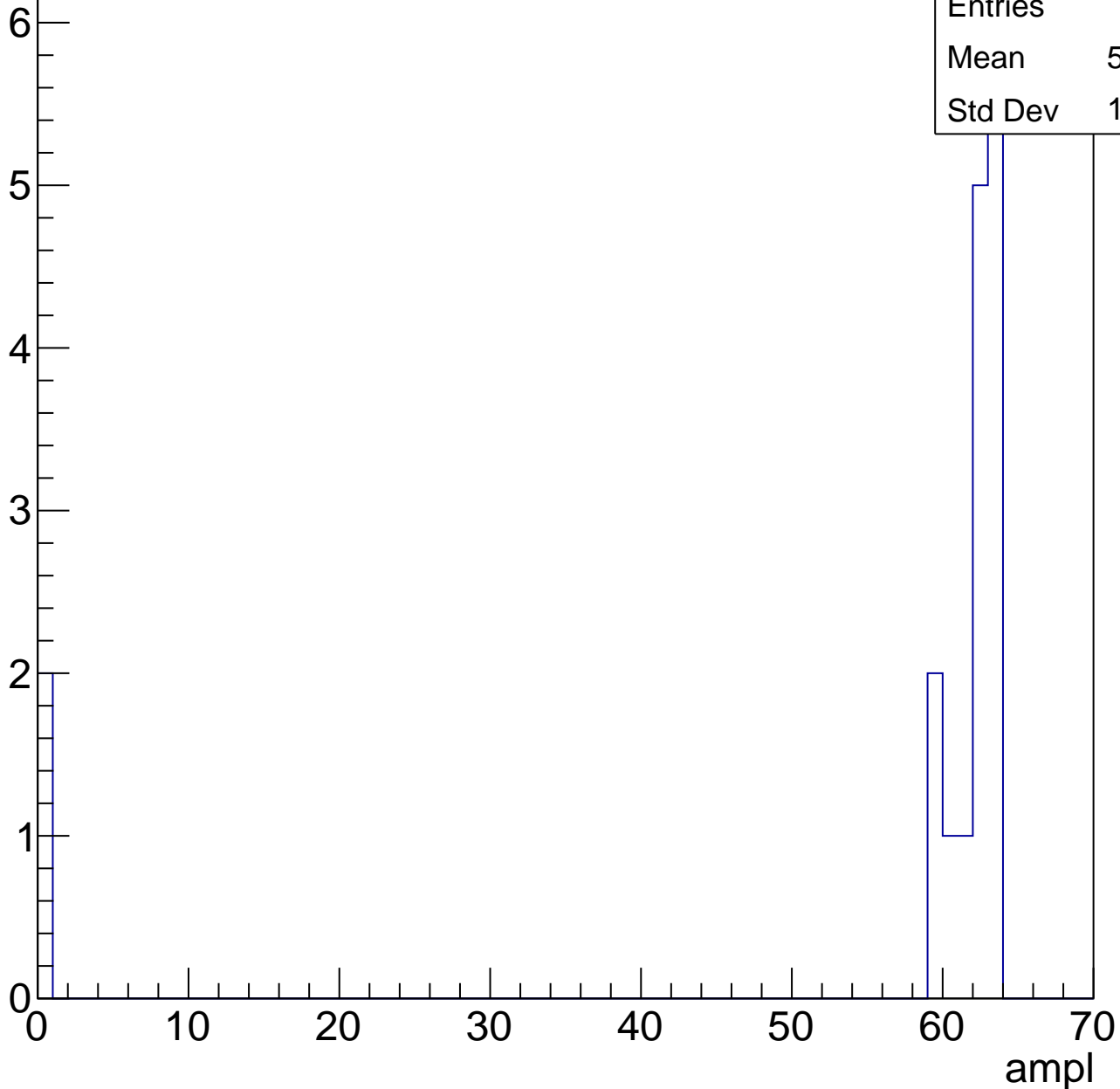


B1L001S, U16-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	54.53
Std Dev	19.95



B1L001S, U16-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

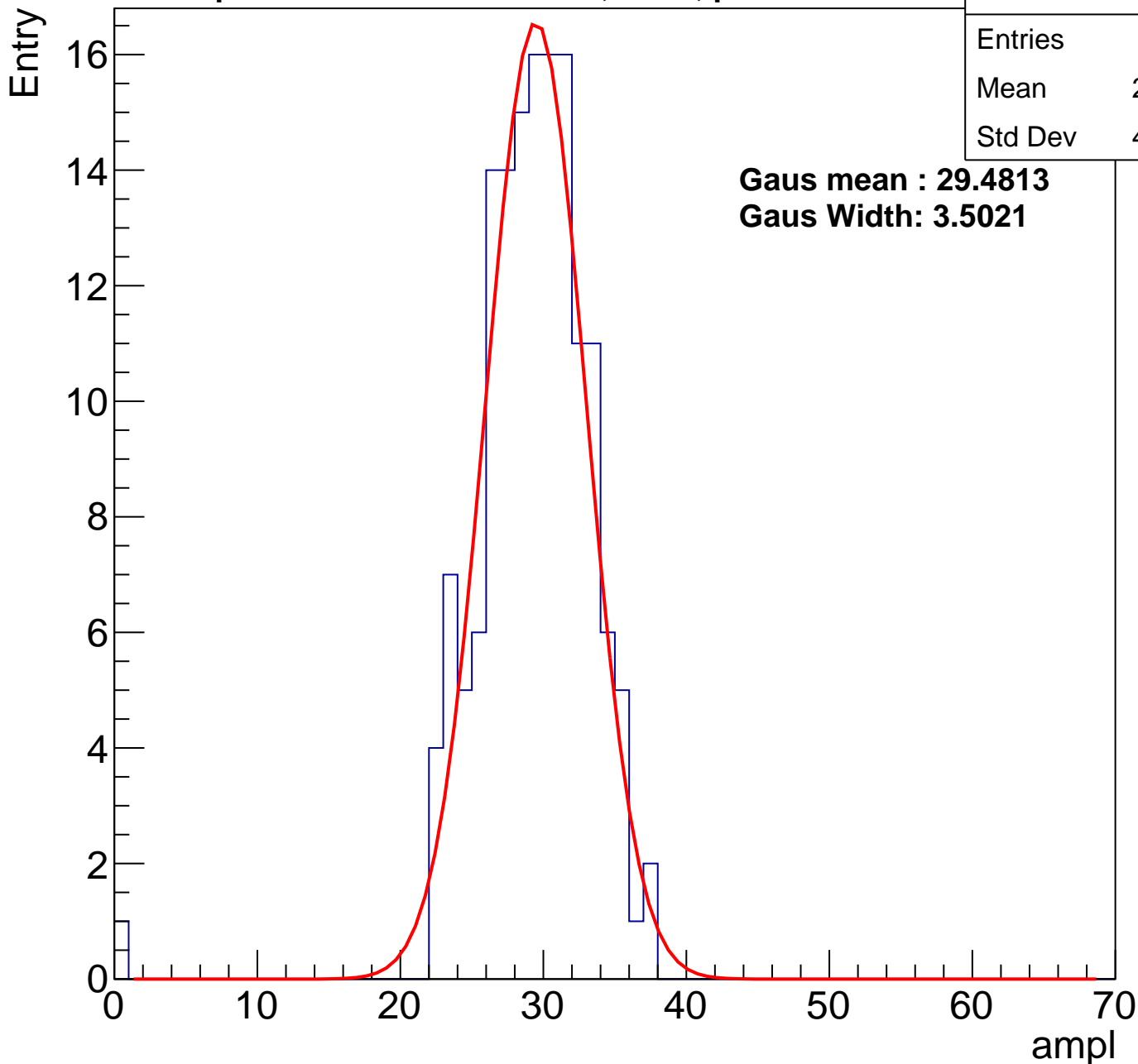
B1L001S, U16-ch96, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	28.83
Std Dev	4.135

Gaus mean : 29.4813

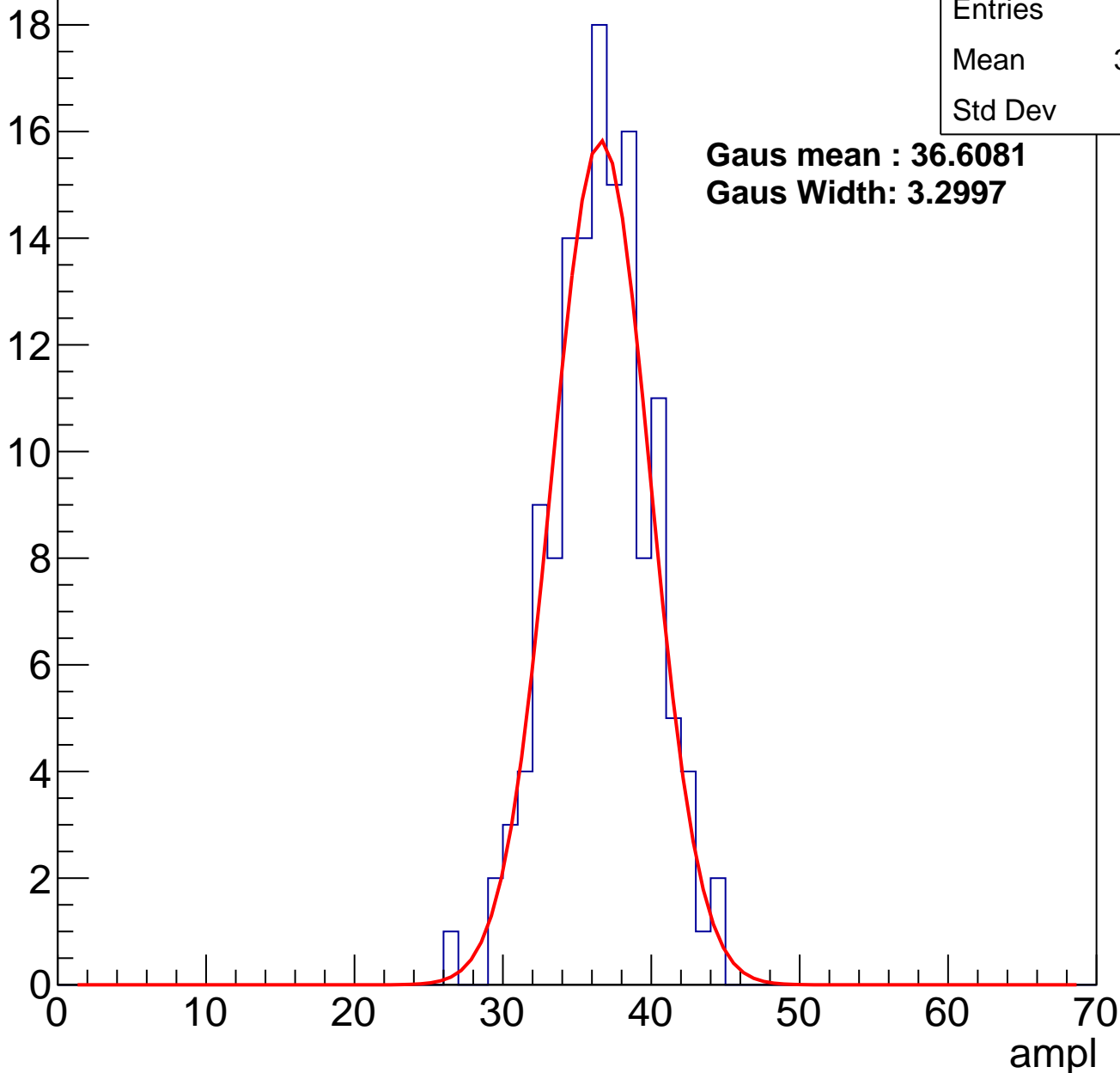
Gaus Width: 3.5021



B1L001S, U16-ch96, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

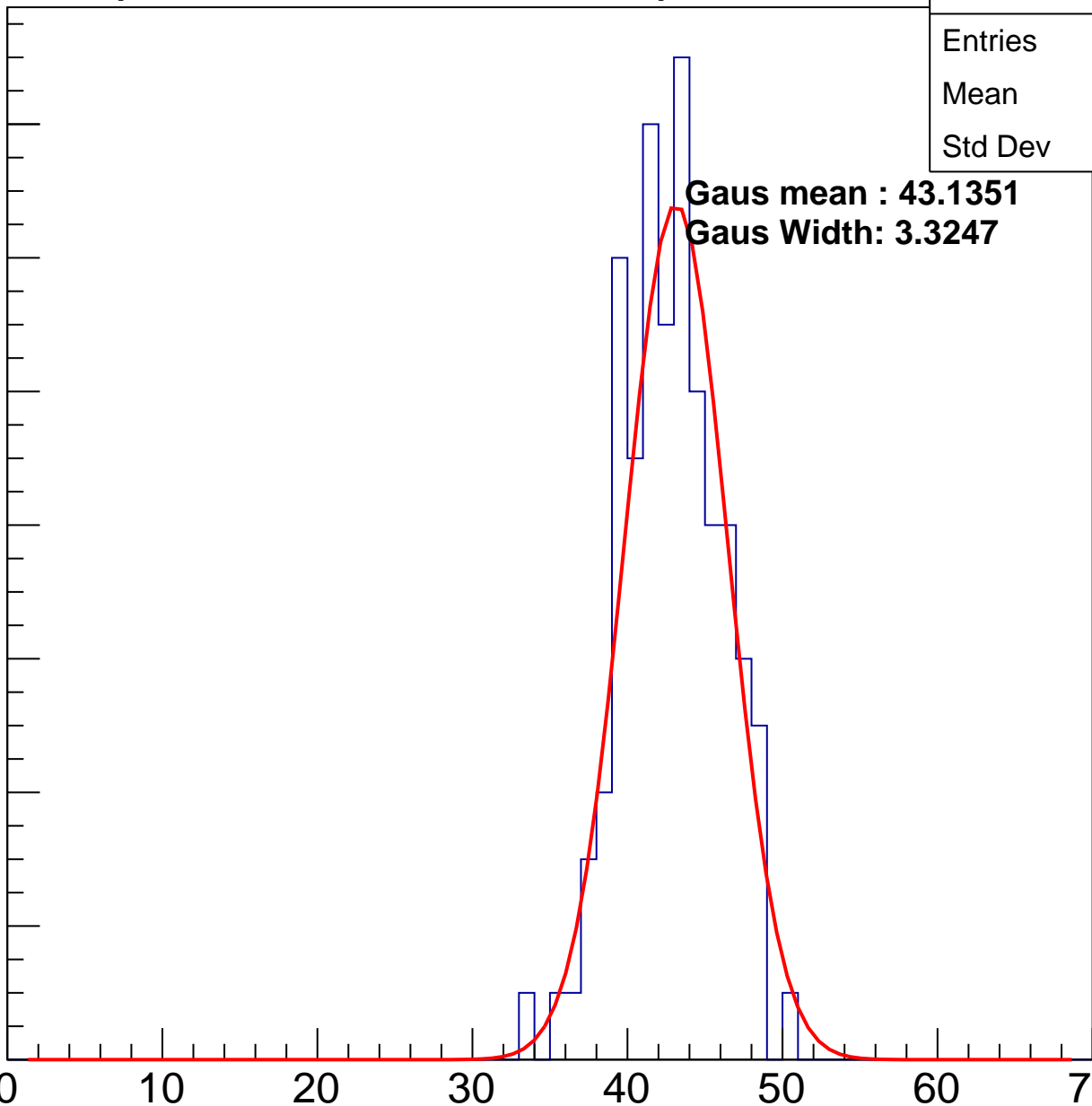
14
12
10
8
6
4
2
0

Entries	109
Mean	42.35
Std Dev	3.175

Gaus mean : 43.1351

Gaus Width: 3.3247

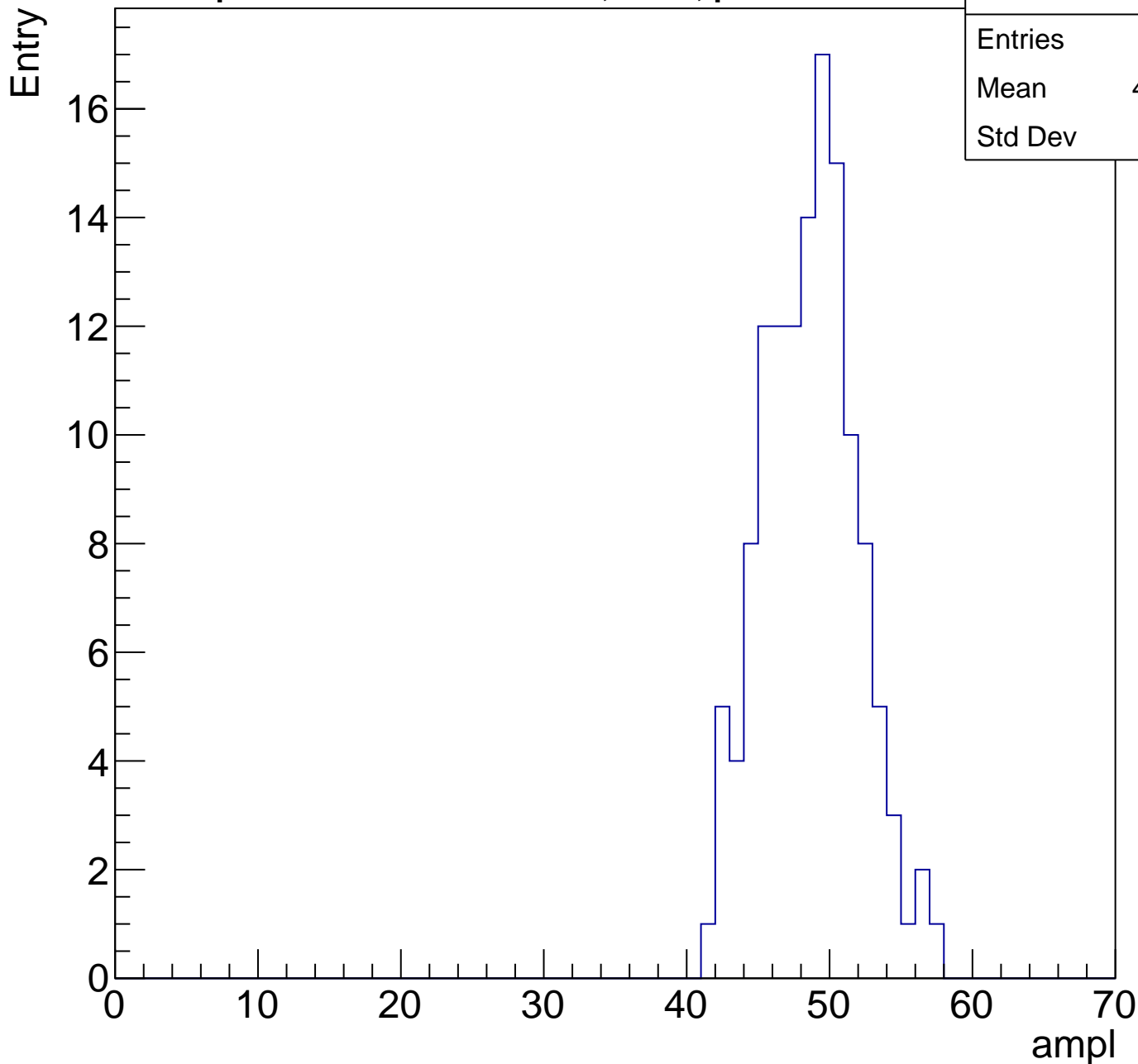
0 10 20 30 40 50 60 70
ampl



B1L001S, U16-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

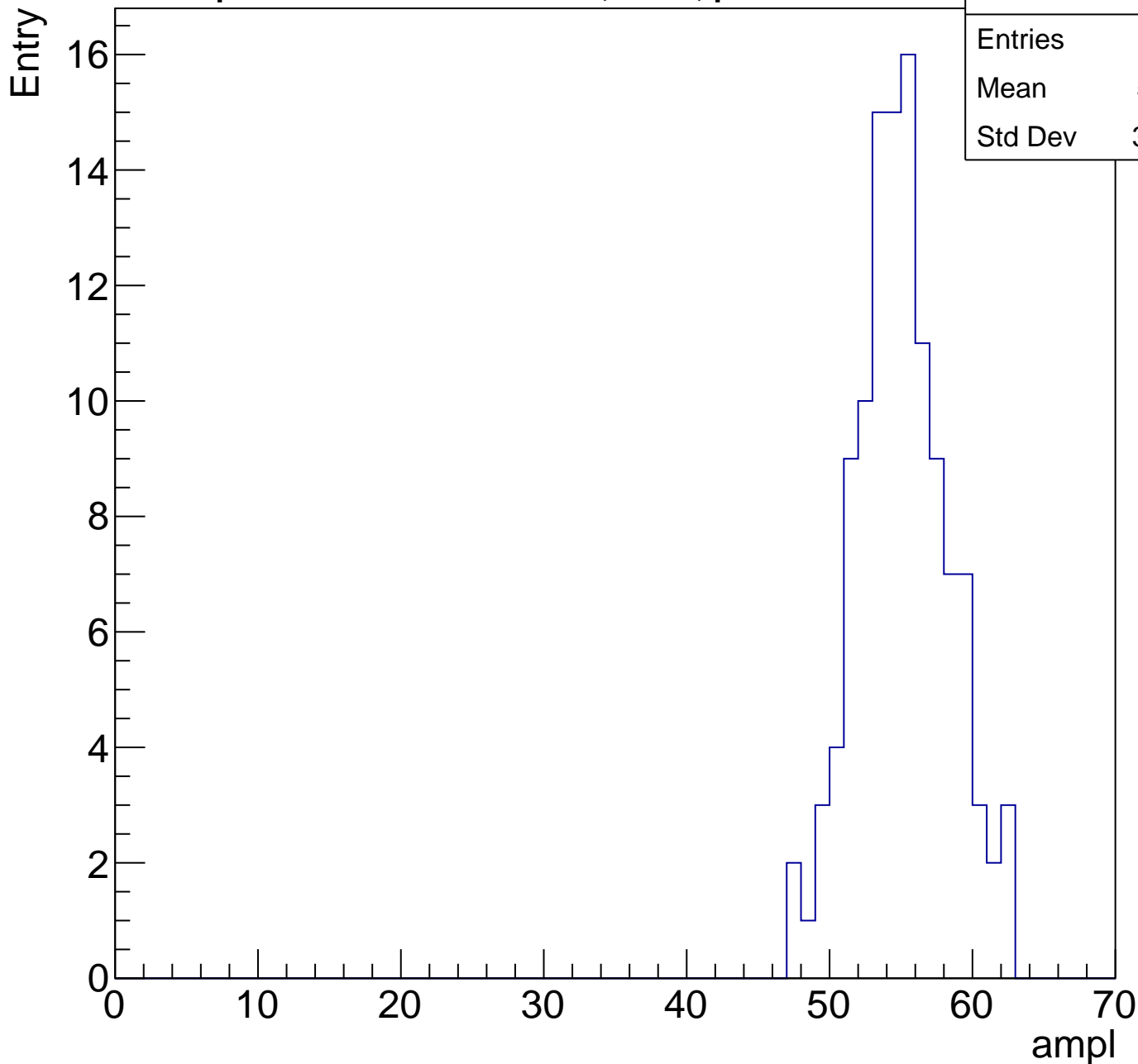
Entries	130
Mean	48.18
Std Dev	3.29



B1L001S, U16-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	54.61
Std Dev	3.192



B1L001S, U16-ch96, adc5

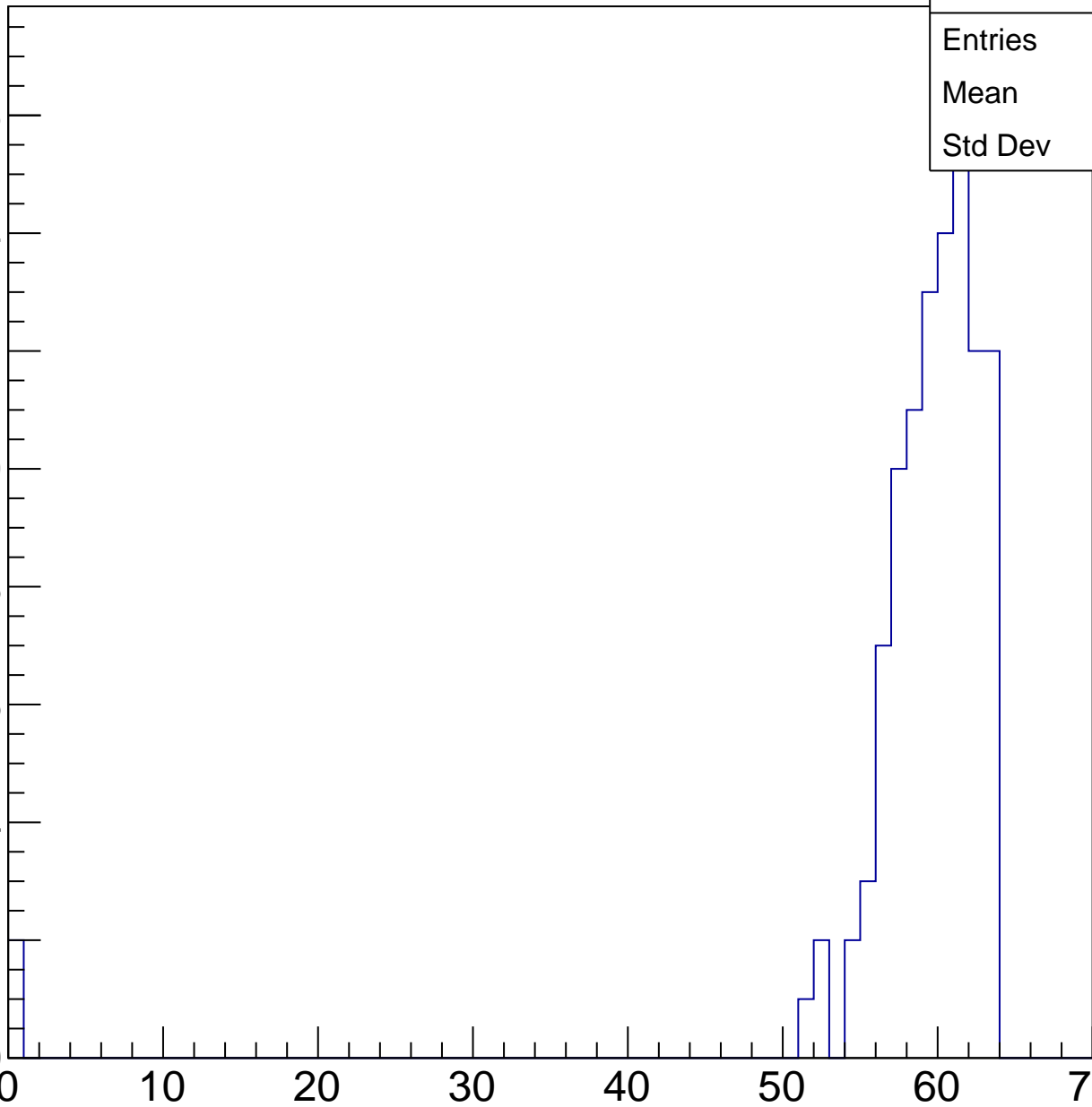
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	58.23
Std Dev	8.498

ampl

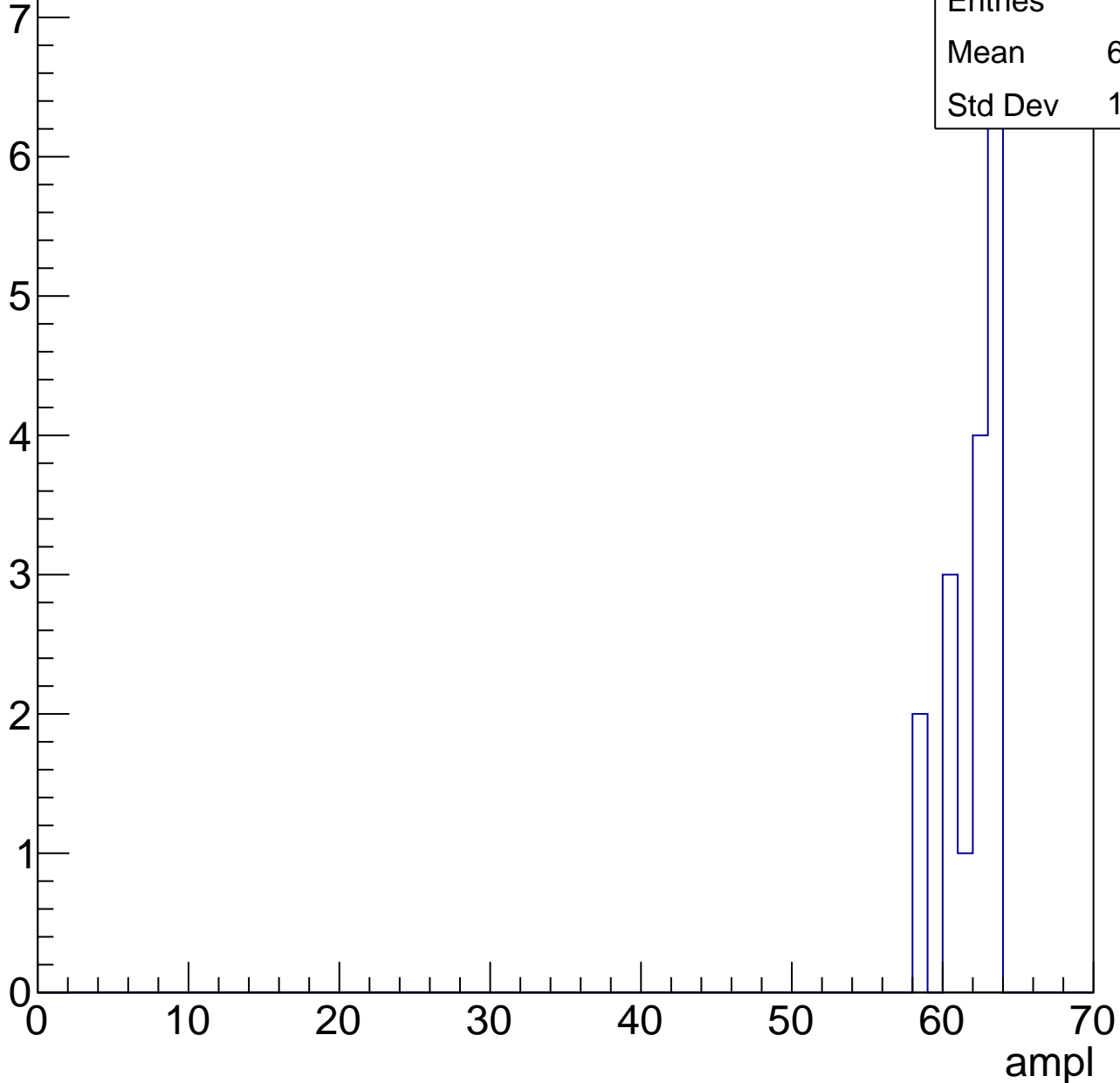


B1L001S, U16-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

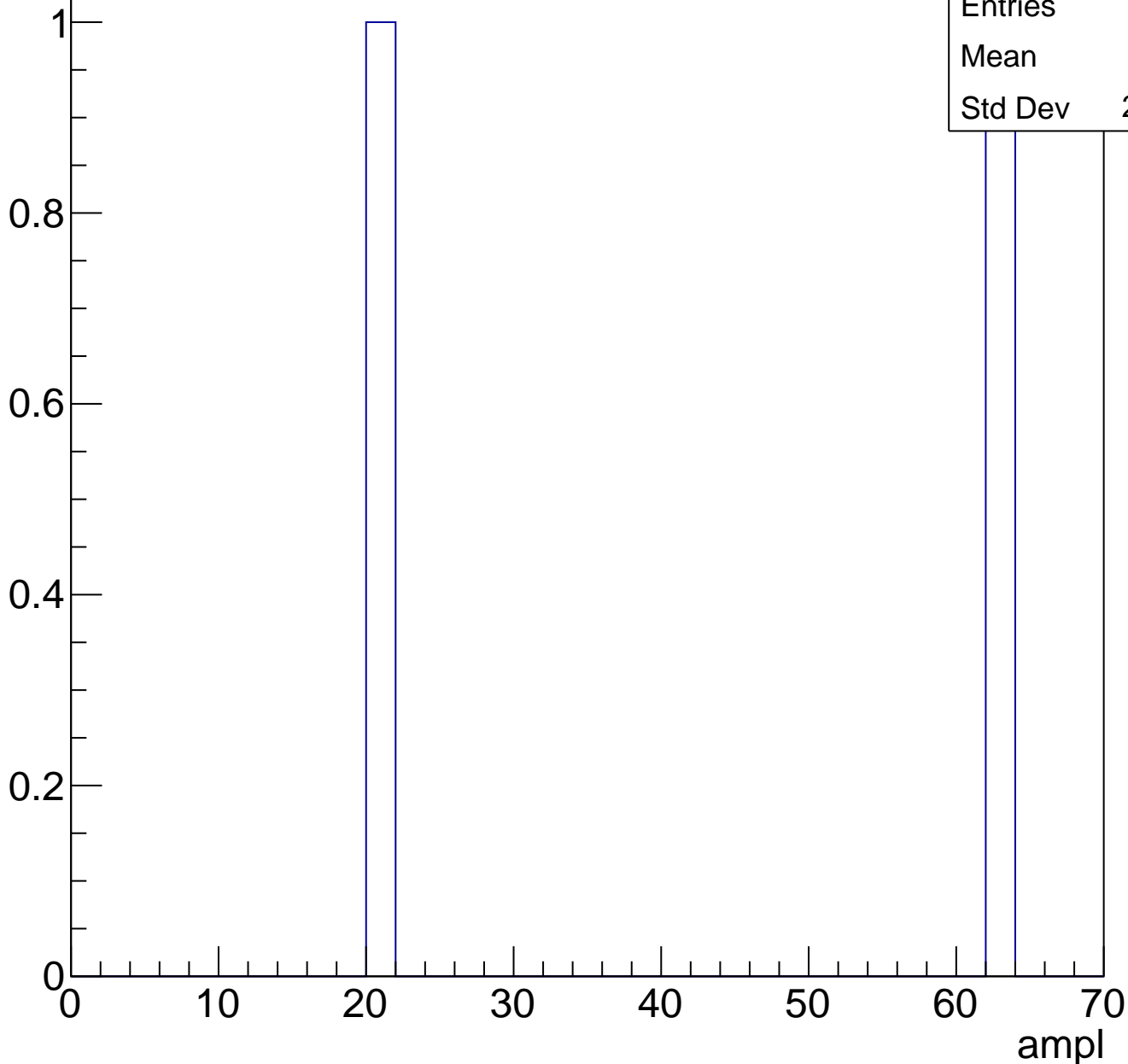
Entries	17
Mean	61.53
Std Dev	1.684



B1L001S, U16-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	30.62
Std Dev	3.197

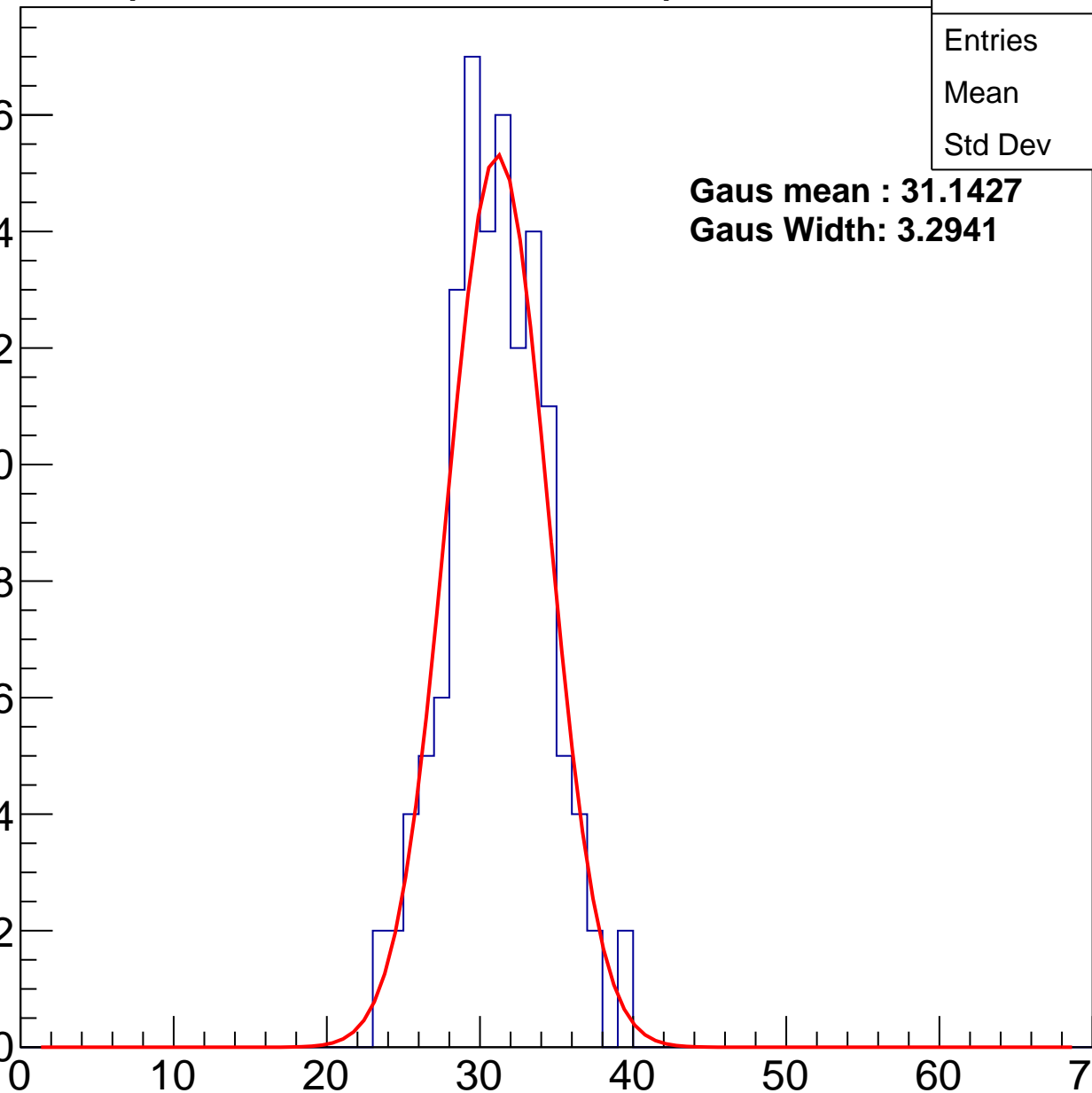
Gaus mean : 31.1427

Gaus Width: 3.2941

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.35
Std Dev	3.374

Gaus mean : 37.8974

Gaus Width: 3.4949

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

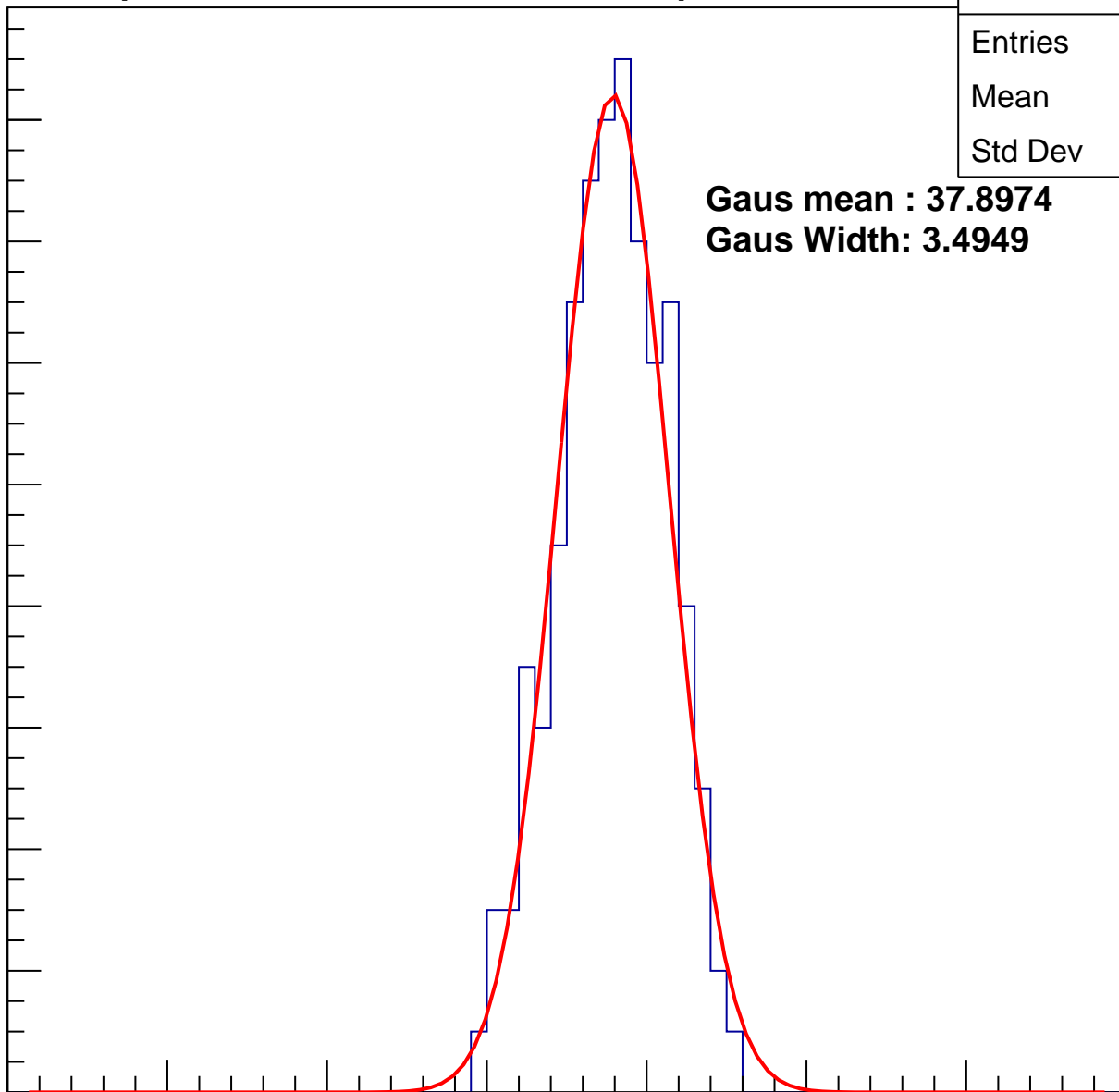
40

50

60

70

ampl



B1L001S, U16-ch97, adc2

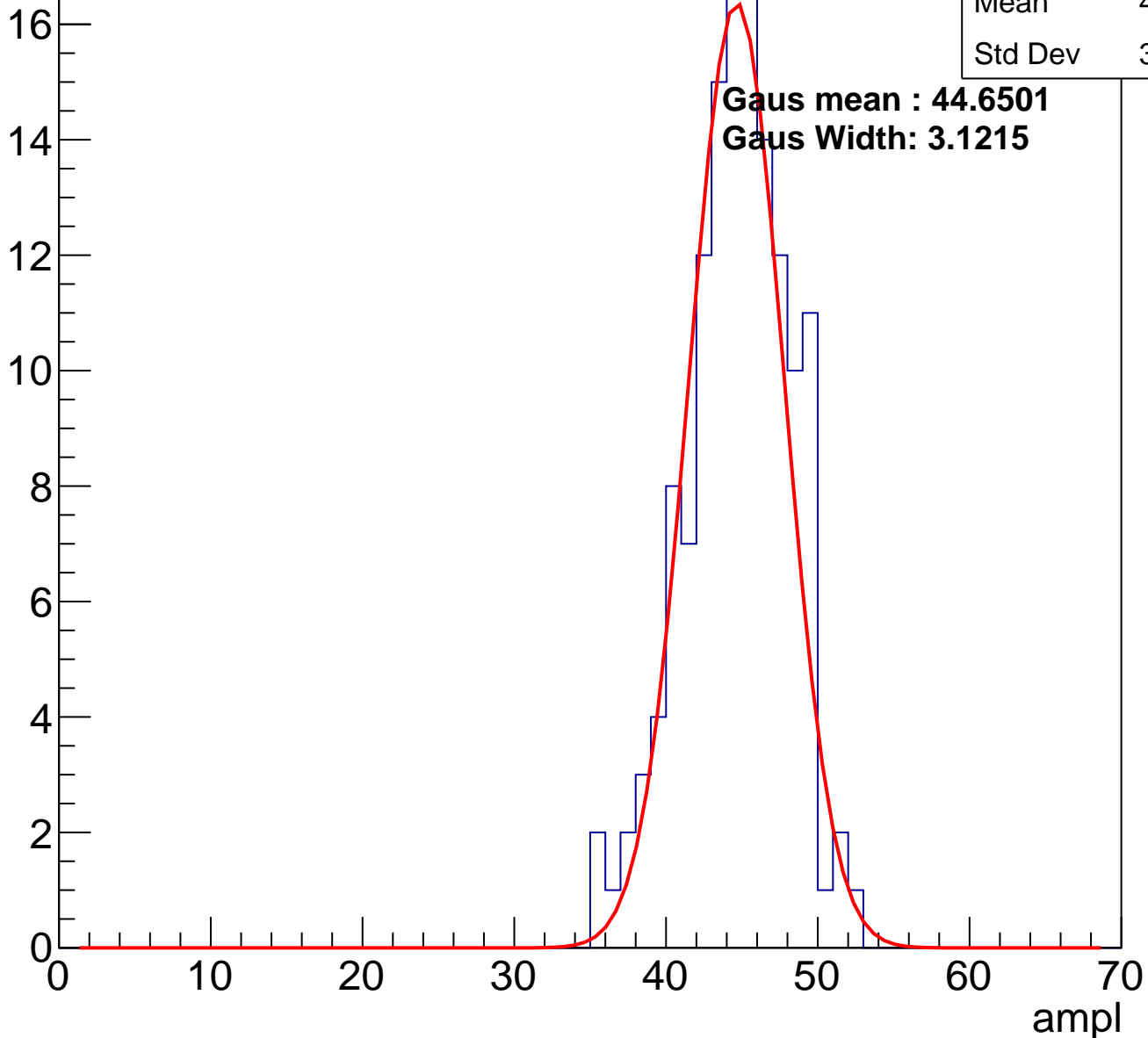
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	139
Mean	44.24
Std Dev	3.384

Gaus mean : 44.6501

Gaus Width: 3.1215

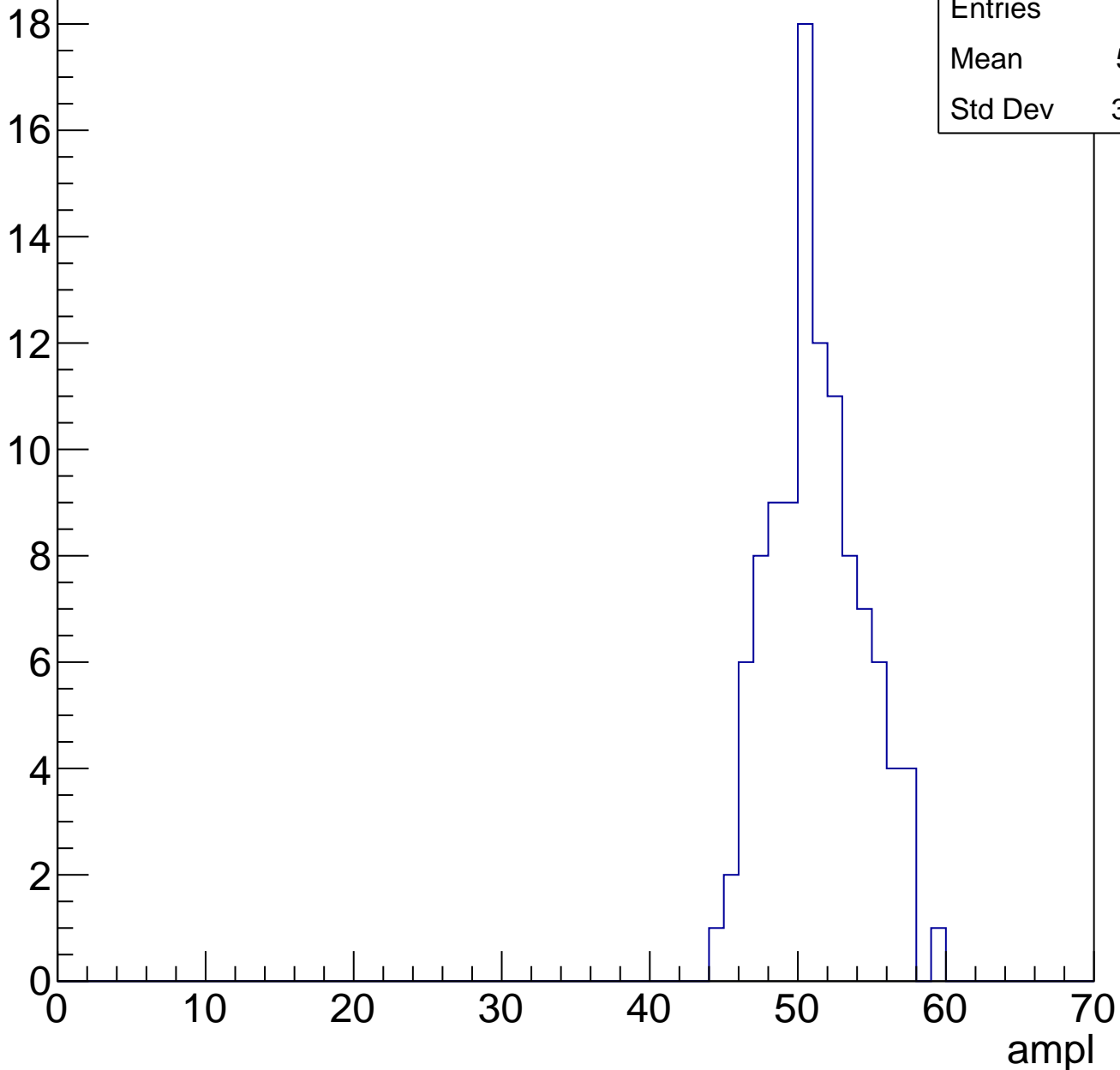


B1L001S, U16-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	50.81
Std Dev	3.133

Entry



B1L001S, U16-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	56.32
Std Dev	3.211

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

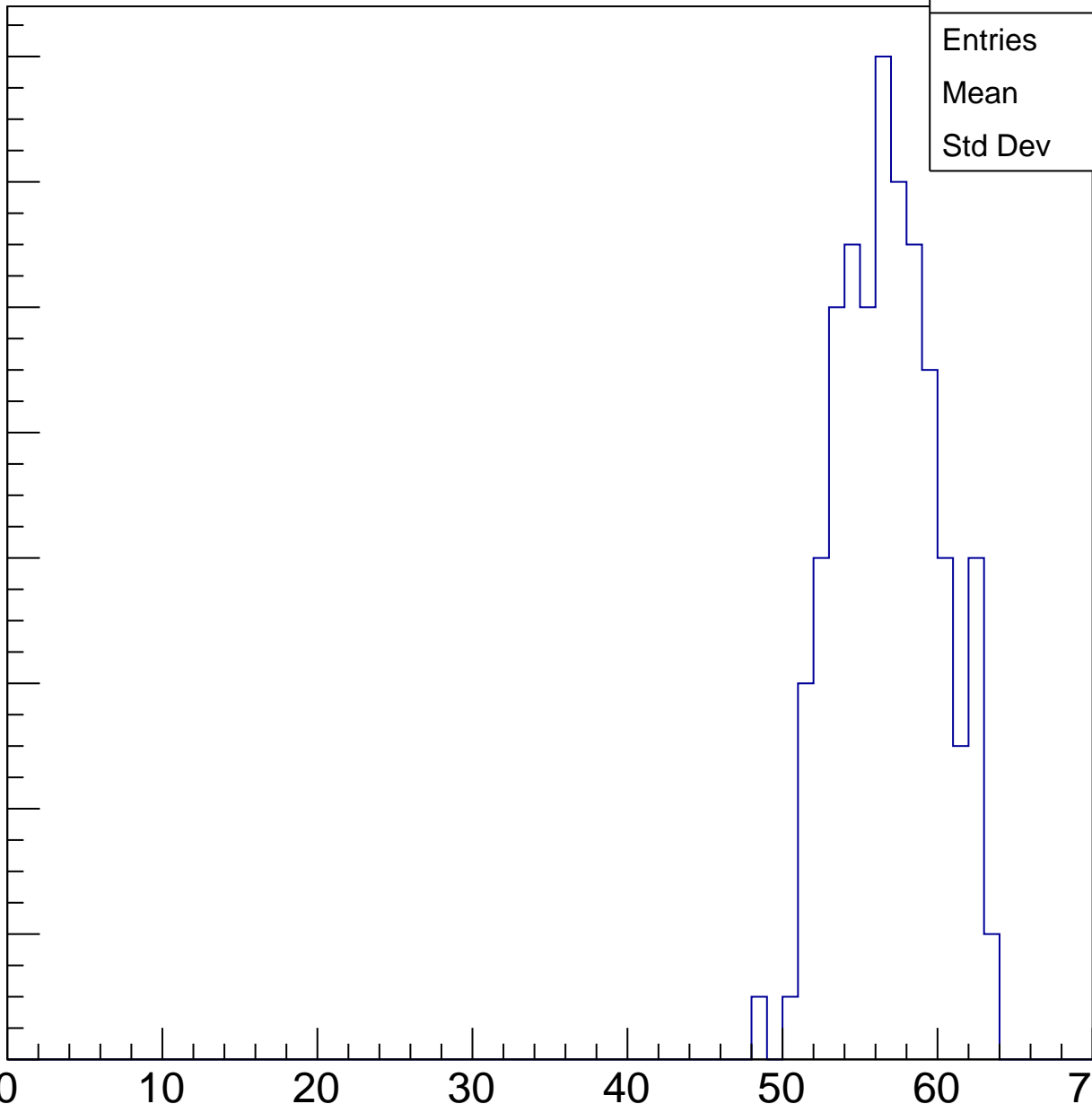
40

50

60

70

ampl



B1L001S, U16-ch97, adc5

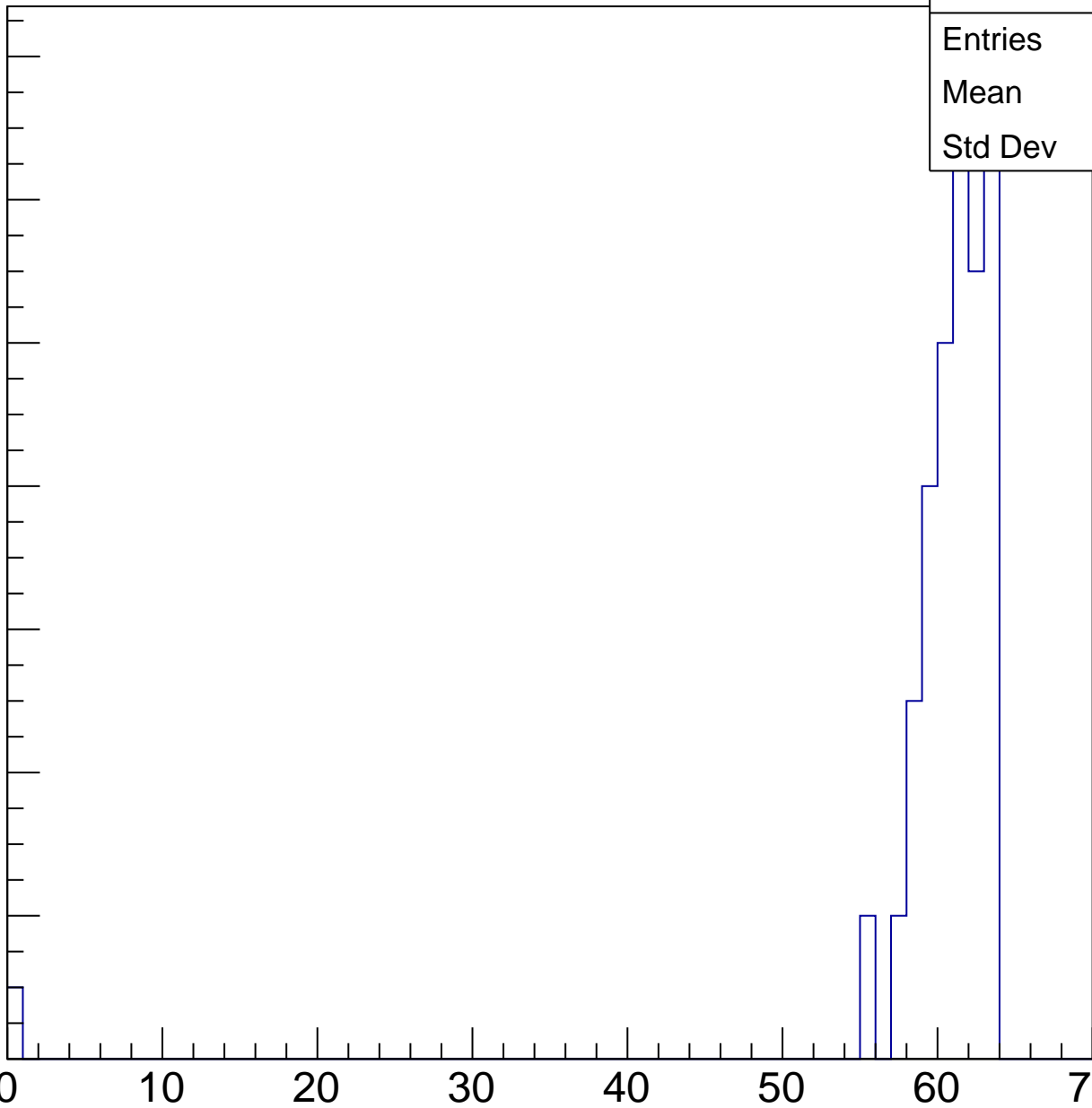
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.74
Std Dev	7.662

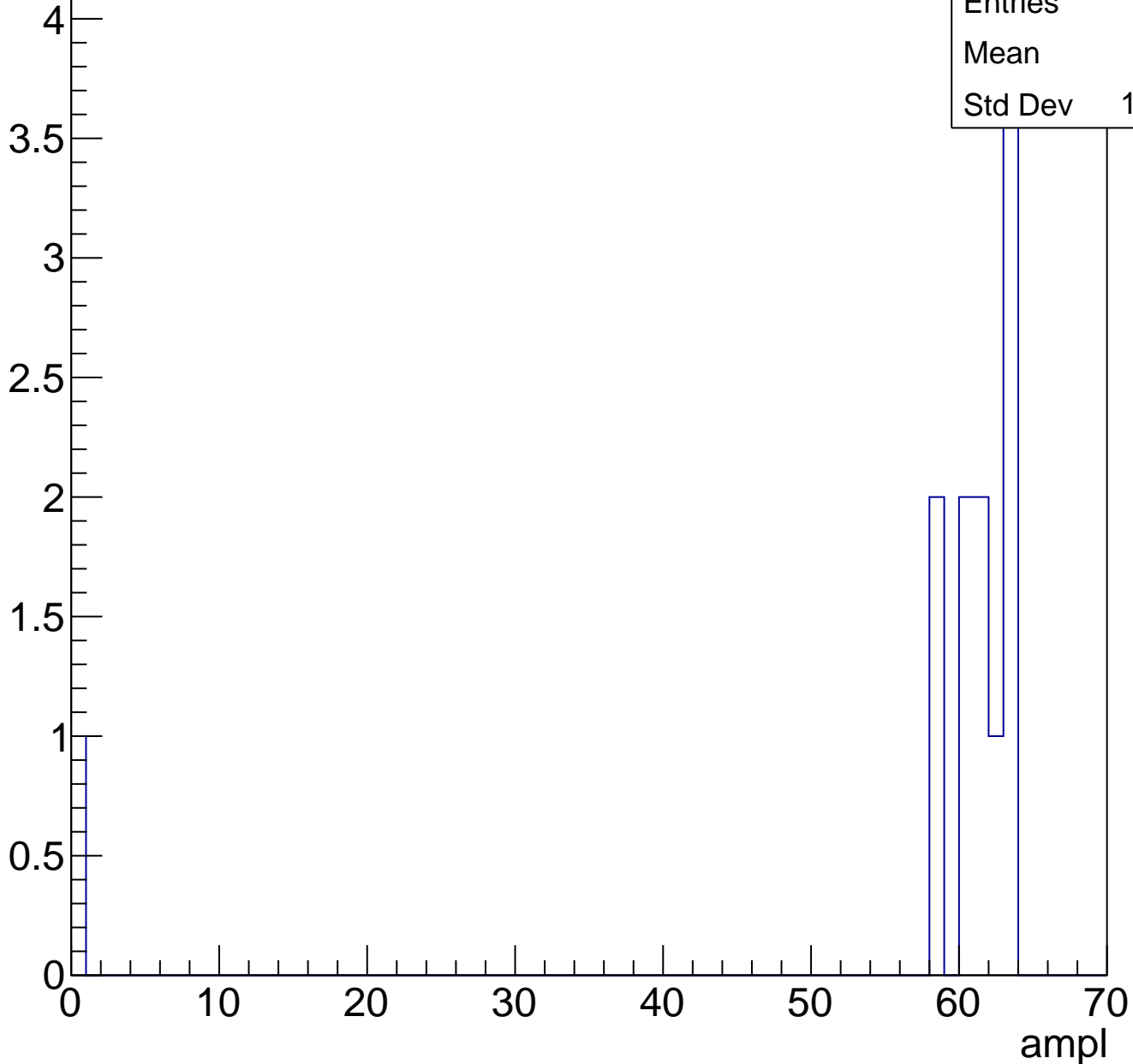
ampl



B1L001S, U16-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U16-ch98, adc0

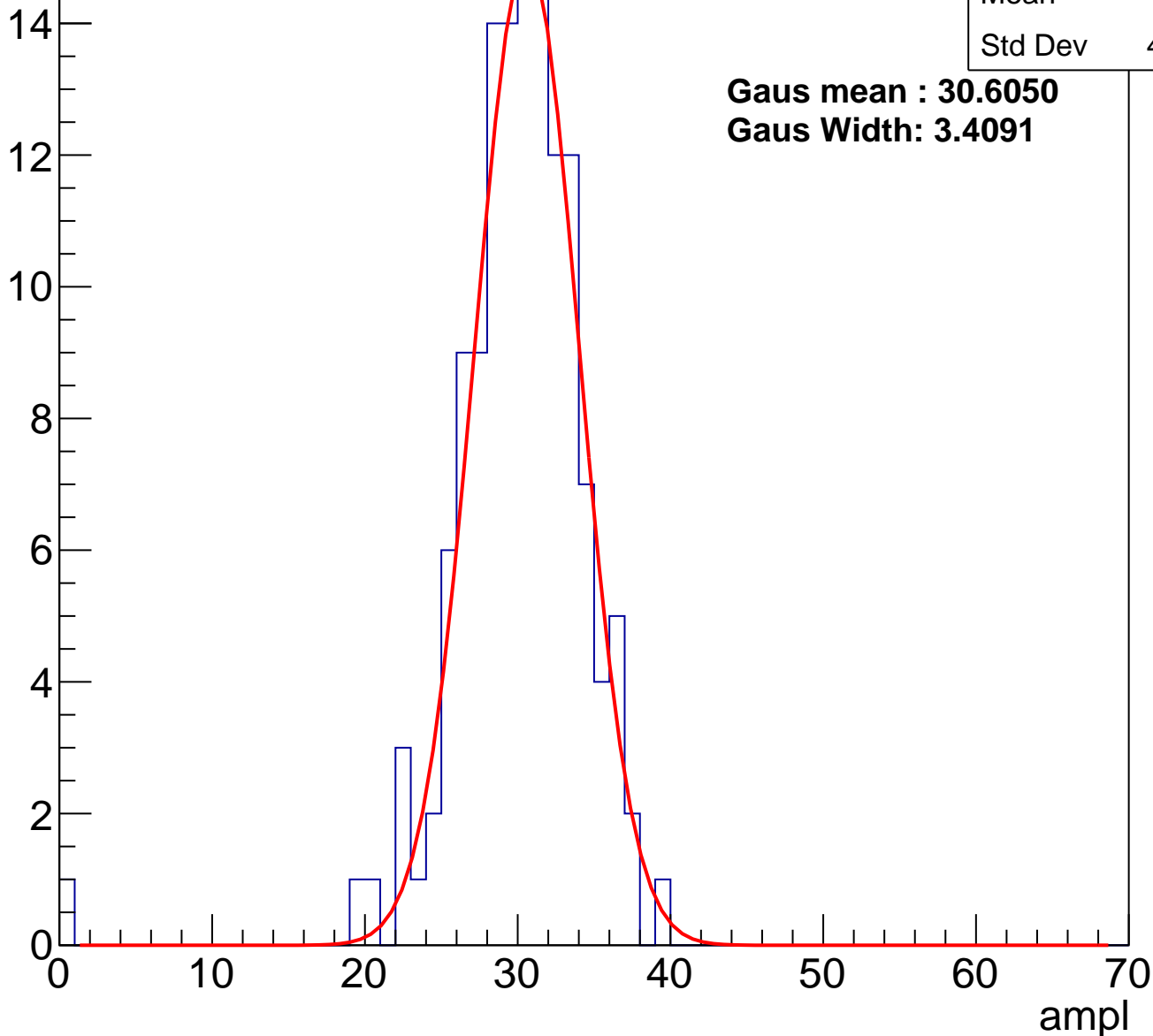
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.6
Std Dev	4.405

Gaus mean : 30.6050

Gaus Width: 3.4091

Entry



B1L001S, U16-ch98, adc1

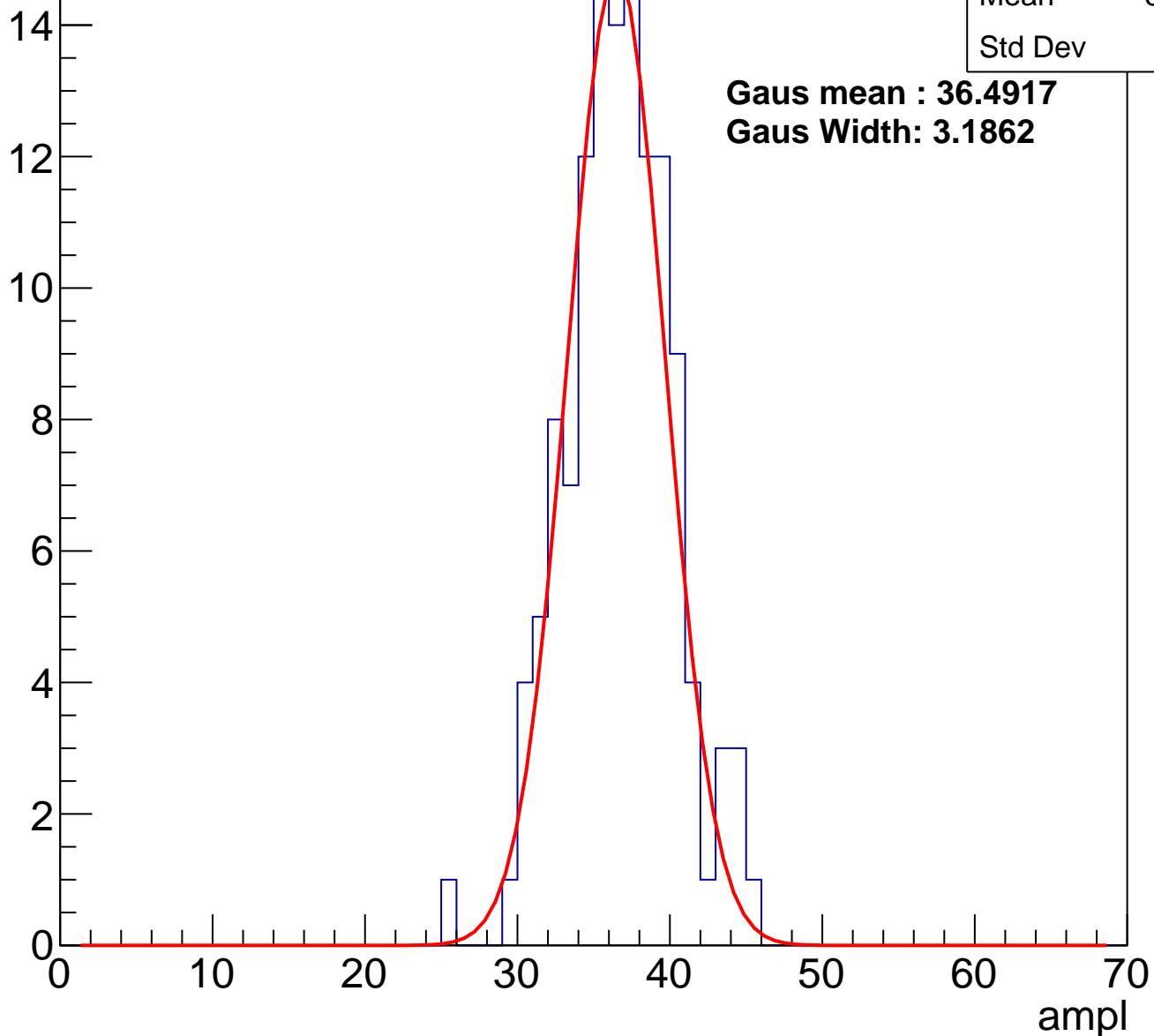
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.25
Std Dev	3.5

Gaus mean : 36.4917

Gaus Width: 3.1862

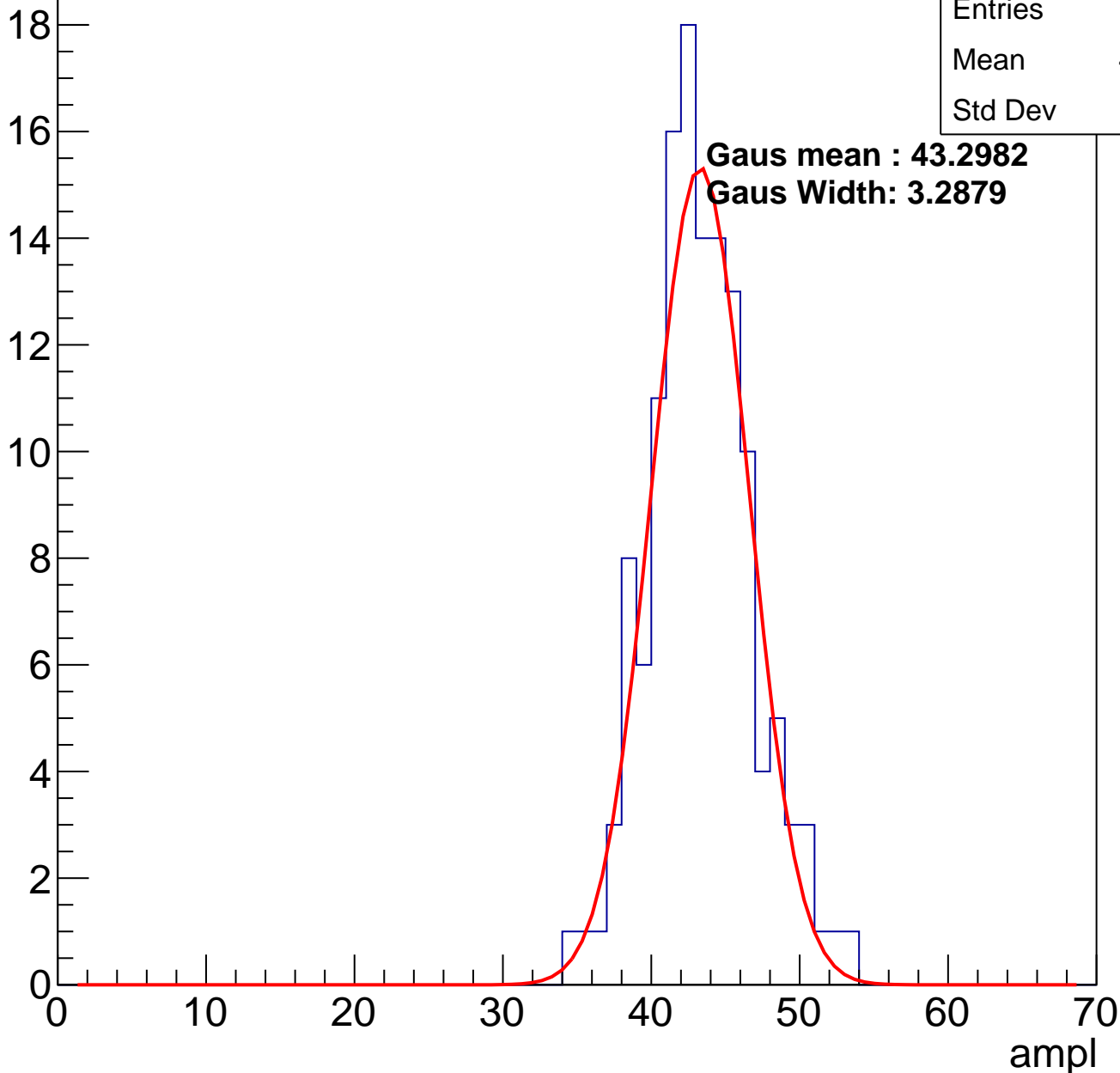
Entry



B1L001S, U16-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

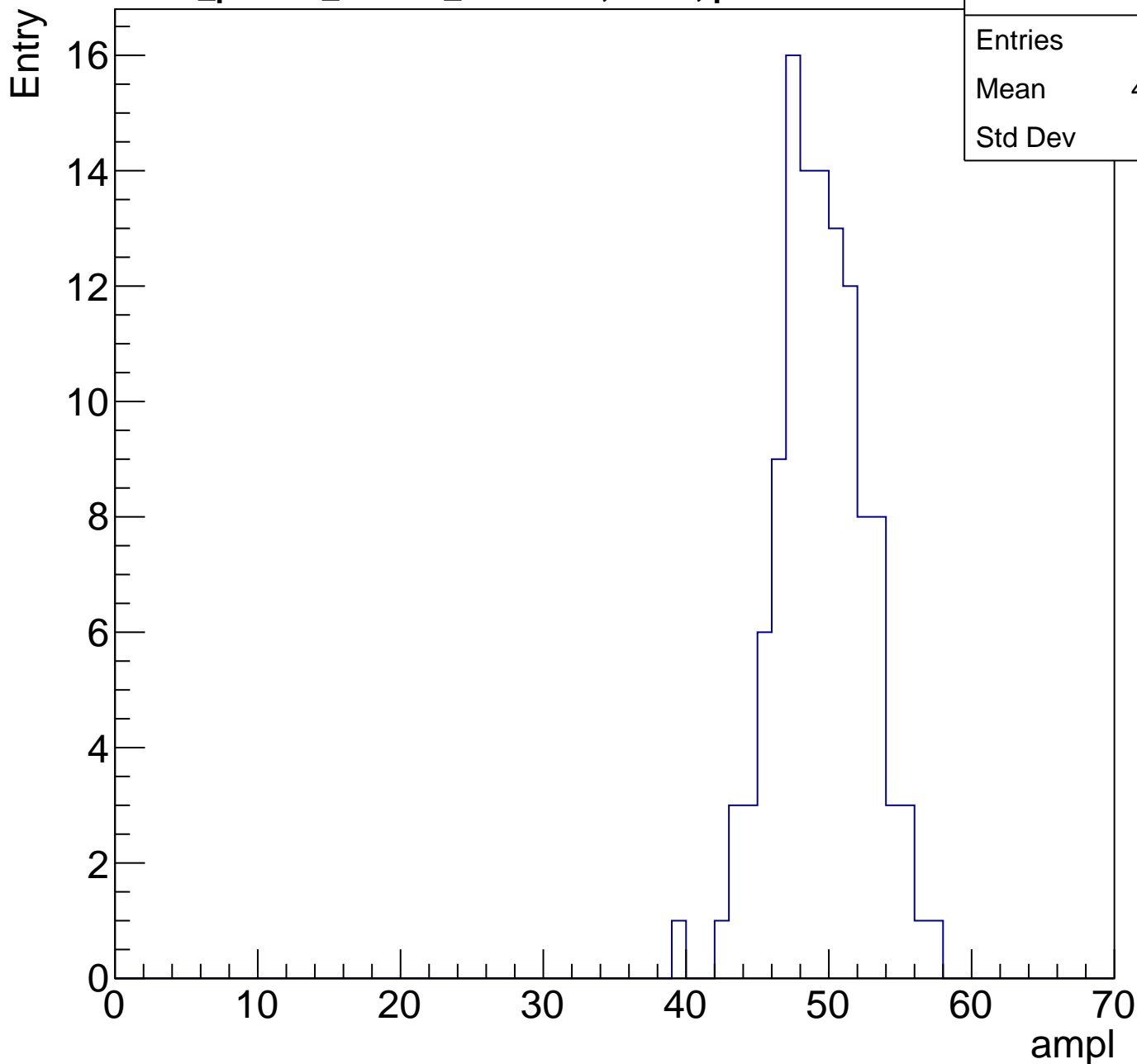
Entry



B1L001S, U16-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	48.95
Std Dev	3.14



B1L001S, U16-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

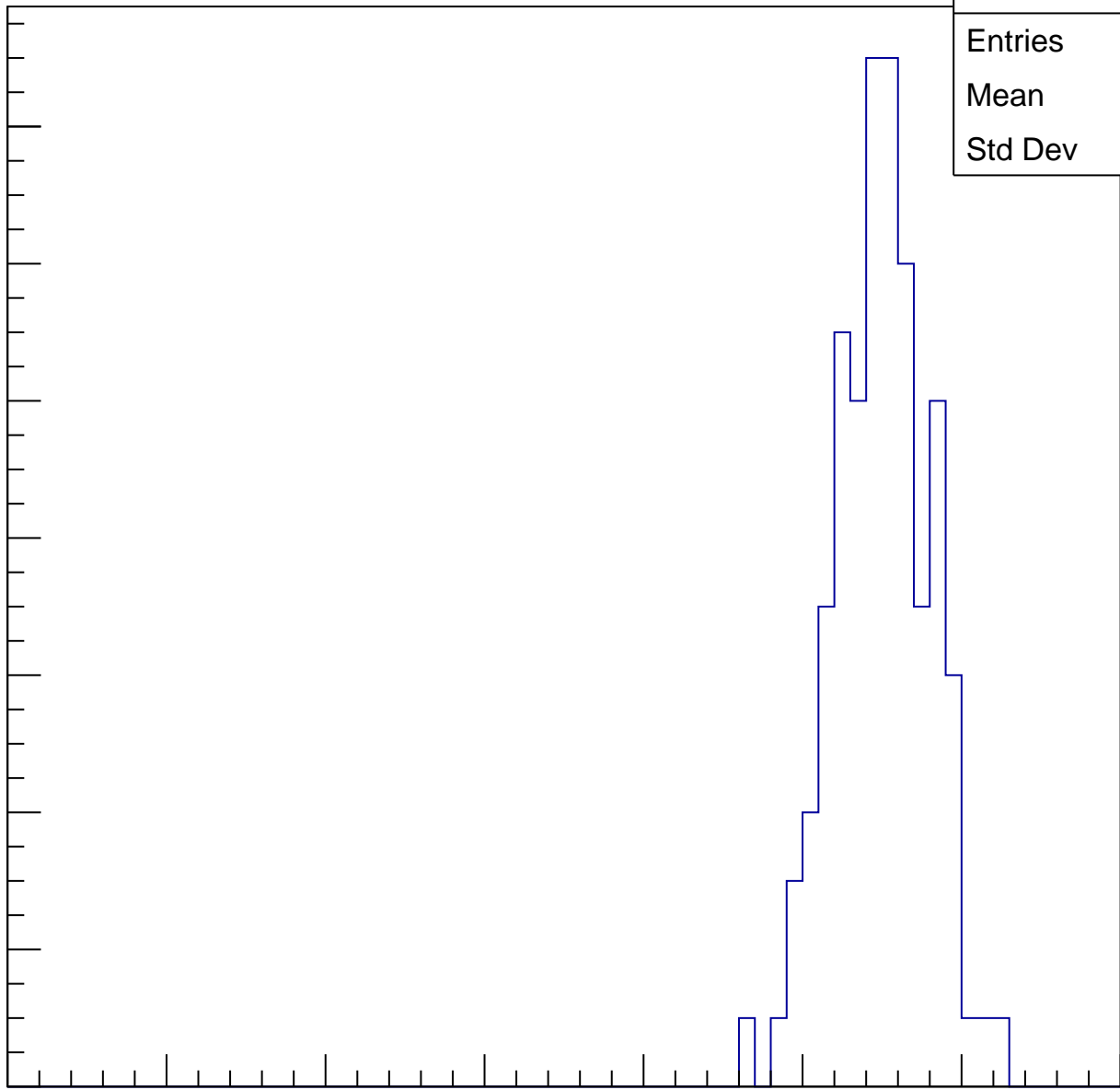
2

0

Entries	105
Mean	54.5
Std Dev	2.951

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch98, adc5

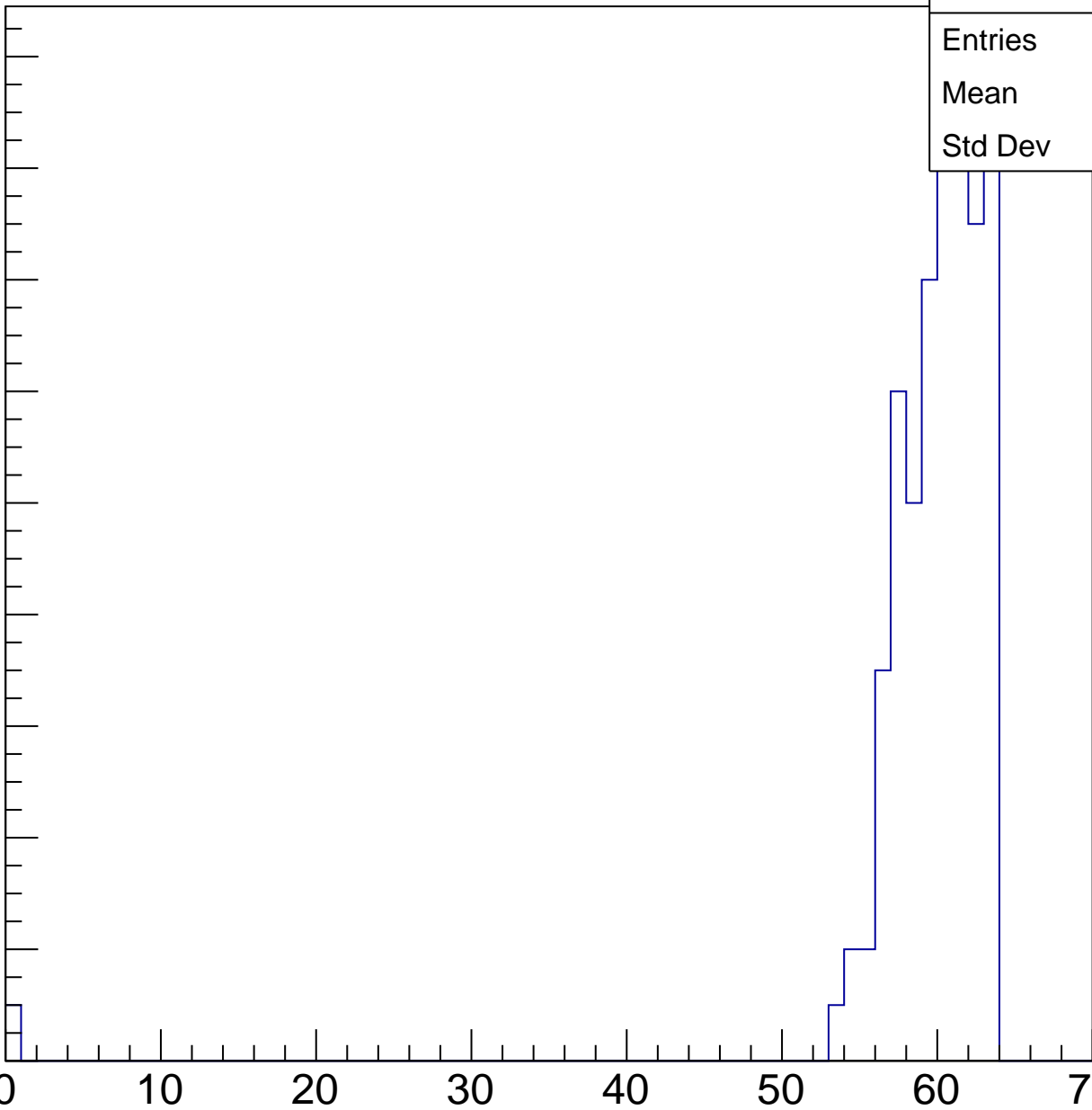
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	114
Mean	59.18
Std Dev	6.062

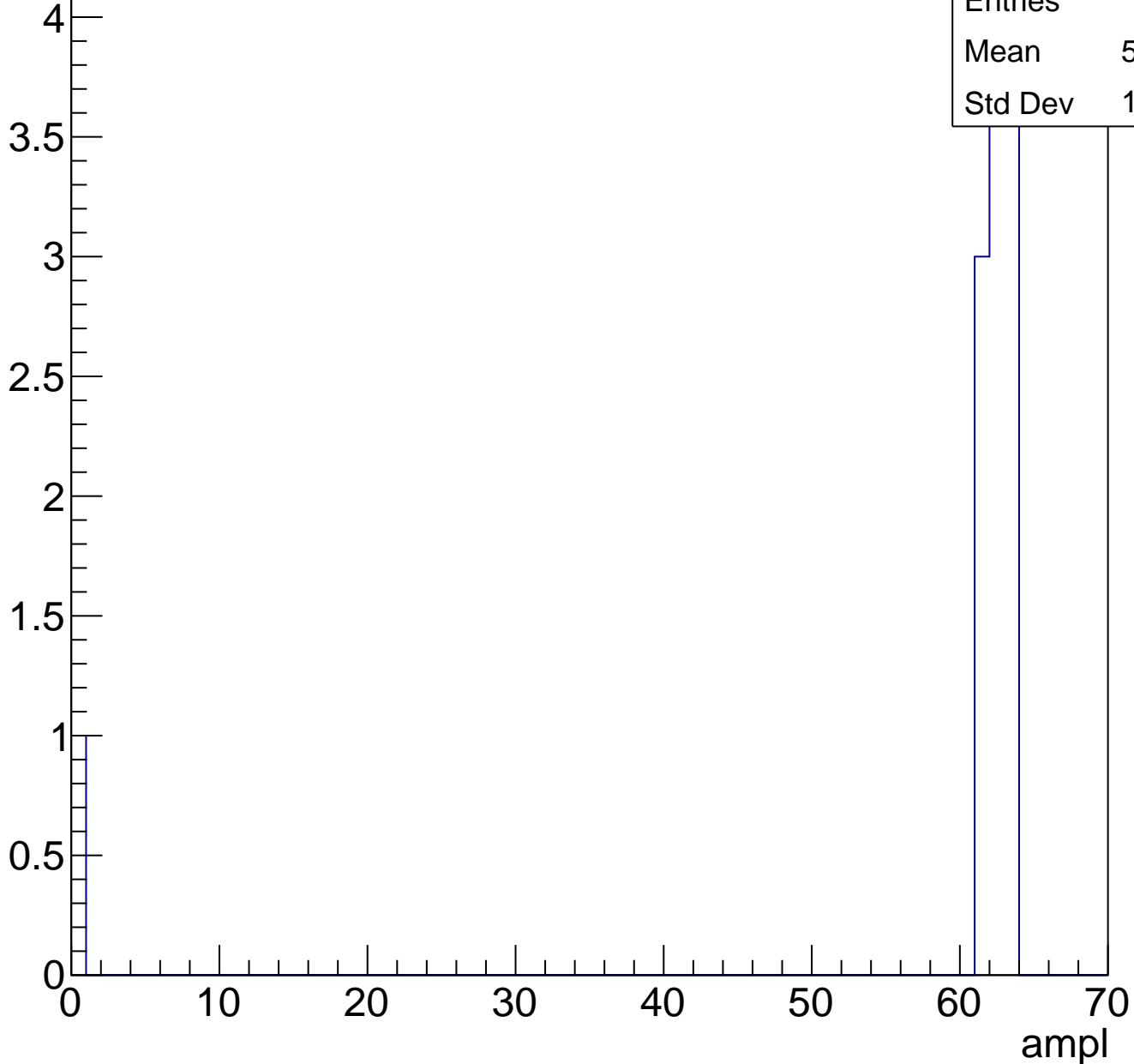
ampl



B1L001S, U16-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch99, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.98
Std Dev	4.035

Gaus mean : 30.7786

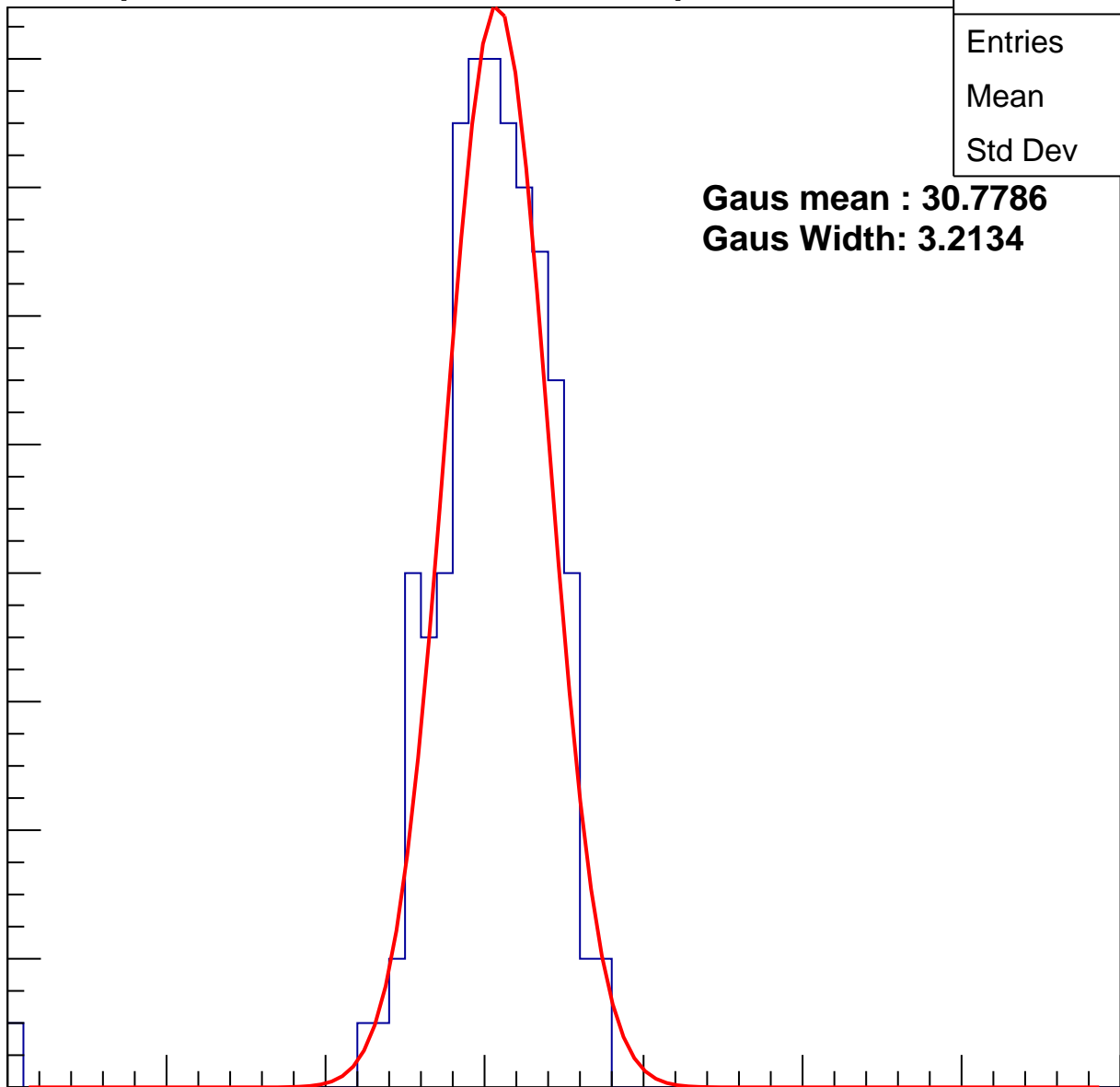
Gaus Width: 3.2134

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch99, adc1

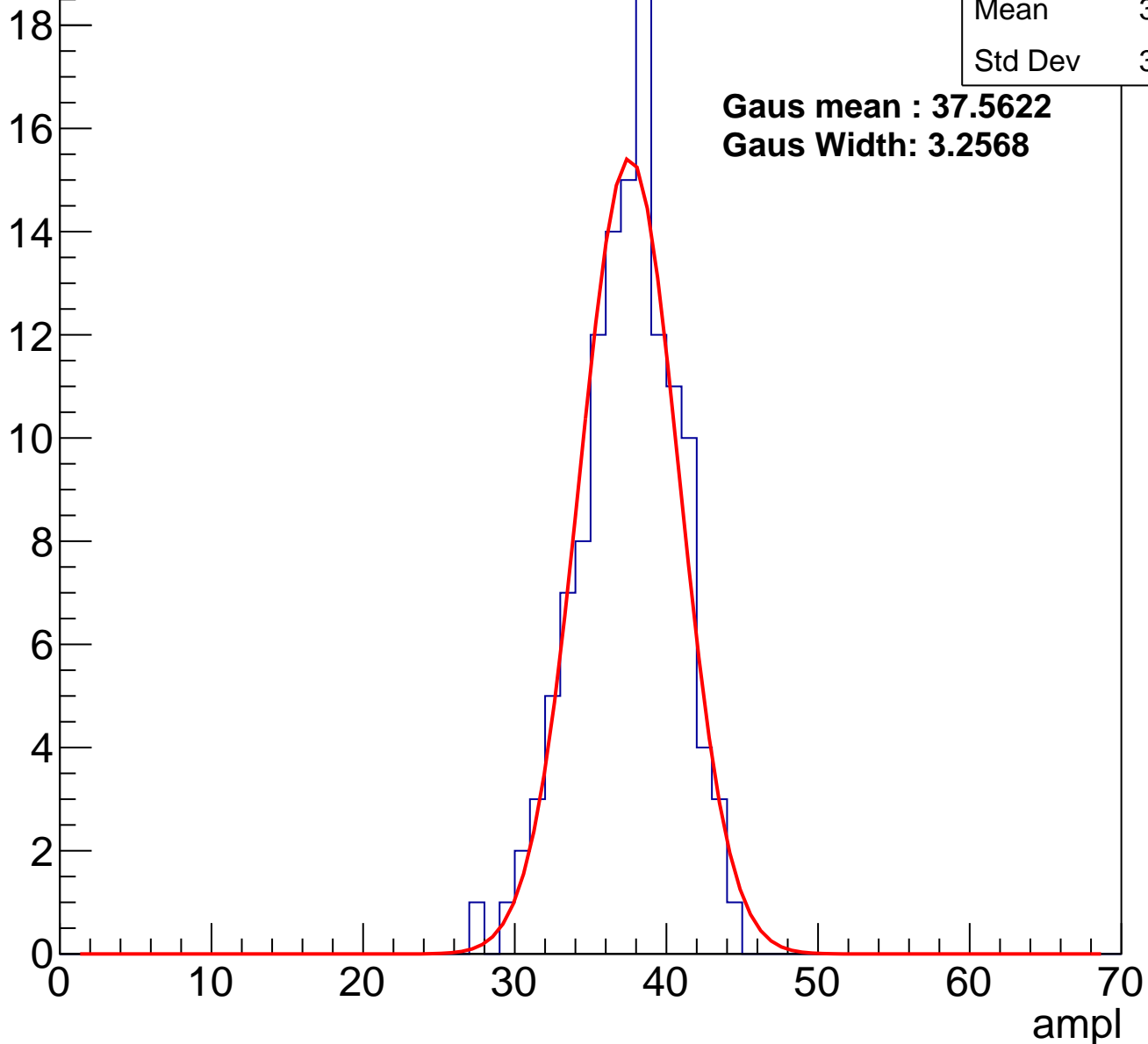
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.97
Std Dev	3.216

Gaus mean : 37.5622

Gaus Width: 3.2568

Entry



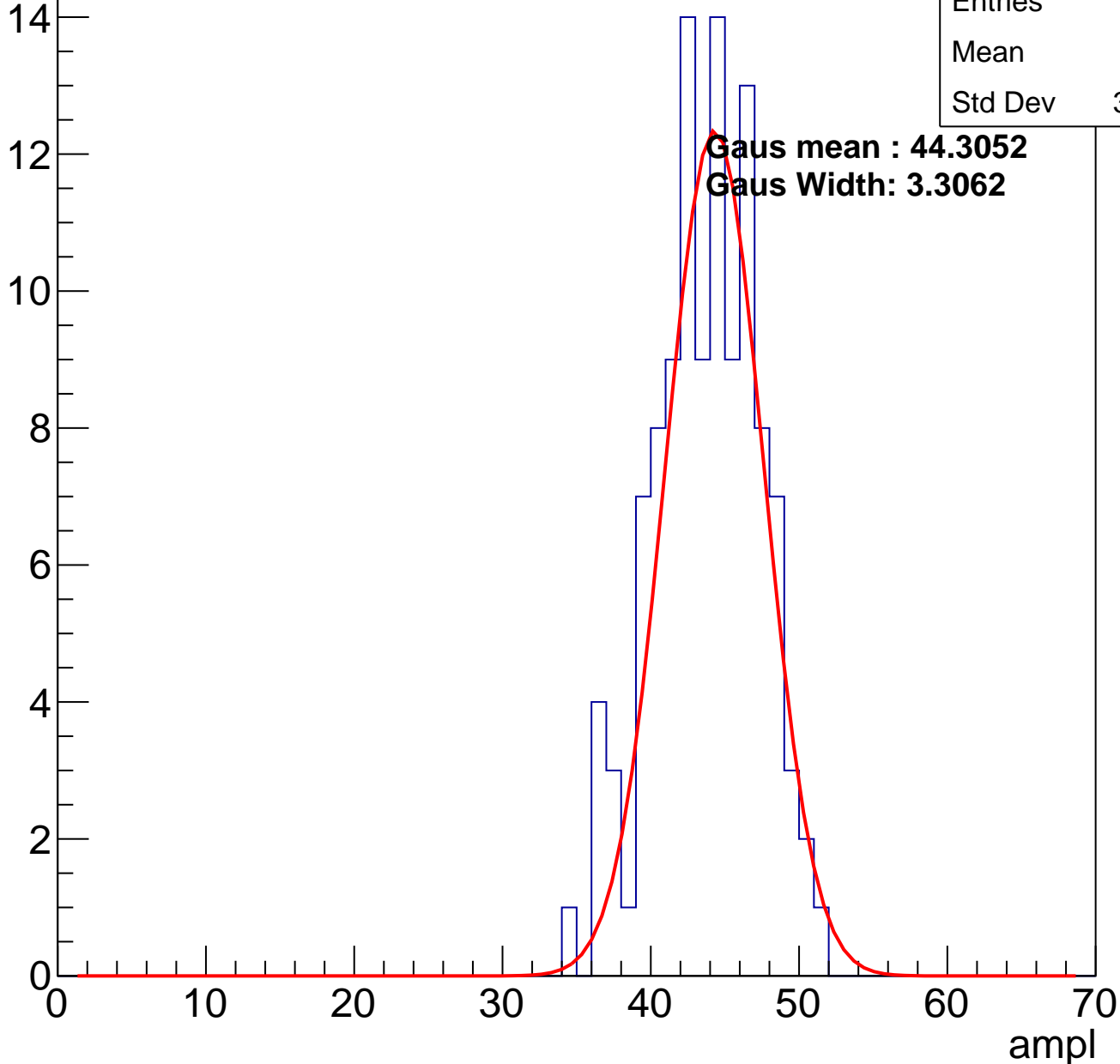
B1L001S, U16-ch99, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	113
Mean	43.3
Std Dev	3.487

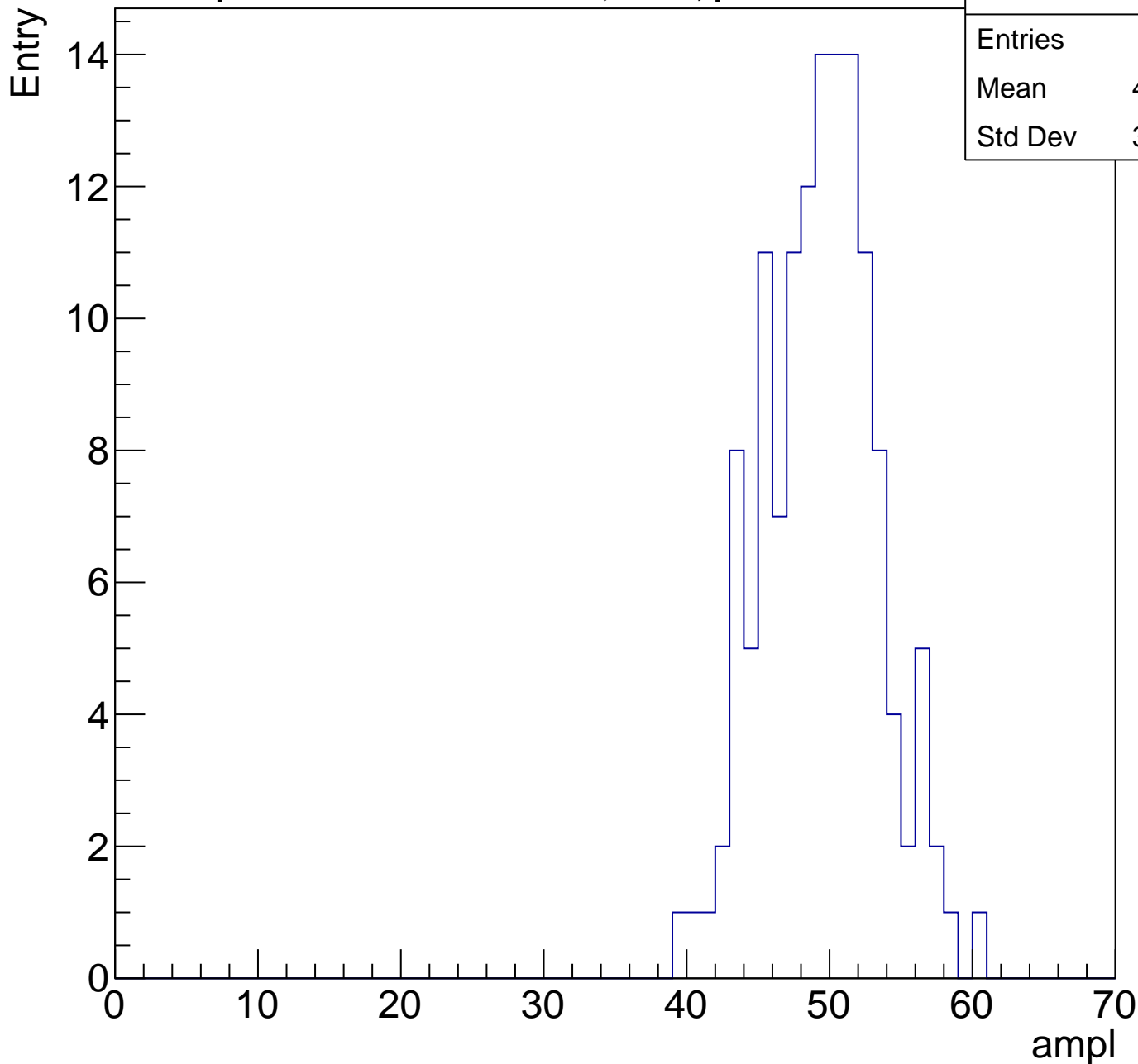
Gaus mean : 44.3052
Gaus Width: 3.3062



B1L001S, U16-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

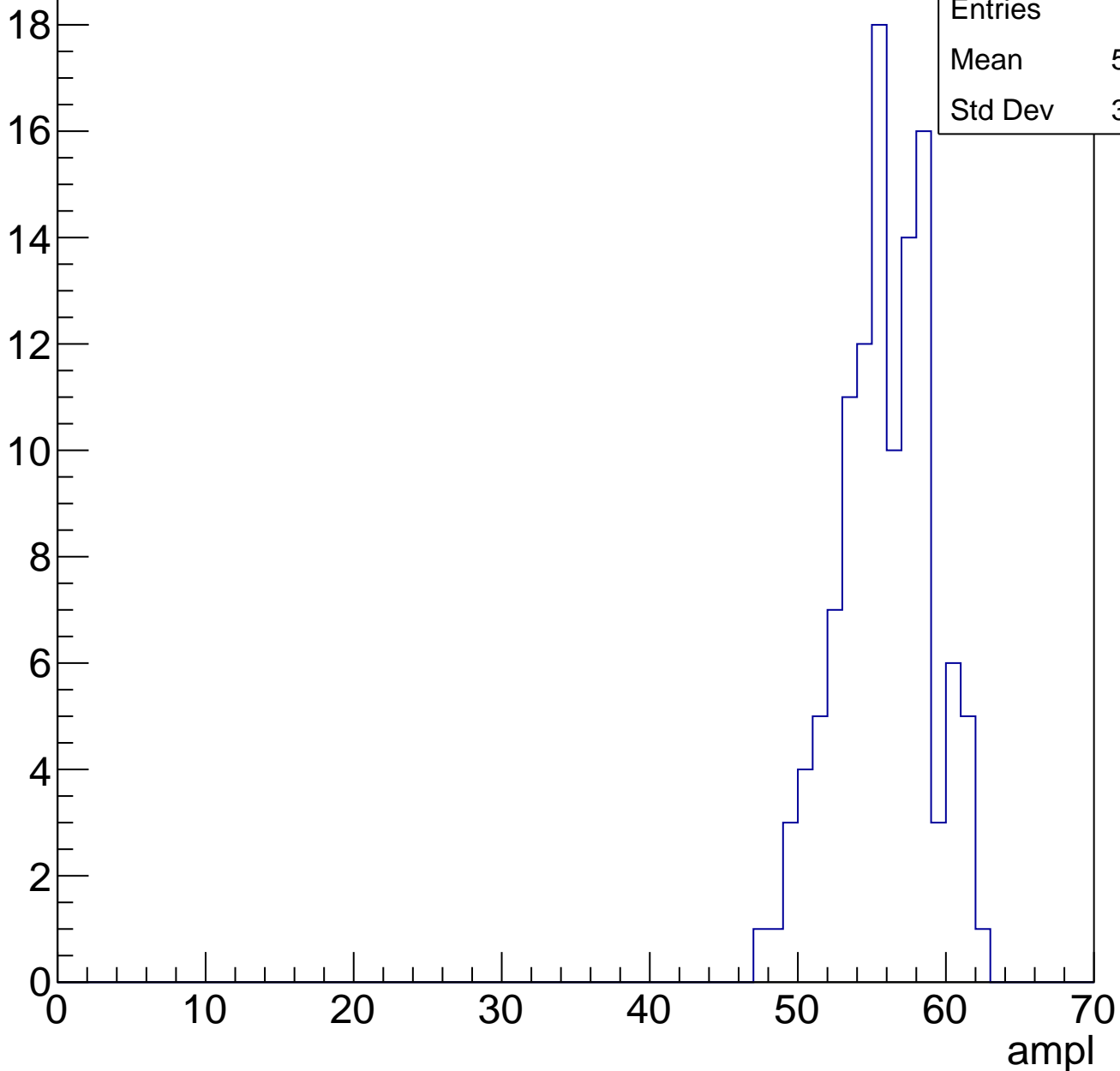
Entries	135
Mean	48.98
Std Dev	3.939



B1L001S, U16-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	117
Mean	55.32
Std Dev	3.129

B1L001S, U16-ch99, adc5

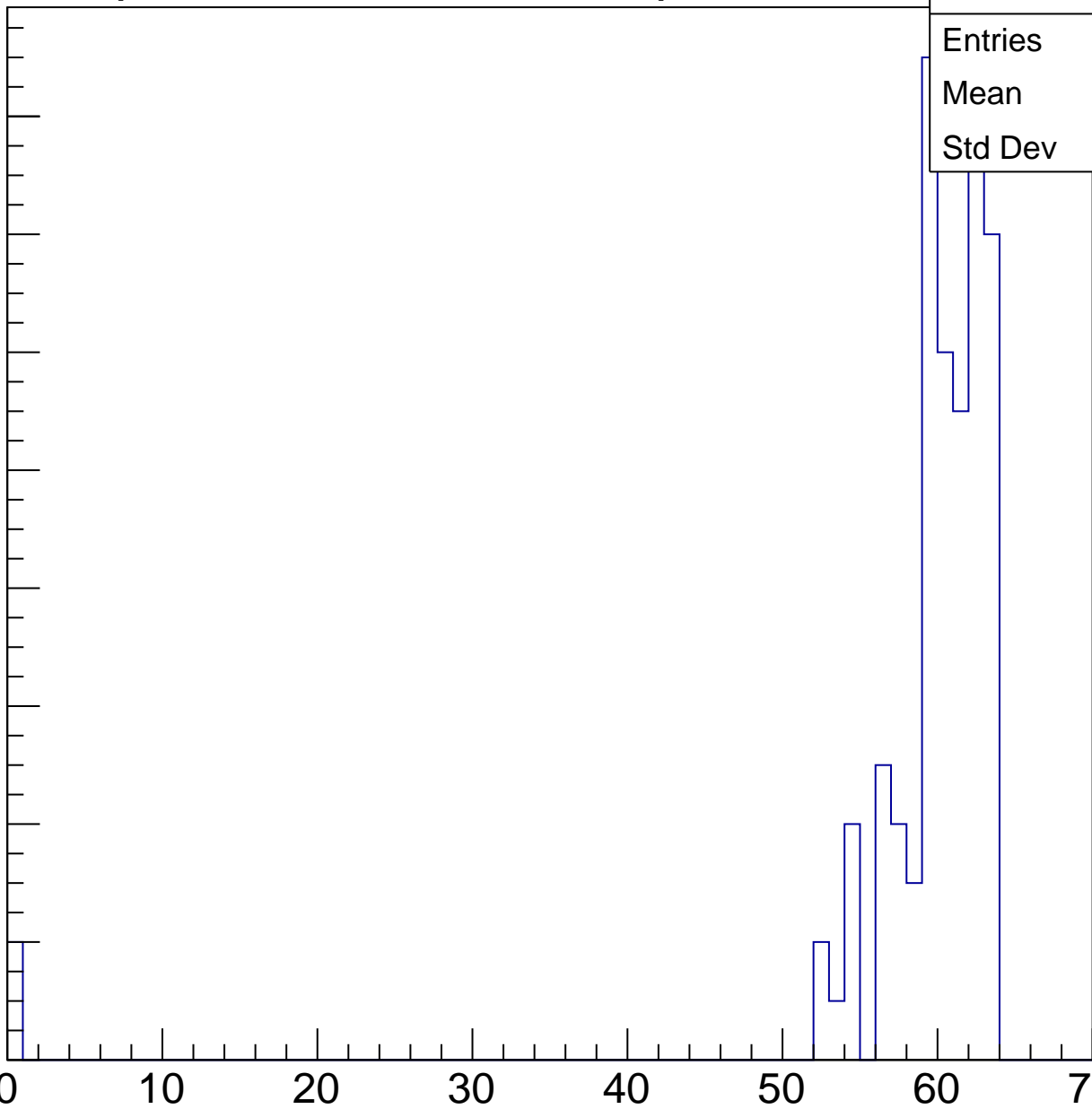
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.53
Std Dev	9.132

ampl

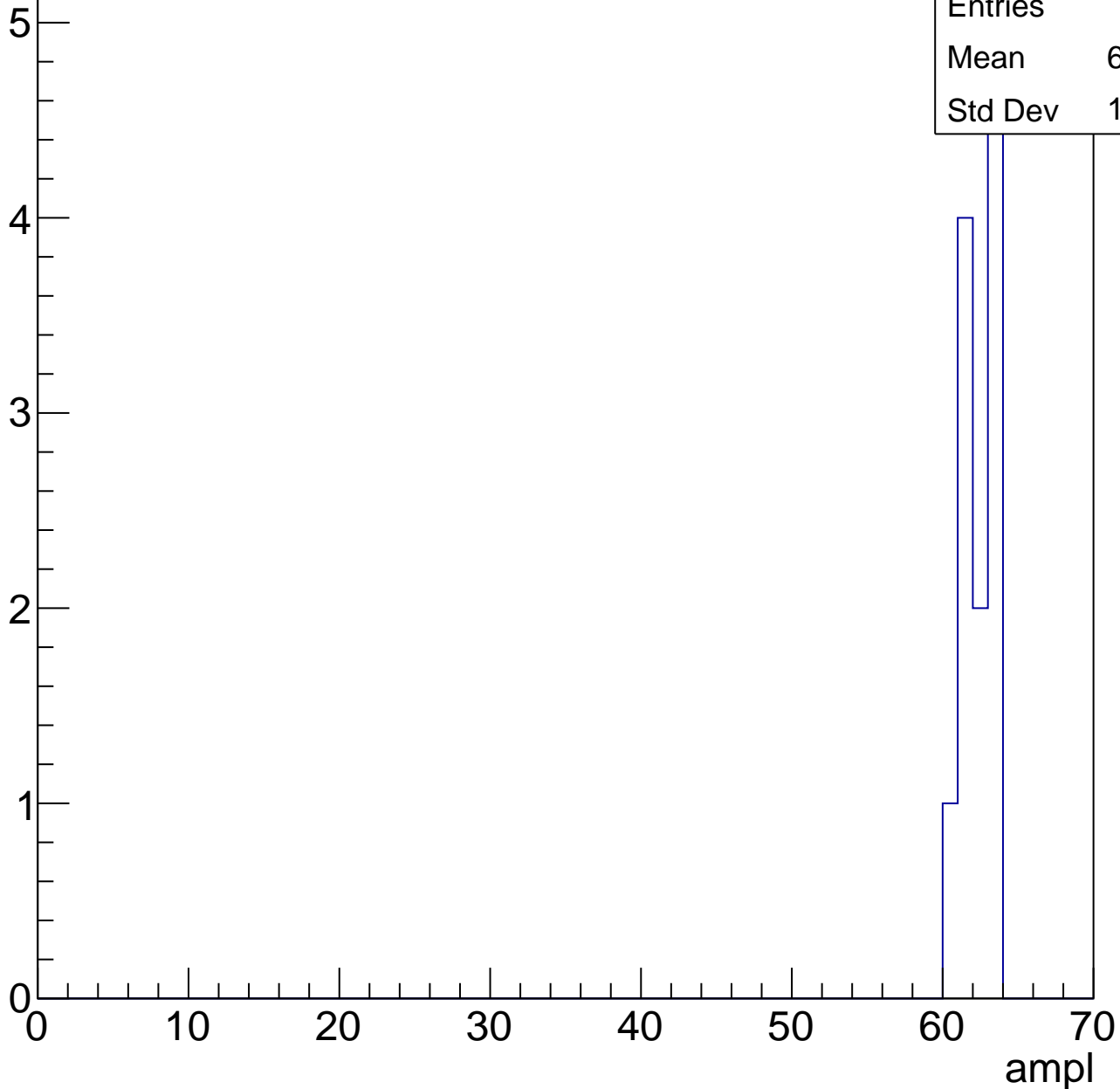


B1L001S, U16-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.92
Std Dev	1.037



B1L001S, U16-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch100, adc0

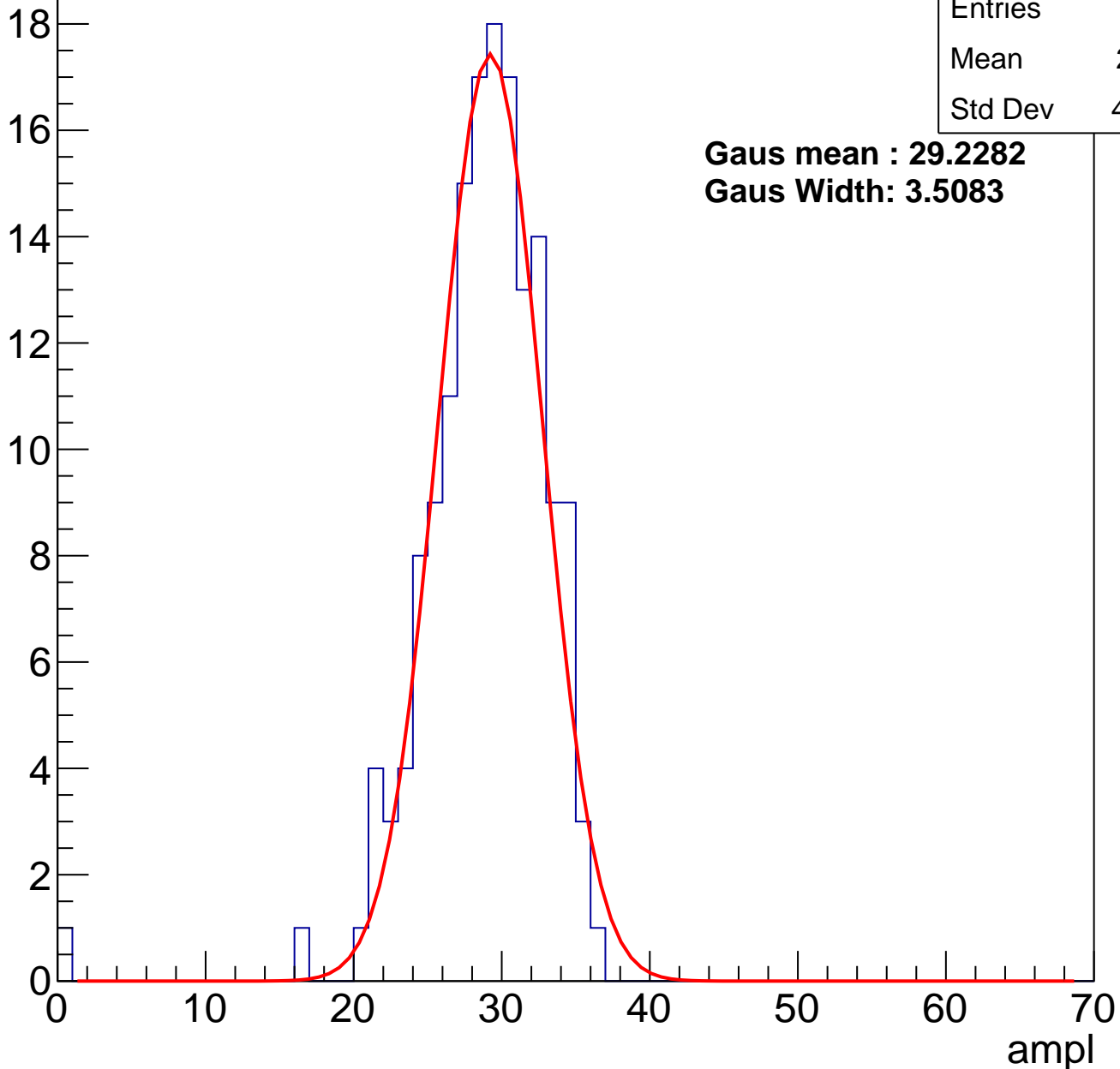
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	28.41
Std Dev	4.228

Gaus mean : 29.2282

Gaus Width: 3.5083

Entry



B1L001S, U16-ch100, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.08
Std Dev	3.309

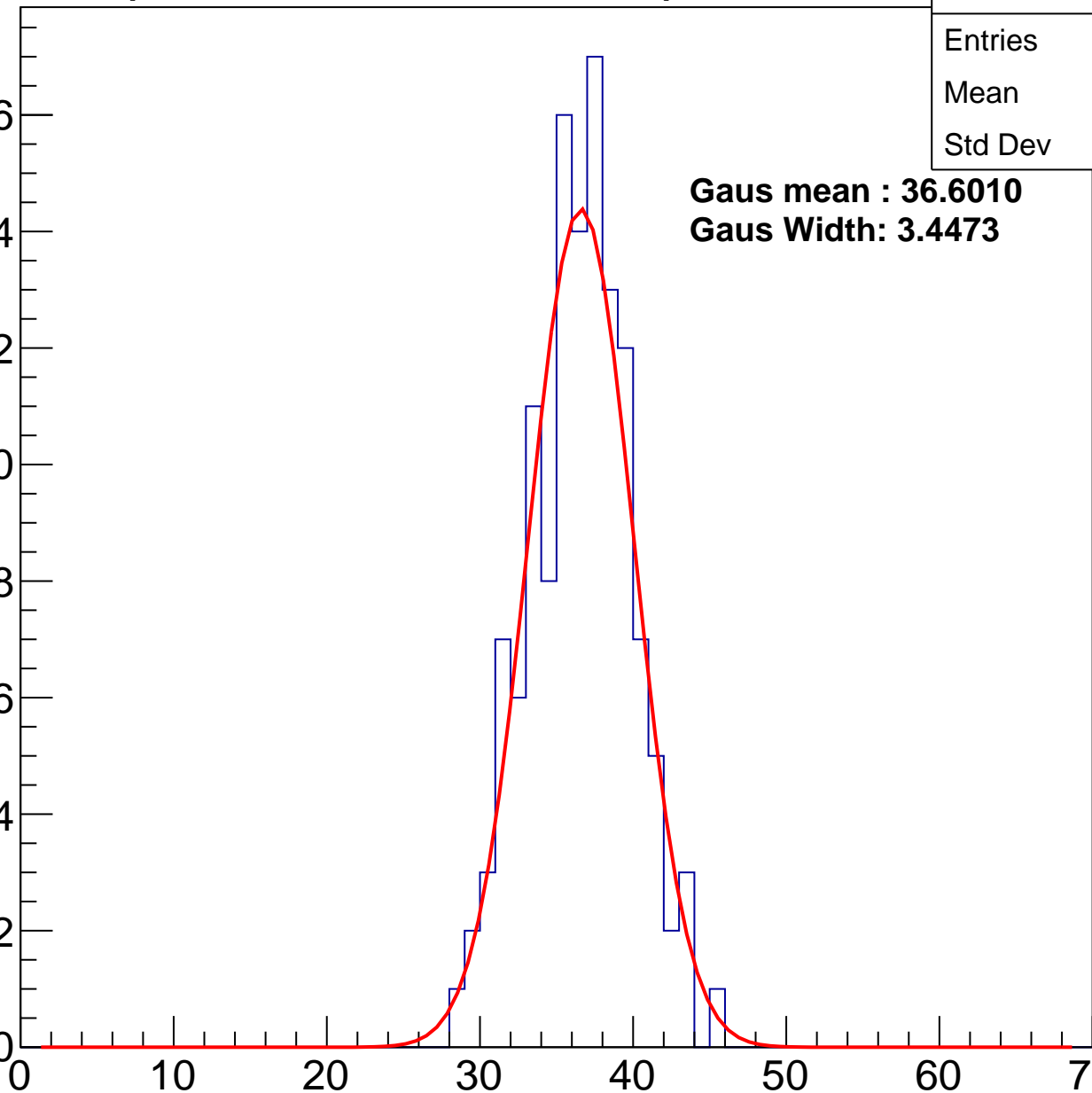
Gaus mean : 36.6010

Gaus Width: 3.4473

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	41.32
Std Dev	3.07

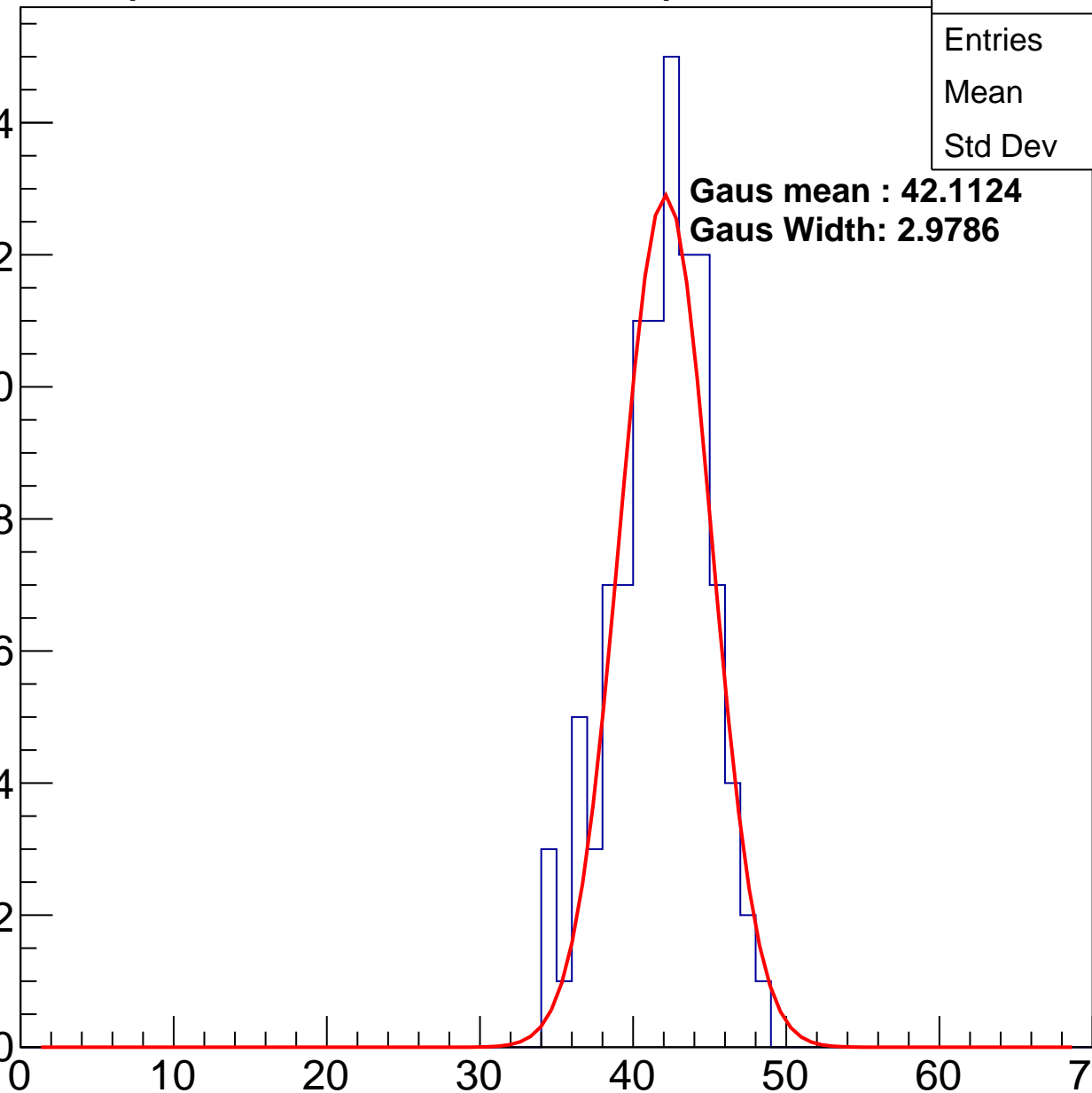
Gaus mean : 42.1124

Gaus Width: 2.9786

Entry

14
12
10
8
6
4
2
0

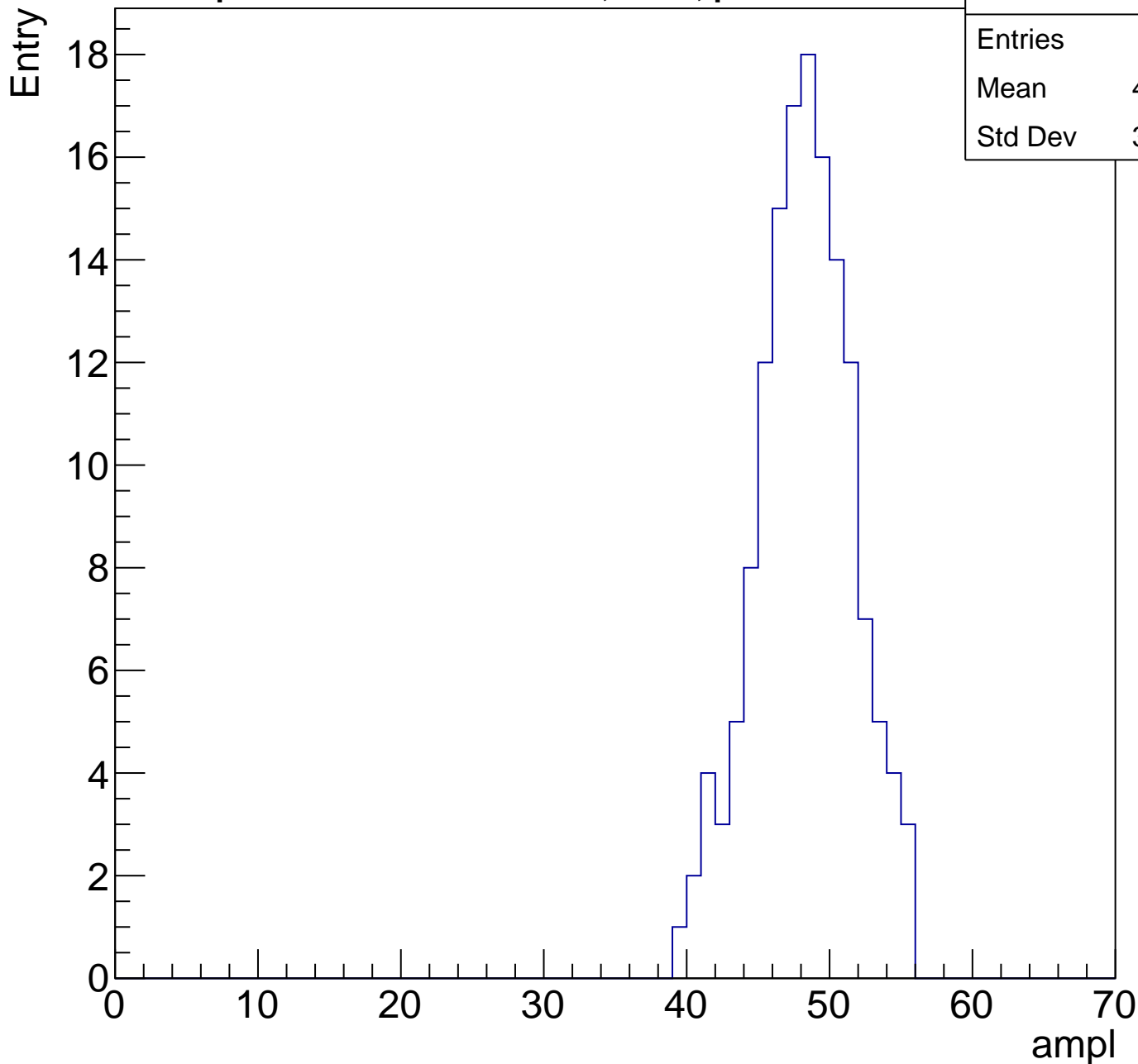
ampl



B1L001S, U16-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

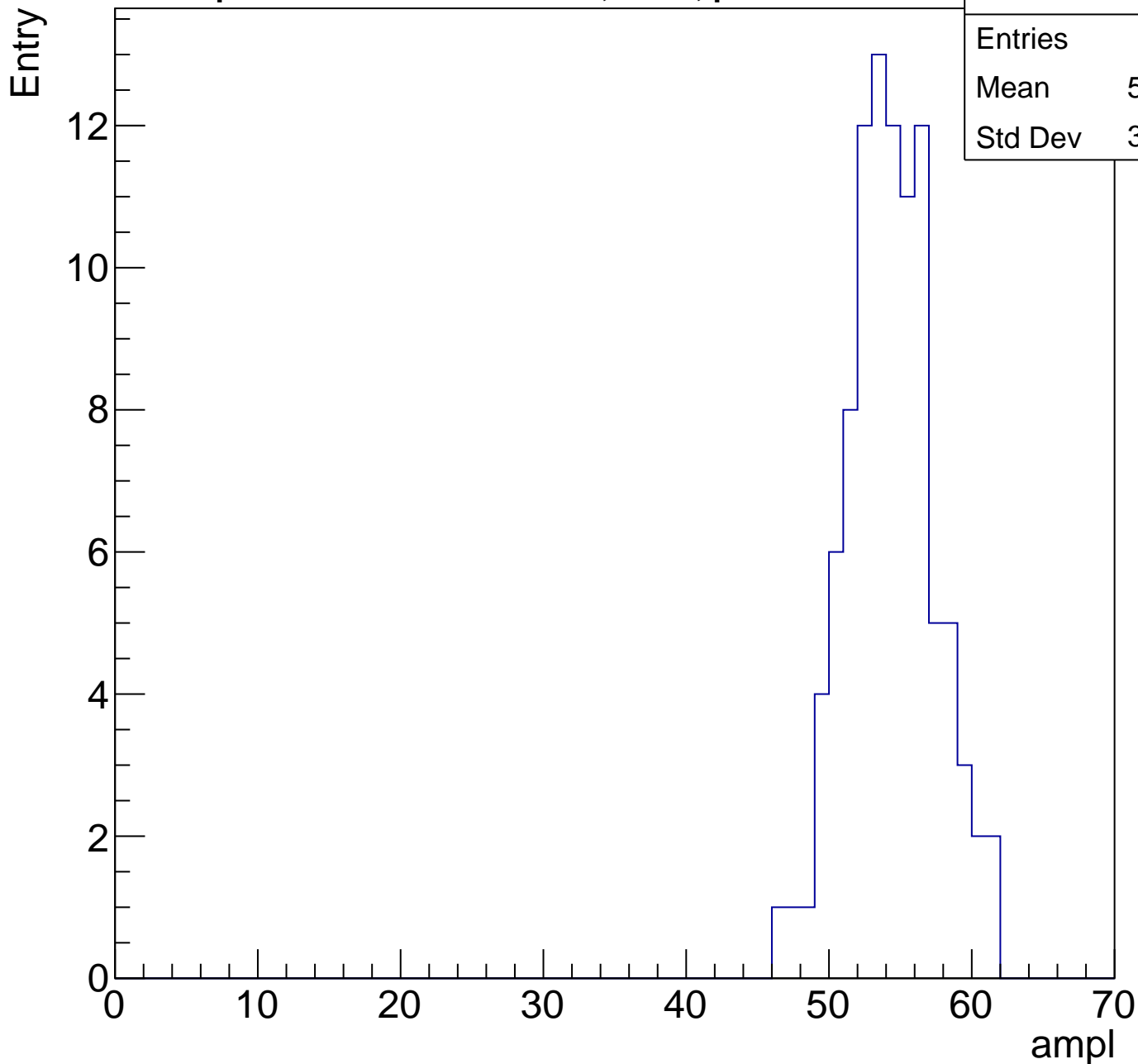
Entries	146
Mean	47.77
Std Dev	3.363



B1L001S, U16-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	53.85
Std Dev	3.038



B1L001S, U16-ch100, adc5

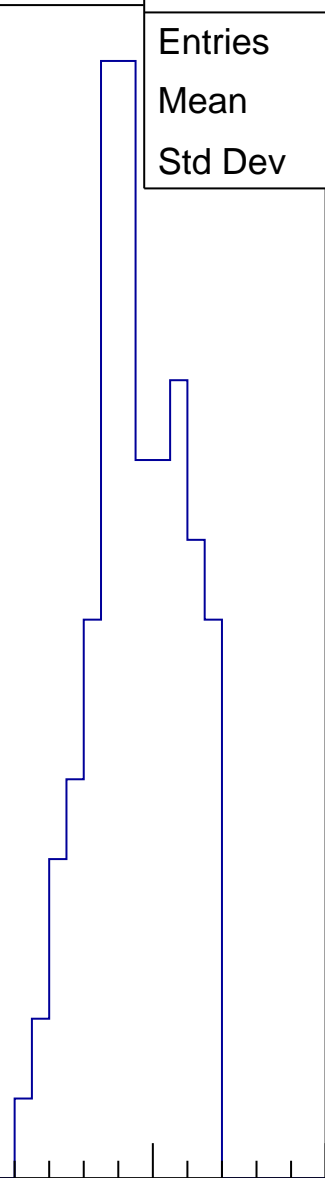
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.54
Std Dev	2.684

ampl



B1L001S, U16-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

56

Mean

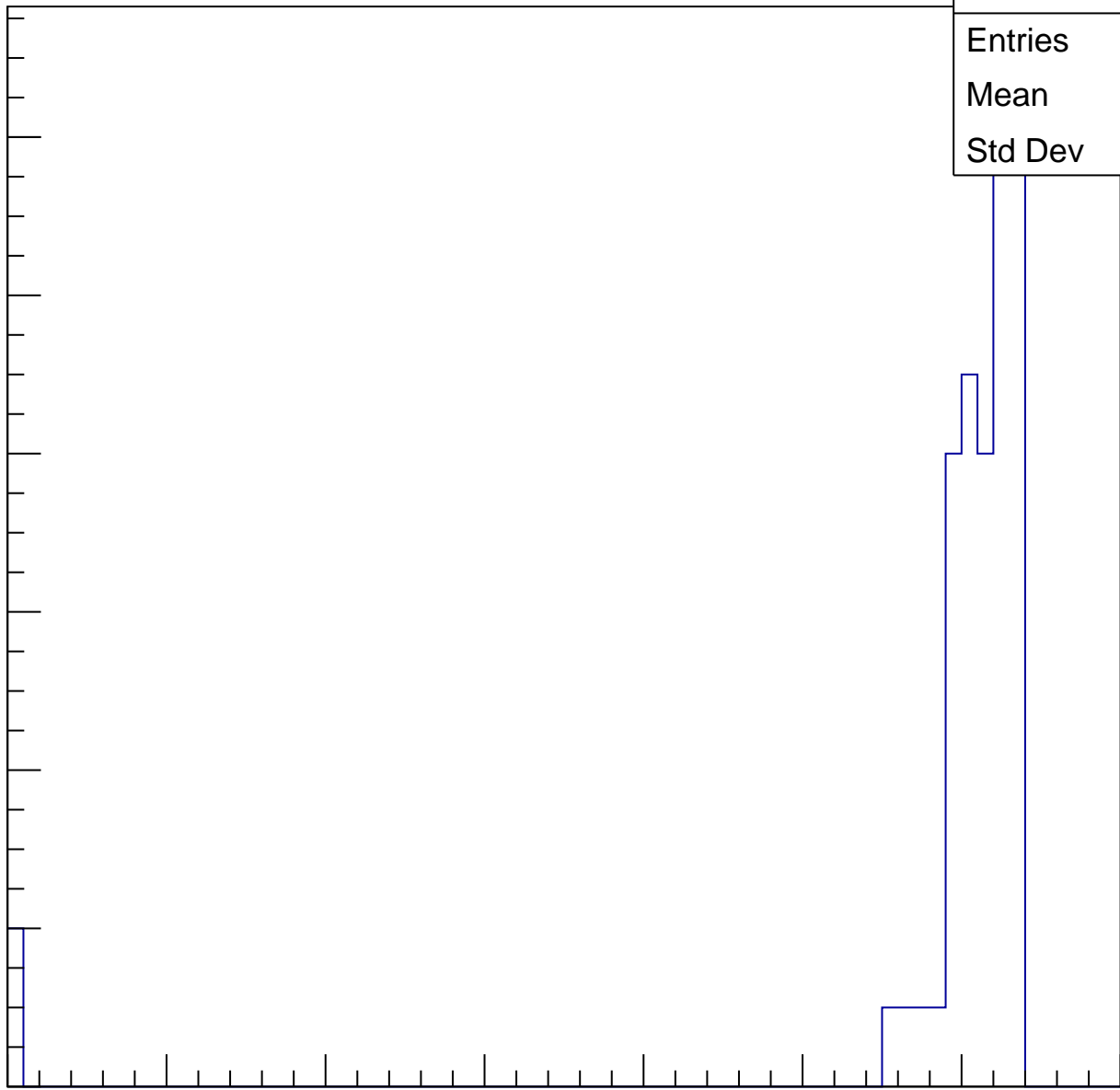
58.73

Std Dev

11.45

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch101, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	30.93
Std Dev	4.074

Gaus mean : 31.4653

Gaus Width: 2.8265

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

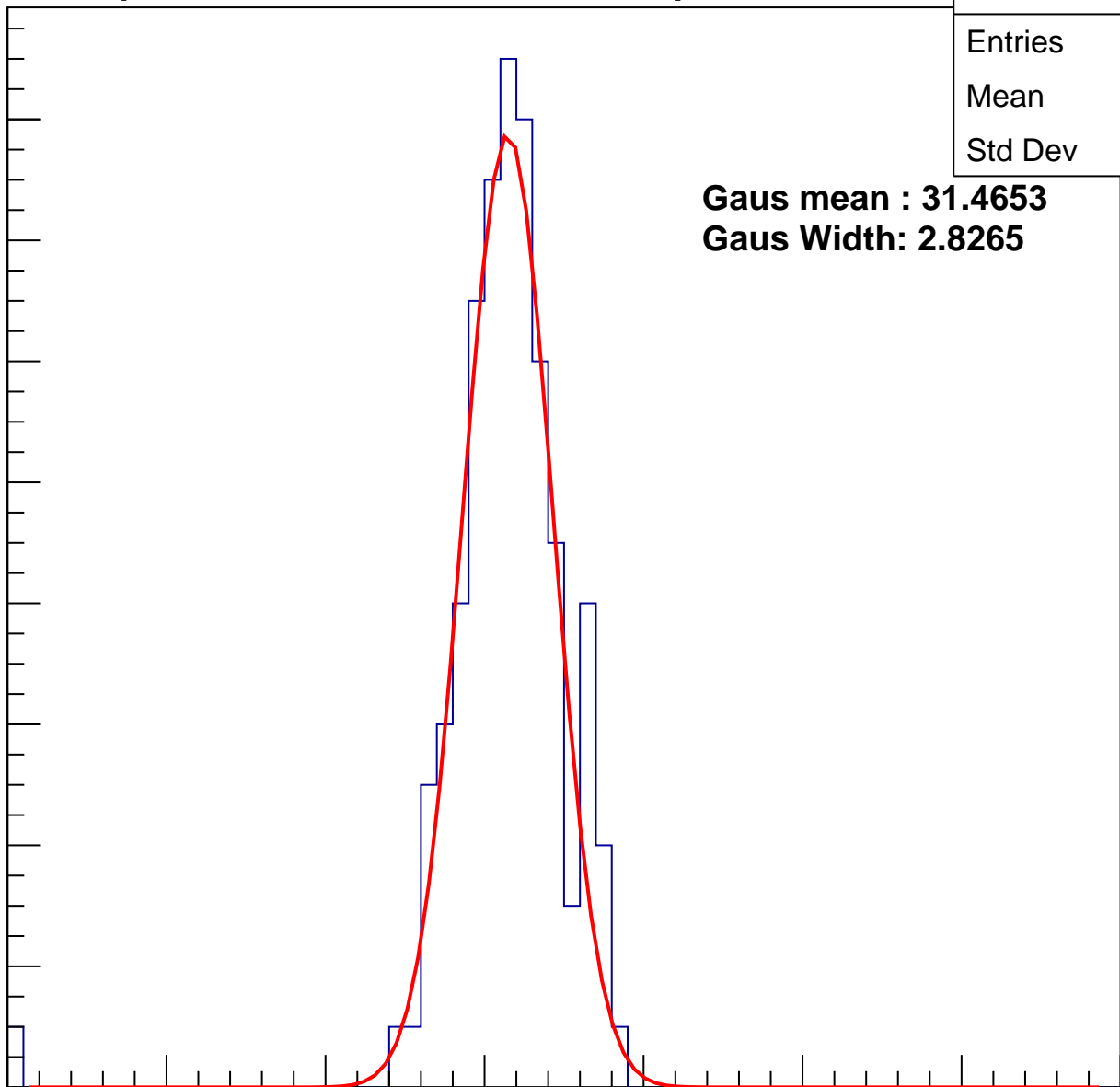
40

50

60

70

ampl



B1L001S, U16-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	38.28
Std Dev	3.642

Gaus mean : 38.8201

Gaus Width: 3.7662

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

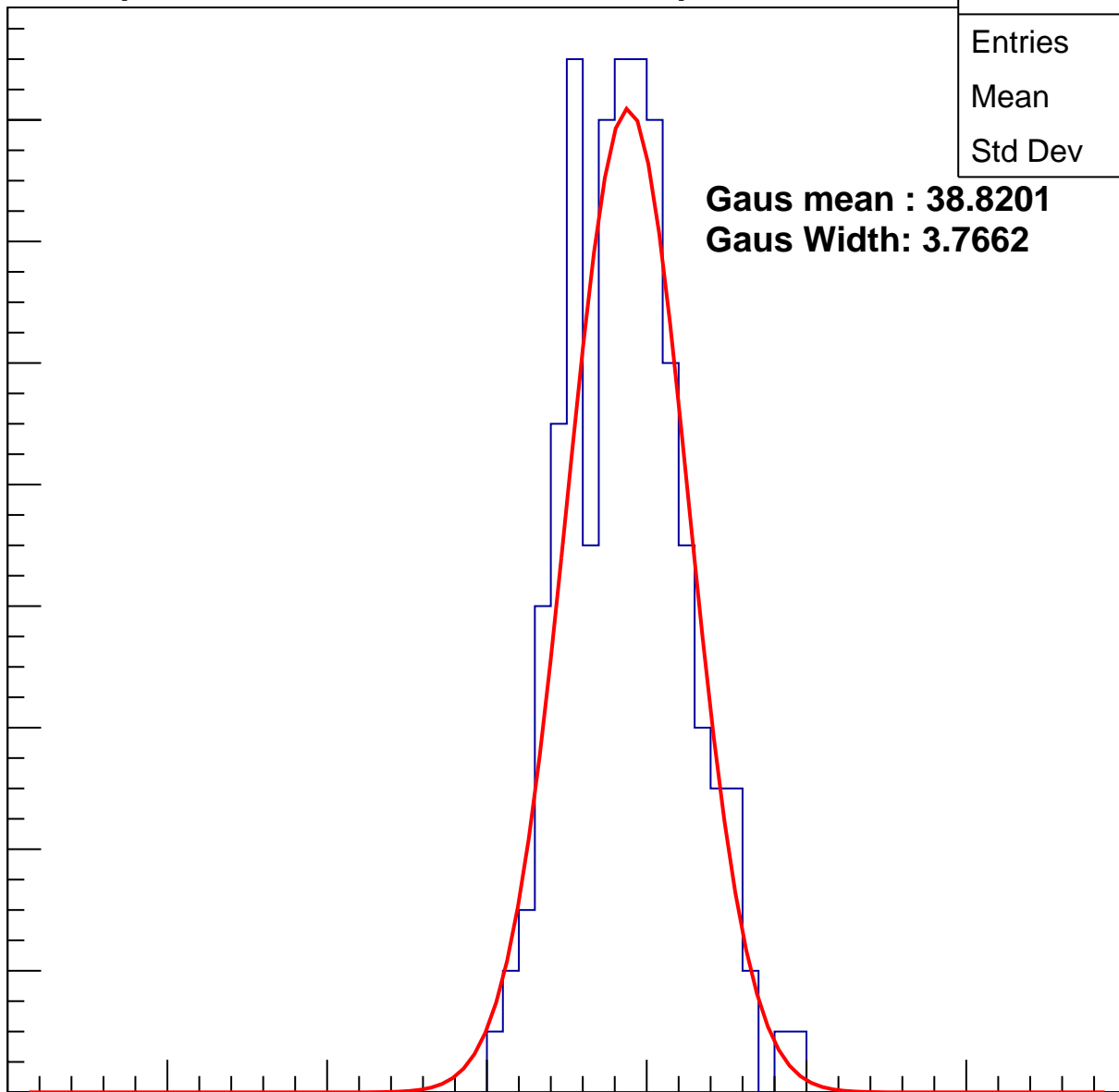
40

50

60

70

ampl



B1L001S, U16-ch101, adc2

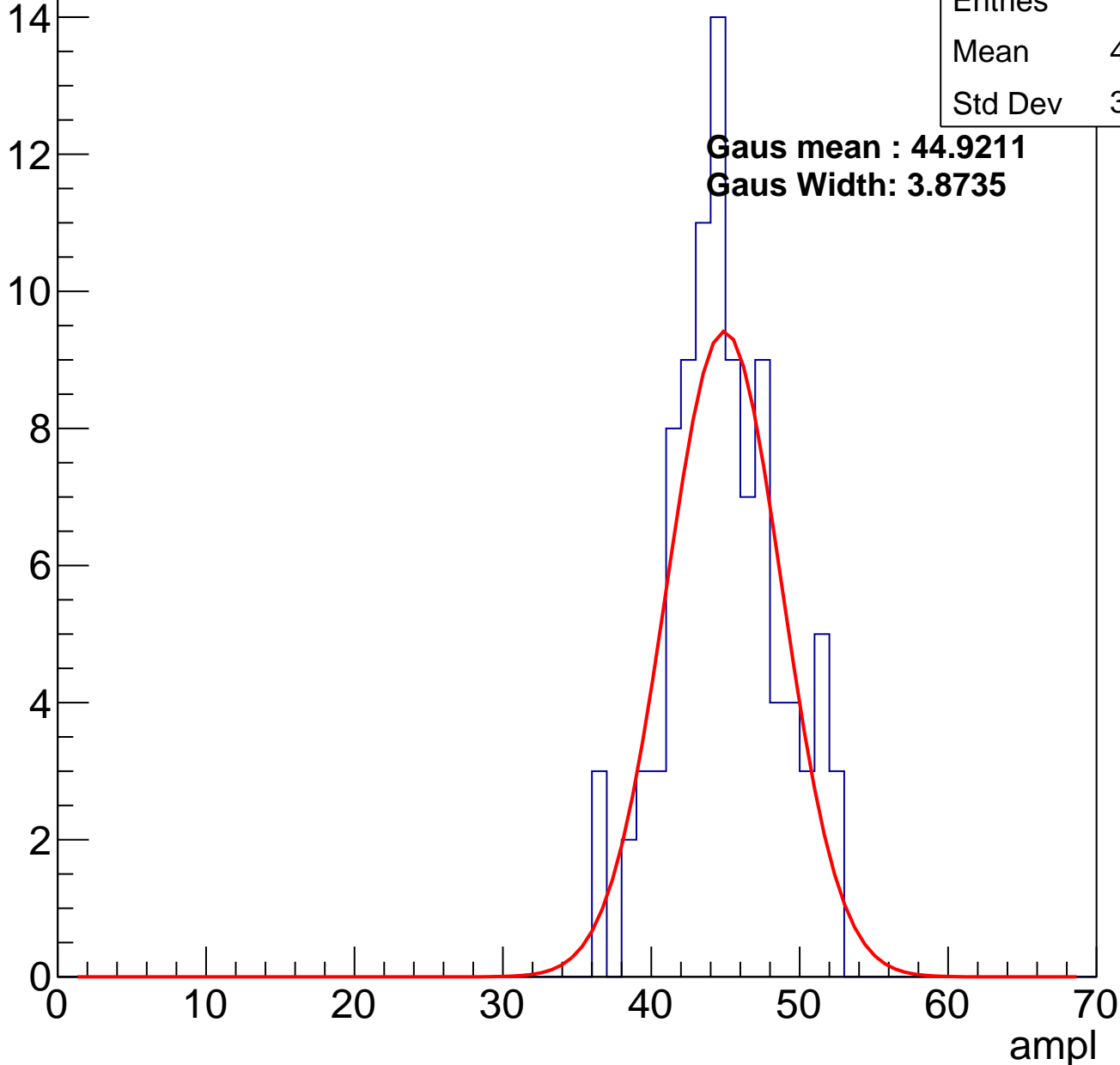
calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	44.48
Std Dev	3.675

Gaus mean : 44.9211

Gaus Width: 3.8735

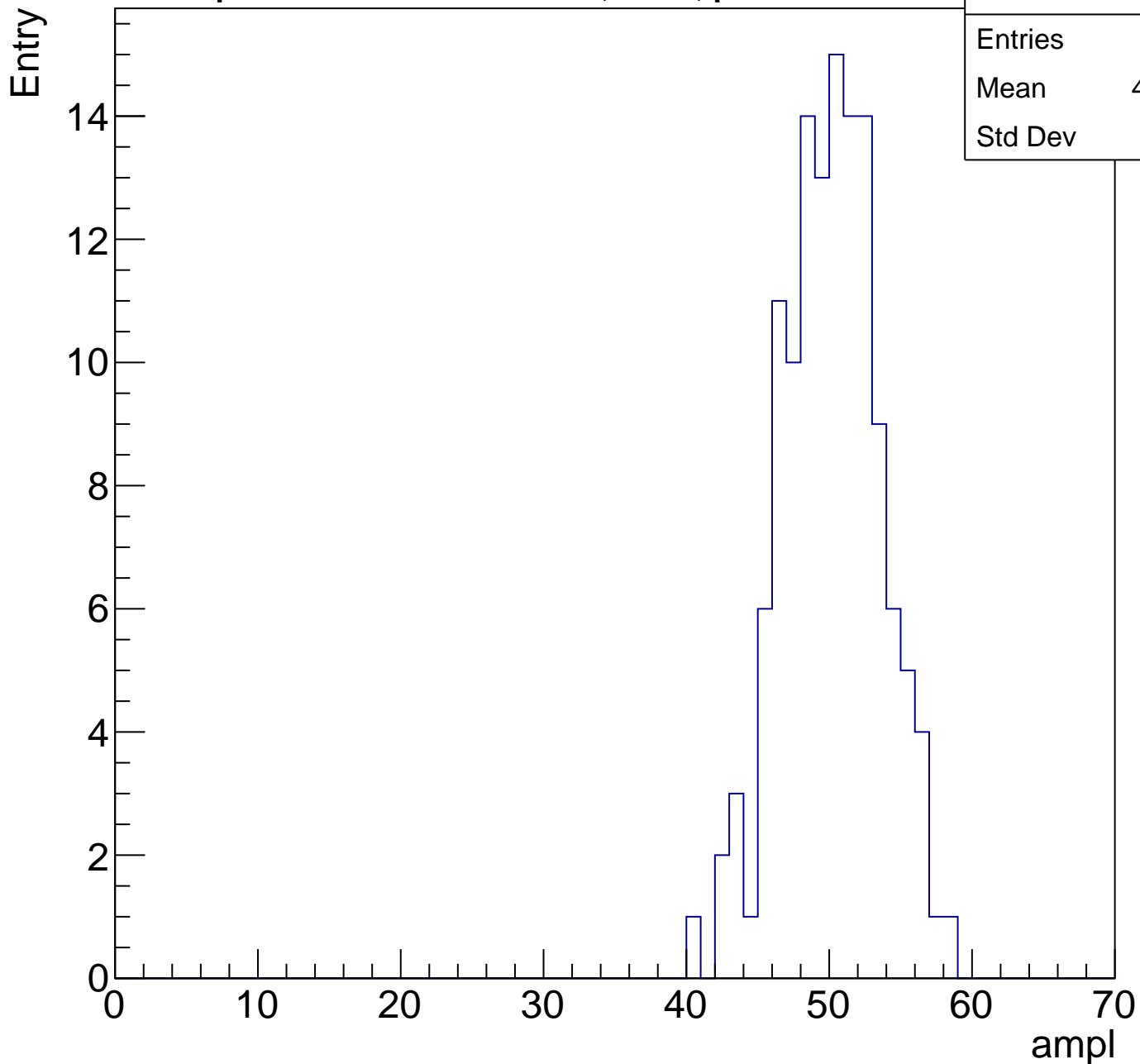
Entry



B1L001S, U16-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	49.68
Std Dev	3.41



B1L001S, U16-ch101, adc4

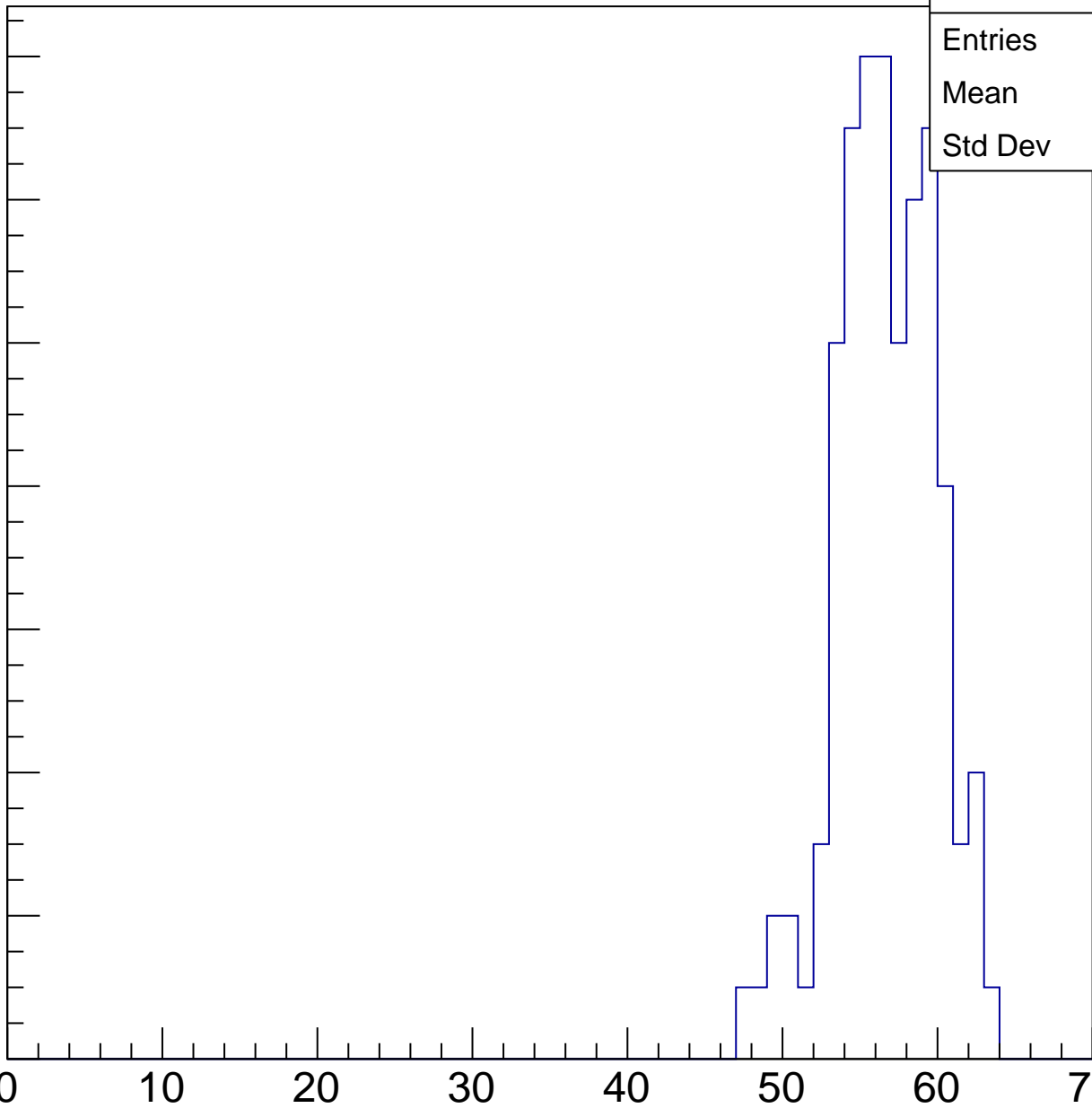
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	56.19
Std Dev	3.15

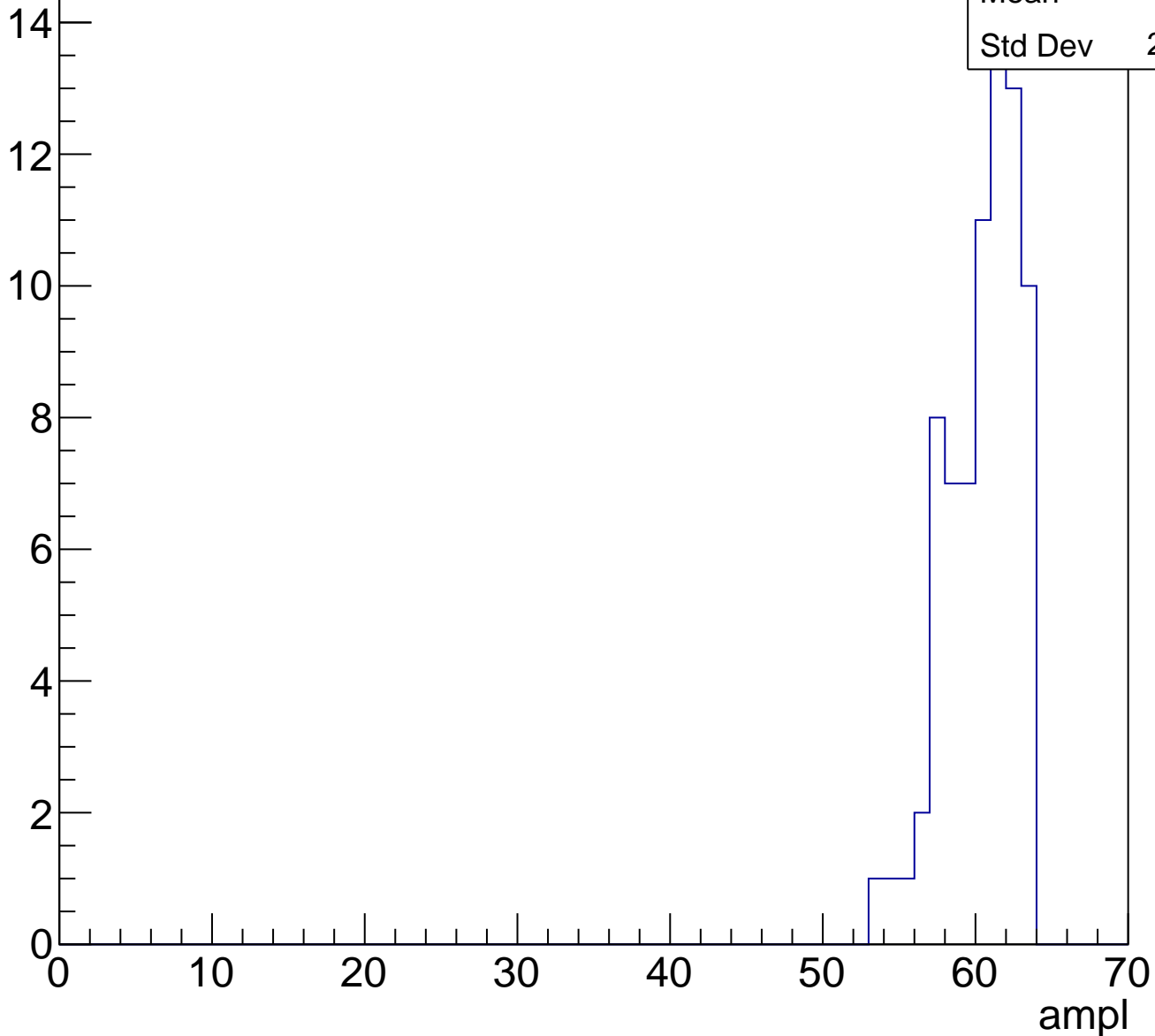
ampl



B1L001S, U16-ch101, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

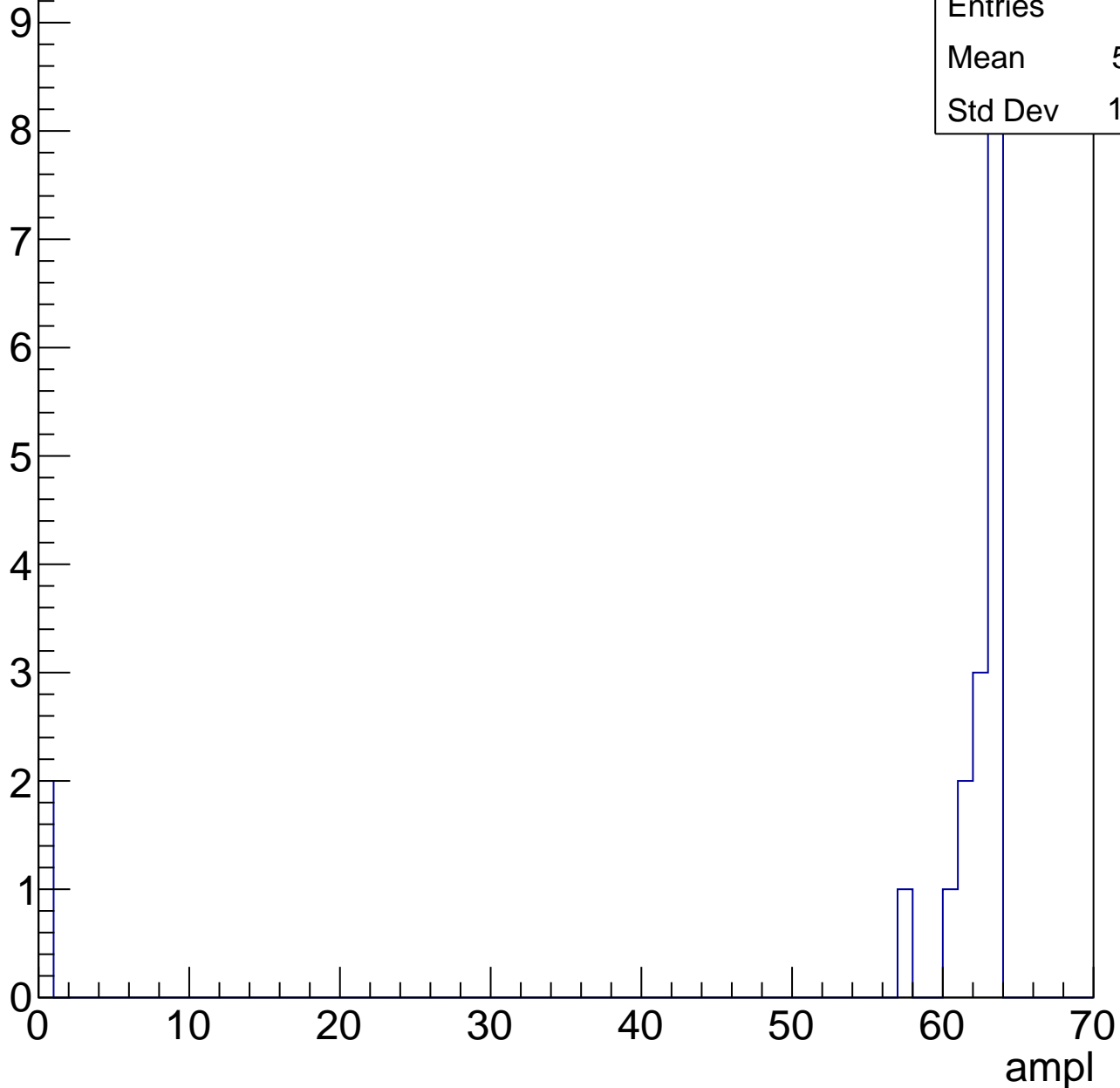


B1L001S, U16-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	55.11
Std Dev	19.54



B1L001S, U16-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch102, adc0

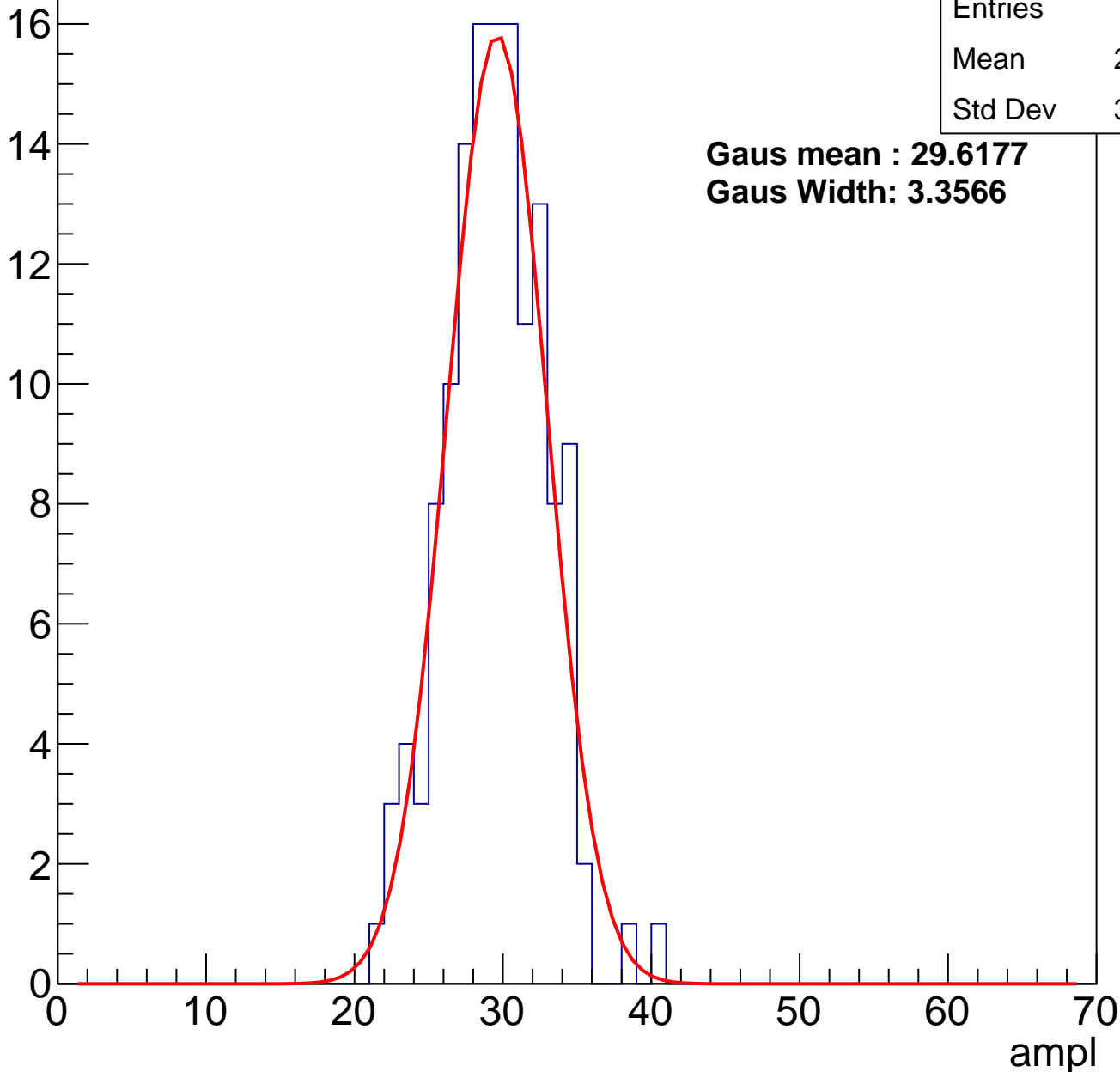
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	29.09
Std Dev	3.346

Gaus mean : 29.6177

Gaus Width: 3.3566

Entry



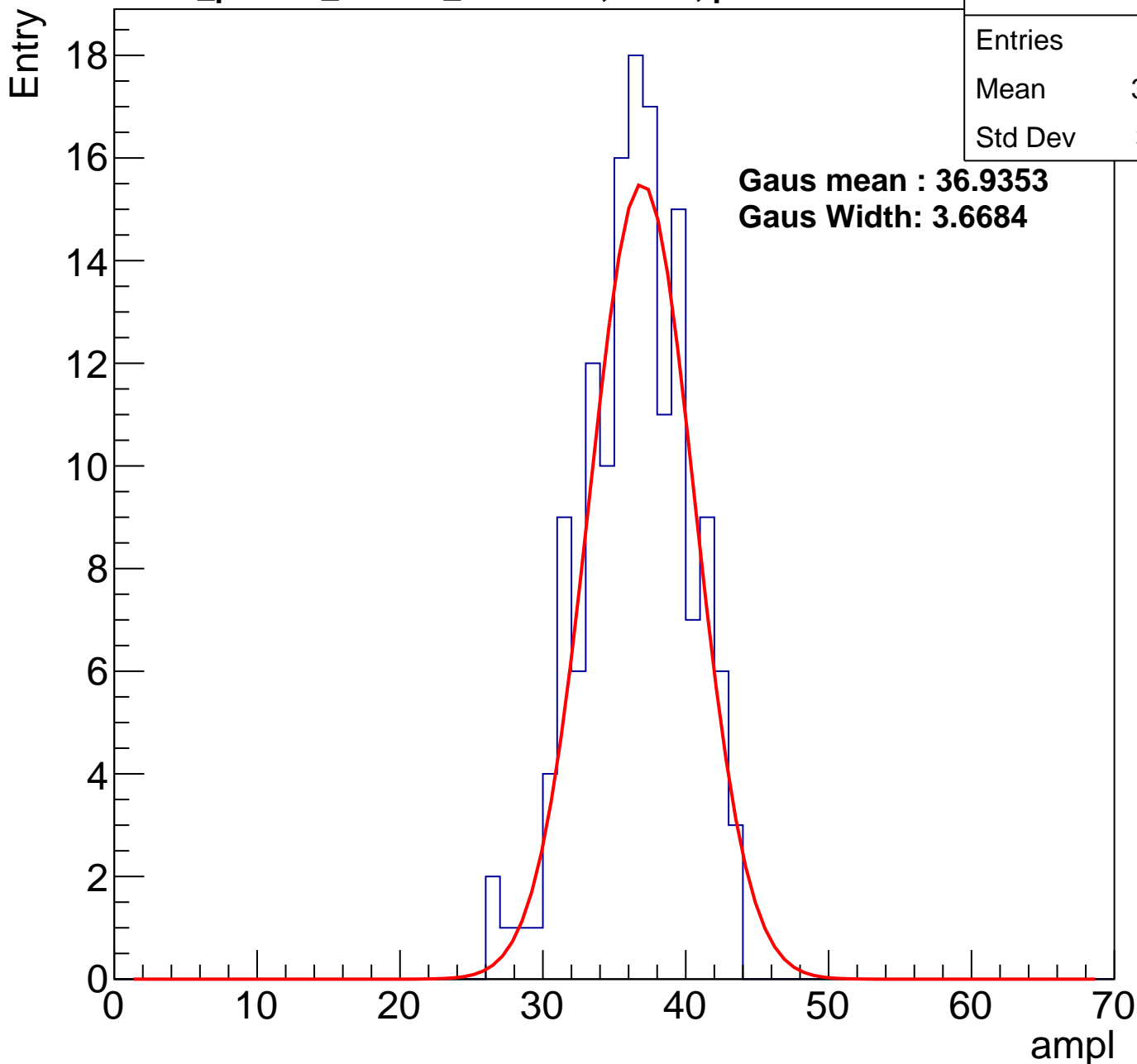
B1L001S, U16-ch102, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.03
Std Dev	3.601

Gaus mean : 36.9353

Gaus Width: 3.6684



B1L001S, U16-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

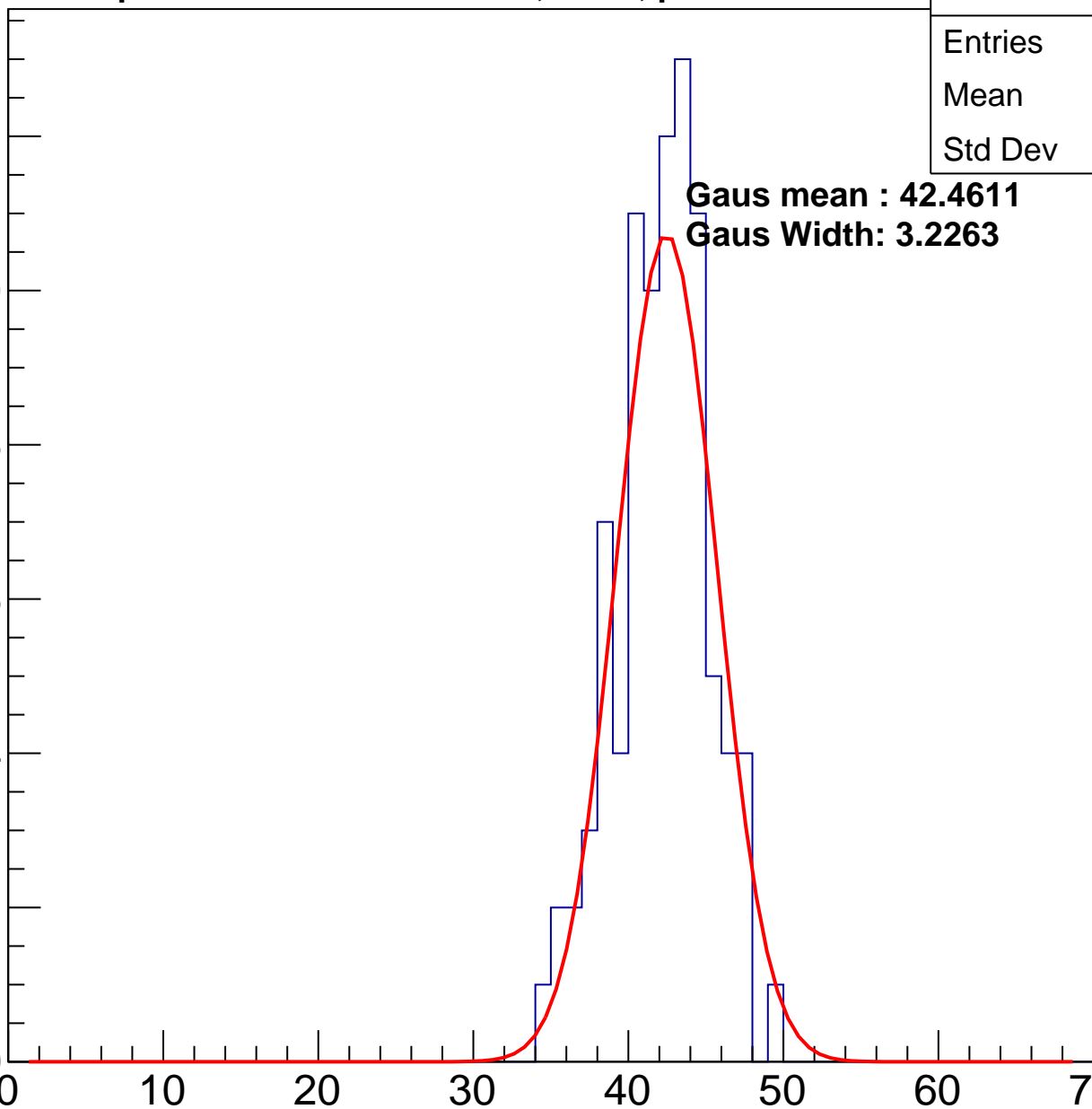
Entries	90
Mean	41.69
Std Dev	3.013

Gaus mean : 42.4611

Gaus Width: 3.2263

0 10 20 30 40 50 60 70

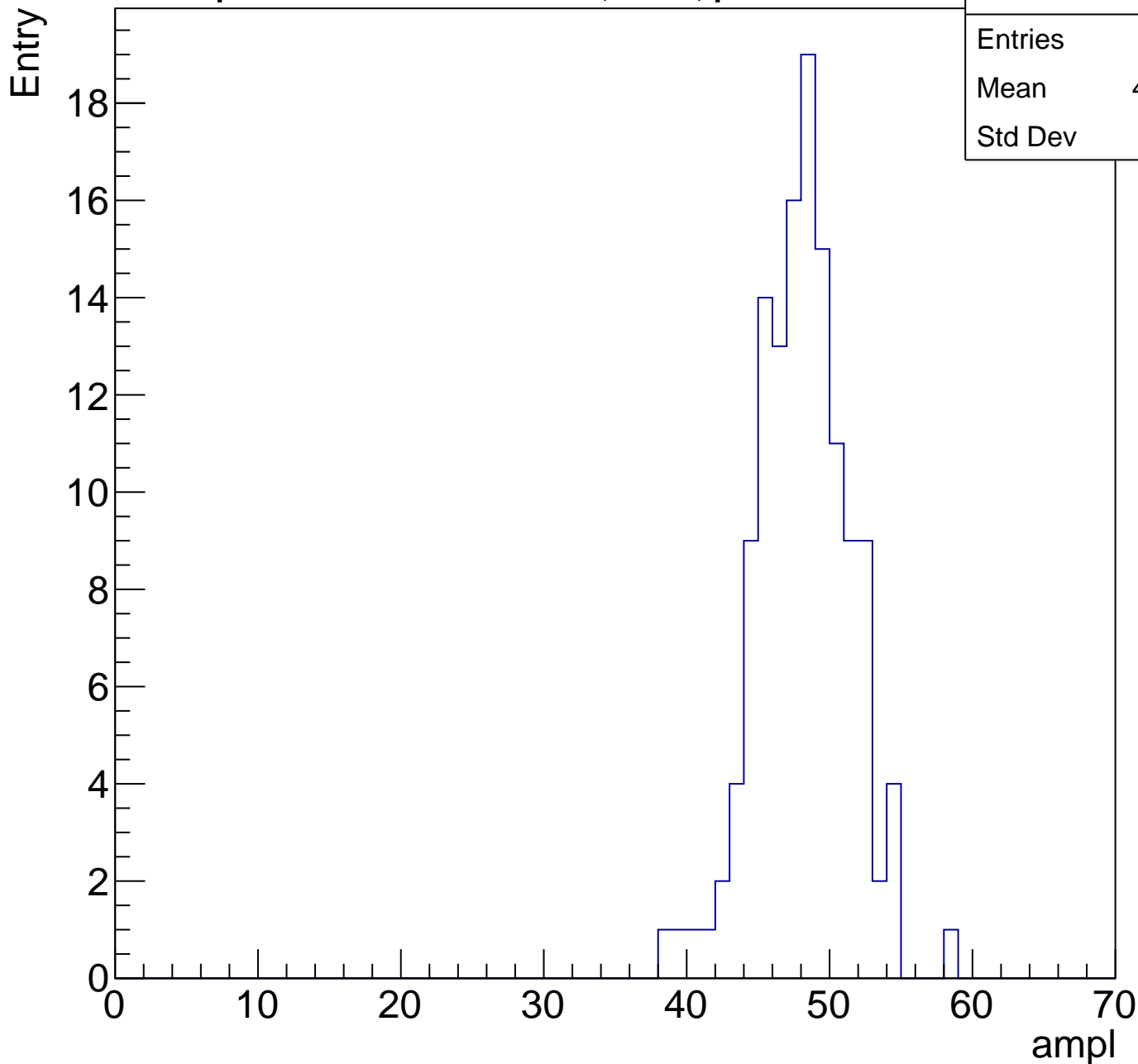
ampl



B1L001S, U16-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

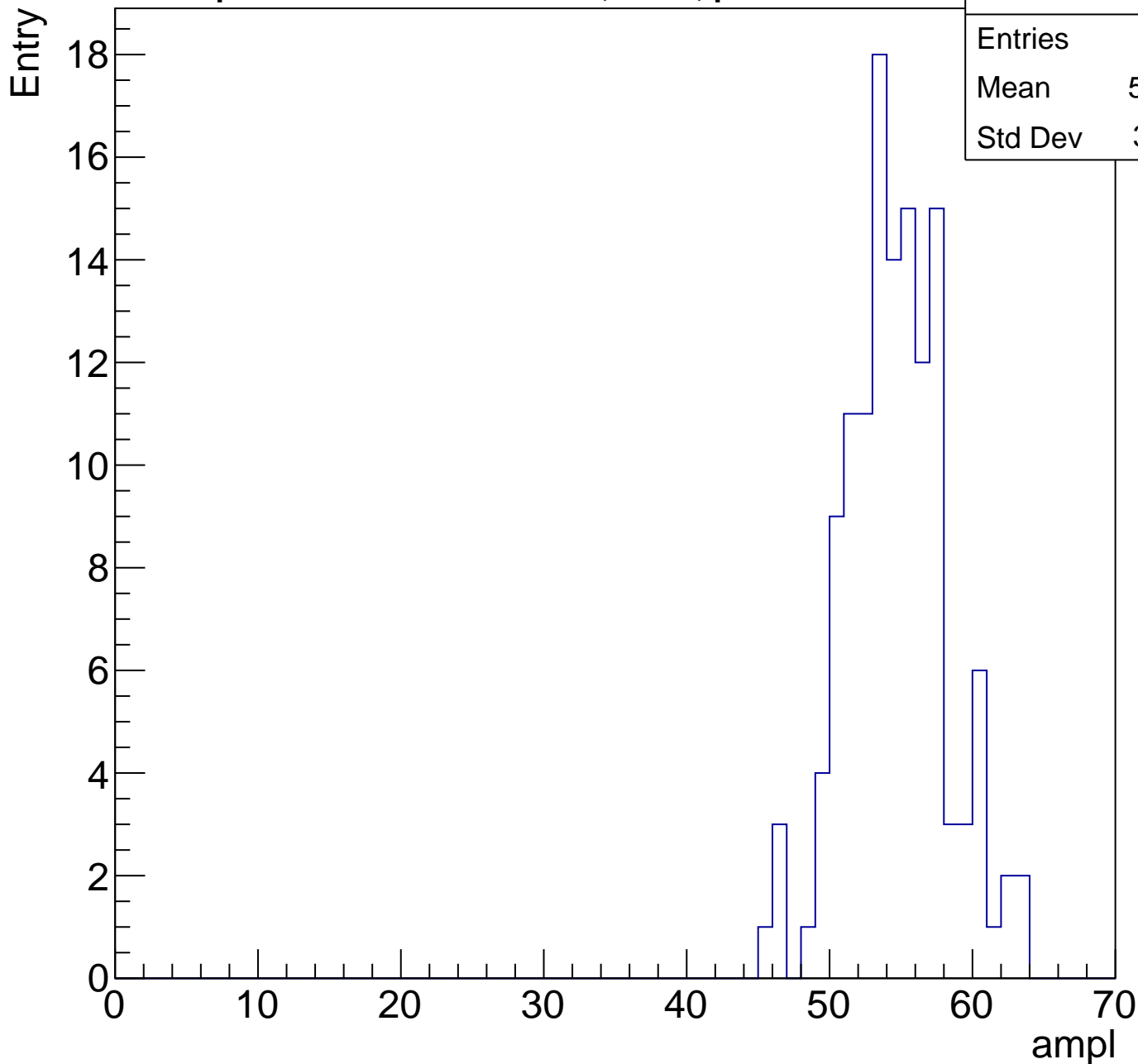
Entries	132
Mean	47.68
Std Dev	3.22



B1L001S, U16-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	54.15
Std Dev	3.481



B1L001S, U16-ch102, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

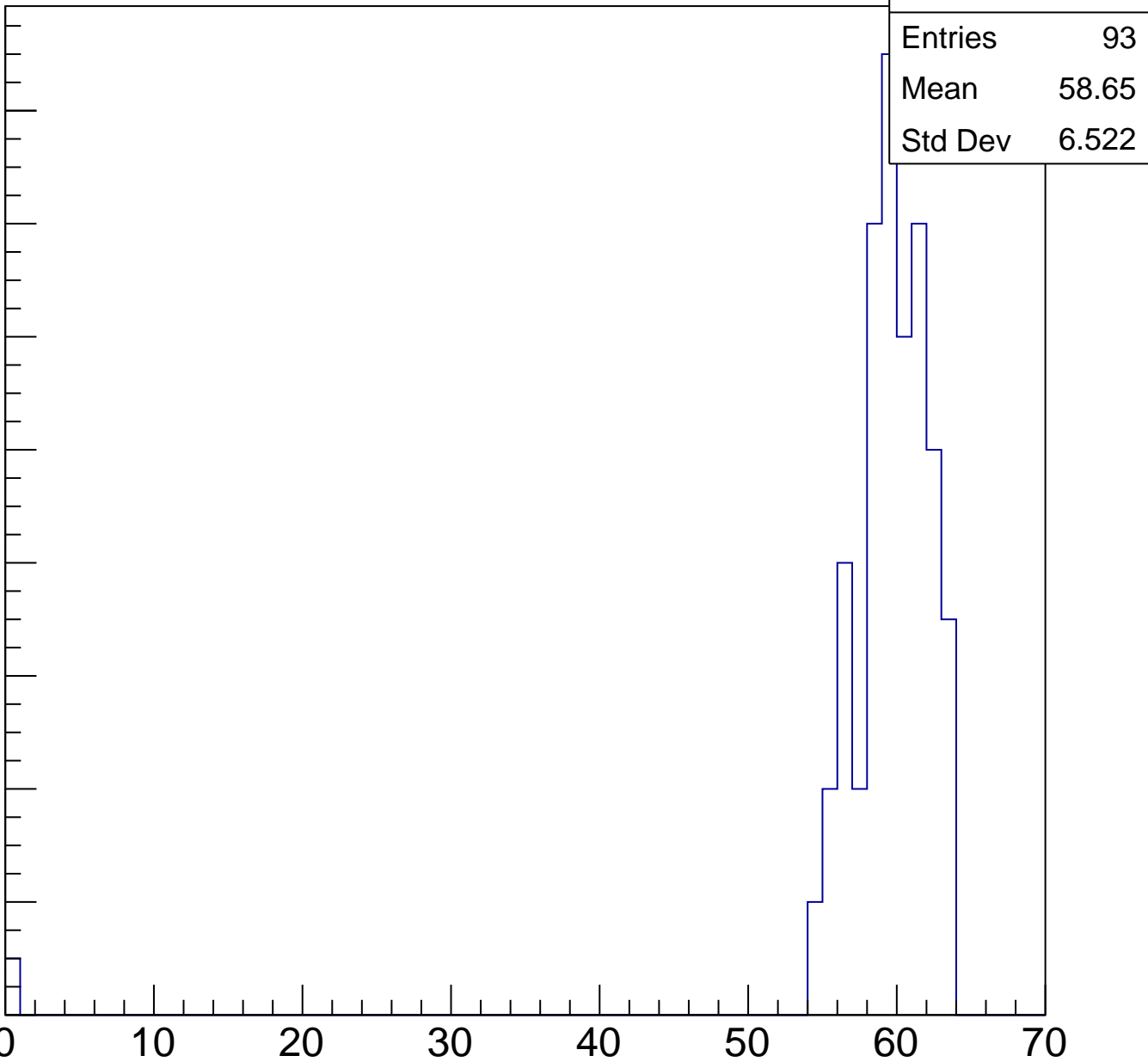
0

Entries 93

Mean 58.65

Std Dev 6.522

ampl



B1L001S, U16-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

Entries	31
Mean	59.42
Std Dev	11.02

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch103, adc0

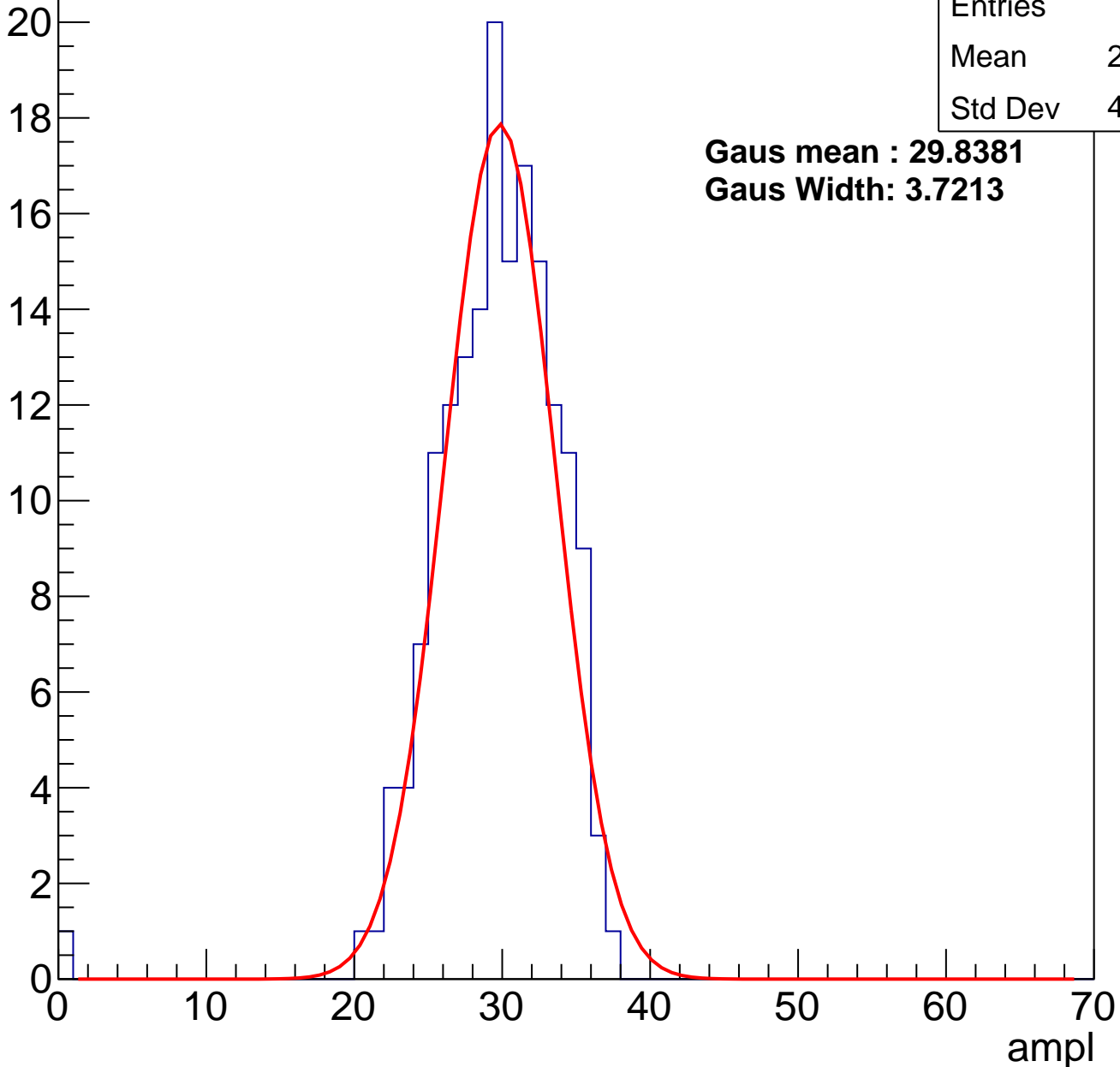
calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	29.16
Std Dev	4.235

Gaus mean : 29.8381

Gaus Width: 3.7213

Entry



B1L001S, U16-ch103, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	36.09
Std Dev	3.175

Gaus mean : 36.4824

Gaus Width: 3.0952

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

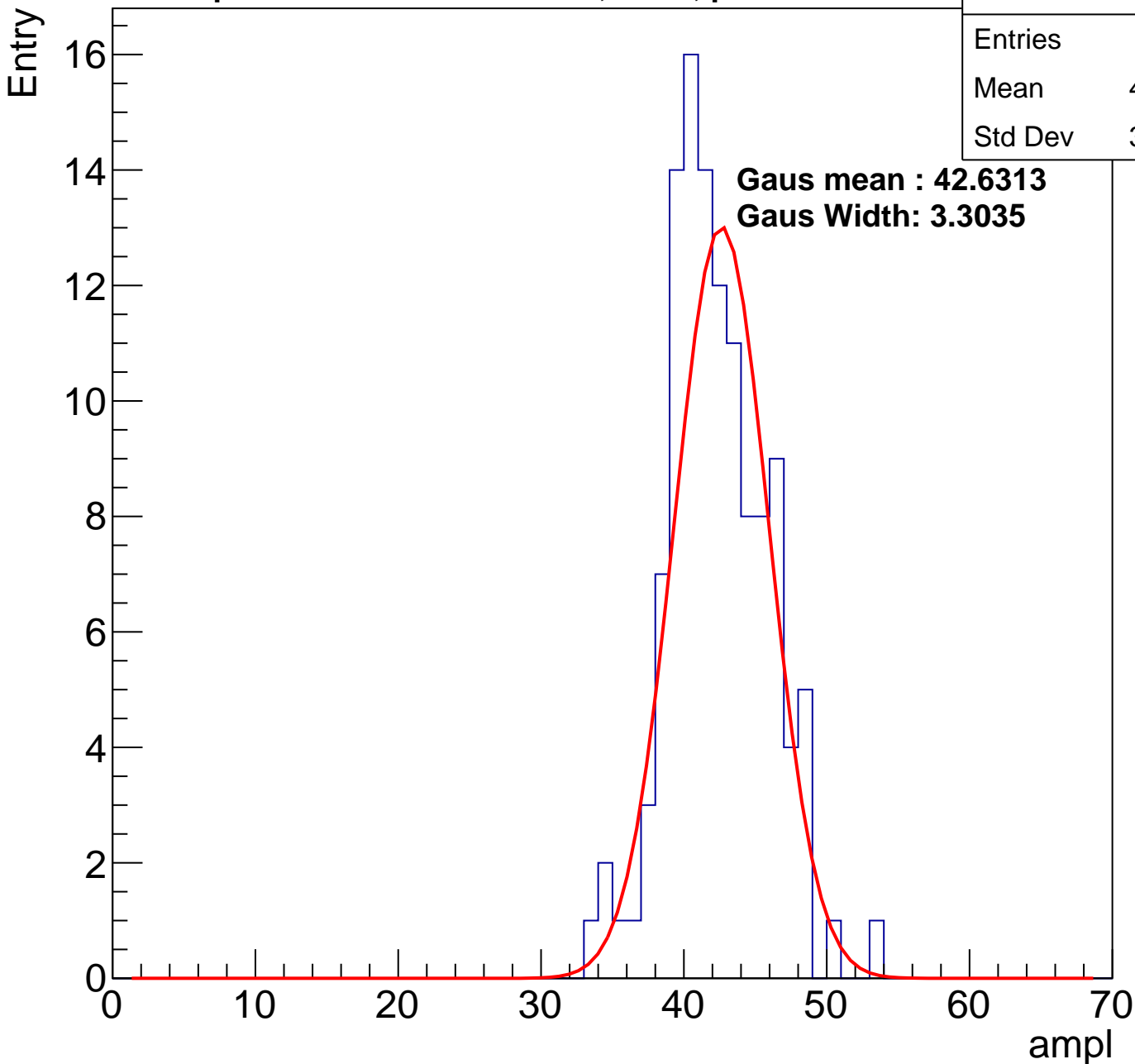
B1L001S, U16-ch103, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	41.89
Std Dev	3.454

Gaus mean : 42.6313

Gaus Width: 3.3035

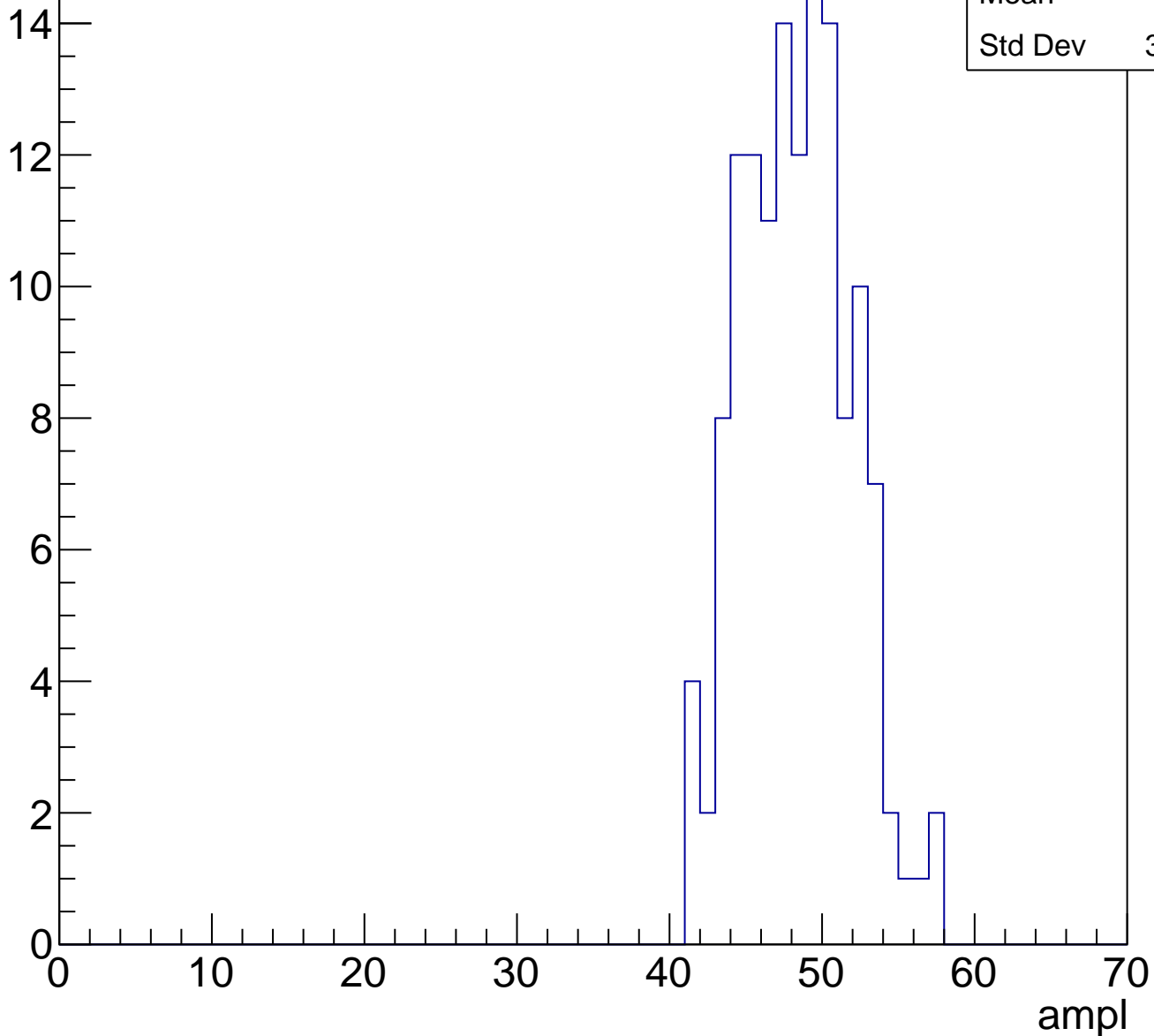


B1L001S, U16-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	47.9
Std Dev	3.496

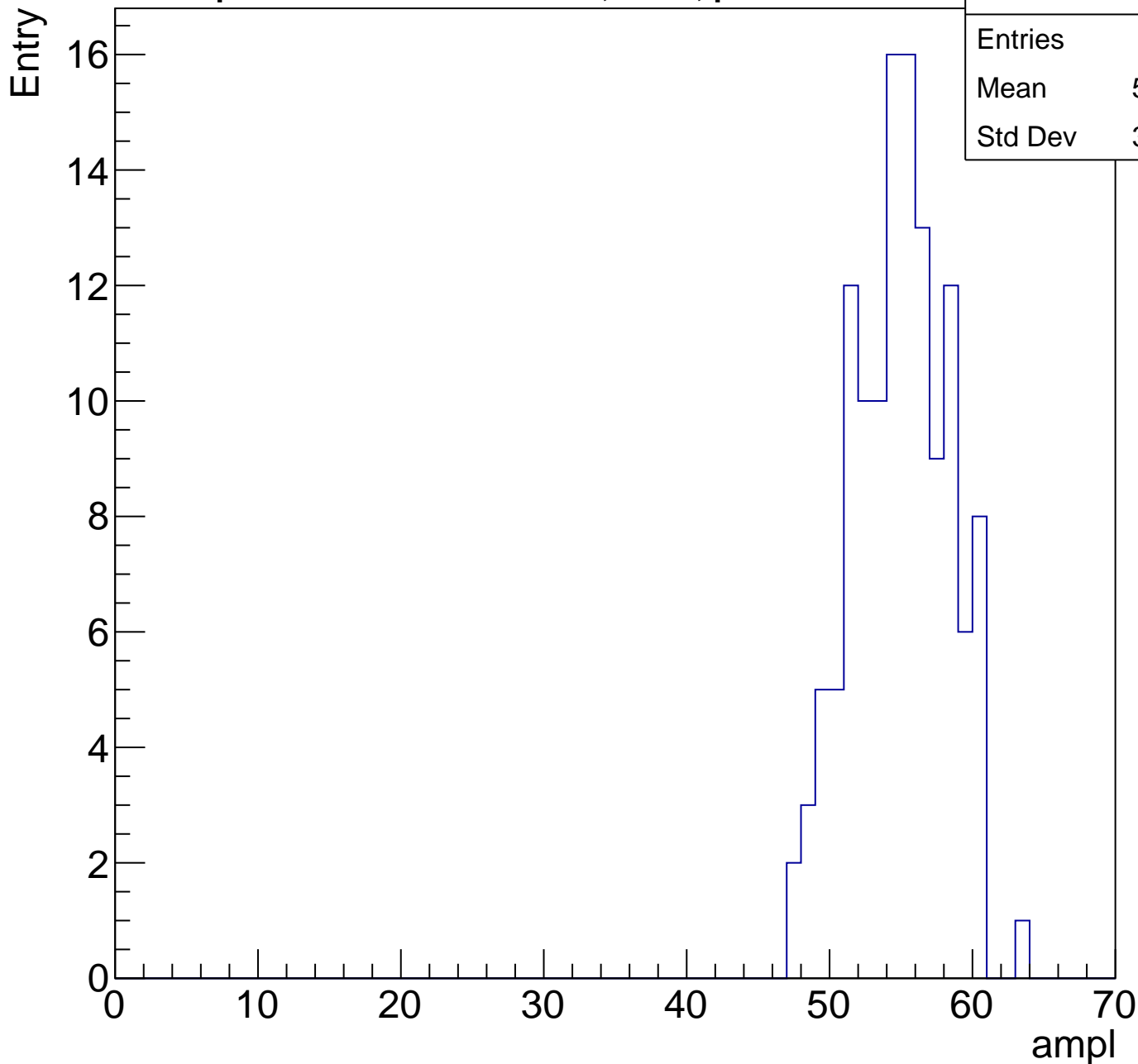
Entry



B1L001S, U16-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	54.48
Std Dev	3.307



B1L001S, U16-ch103, adc5

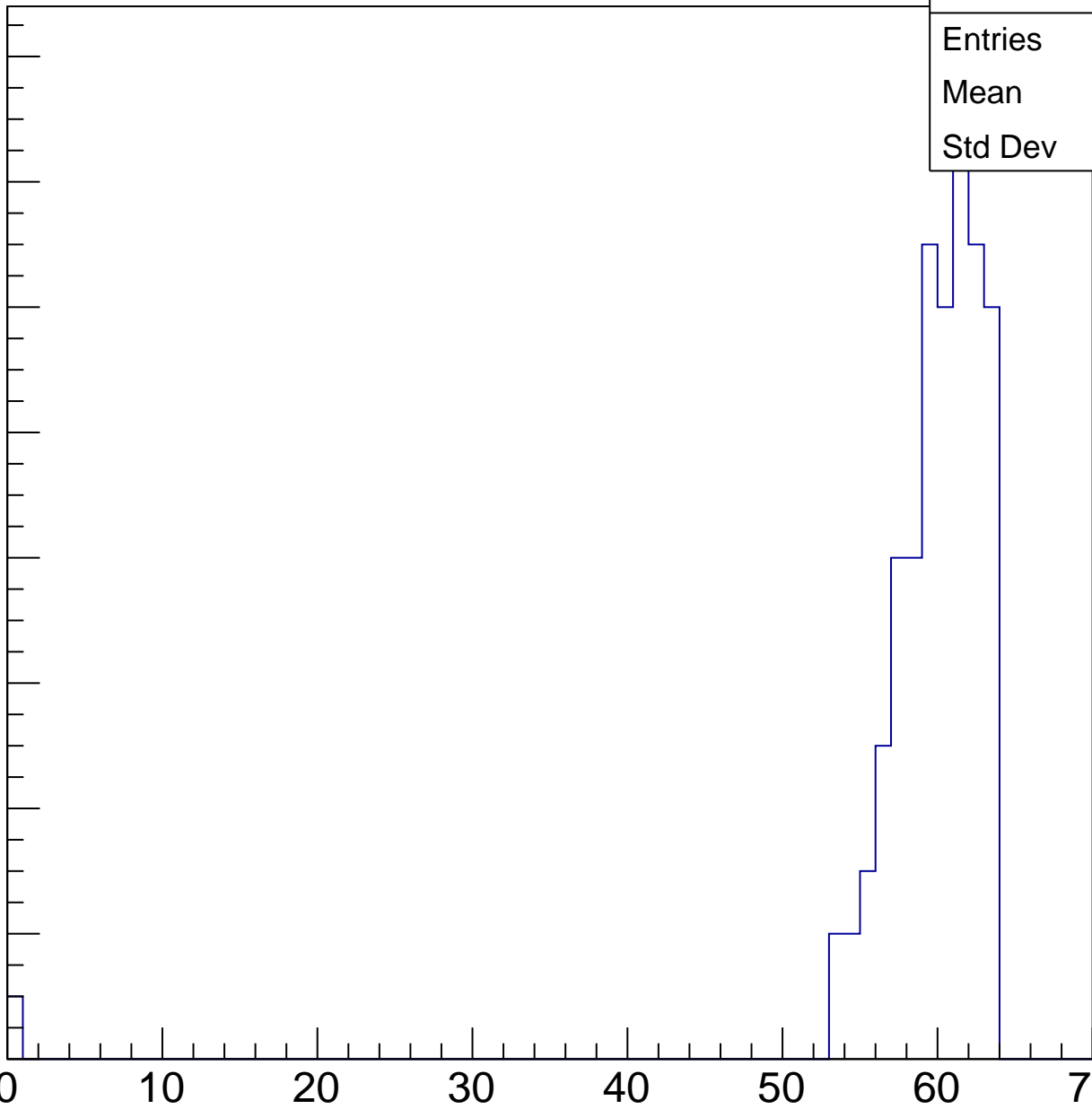
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.99
Std Dev	6.586

ampl

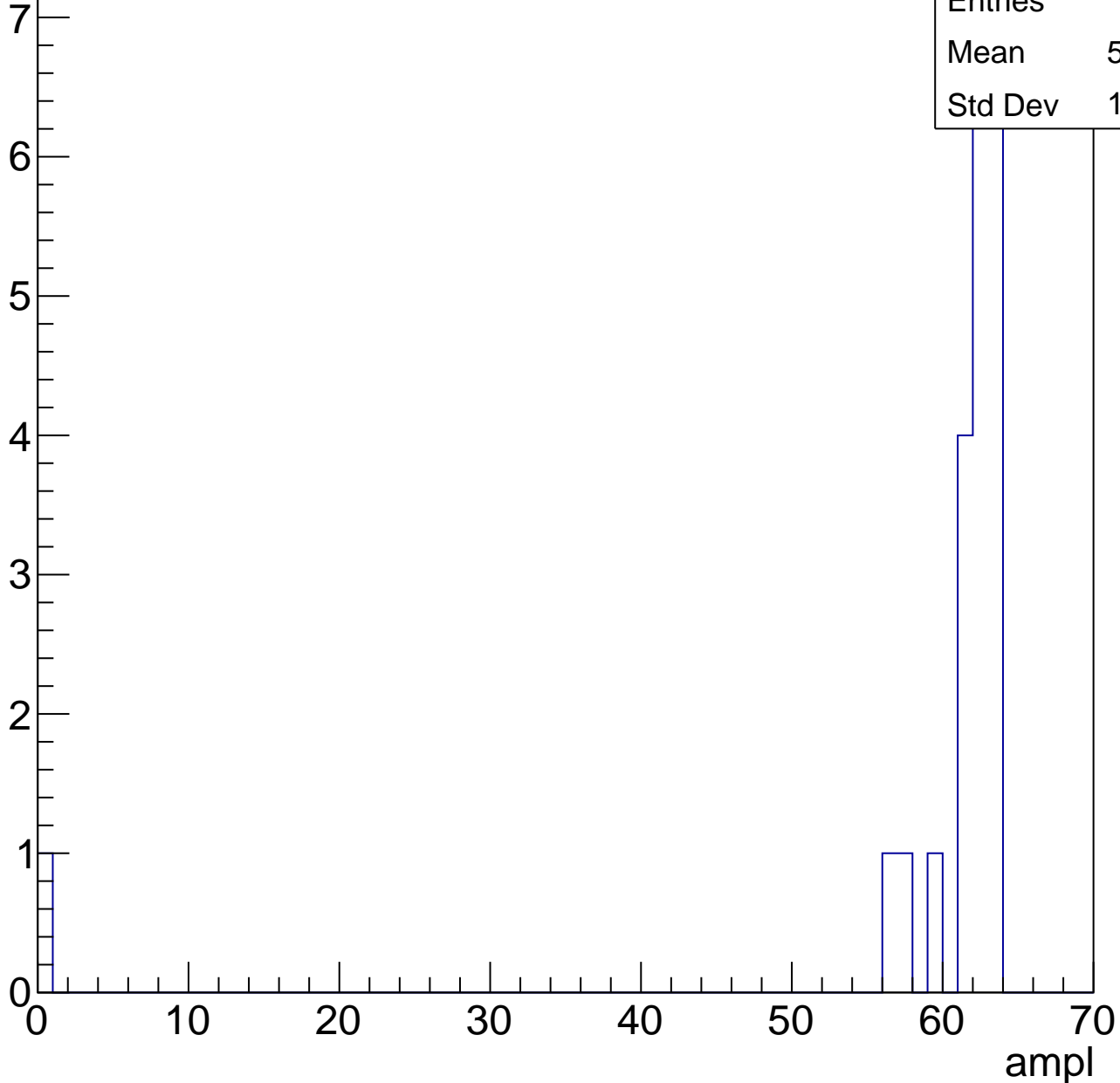


B1L001S, U16-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	58.68
Std Dev	12.94



B1L001S, U16-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L001S, U16-ch104, adc0

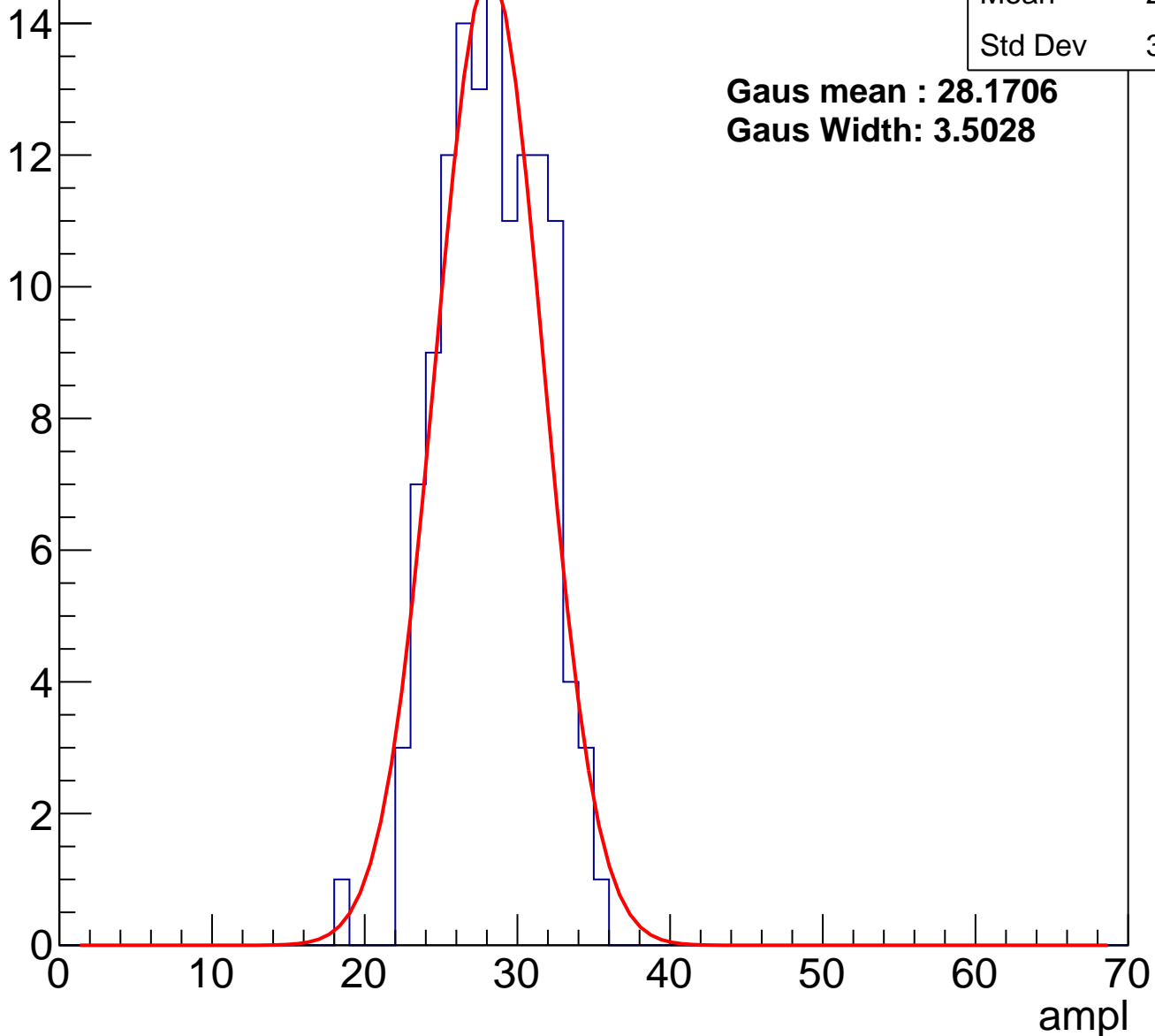
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	27.88
Std Dev	3.189

Gaus mean : 28.1706

Gaus Width: 3.5028

Entry



B1L001S, U16-ch104, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	34.14
Std Dev	3.263

Gaus mean : 35.0084

Gaus Width: 3.3968

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

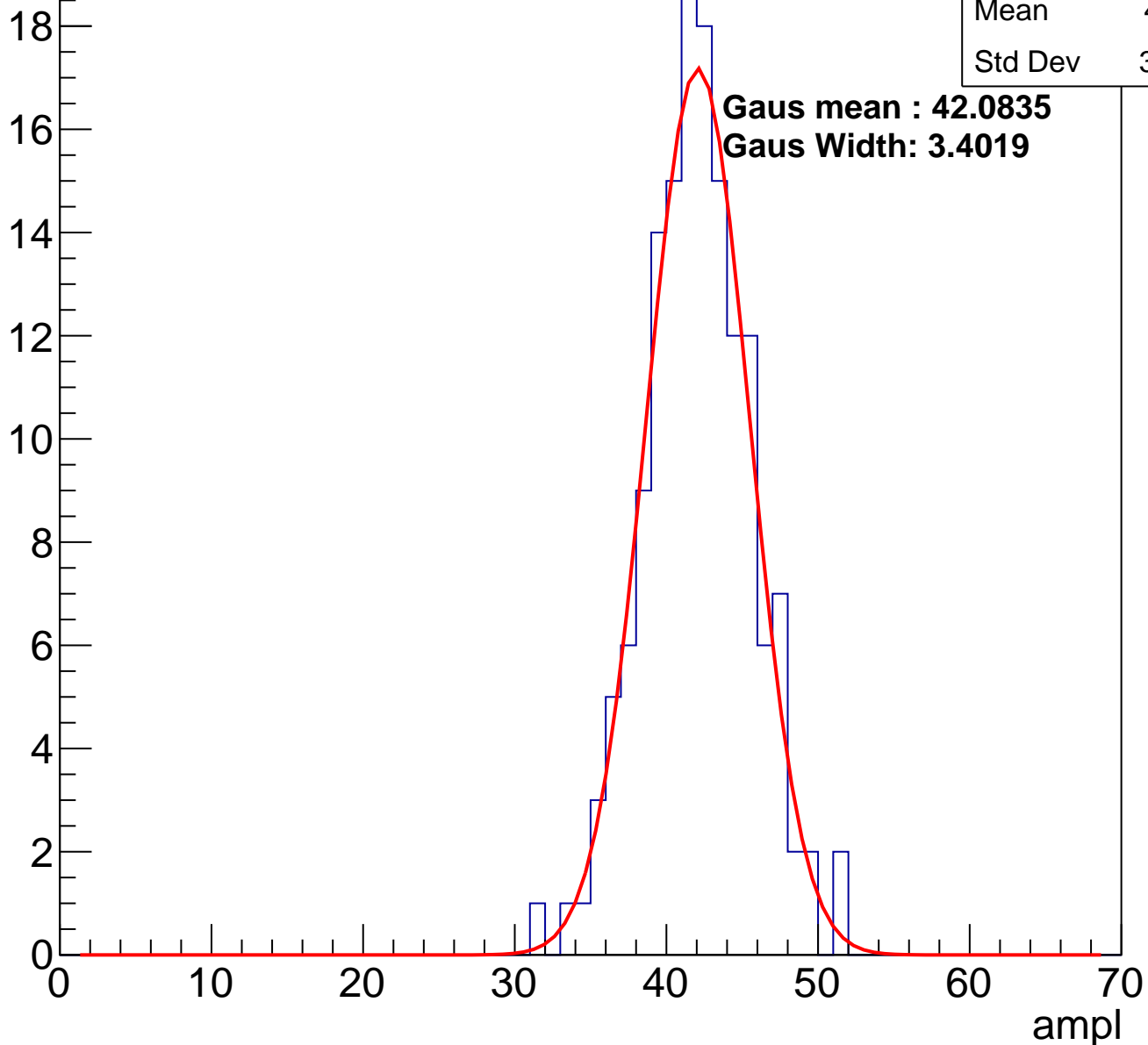
60

70

B1L001S, U16-ch104, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch104, adc3

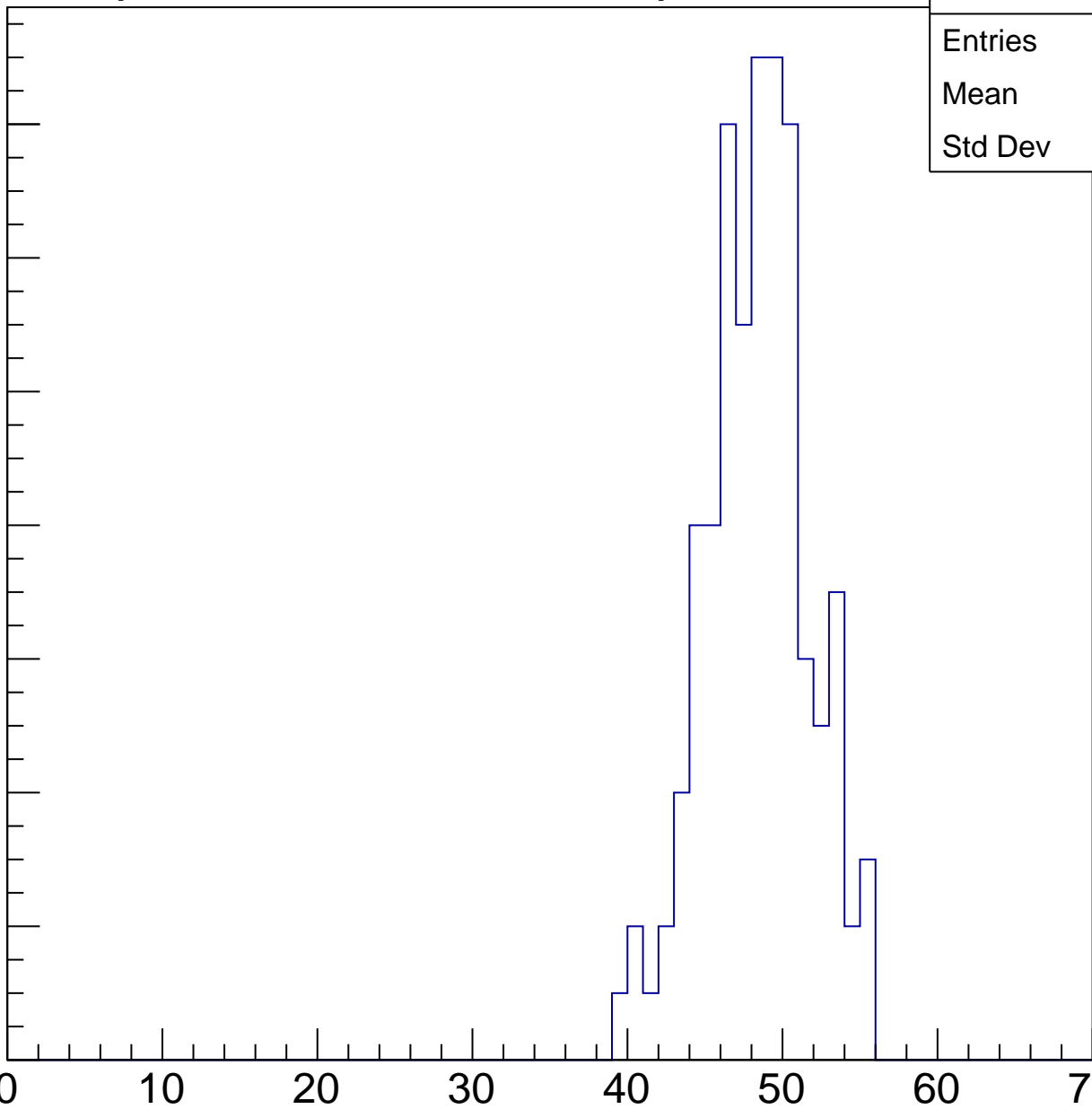
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	47.92
Std Dev	3.313

ampl



B1L001S, U16-ch104, adc4

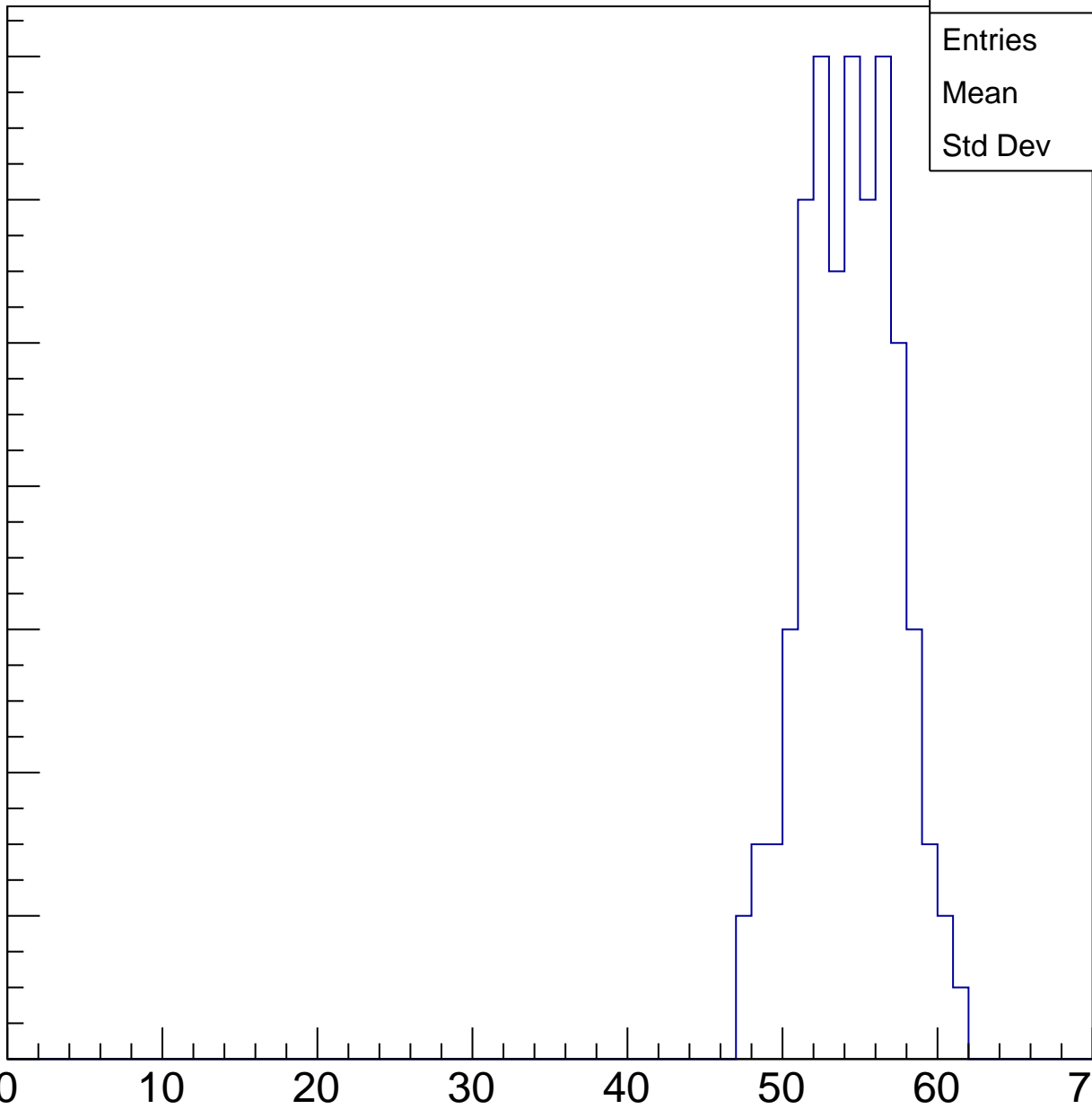
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	53.84
Std Dev	2.974

ampl



B1L001S, U16-ch104, adc5

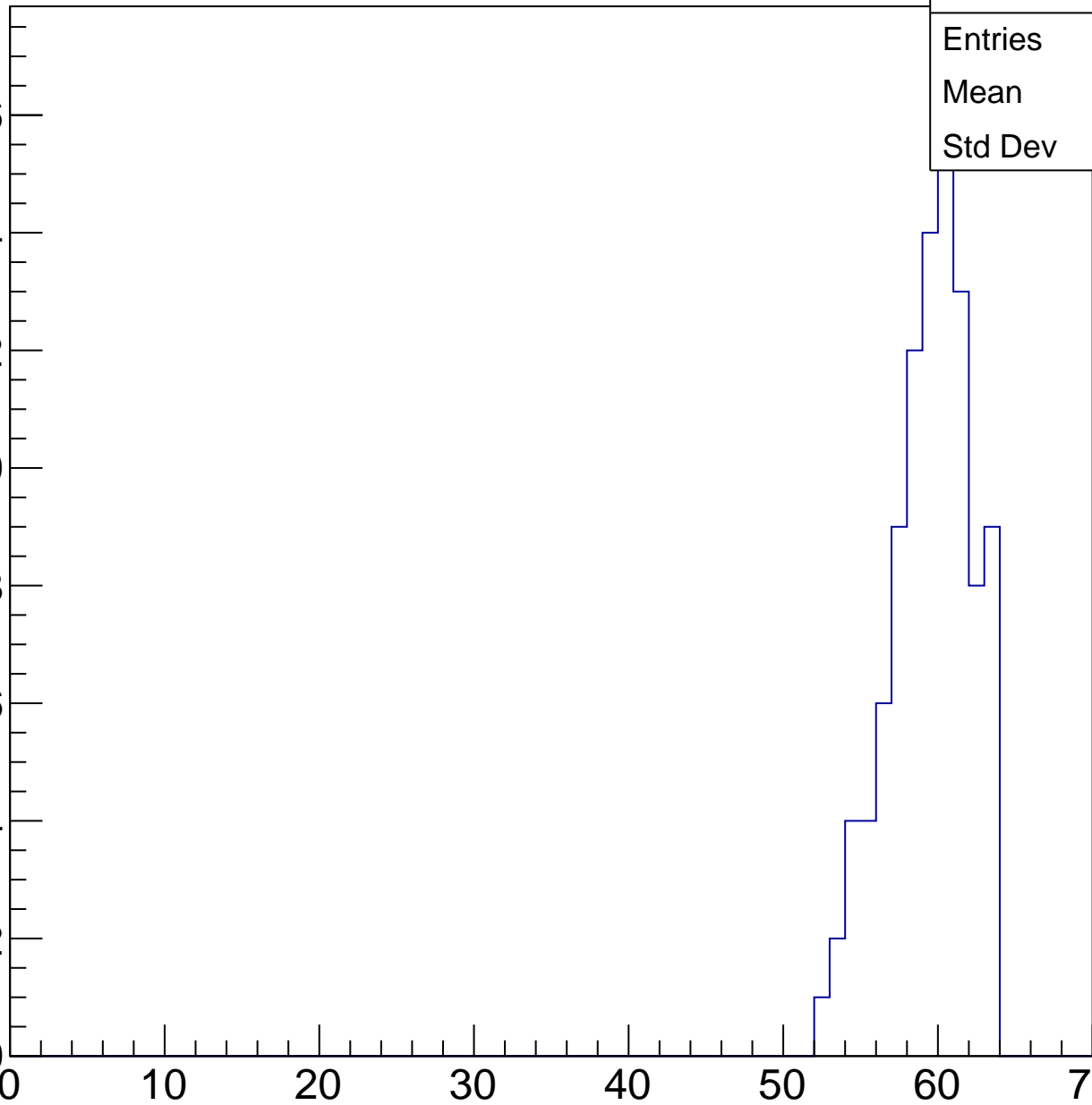
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59
Std Dev	2.605

ampl



B1L001S, U16-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

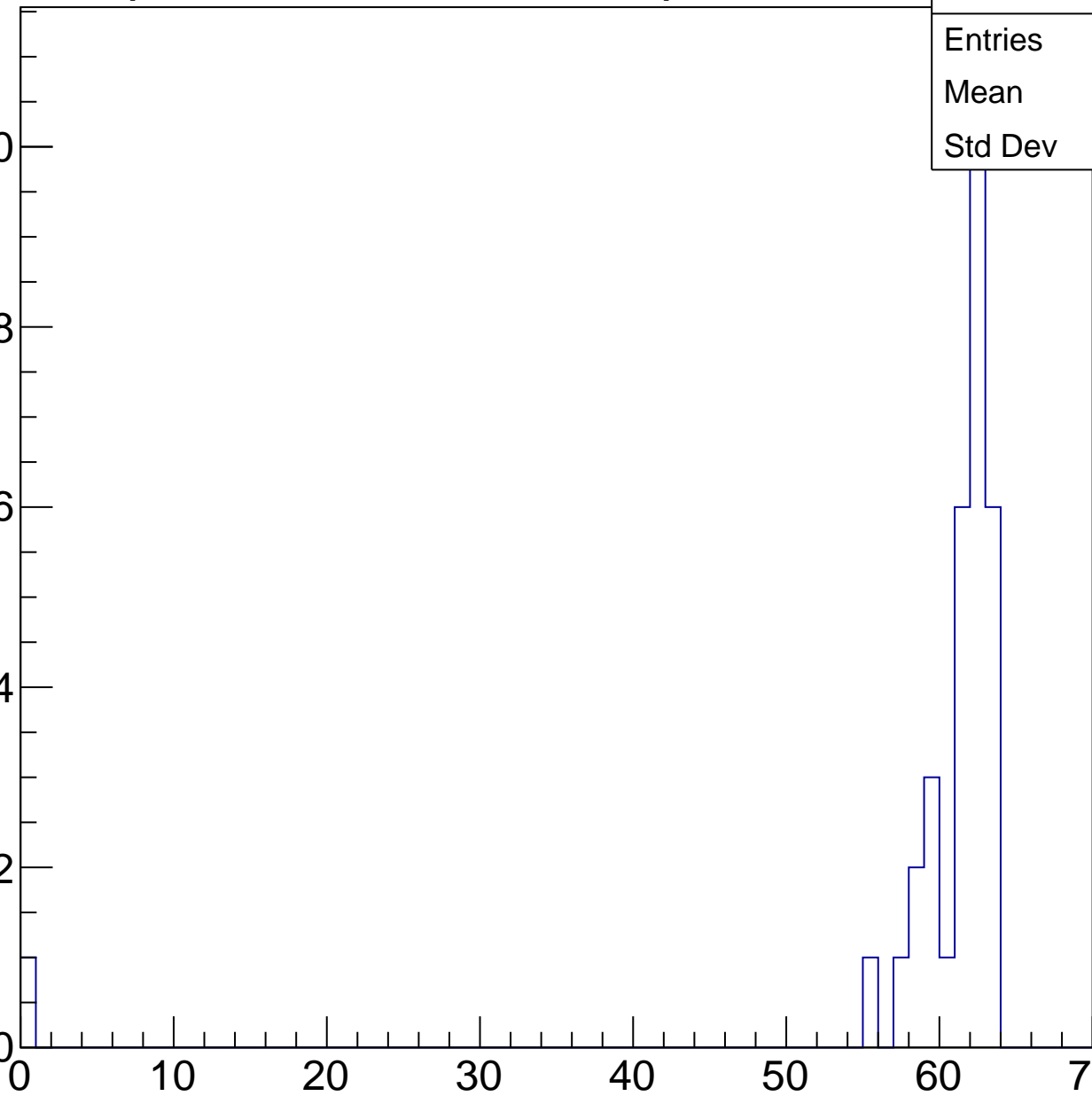
4

2

0

Entries	32
Mean	59.09
Std Dev	10.79

ampl

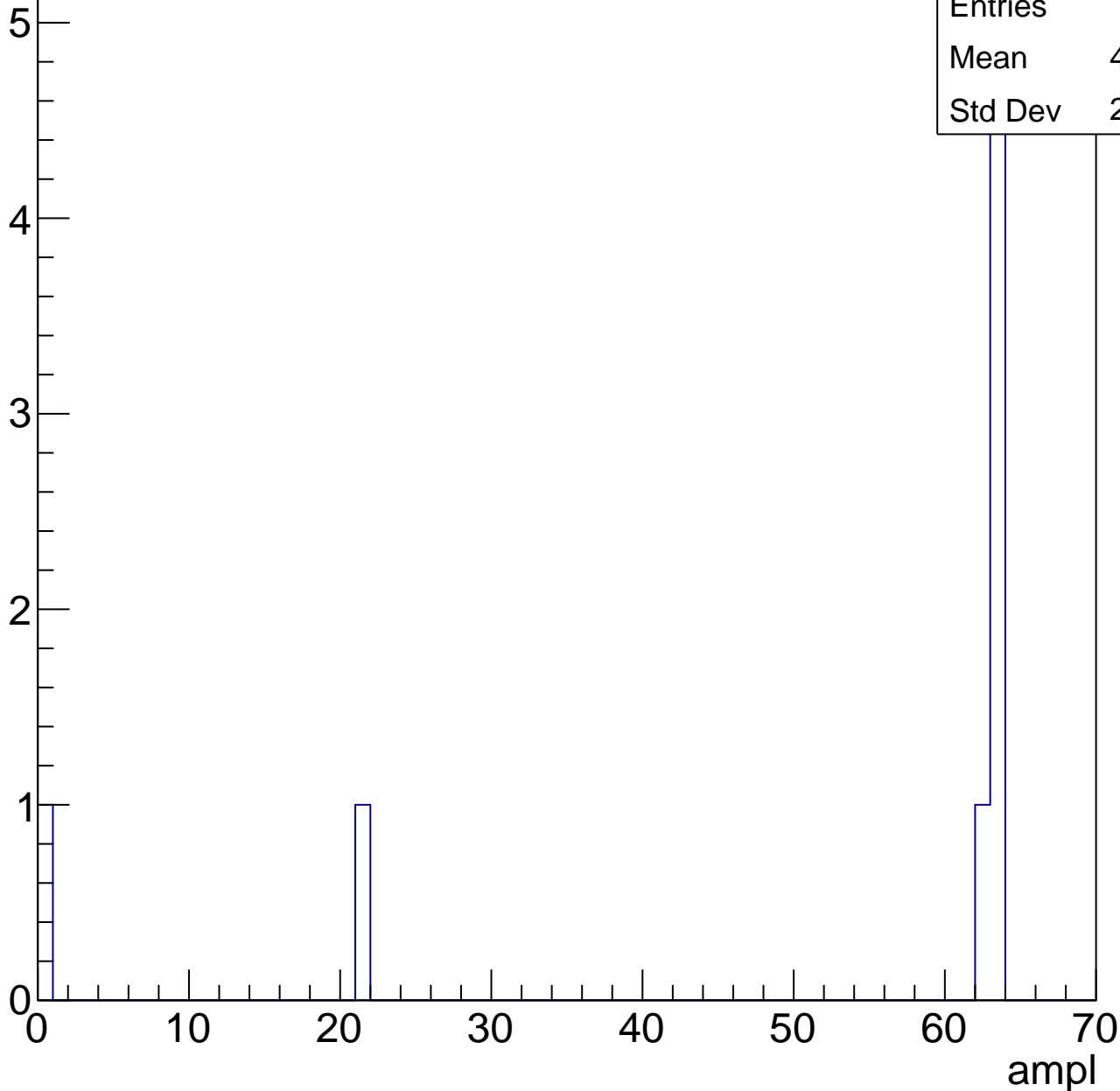


B1L001S, U16-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	49.75
Std Dev	23.26



B1L001S, U16-ch105, adc0

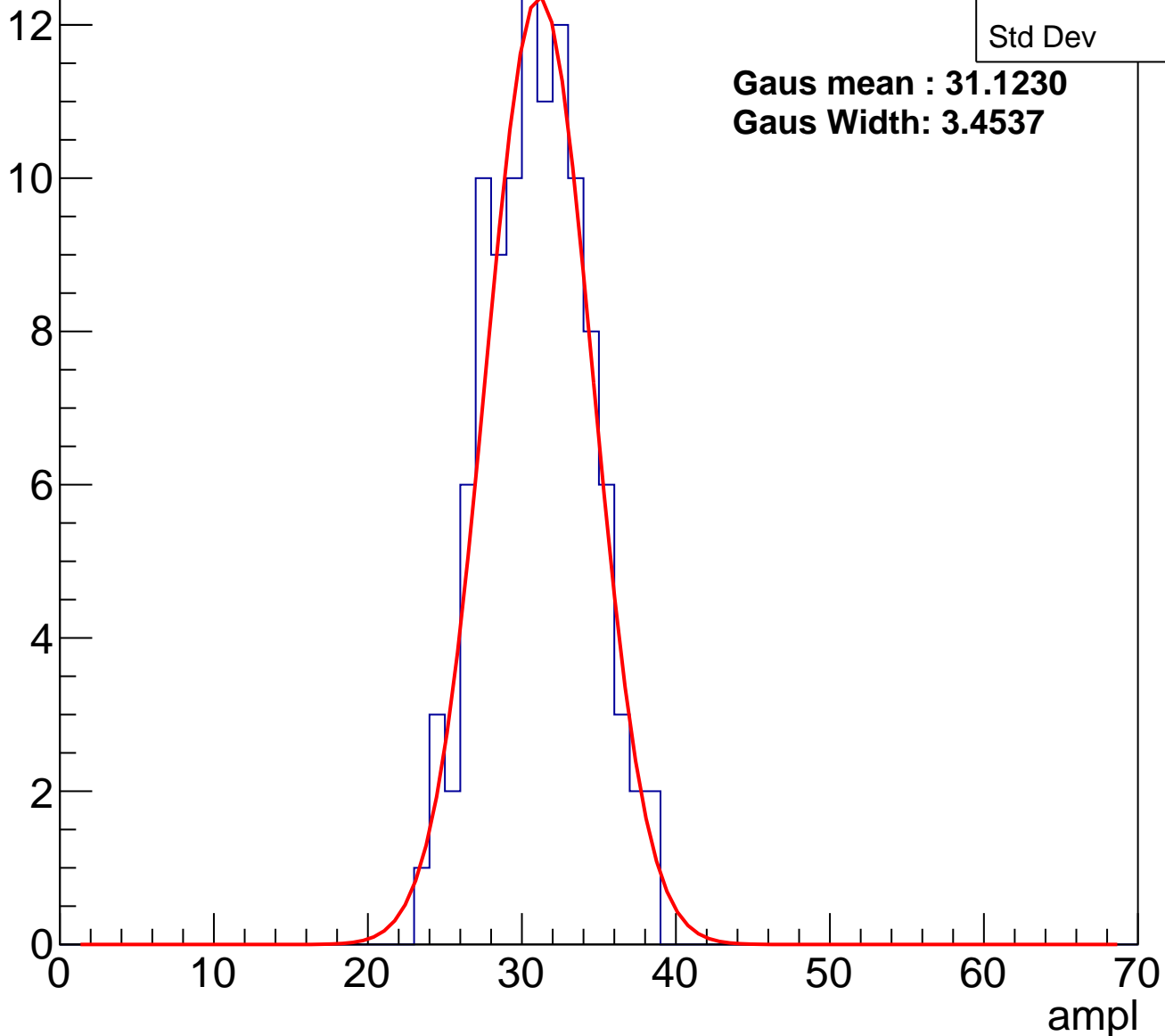
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	30.54
Std Dev	3.29

Gaus mean : 31.1230

Gaus Width: 3.4537

Entry



B1L001S, U16-ch105, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	36.36
Std Dev	3.741

Gaus mean : 36.7873

Gaus Width: 3.9840

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

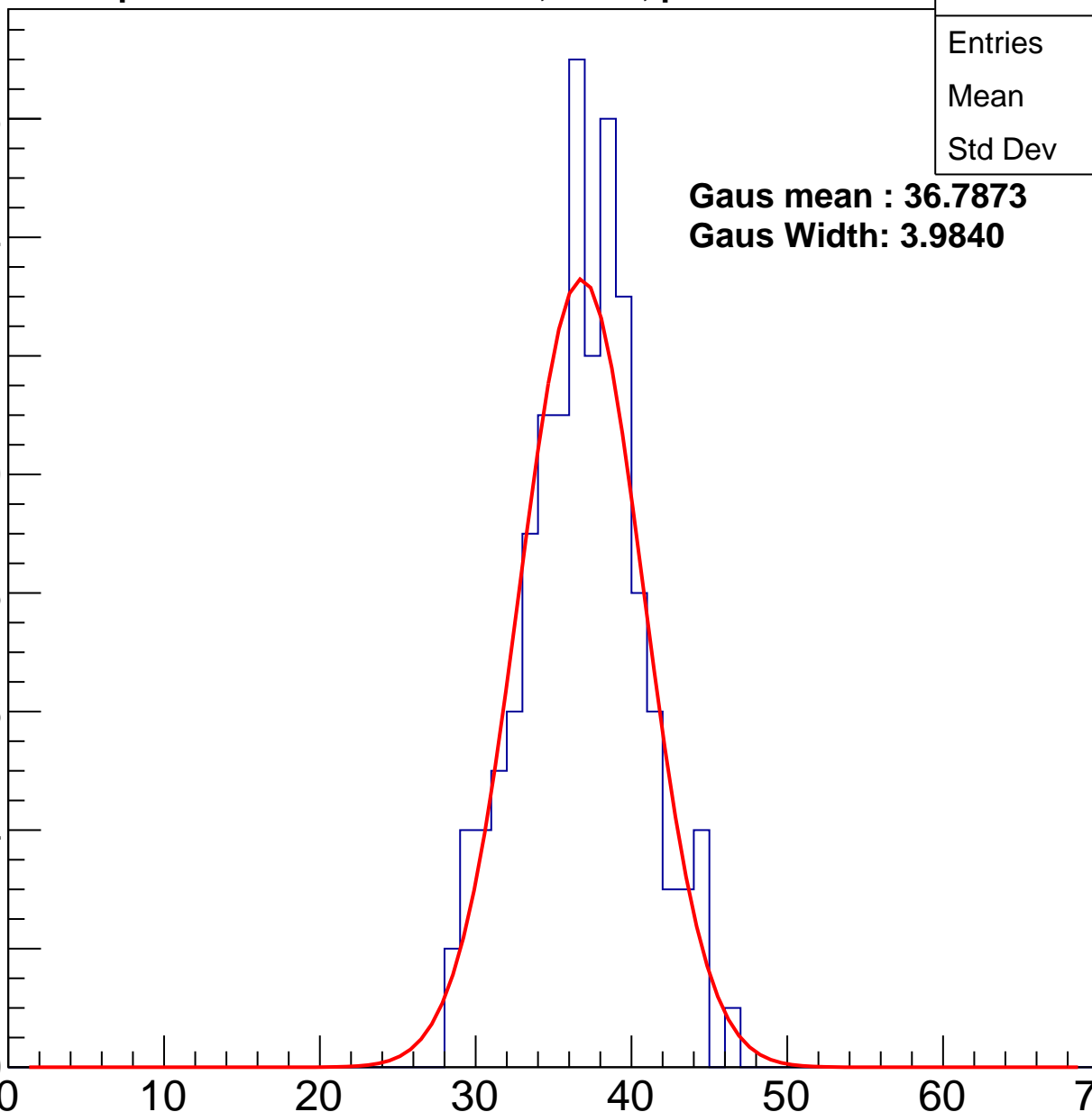
40

50

60

70

ampl



B1L001S, U16-ch105, adc2

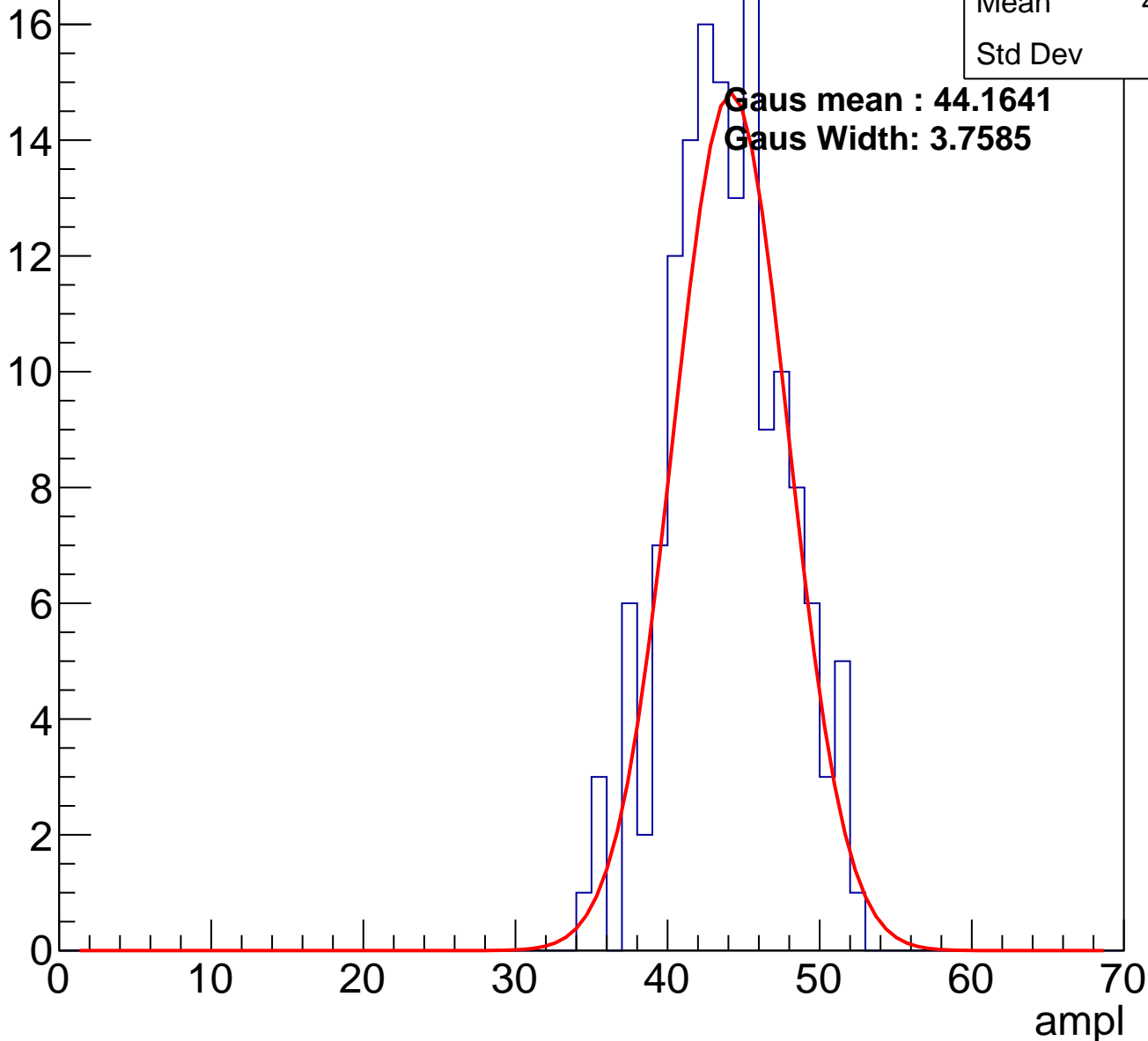
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	148
Mean	43.49
Std Dev	3.75

Gaus mean : 44.1641

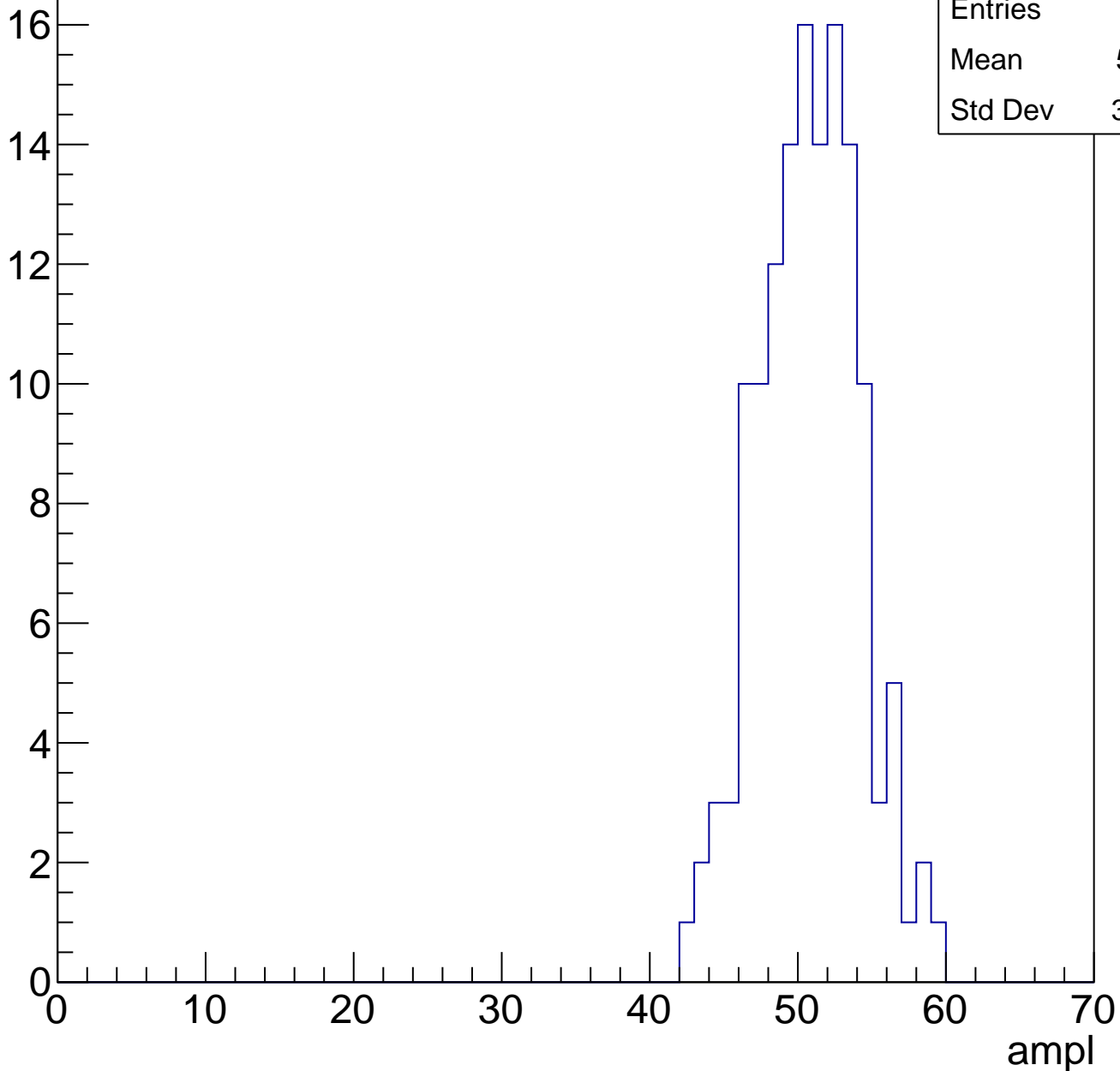
Gaus Width: 3.7585



B1L001S, U16-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

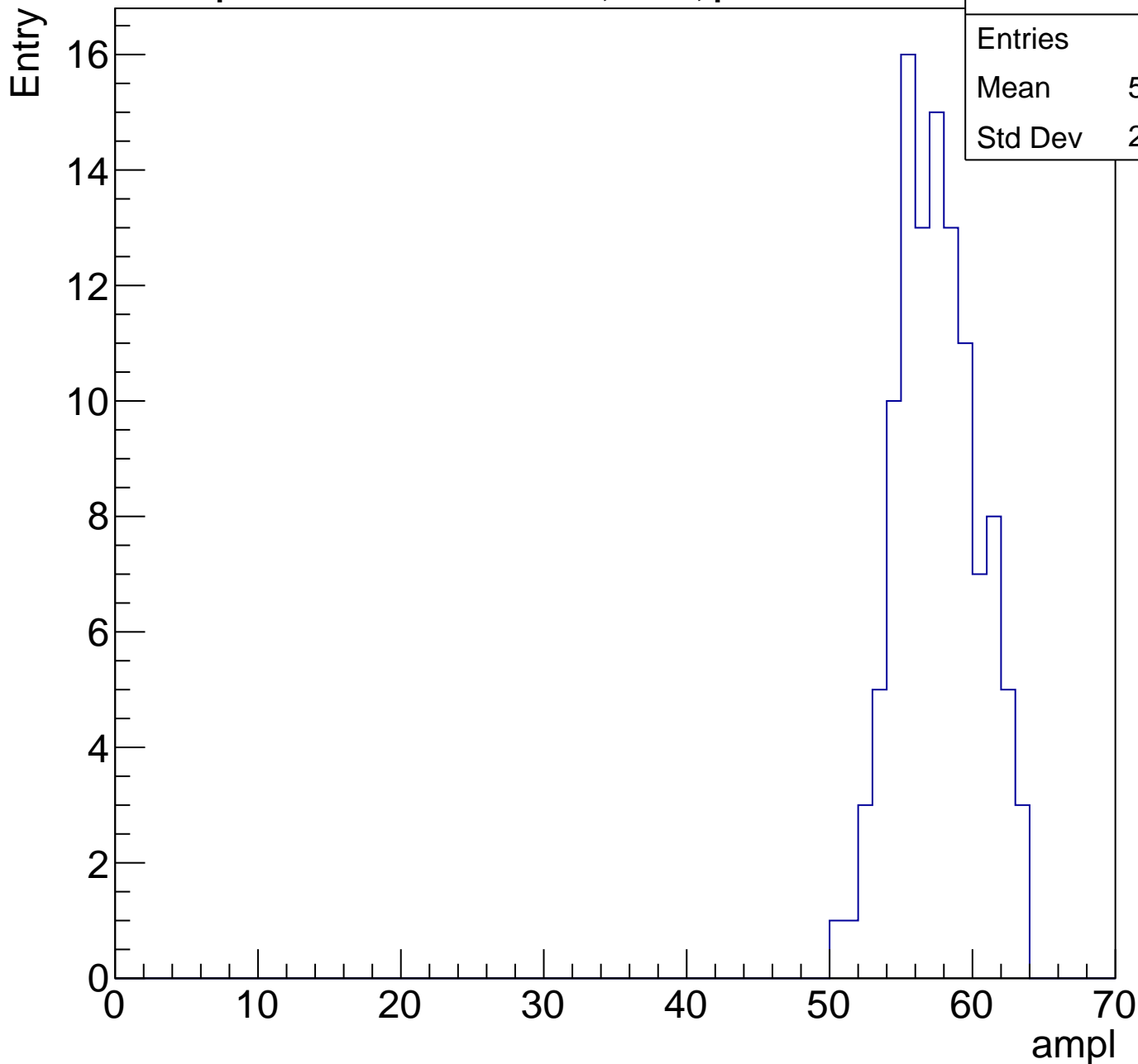


Entries	137
Mean	50.31
Std Dev	3.332

B1L001S, U16-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	57.07
Std Dev	2.828



B1L001S, U16-ch105, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

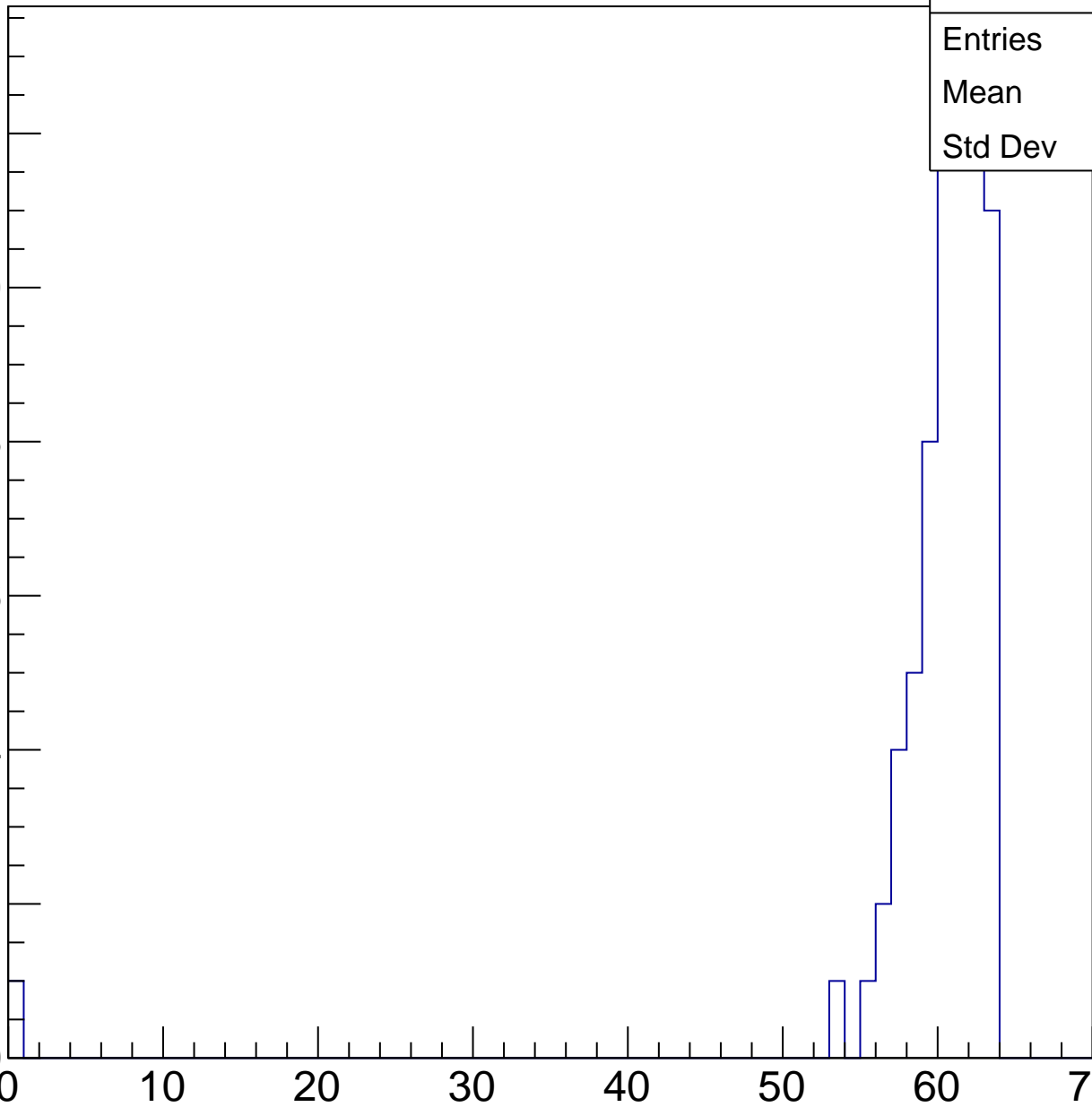
4

2

0

Entries	71
Mean	59.45
Std Dev	7.422

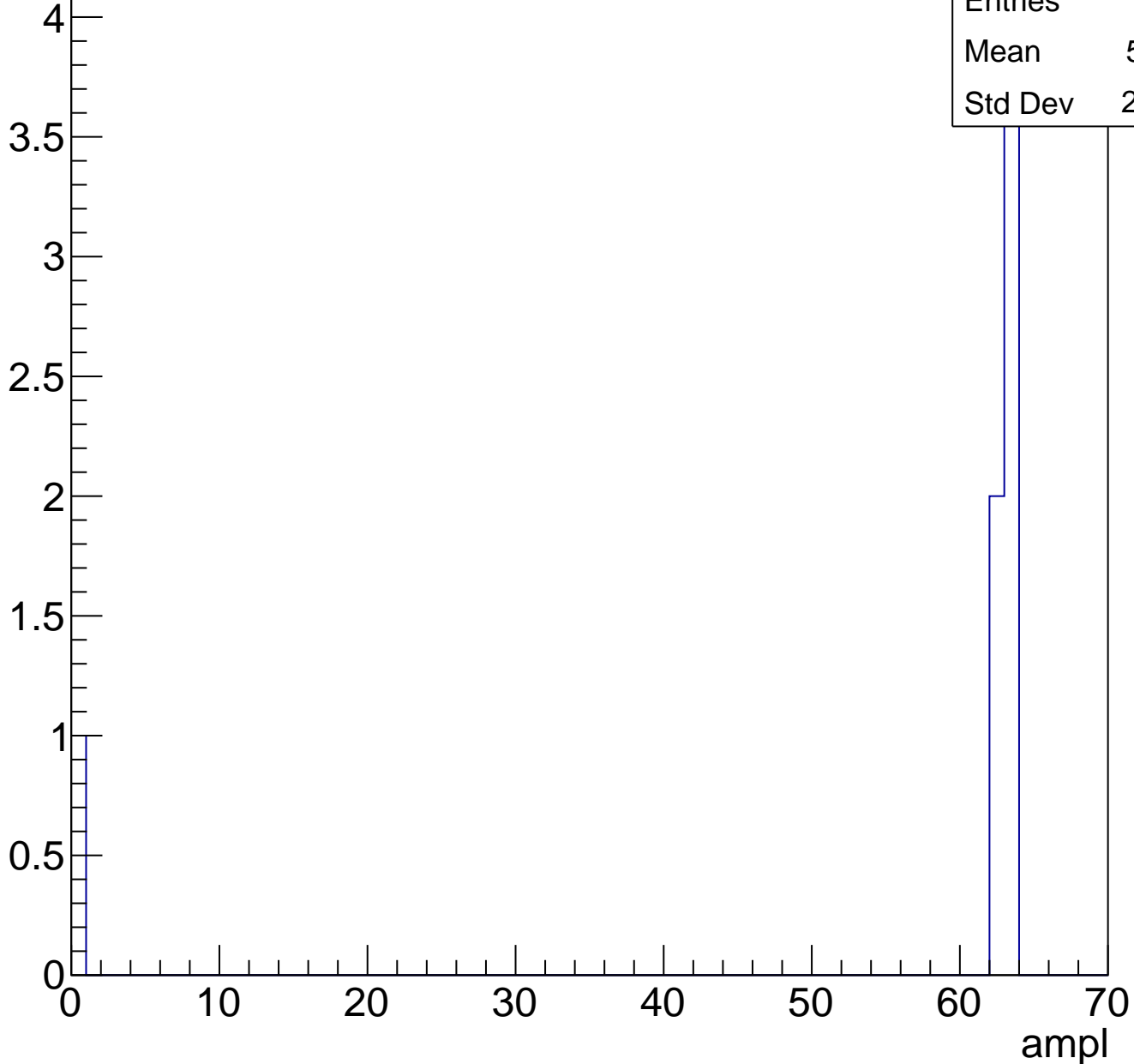
ampl



B1L001S, U16-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch106, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	27.42
Std Dev	4.016

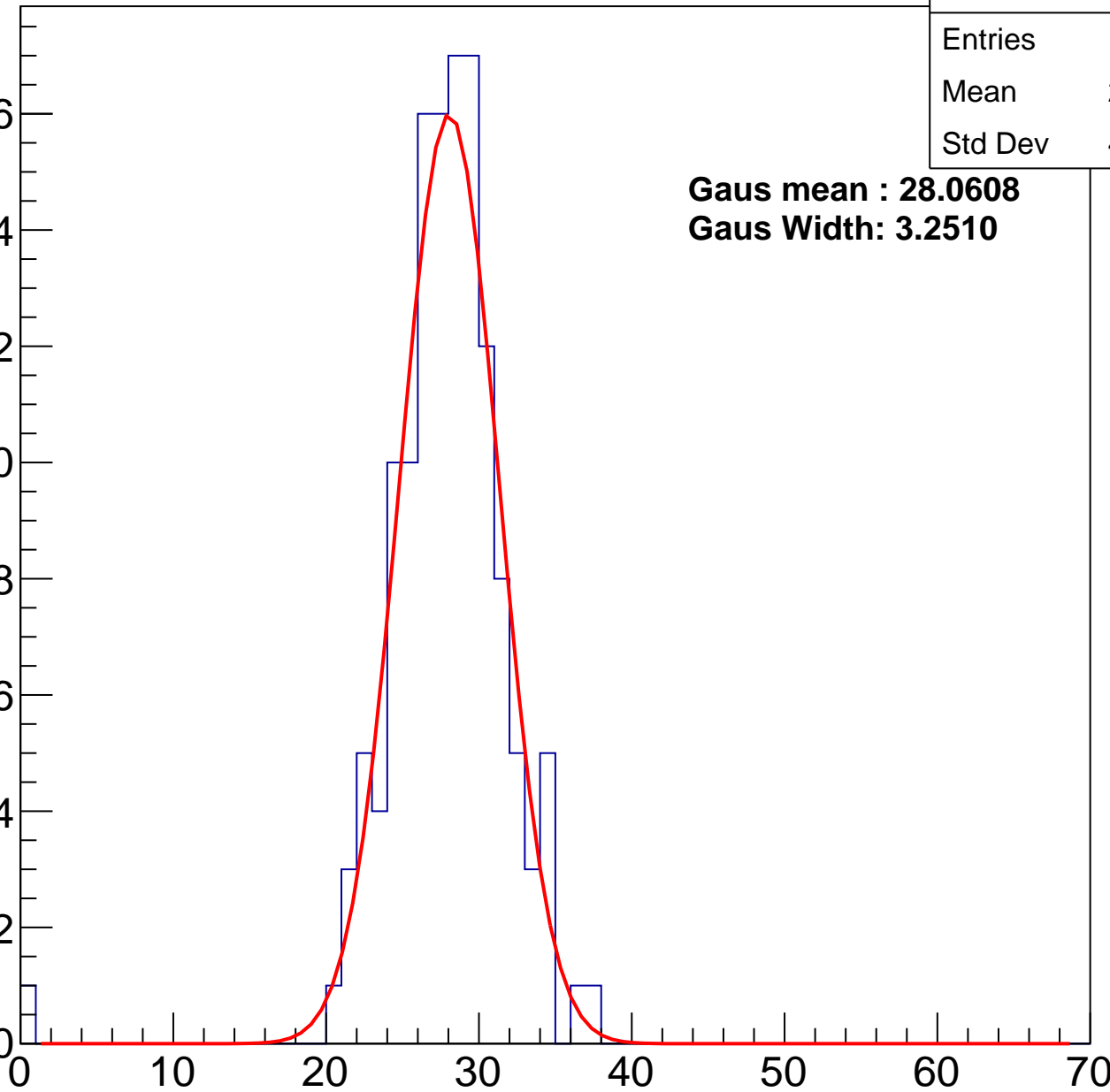
Gaus mean : 28.0608

Gaus Width: 3.2510

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	33.98
Std Dev	3.228

Gaus mean : 34.7676

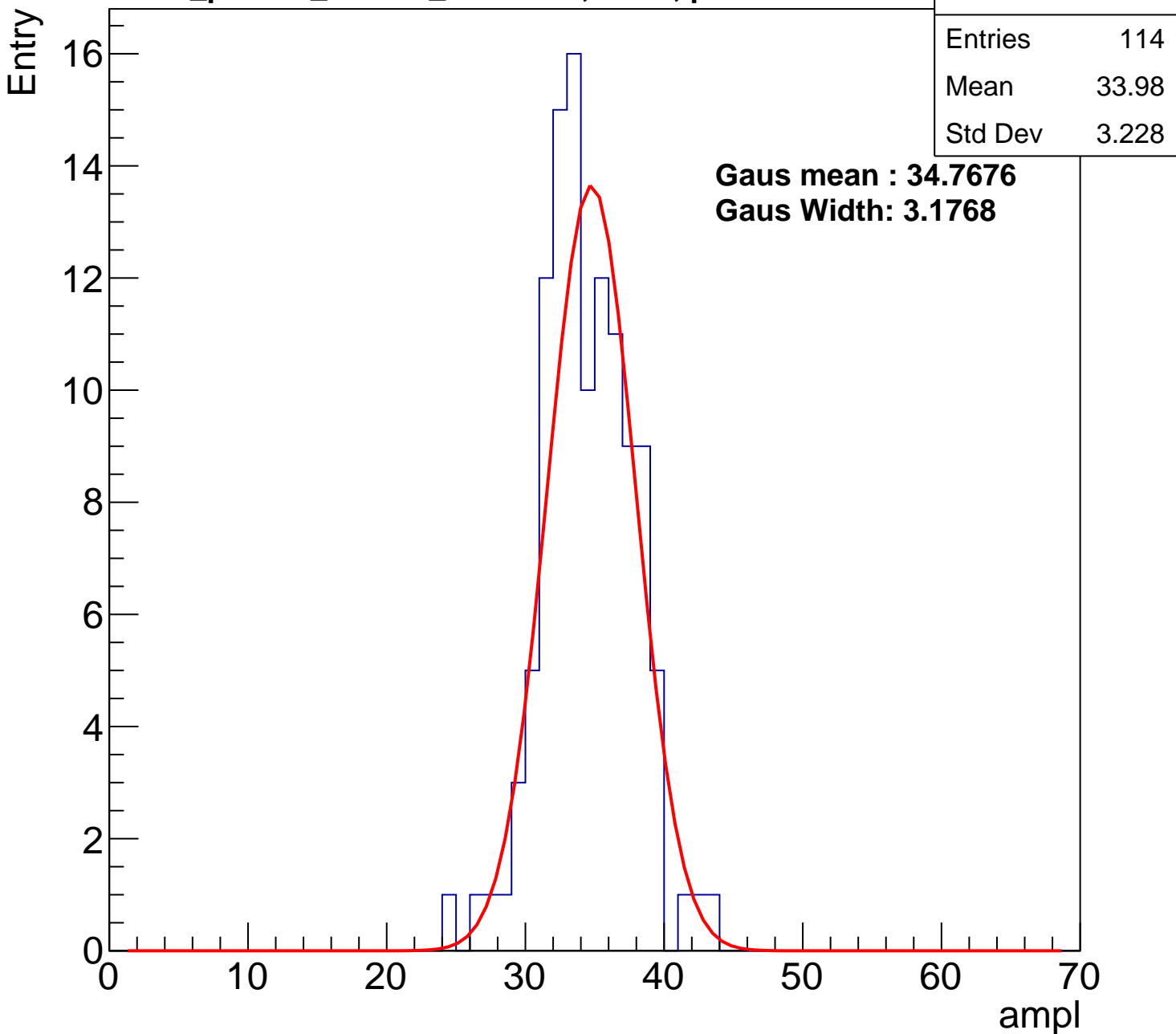
Gaus Width: 3.1768

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

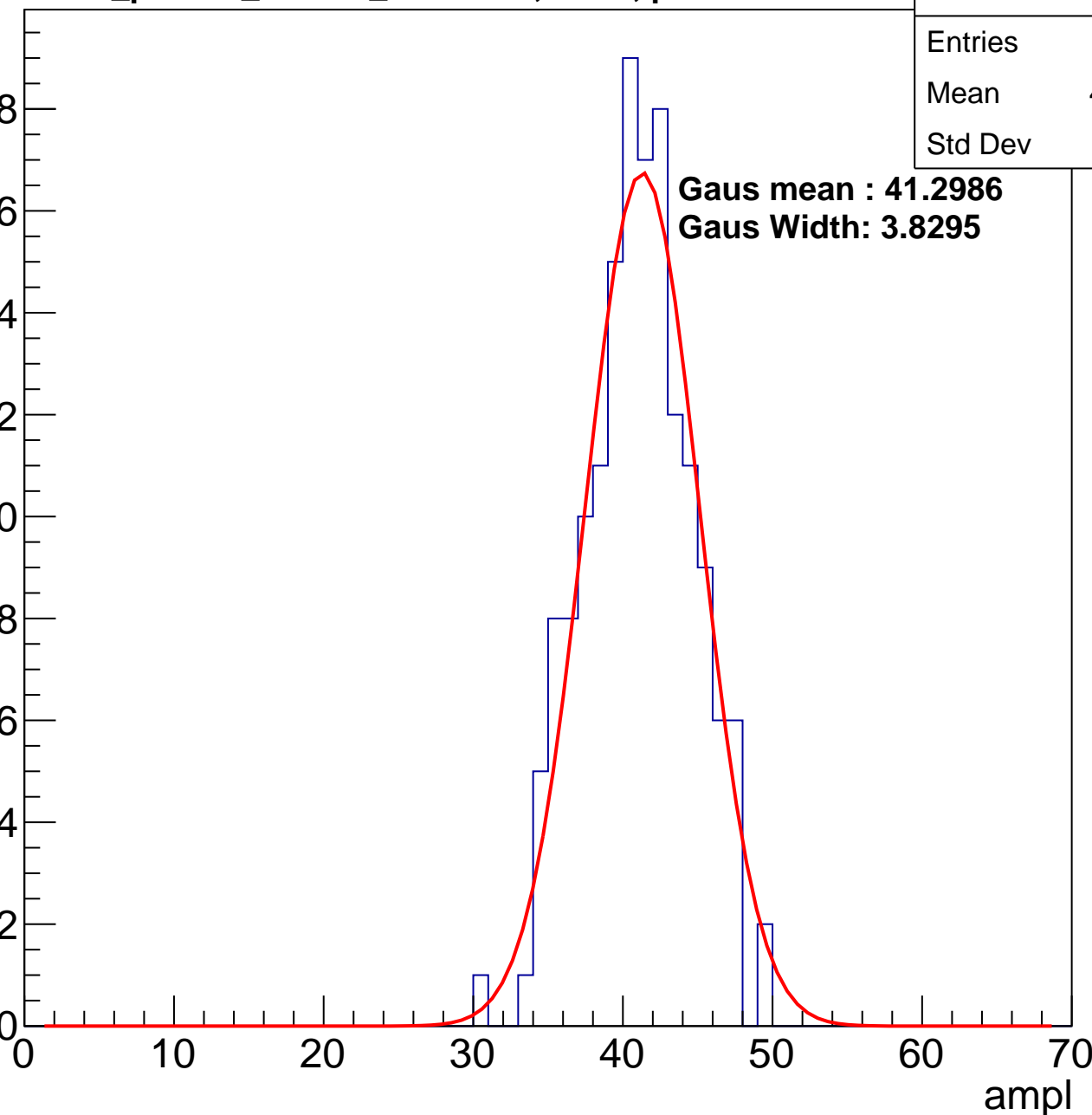
Entry

18
16
14
12
10
8
6
4
2
0

Entries	159
Mean	40.55
Std Dev	3.571

Gaus mean : 41.2986

Gaus Width: 3.8295



B1L001S, U16-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

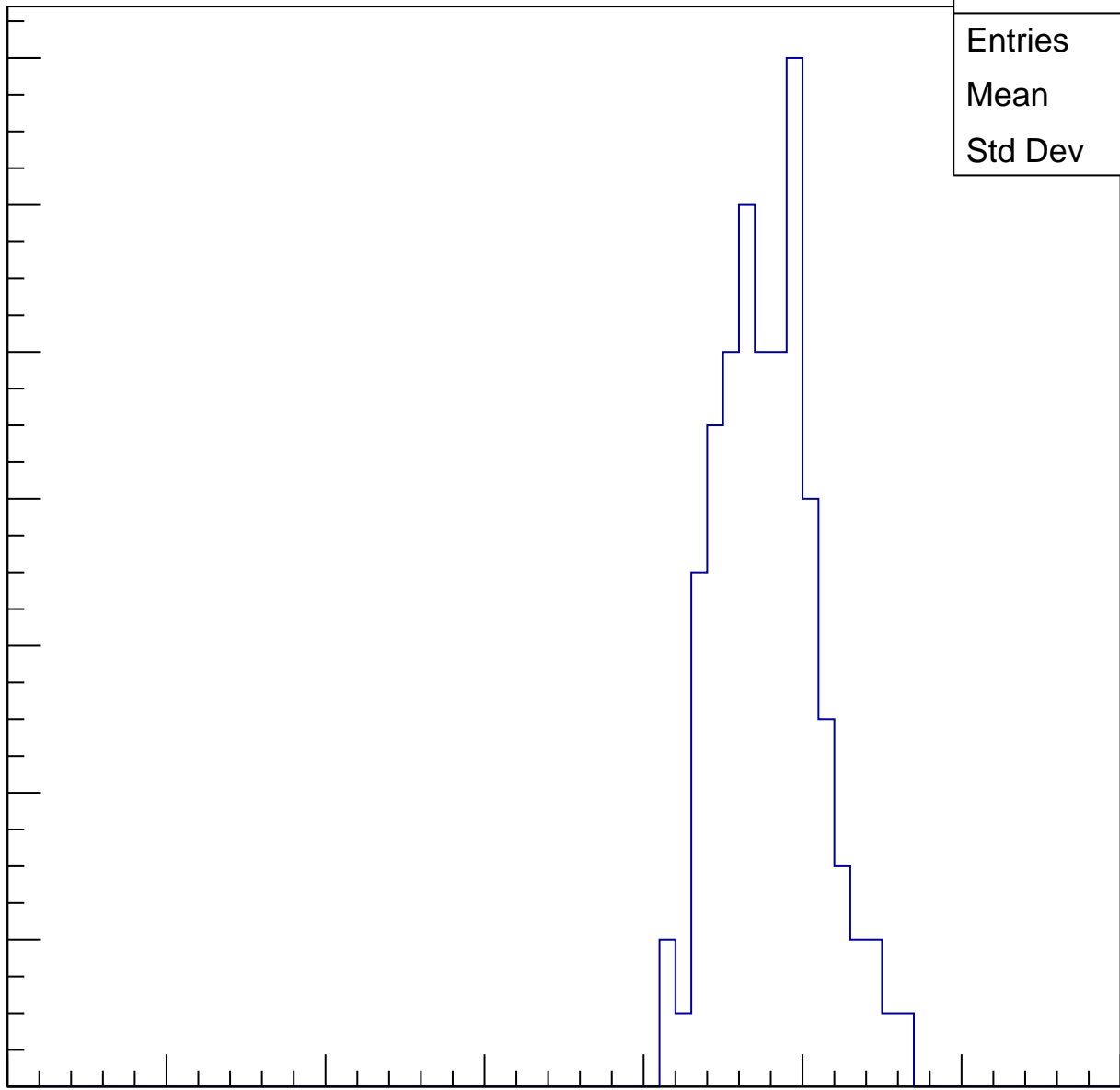
Entries	97
Mean	47.37
Std Dev	3.107

Entry

14
12
10
8
6
4
2
0

ampl

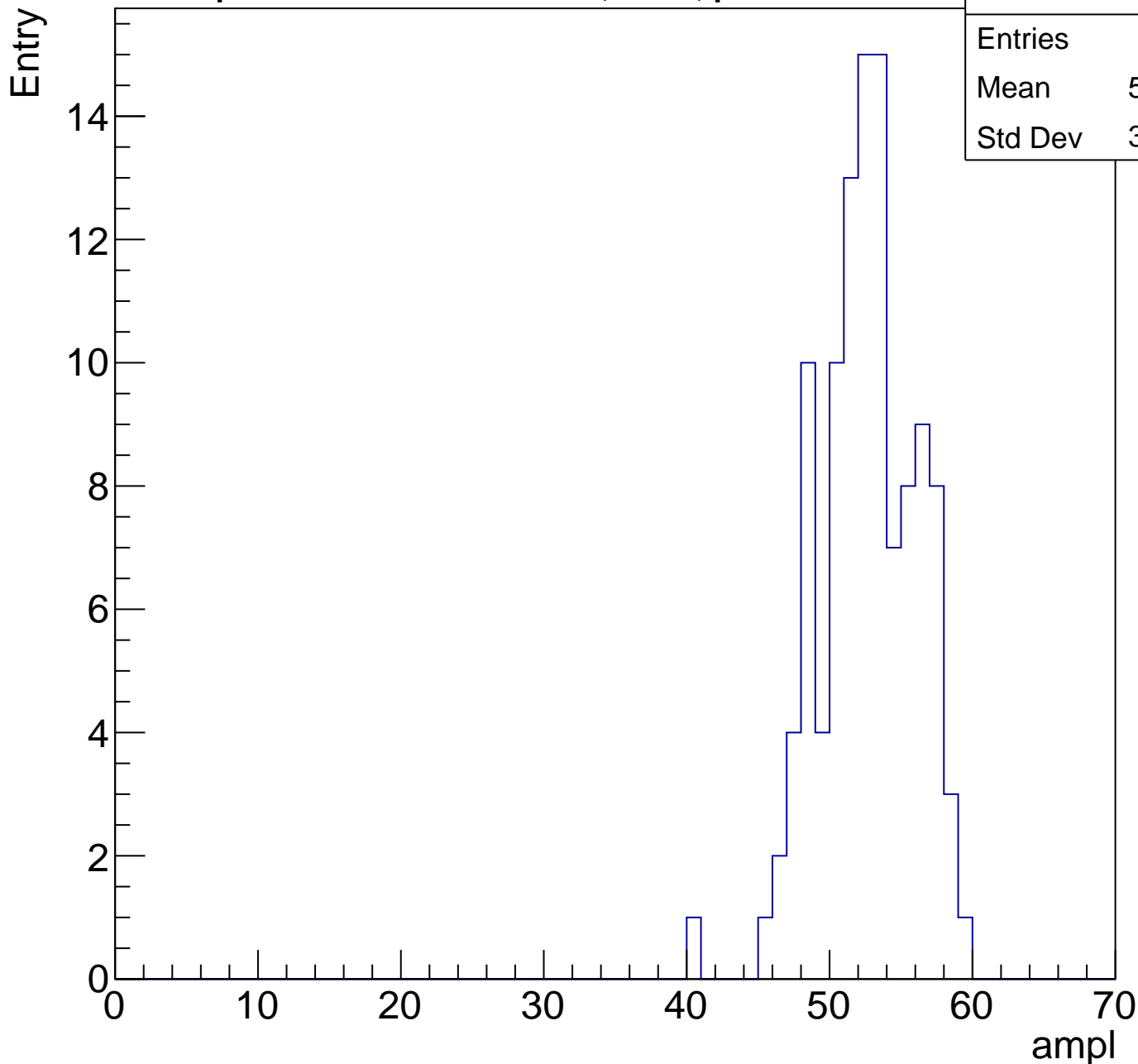
0 10 20 30 40 50 60 70



B1L001S, U16-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	52.16
Std Dev	3.325



B1L001S, U16-ch106, adc5

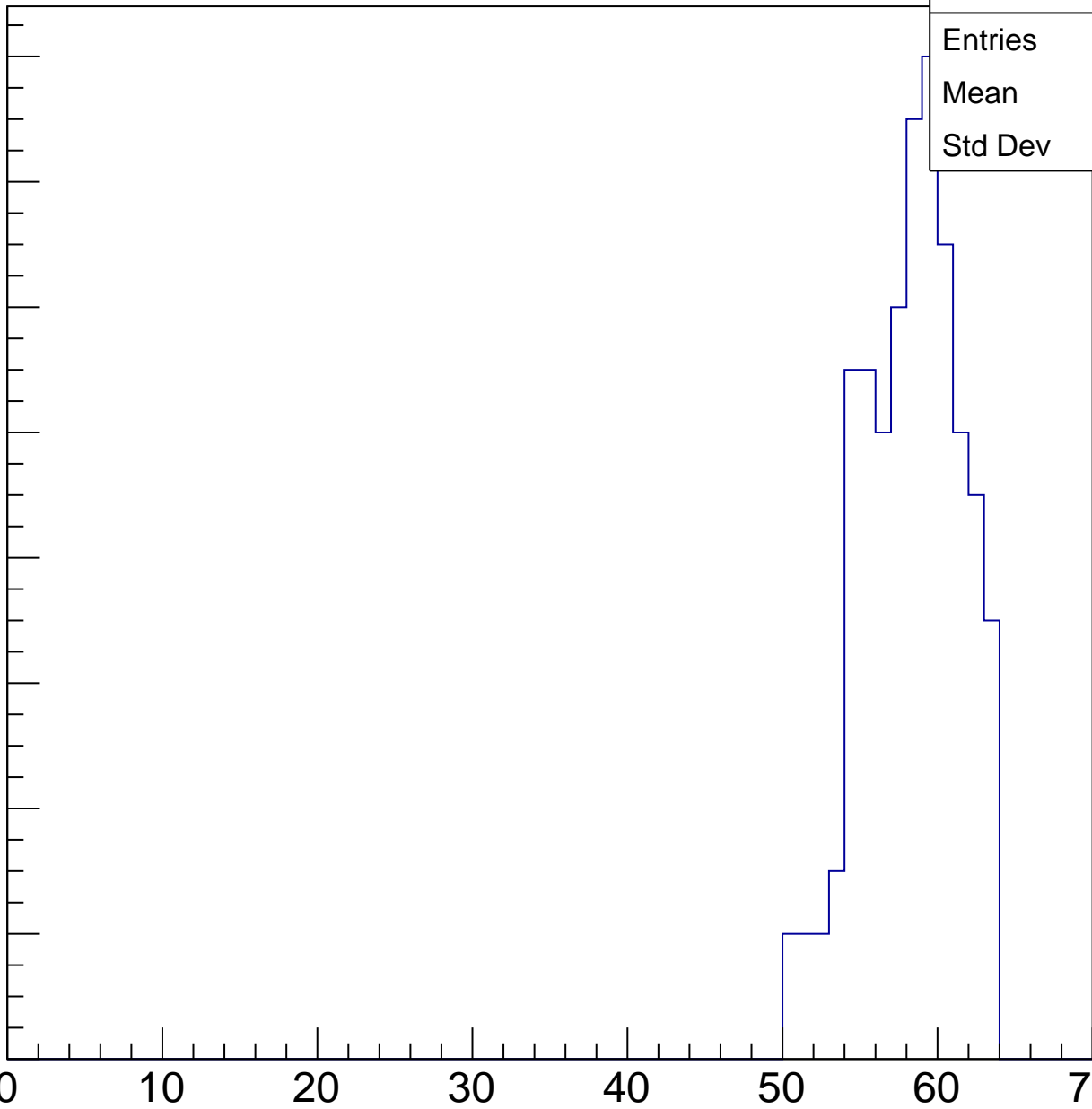
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	57.81
Std Dev	3.088

ampl



B1L001S, U16-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

46

Mean

58.67

Std Dev

12.59

ampl

0

10

20

30

40

50

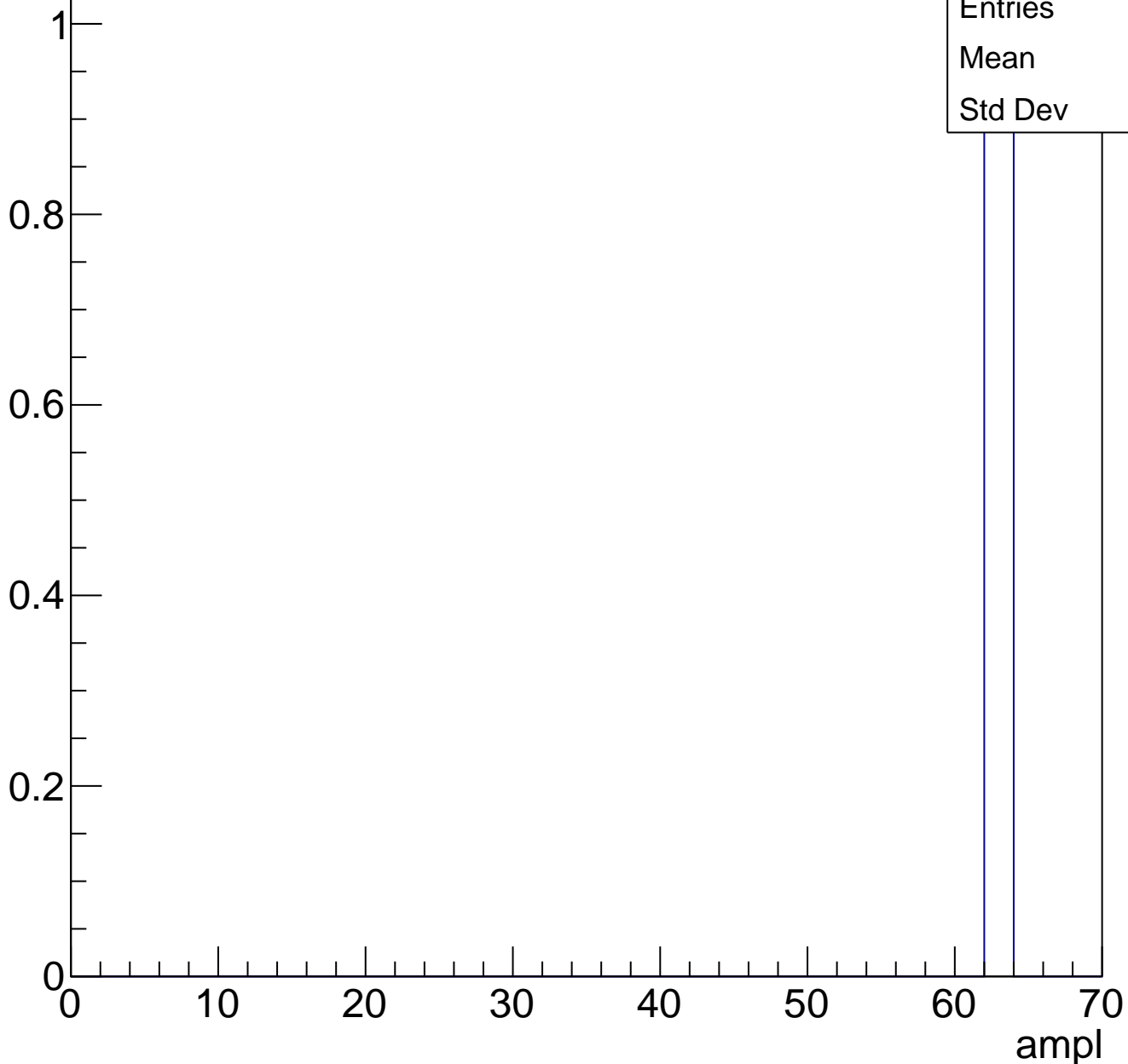
60

70

B1L001S, U16-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



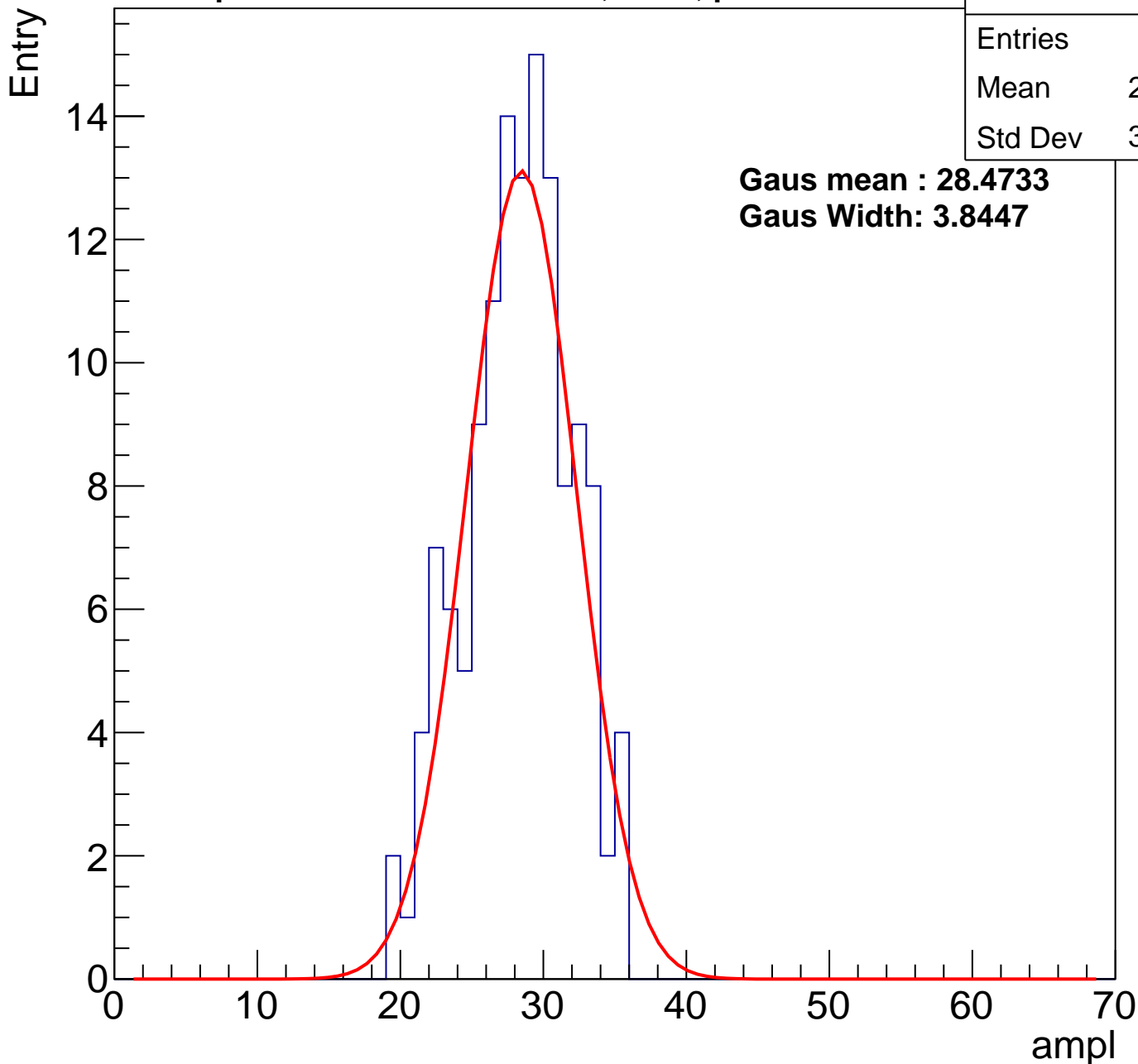
B1L001S, U16-ch107, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	27.79
Std Dev	3.694

Gaus mean : 28.4733

Gaus Width: 3.8447



B1L001S, U16-ch107, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	35.33
Std Dev	3.398

Gaus mean : 35.9499

Gaus Width: 3.6316

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

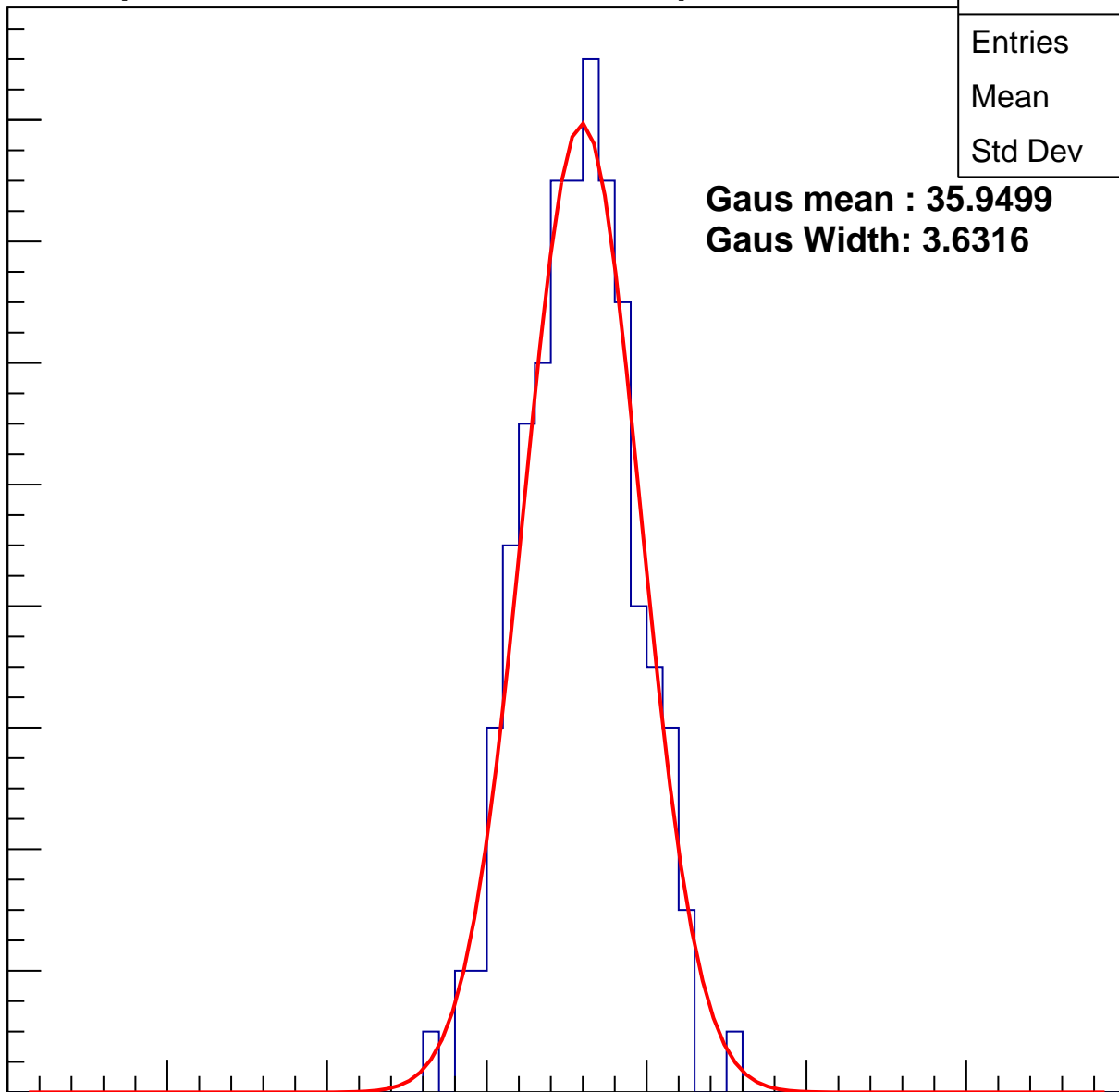
40

50

60

70

ampl



B1L001S, U16-ch107, adc2

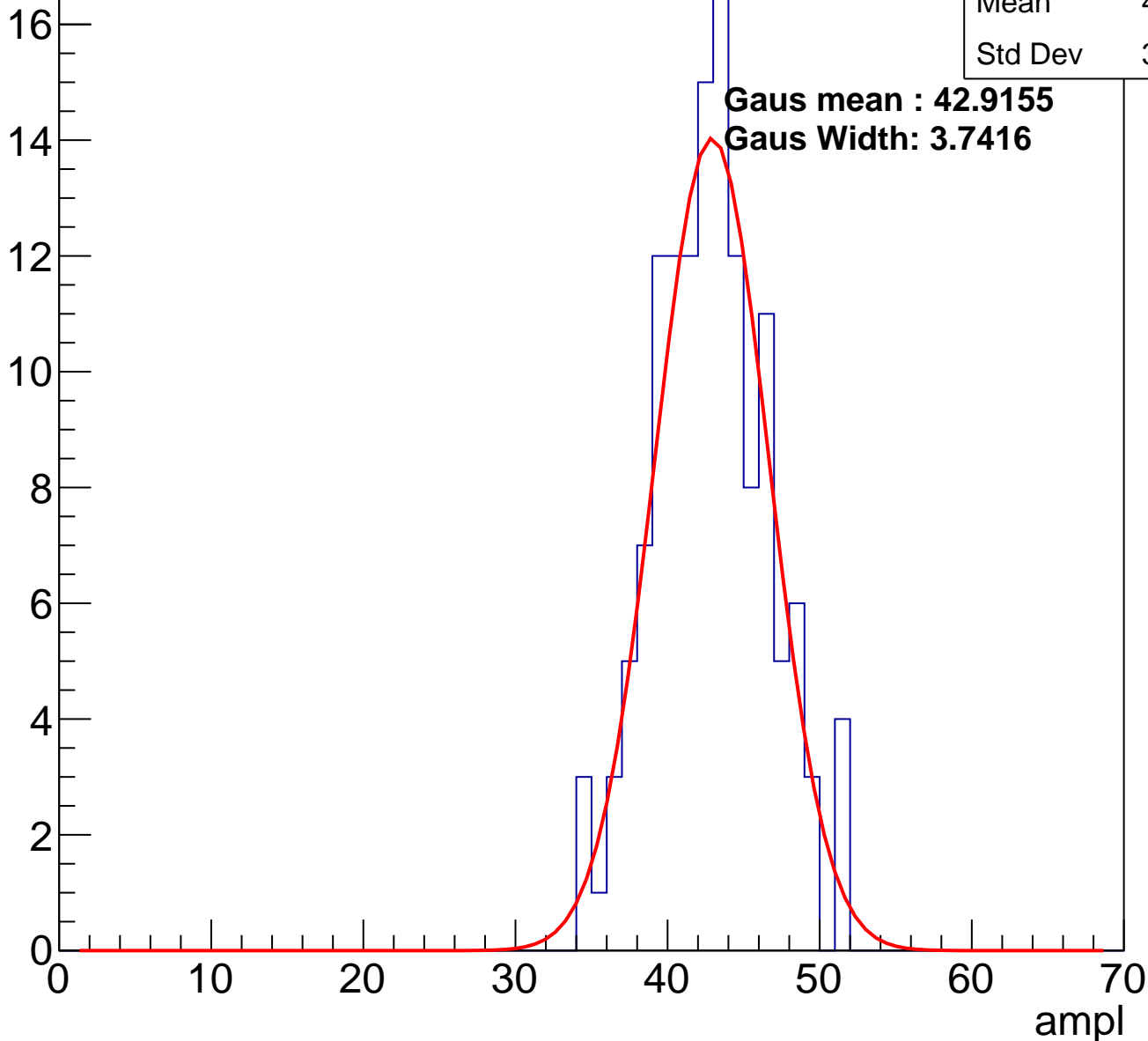
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	136
Mean	42.39
Std Dev	3.697

Gaus mean : 42.9155

Gaus Width: 3.7416



B1L001S, U16-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	47.79
Std Dev	3.231

Entry

12

10

8

6

4

2

0

0

10

20

30

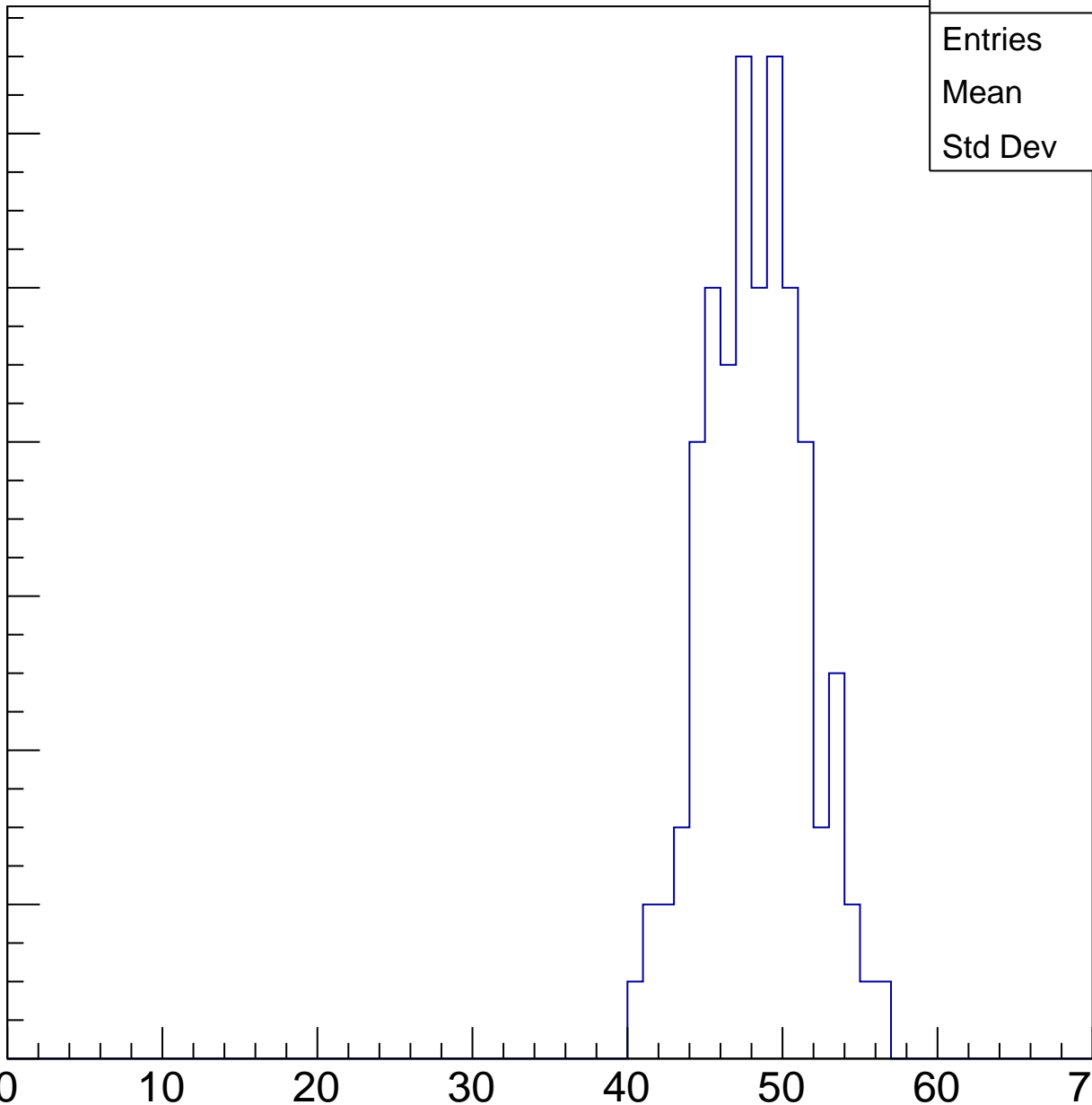
40

50

60

70

ampl

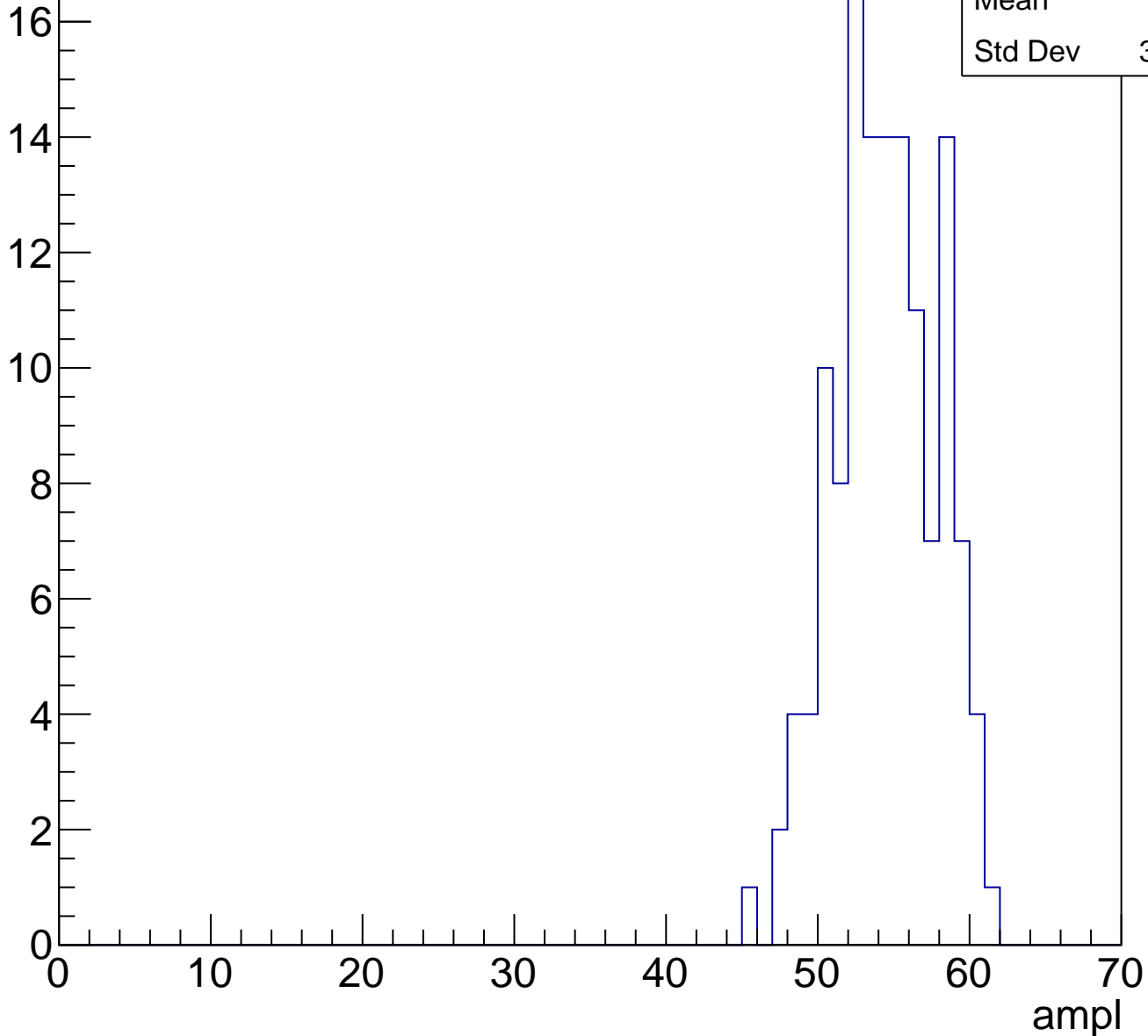


B1L001S, U16-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	132
Mean	54
Std Dev	3.314



B1L001S, U16-ch107, adc5

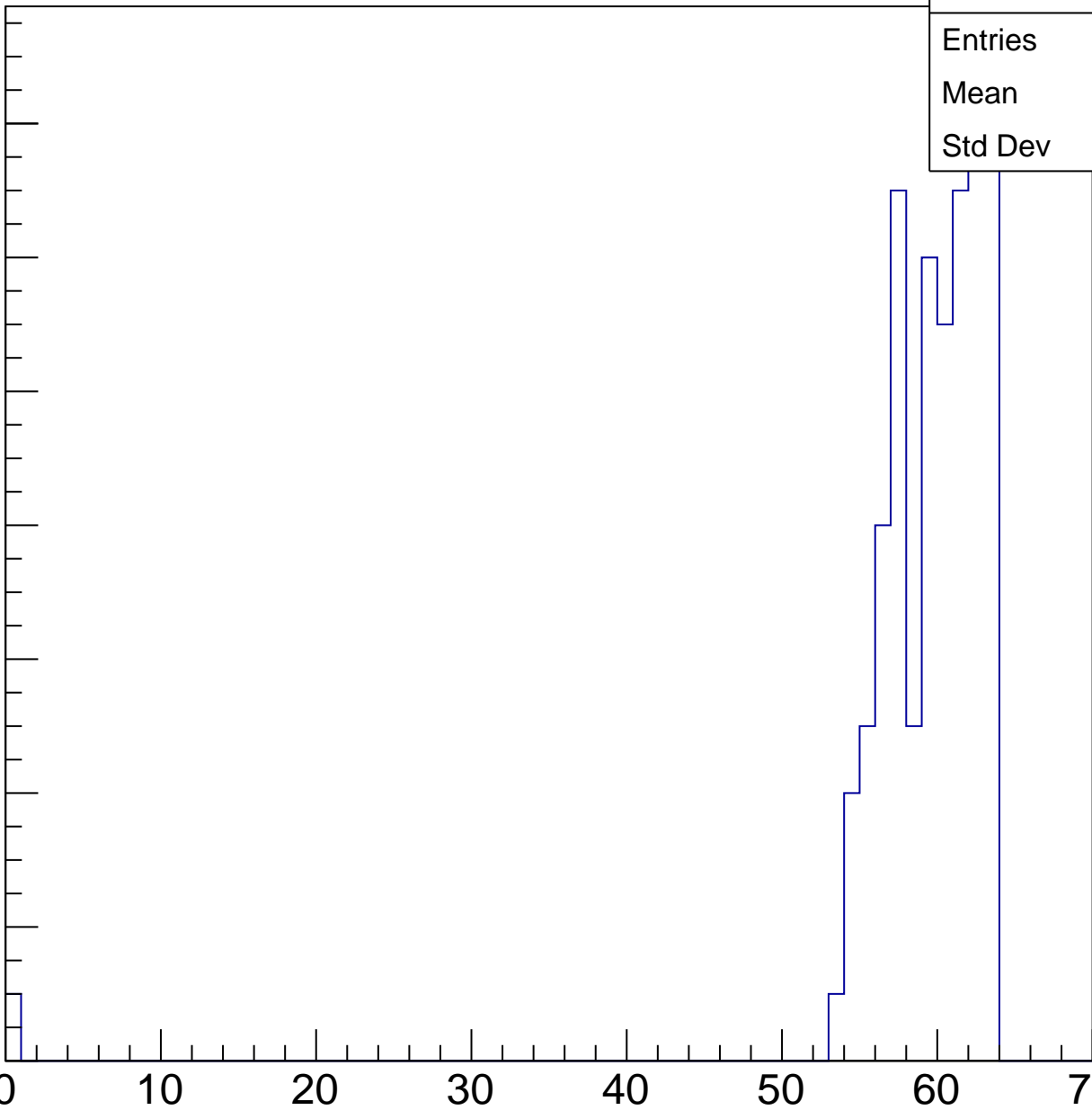
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	58.79
Std Dev	6.455

ampl

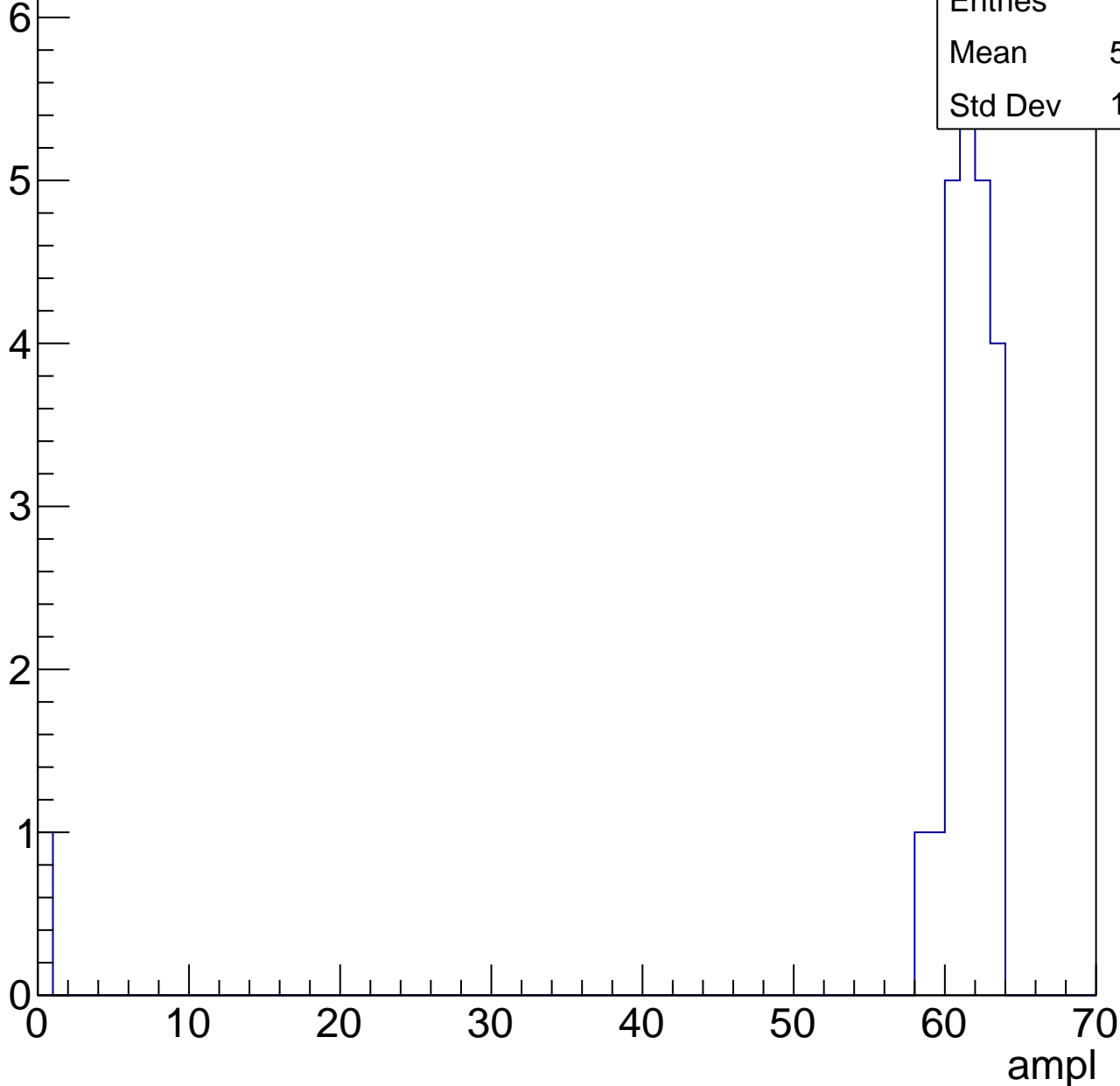


B1L001S, U16-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

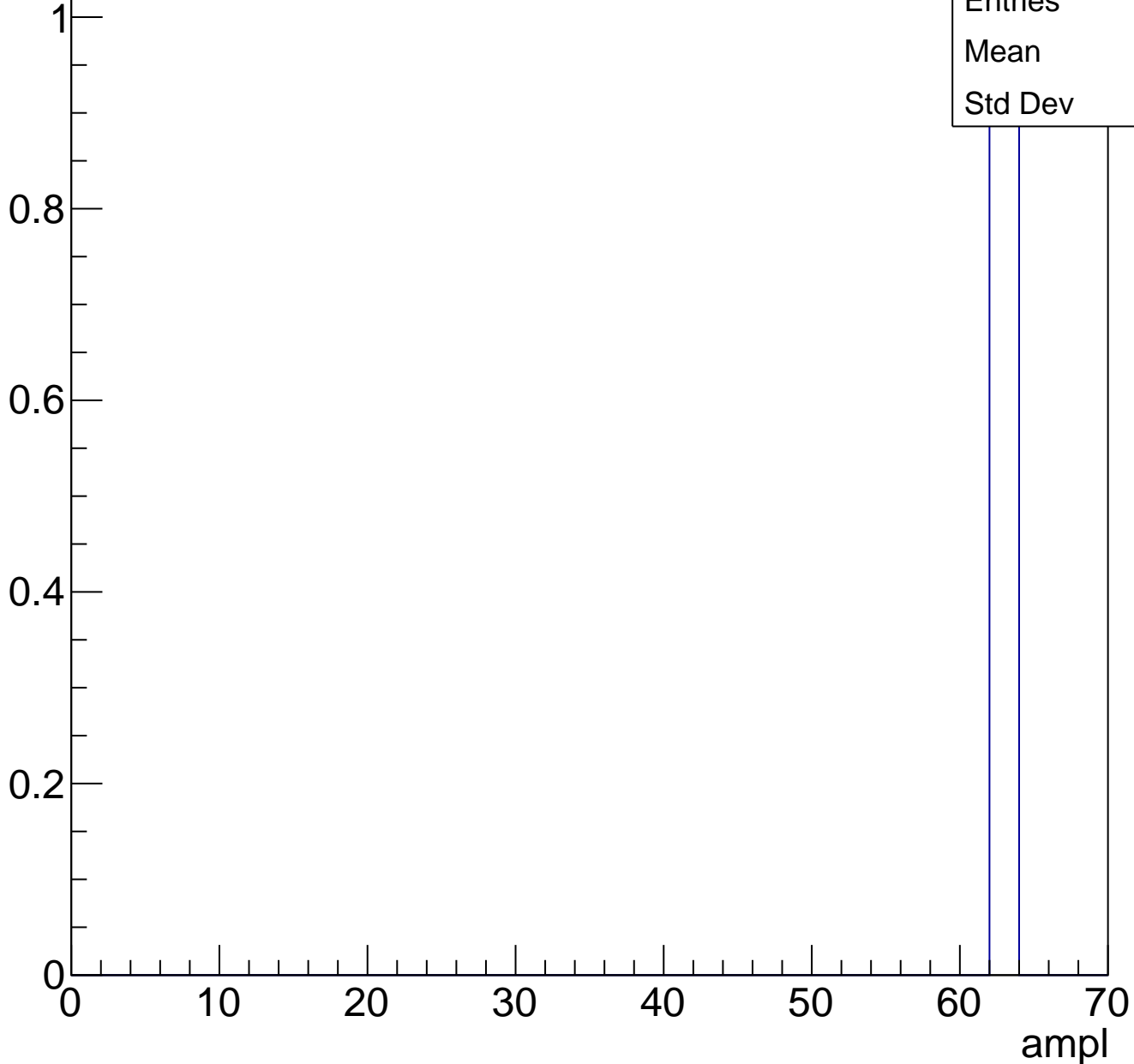
Entries	23
Mean	58.48
Std Dev	12.53



B1L001S, U16-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



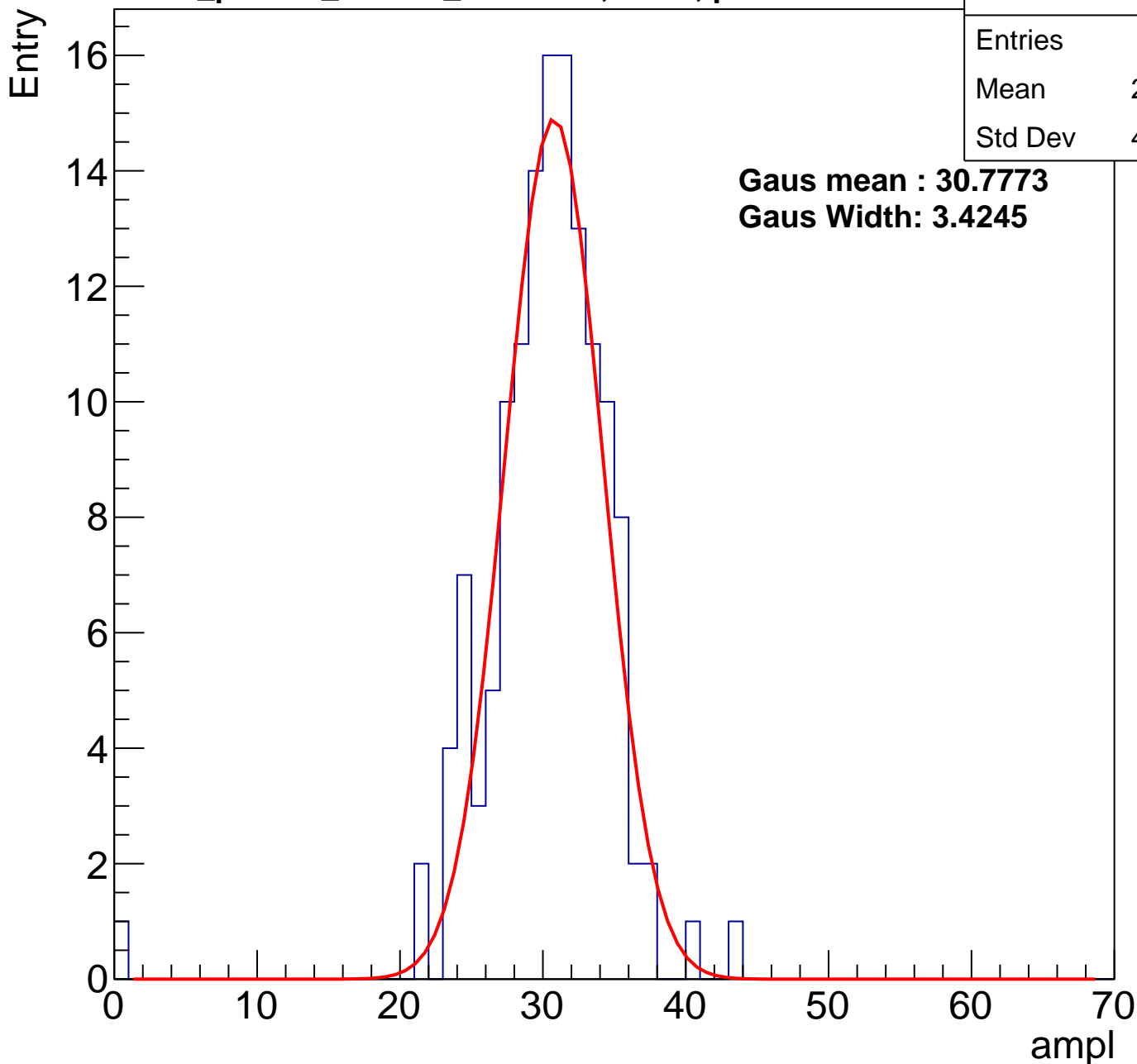
B1L001S, U16-ch108, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.89
Std Dev	4.503

Gaus mean : 30.7773

Gaus Width: 3.4245



B1L001S, U16-ch108, adc1

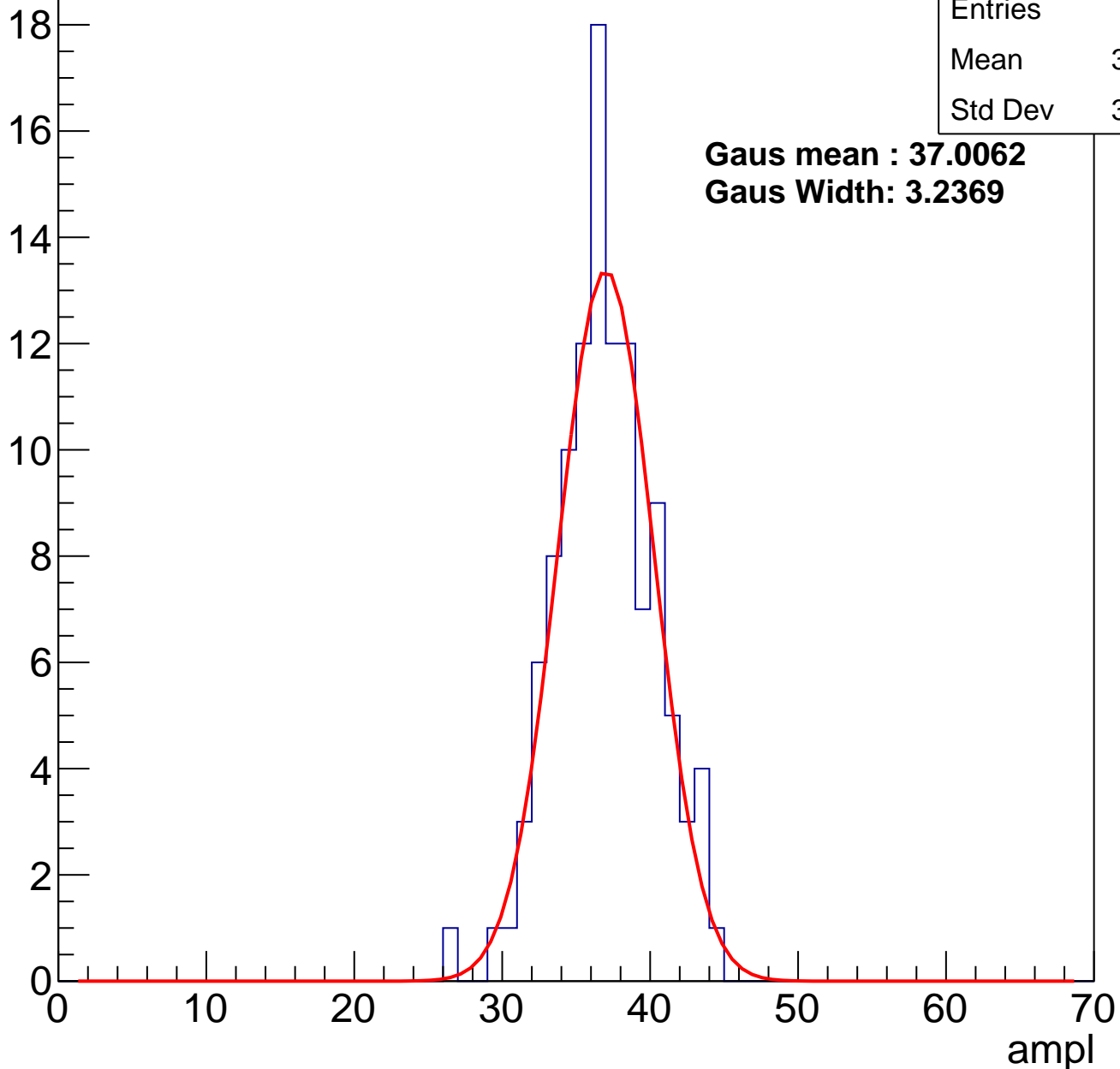
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	36.48
Std Dev	3.278

Gaus mean : 37.0062

Gaus Width: 3.2369

Entry



B1L001S, U16-ch108, adc2

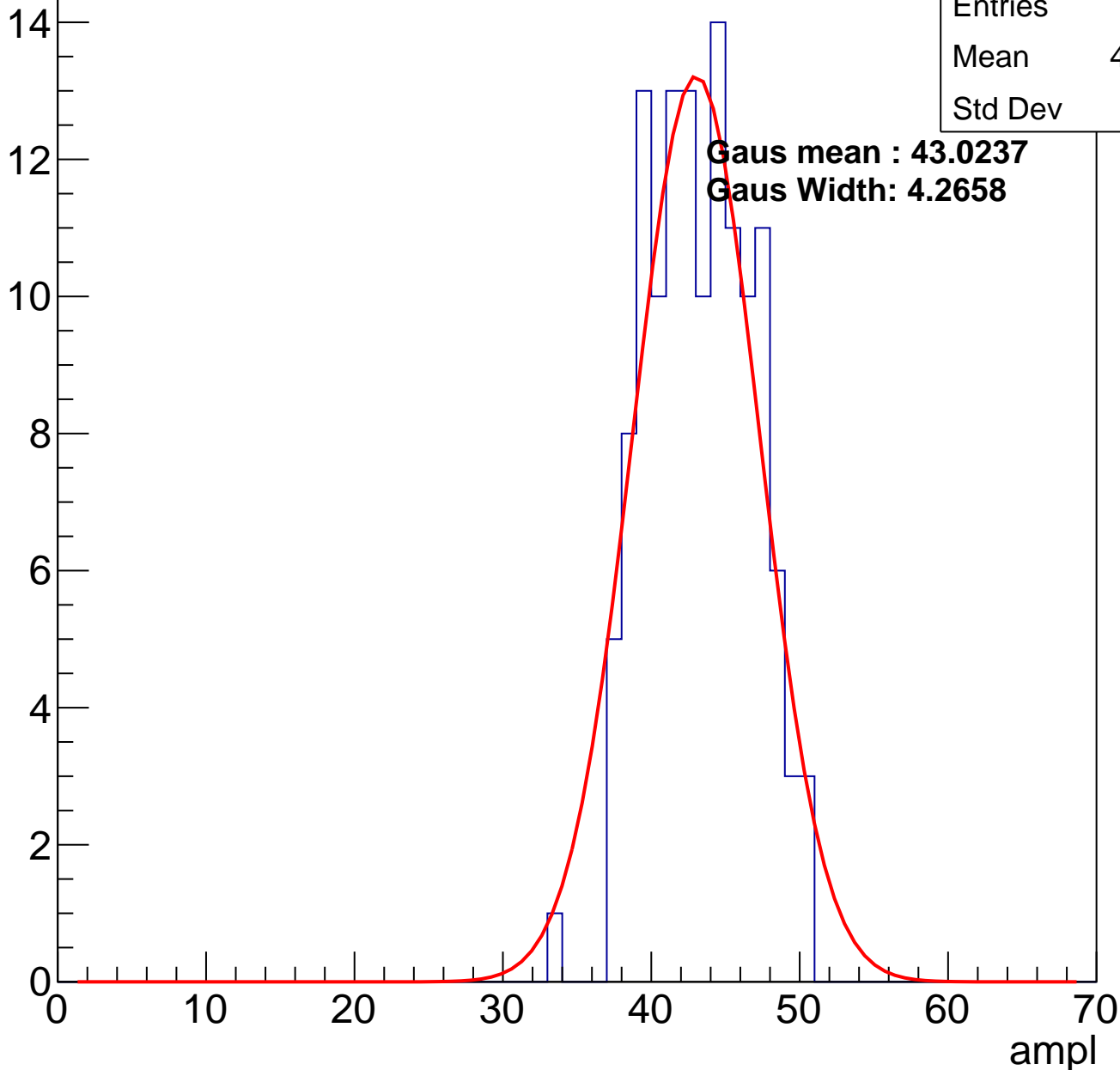
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	131
Mean	42.83
Std Dev	3.46

Gaus mean : 43.0237

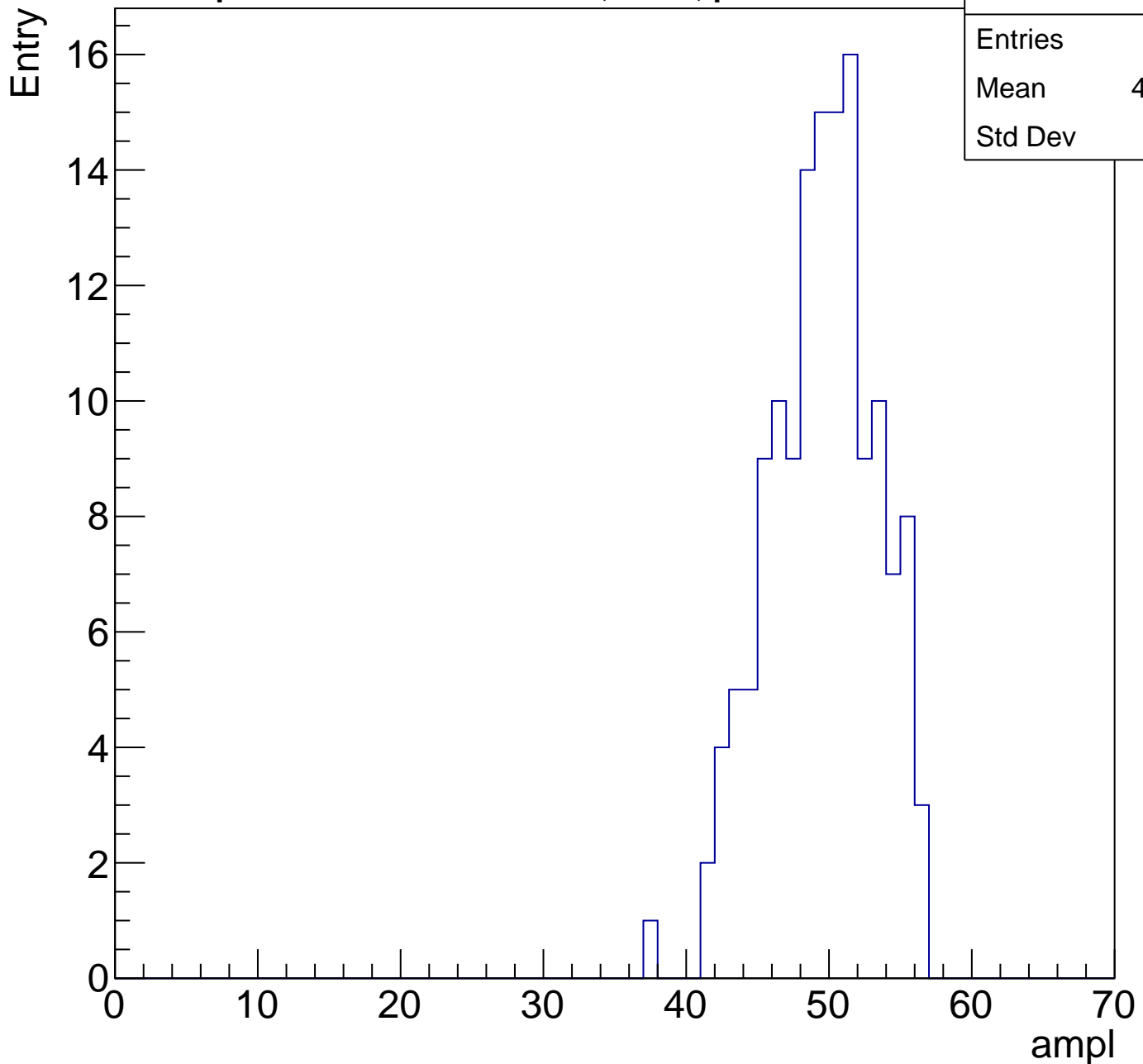
Gaus Width: 4.2658



B1L001S, U16-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

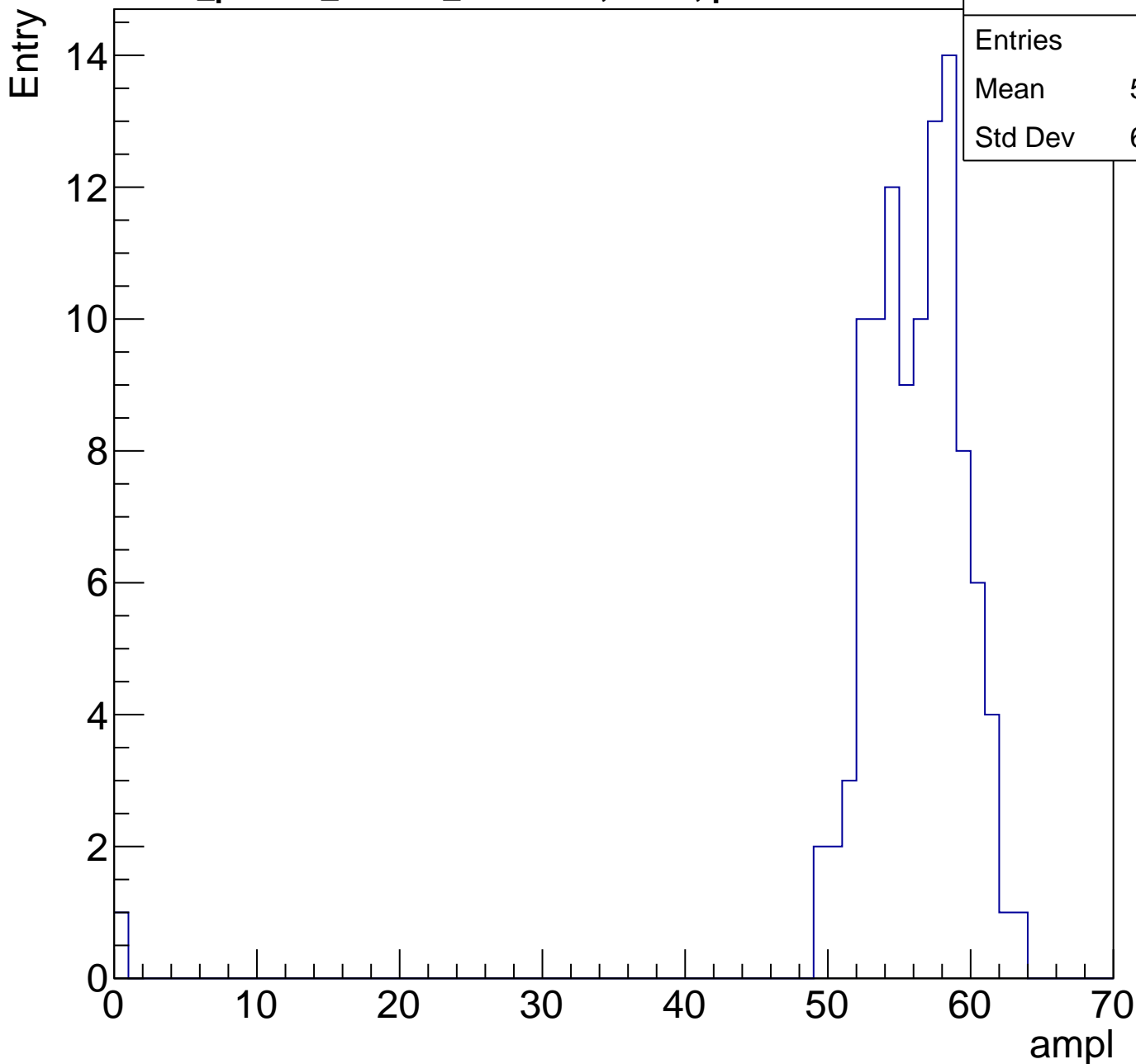
Entries	142
Mean	49.06
Std Dev	3.74



B1L001S, U16-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	55.26
Std Dev	6.183



B1L001S, U16-ch108, adc5

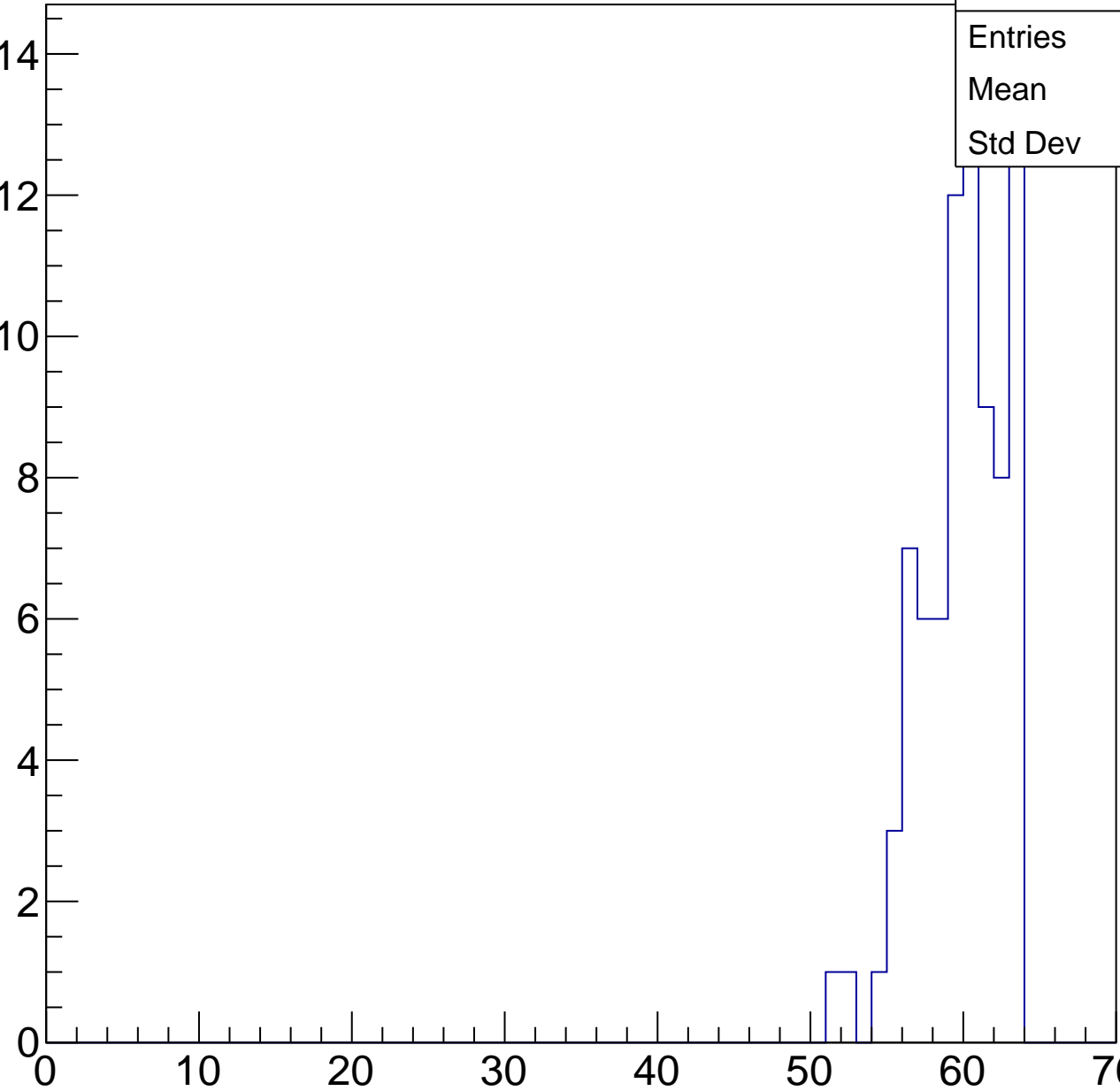
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	59.46
Std Dev	2.704

ampl



B1L001S, U16-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	26
Mean	59.38
Std Dev	11.94

Entry

10

8

6

4

2

0

0

10

20

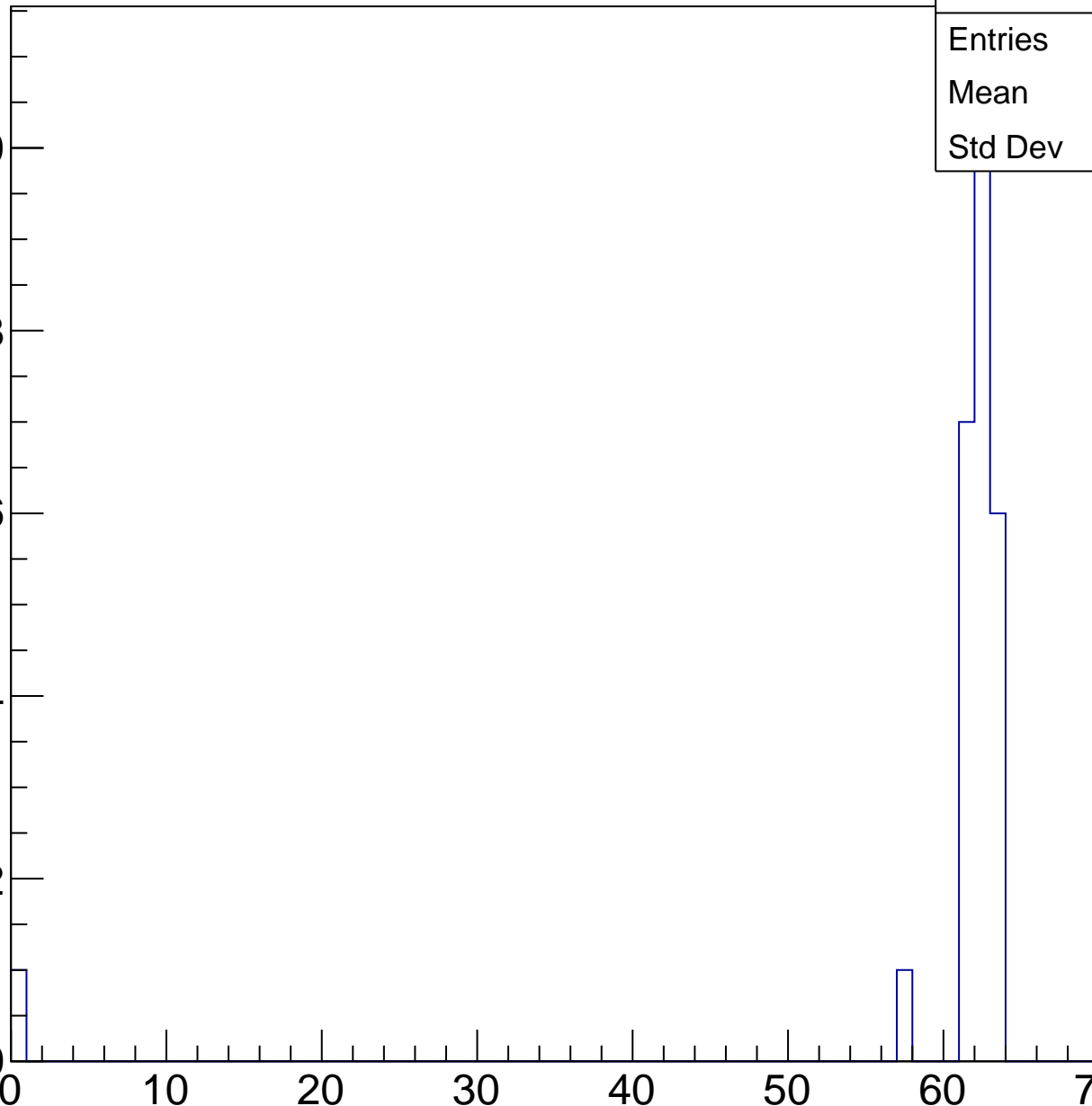
30

40

50

60

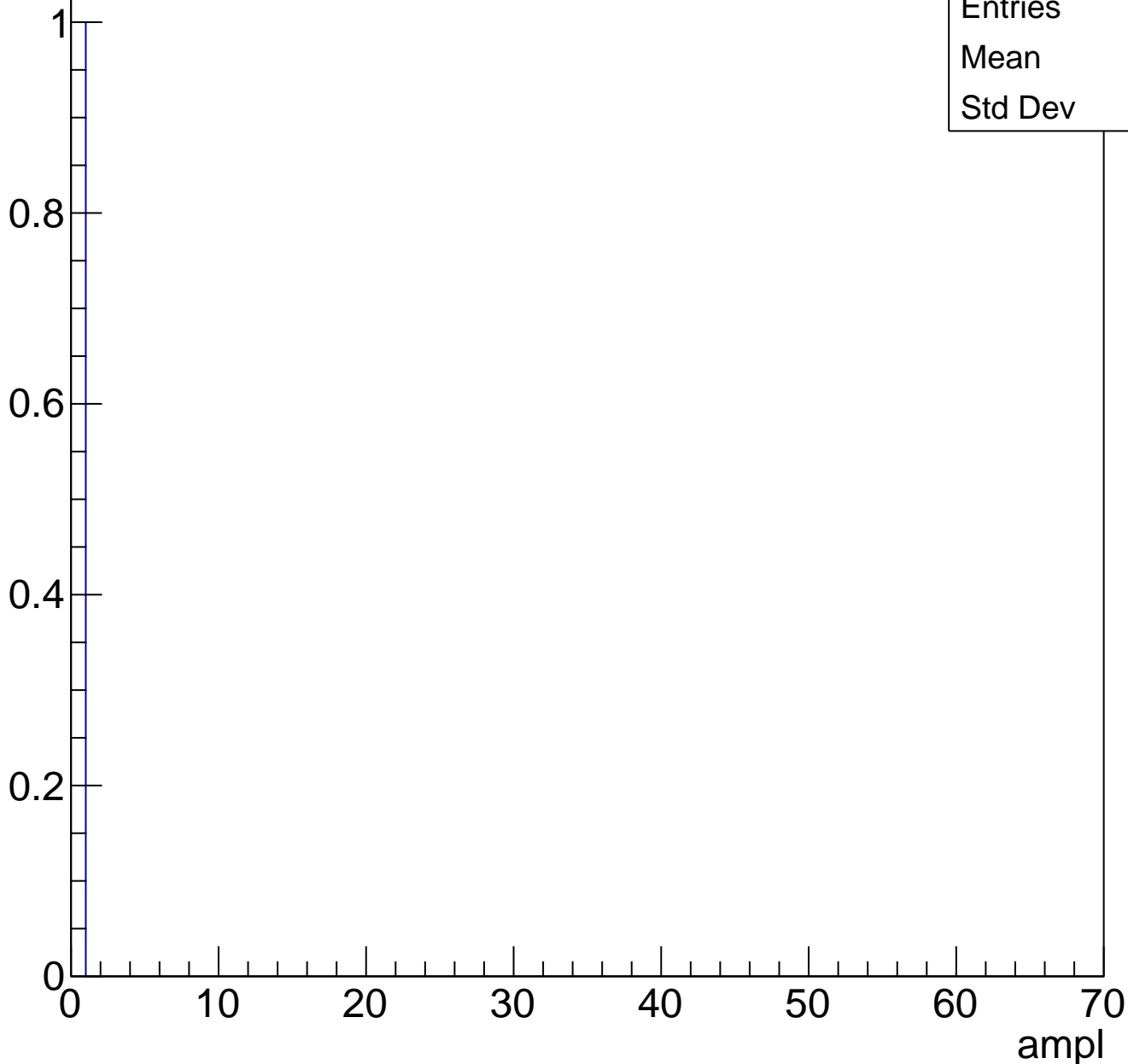
ampl



B1L001S, U16-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	185
Mean	29.85
Std Dev	4.797

Gaus mean : 30.5267

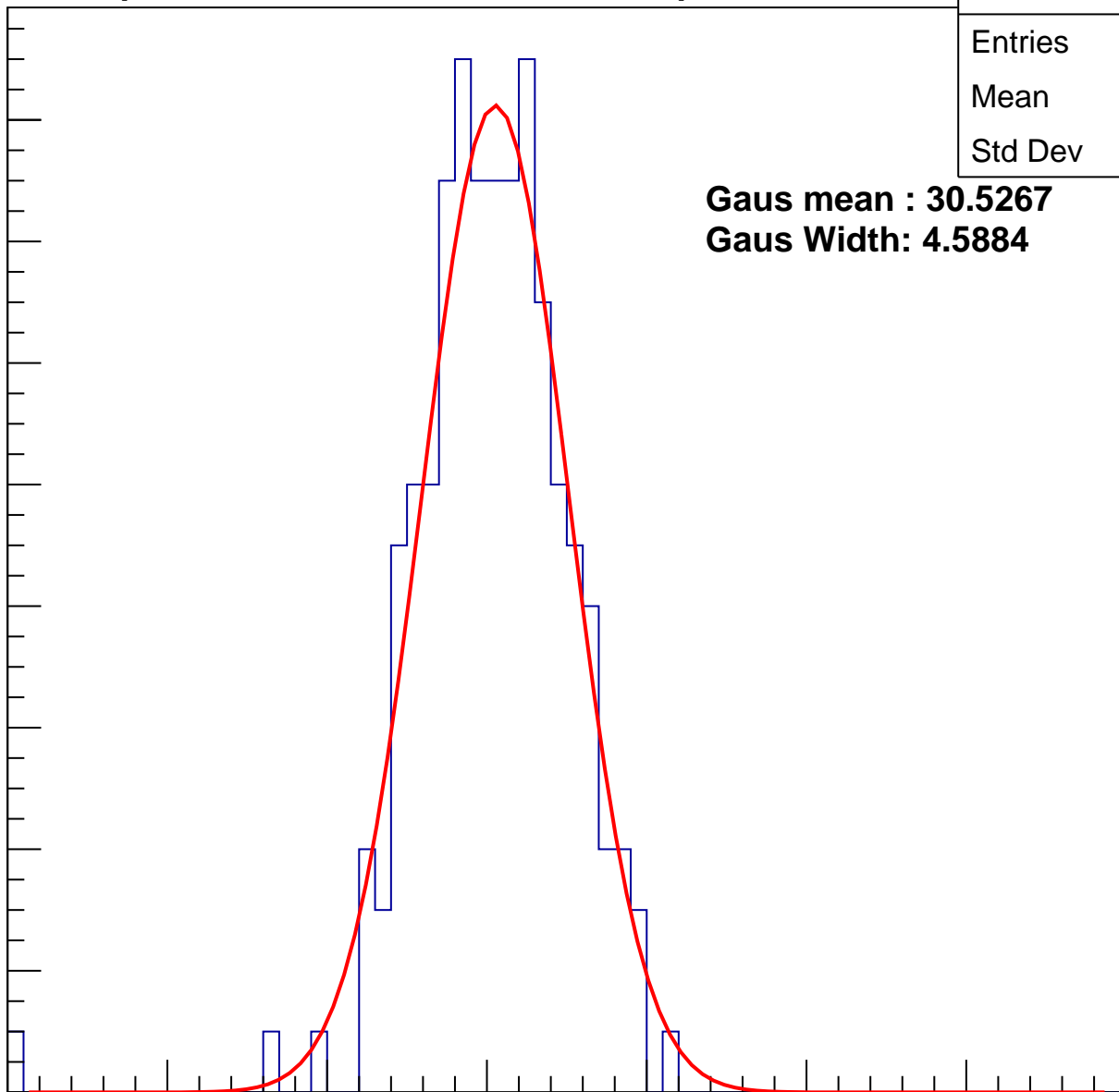
Gaus Width: 4.5884

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch109, adc1

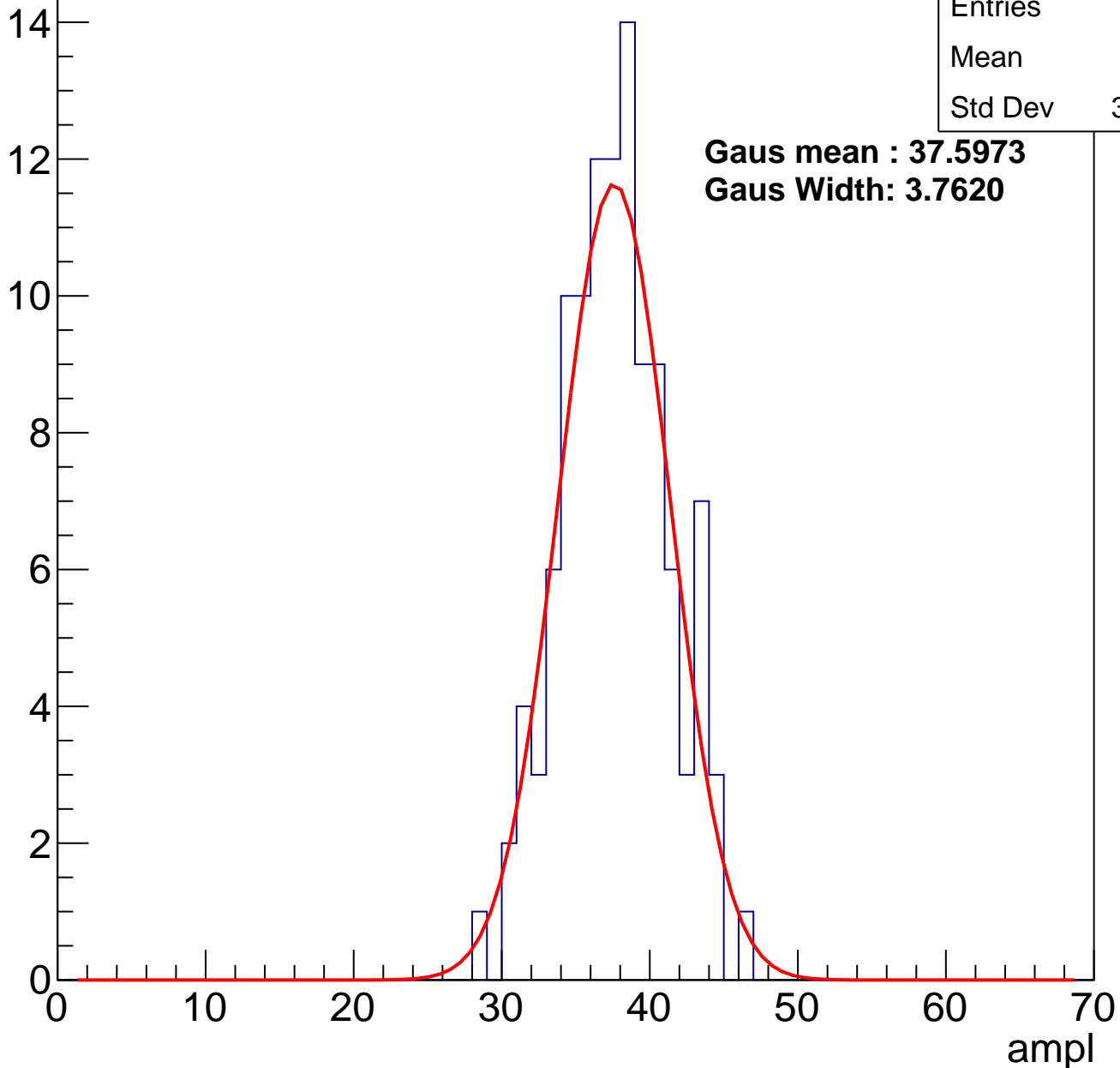
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	37.2
Std Dev	3.553

Gaus mean : 37.5973

Gaus Width: 3.7620

Entry



B1L001S, U16-ch109, adc2

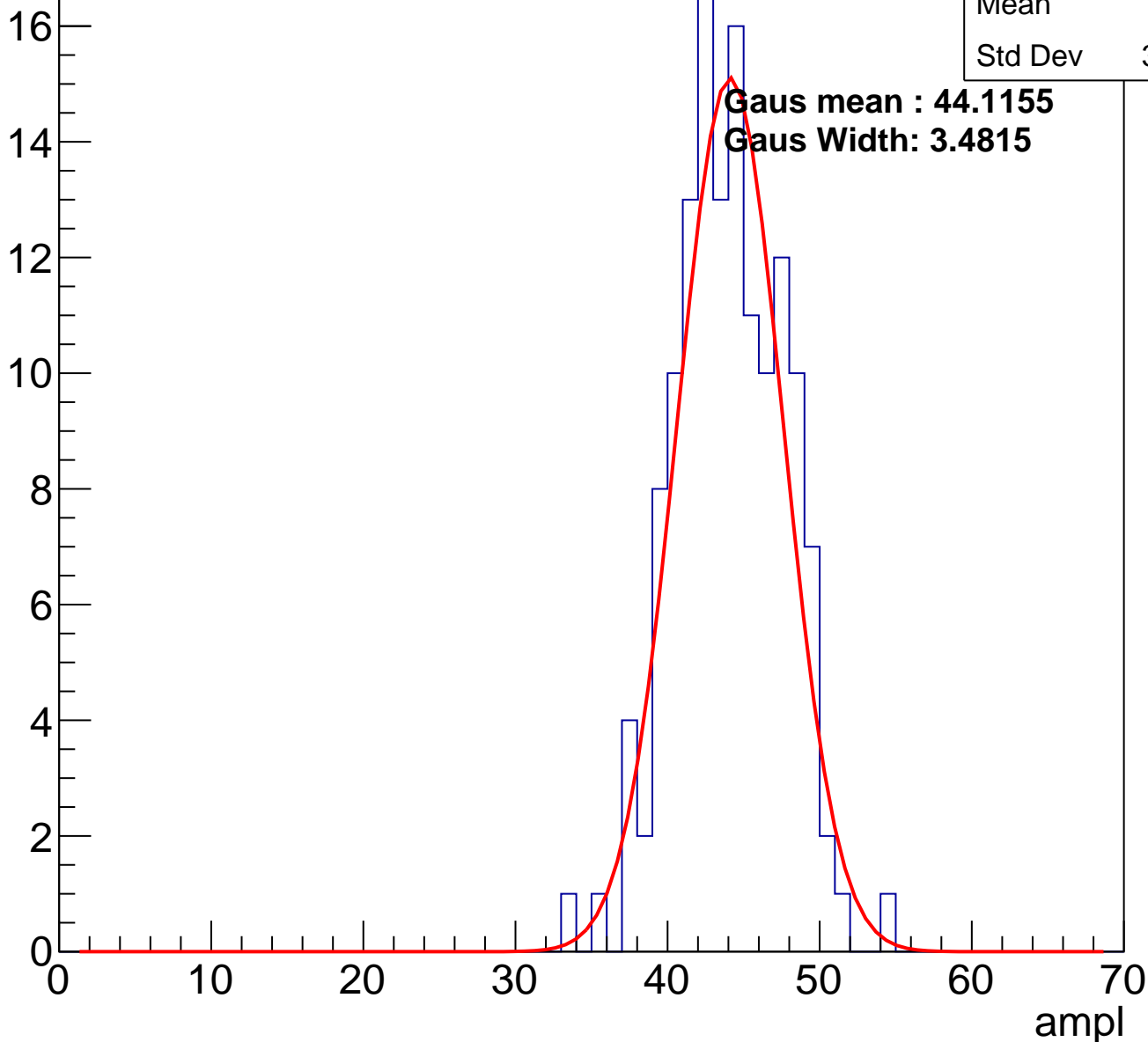
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	139
Mean	43.6
Std Dev	3.519

Gaus mean : 44.1155

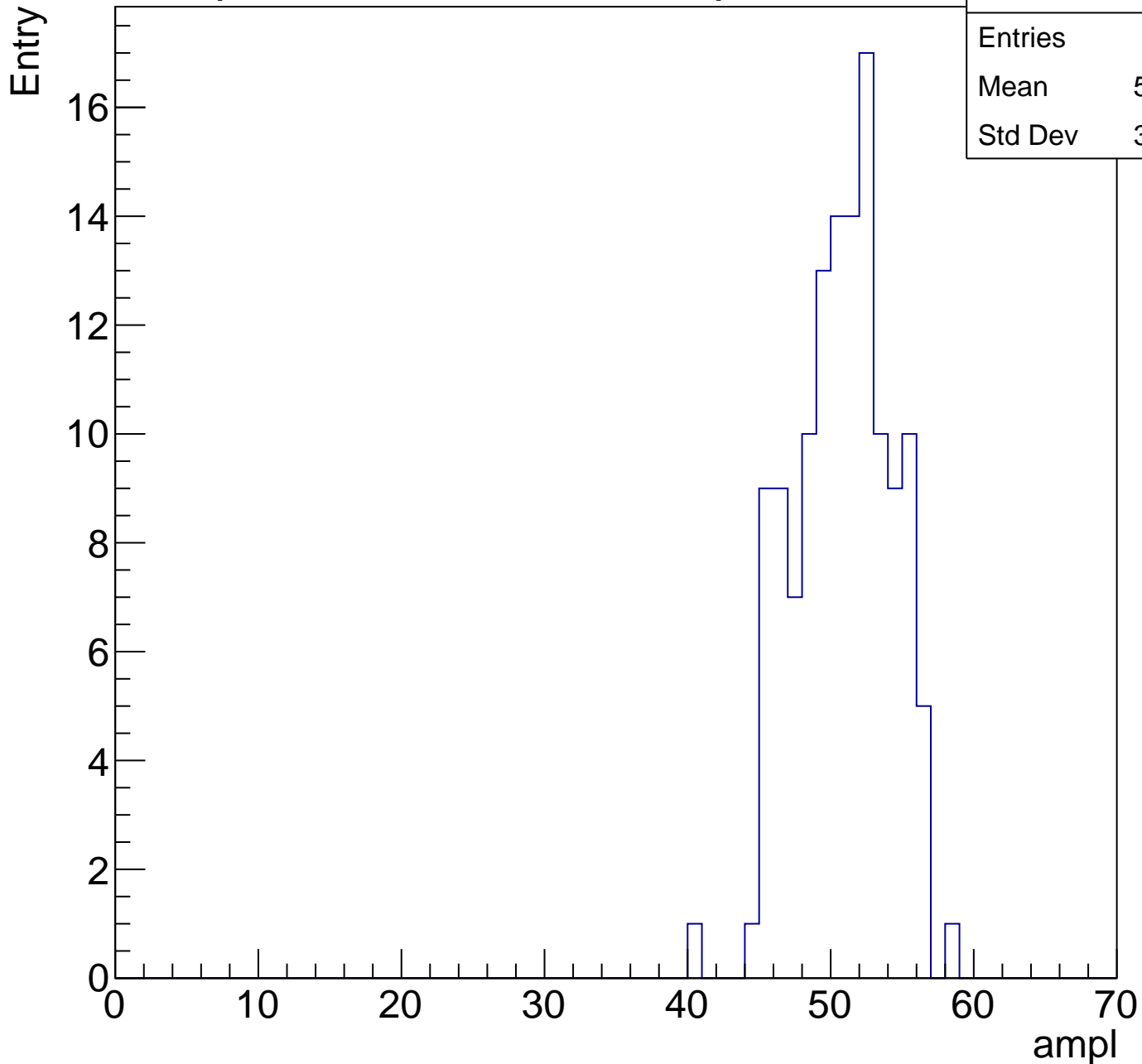
Gaus Width: 3.4815



B1L001S, U16-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	50.39
Std Dev	3.295



B1L001S, U16-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

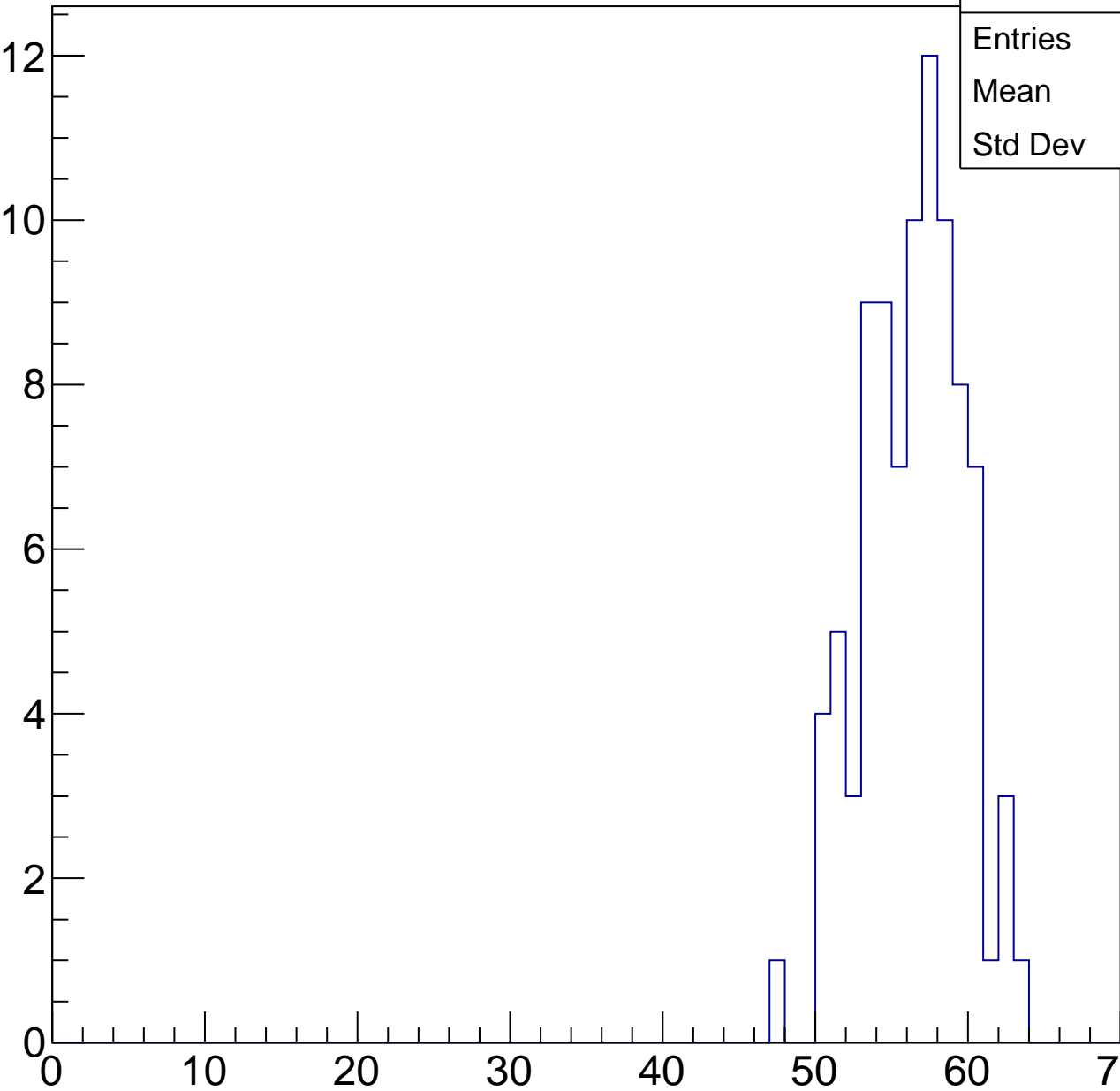
Entries	90
Mean	55.91
Std Dev	3.231

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch109, adc5

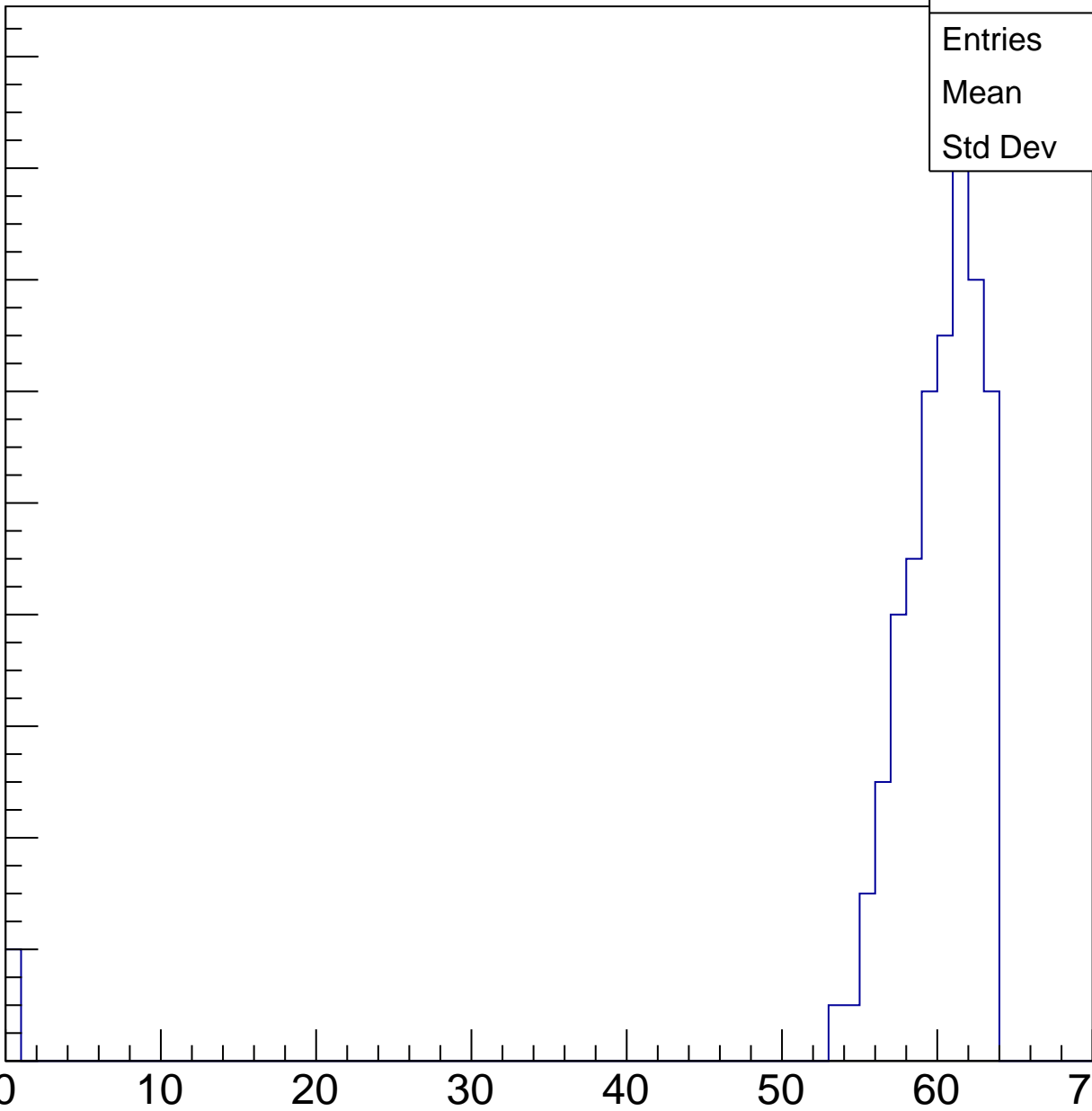
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.57
Std Dev	8.773

ampl

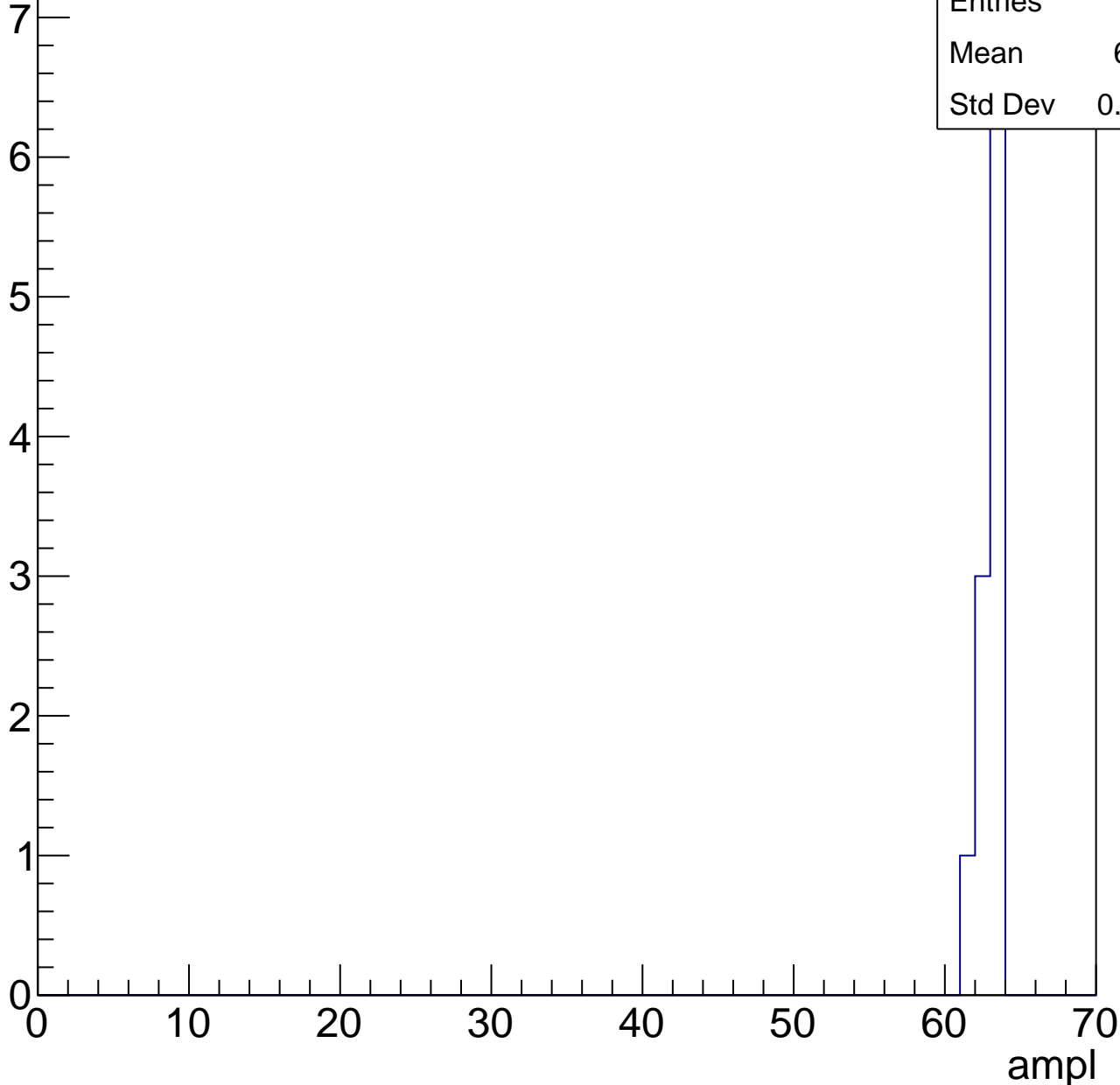


B1L001S, U16-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.55
Std Dev	0.6556



B1L001S, U16-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U16-ch110, adc0

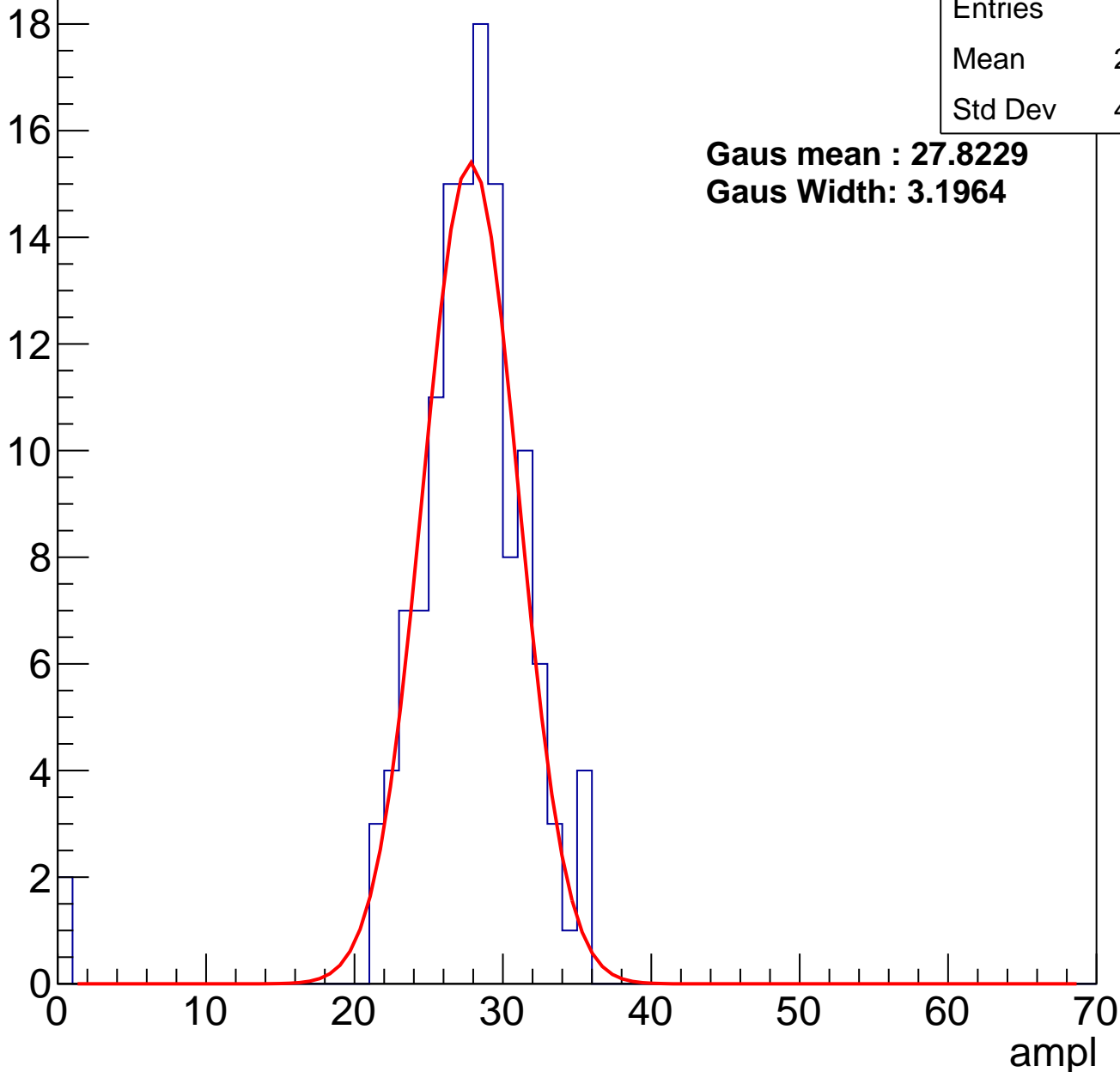
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	27.16
Std Dev	4.634

Gaus mean : 27.8229

Gaus Width: 3.1964

Entry



B1L001S, U16-ch110, adc1

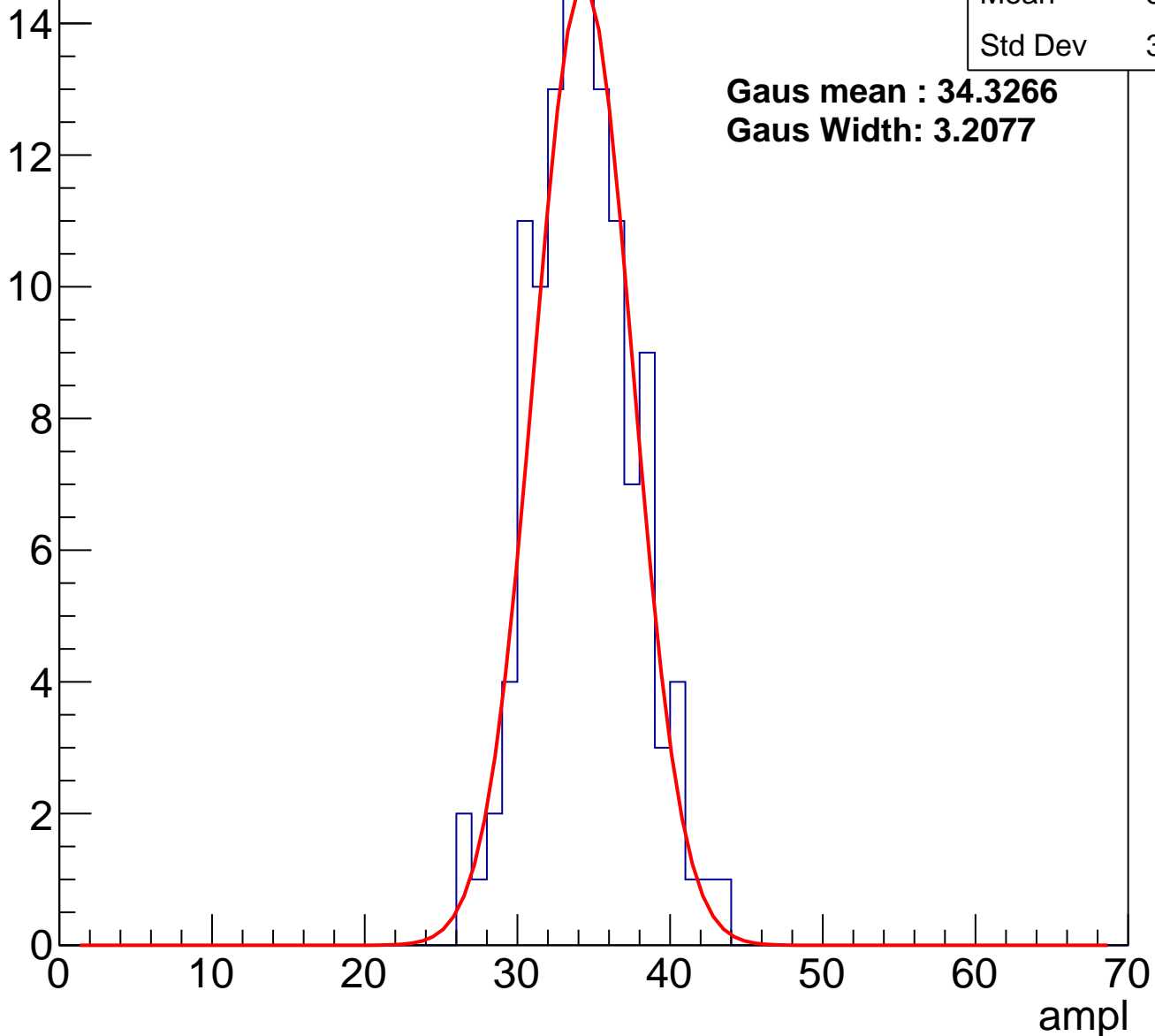
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	33.88
Std Dev	3.322

Gaus mean : 34.3266

Gaus Width: 3.2077

Entry



B1L001S, U16-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	40.68
Std Dev	3.474

Gaus mean : 41.2198

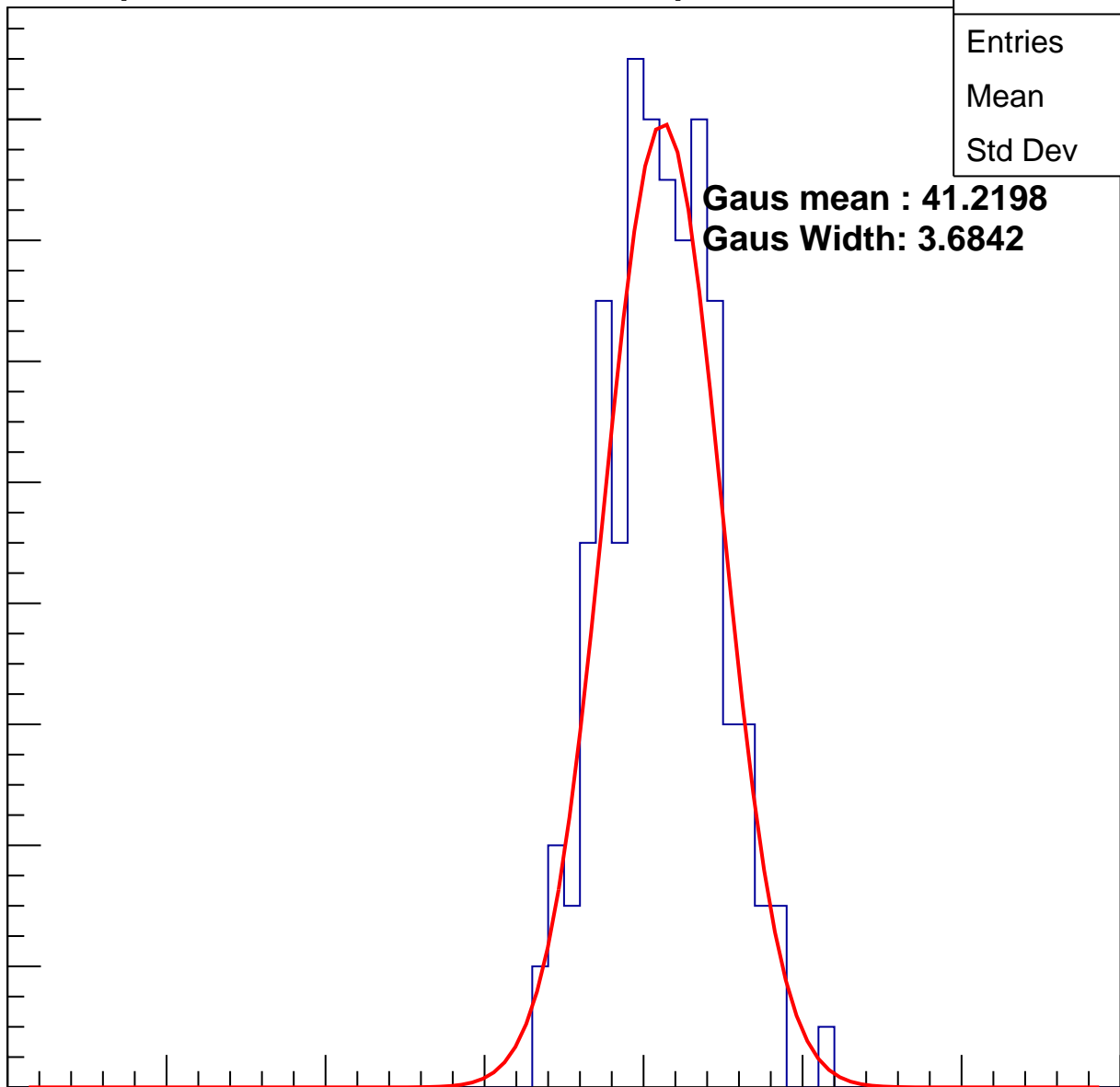
Gaus Width: 3.6842

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

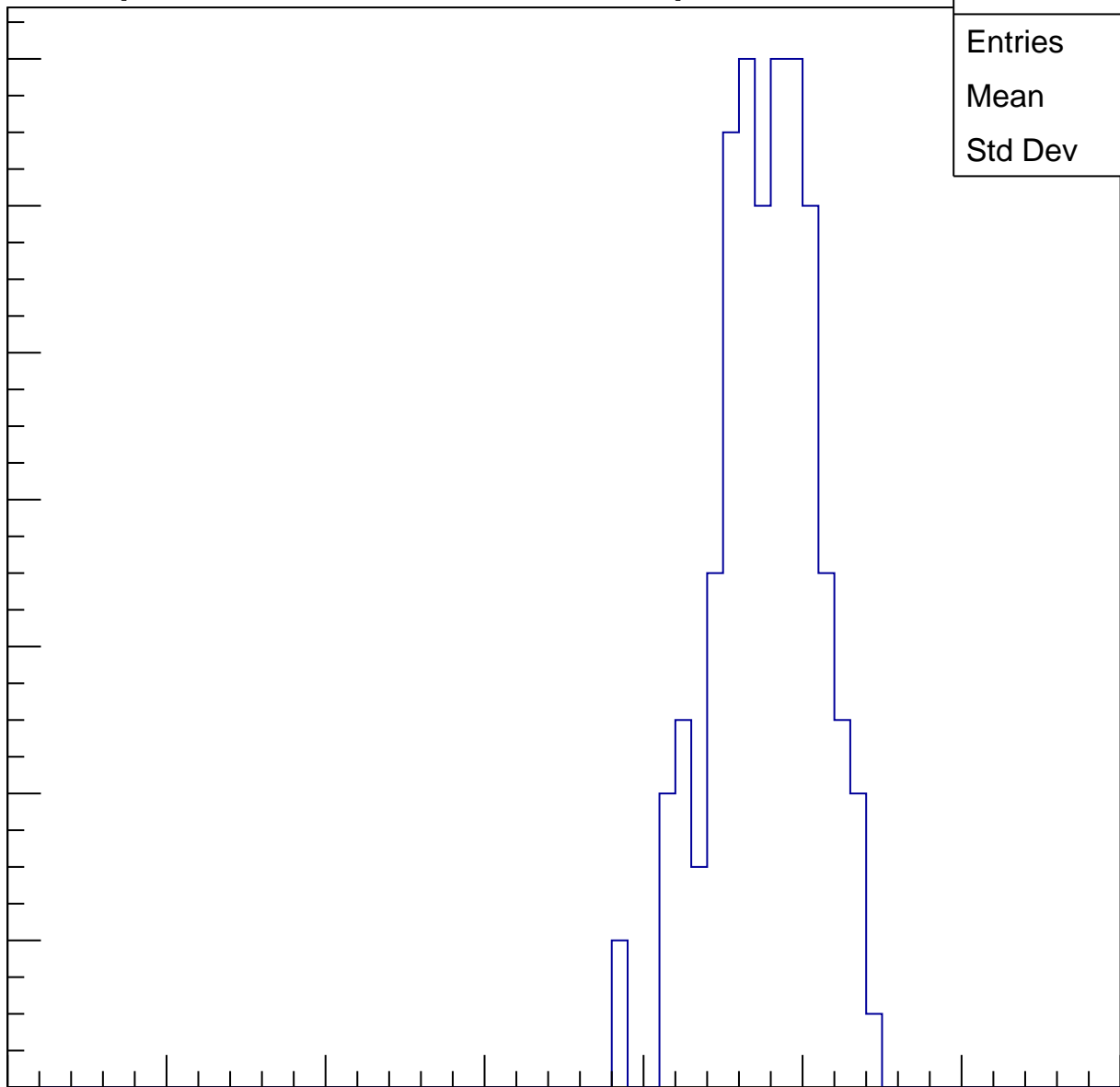
Entries	117
Mean	47.19
Std Dev	3.218

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

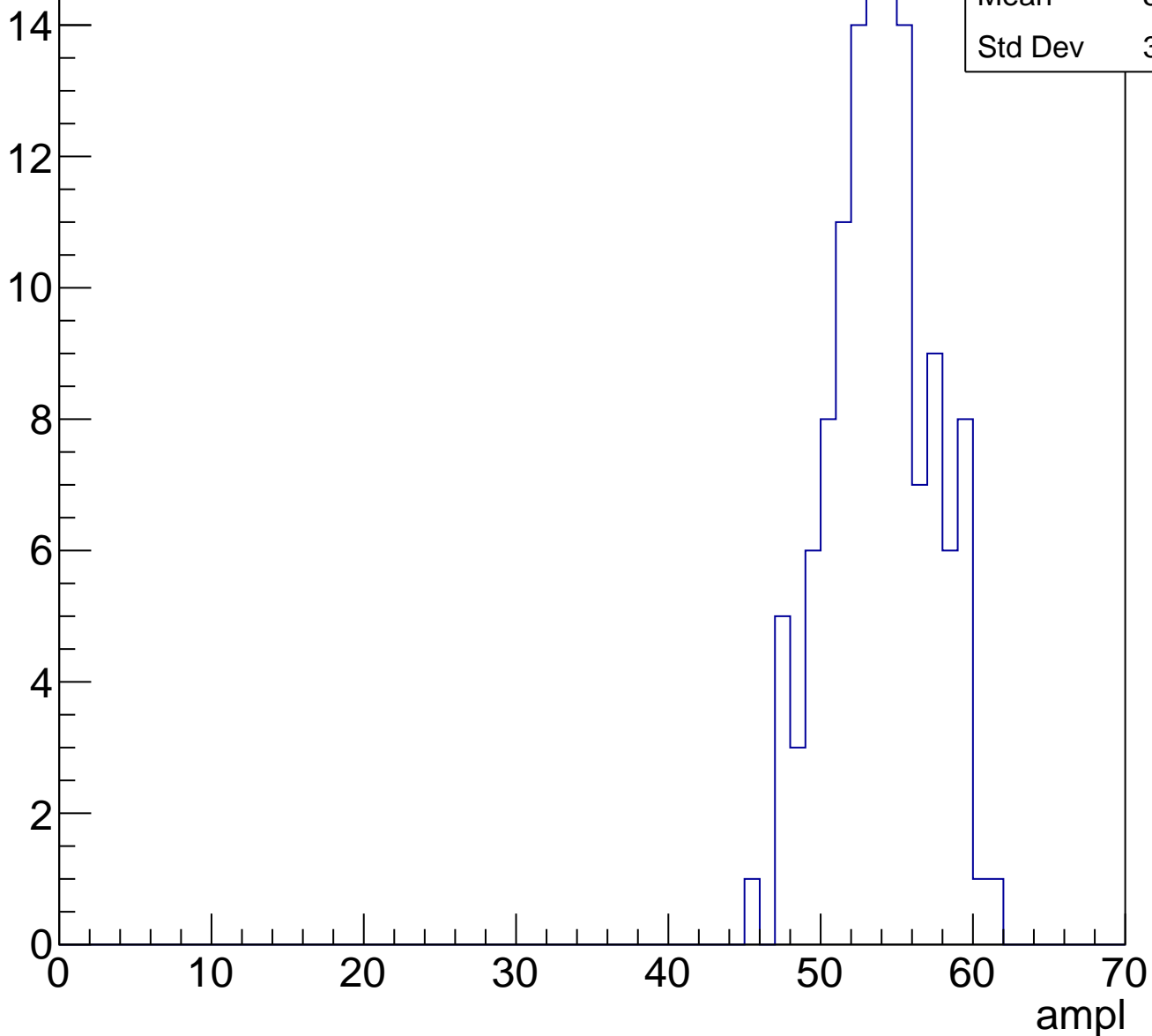


B1L001S, U16-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	53.45
Std Dev	3.298

Entry

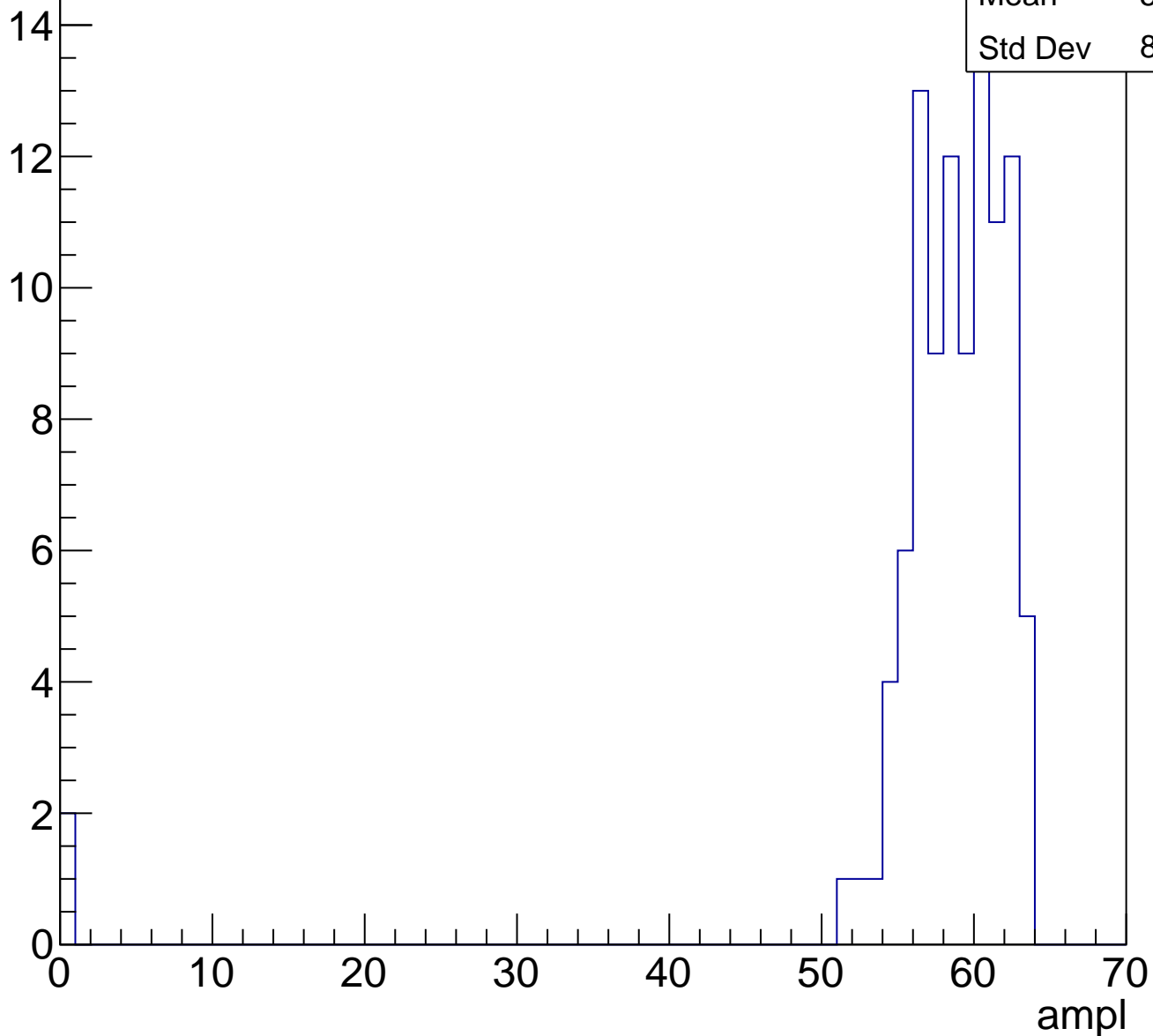


B1L001S, U16-ch110, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	57.43
Std Dev	8.599

Entry



B1L001S, U16-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

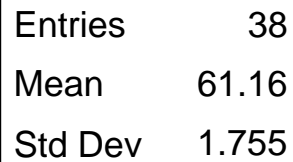
50

60

70

ampl

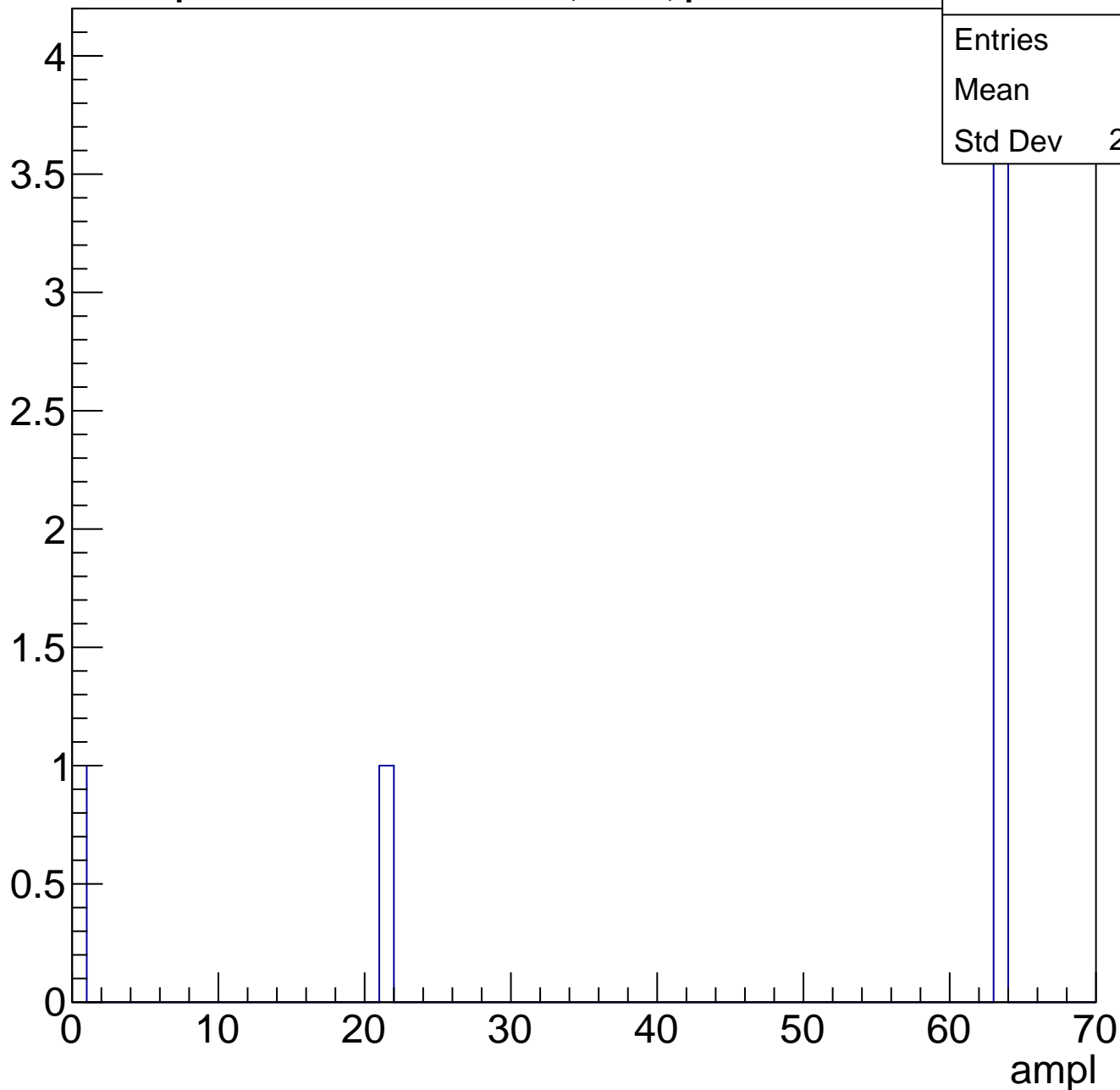
Entries	38
Mean	61.16
Std Dev	1.755



B1L001S, U16-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch111, adc0

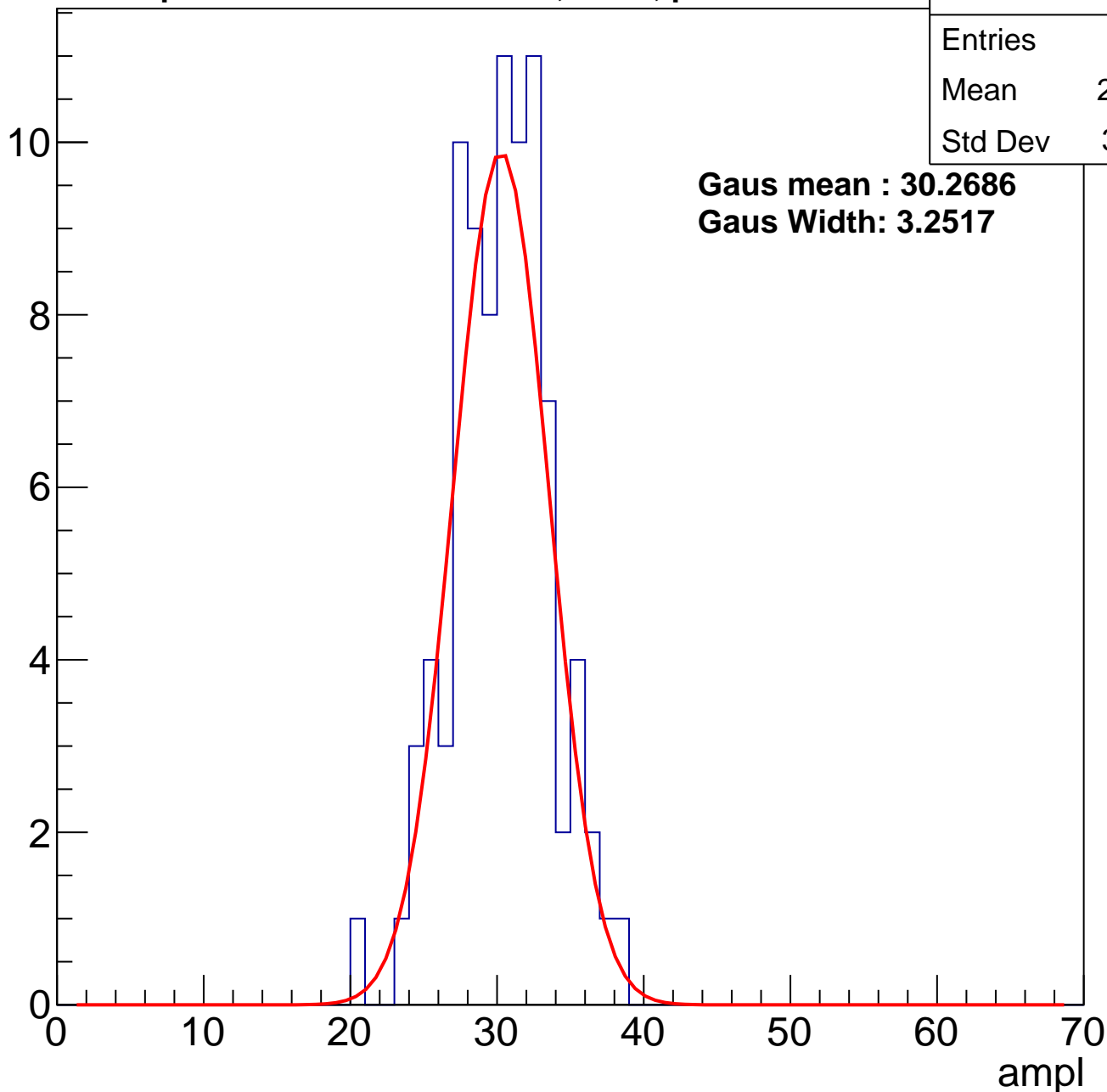
calib_packv5_042523_0143.root, FC#2, port C2

Entries	88
Mean	29.83
Std Dev	3.321

Gaus mean : 30.2686

Gaus Width: 3.2517

Entry



B1L001S, U16-ch111, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	34.69
Std Dev	3.602

Gaus mean : 34.9437

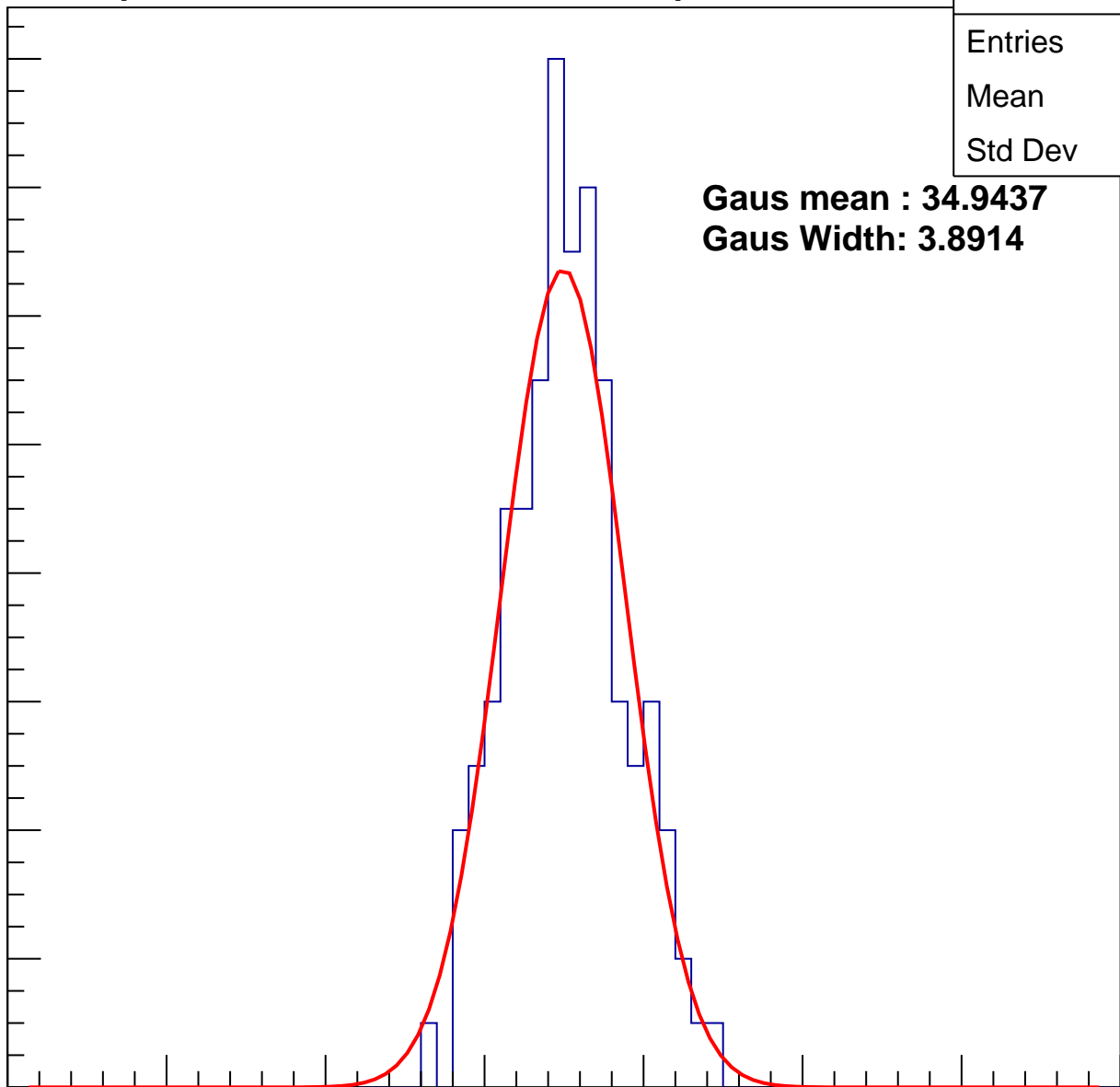
Gaus Width: 3.8914

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch111, adc2

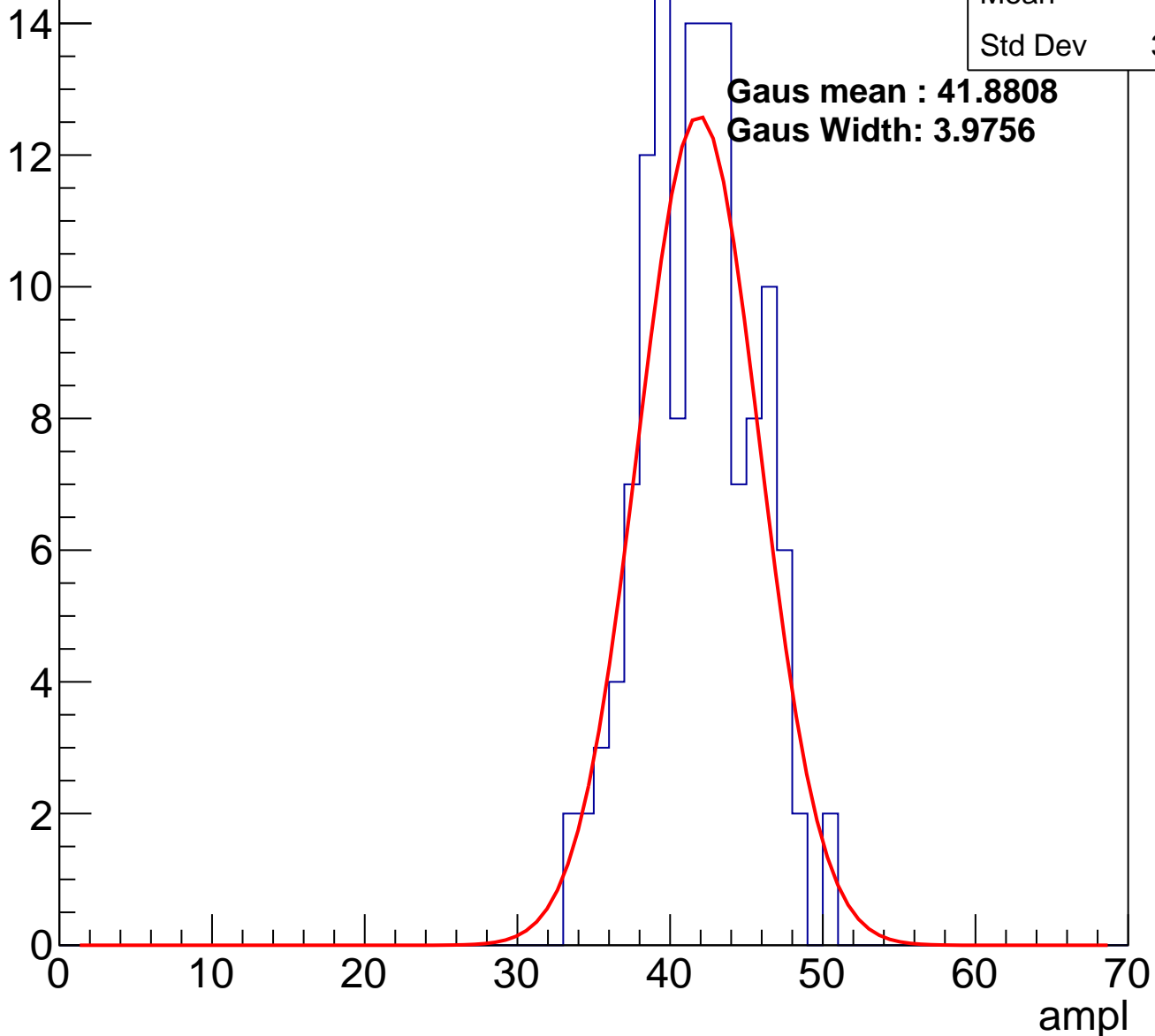
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	41.33
Std Dev	3.611

Gaus mean : 41.8808

Gaus Width: 3.9756

Entry



B1L001S, U16-ch111, adc3

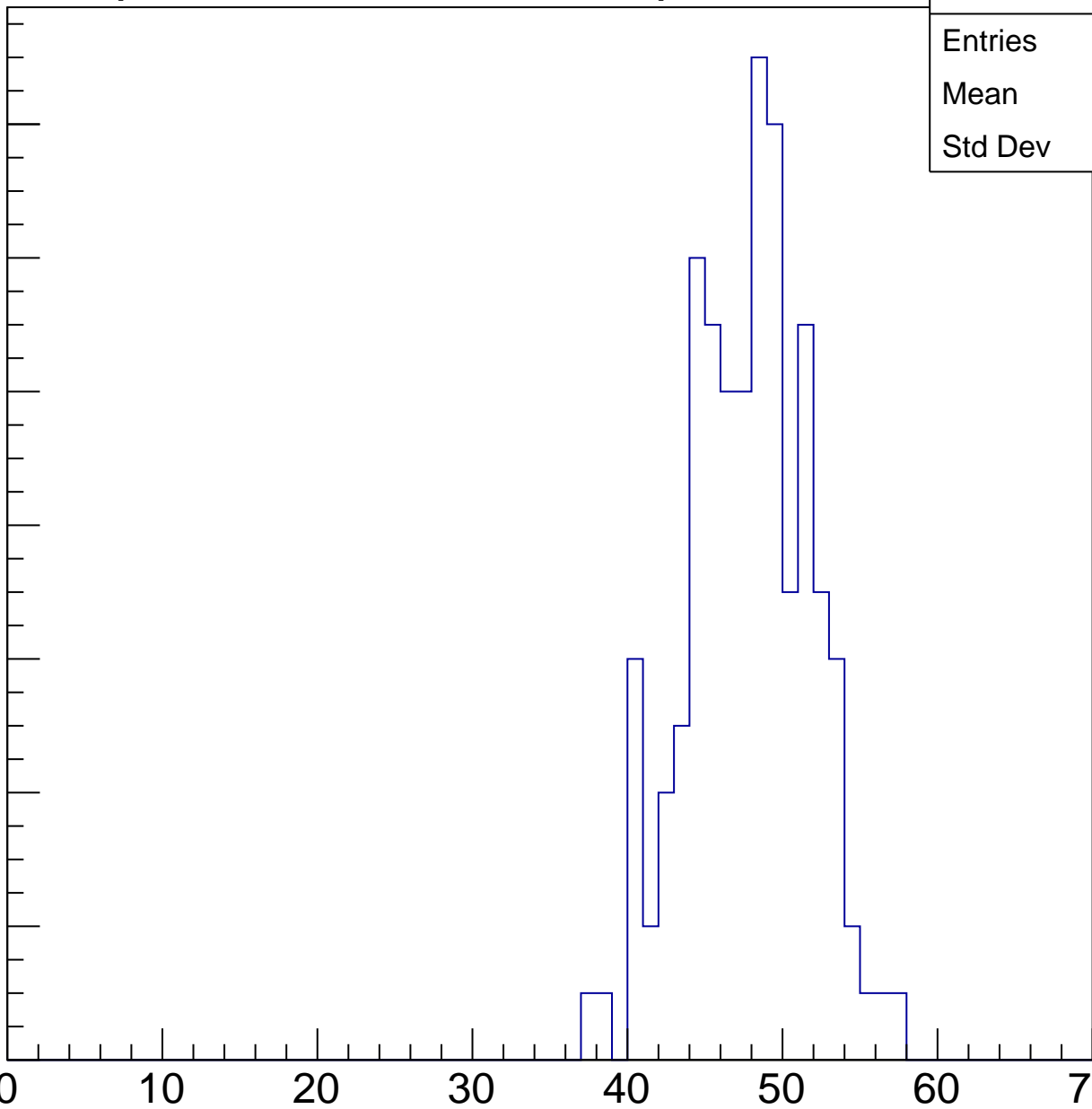
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	47.31
Std Dev	3.889

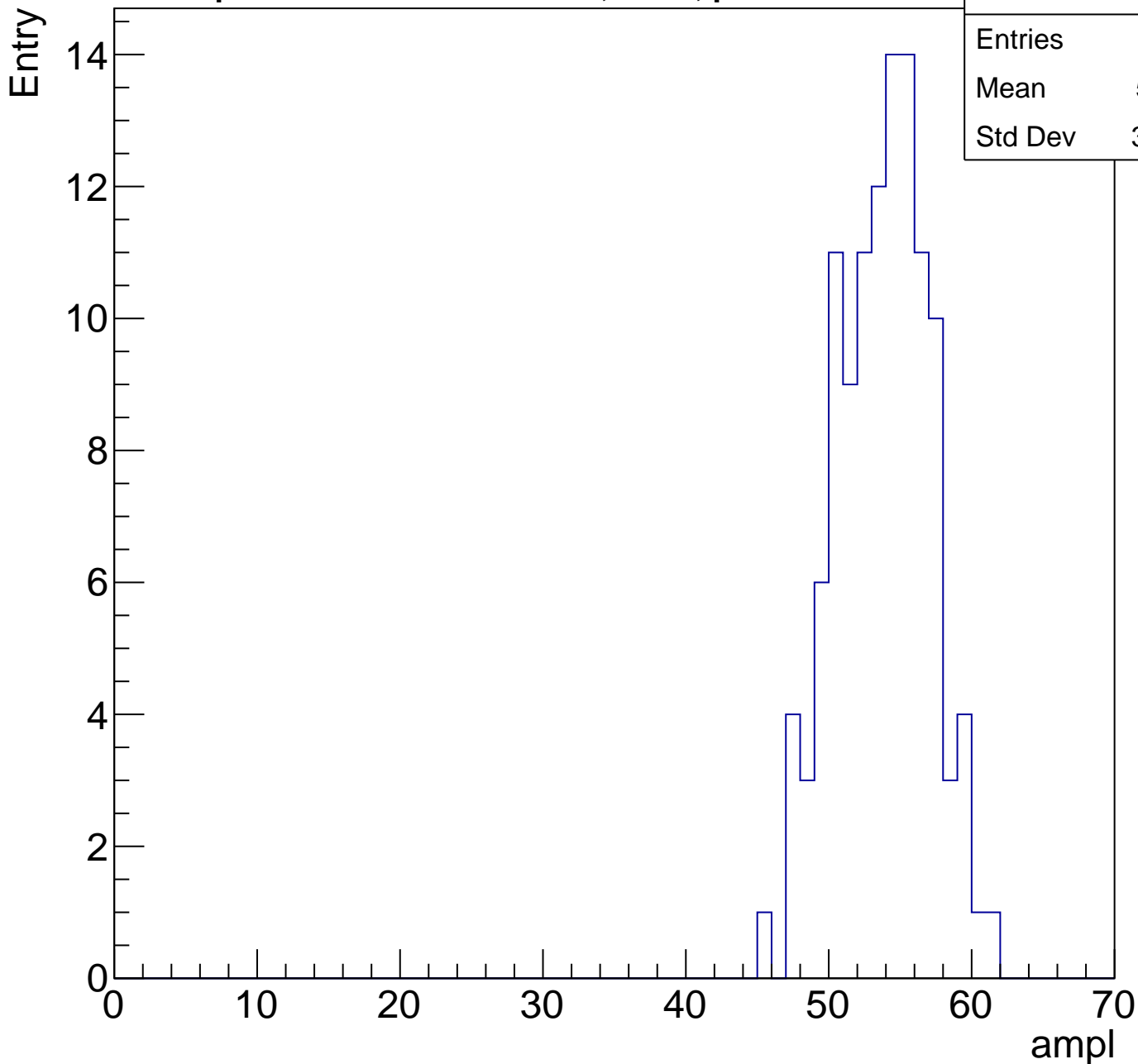
ampl



B1L001S, U16-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	53.31
Std Dev	3.196



B1L001S, U16-ch111, adc5

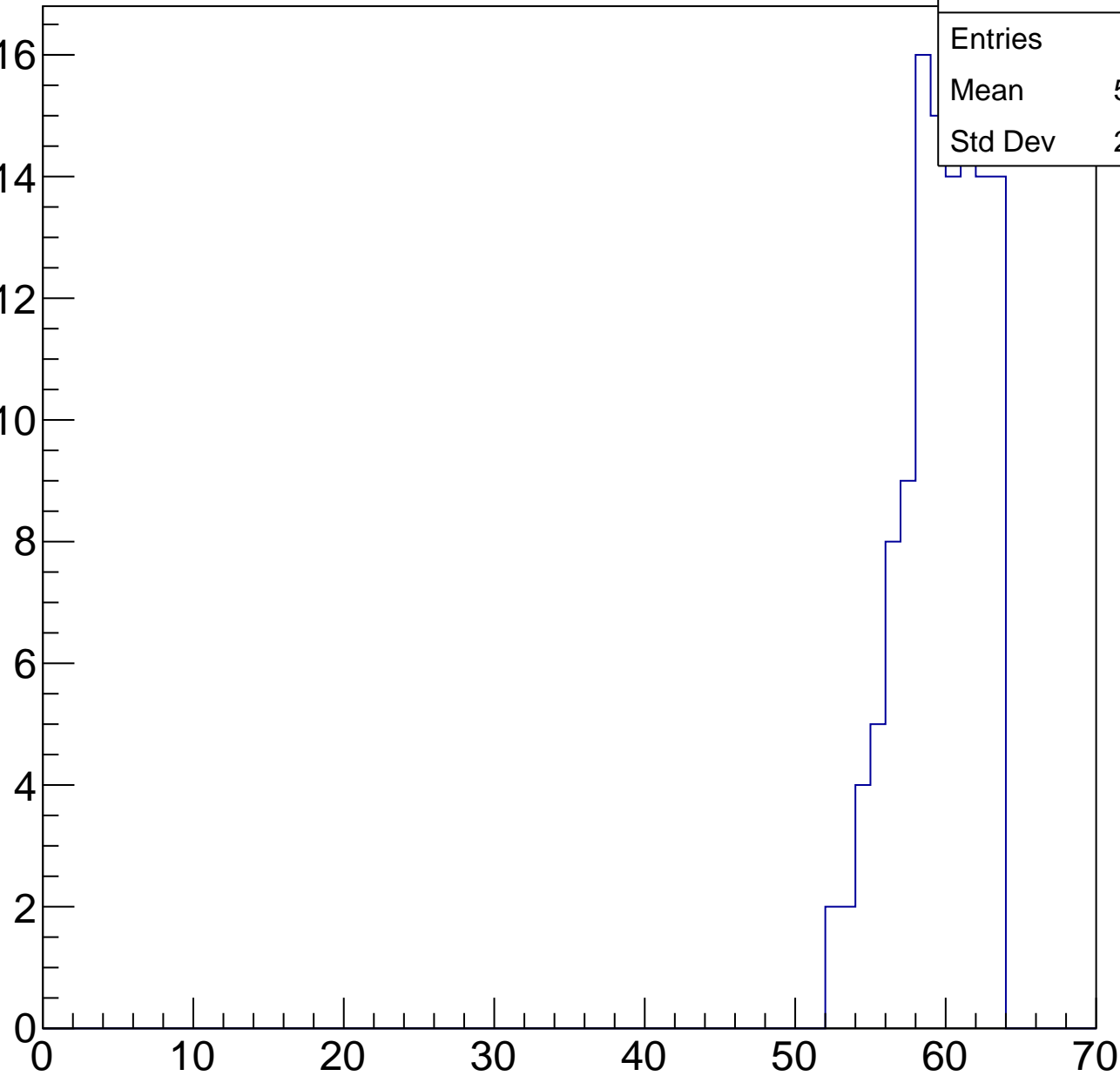
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	59.15
Std Dev	2.754

ampl

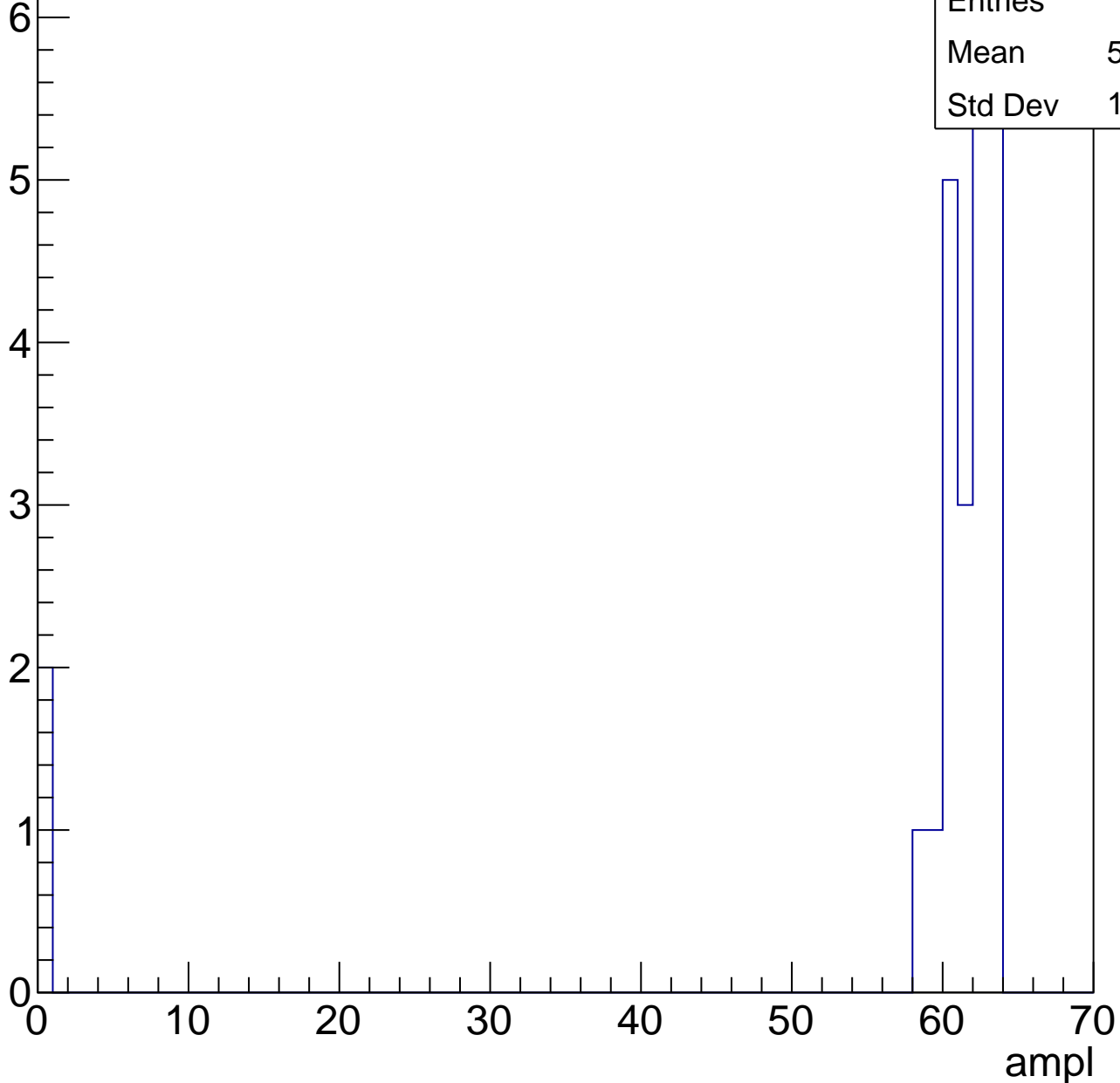


B1L001S, U16-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

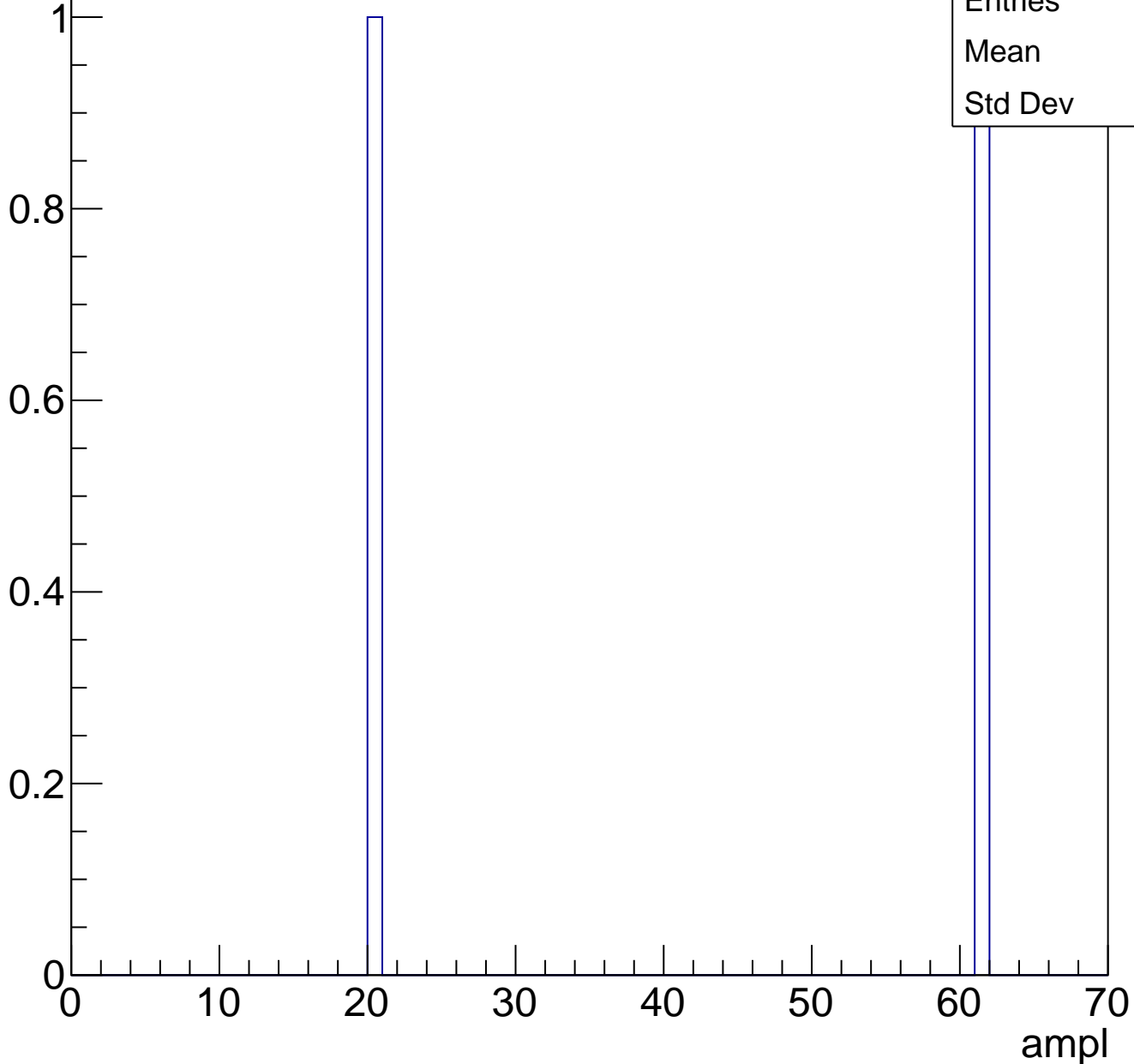
Entries	24
Mean	56.25
Std Dev	17.02



B1L001S, U16-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	40.5
Std Dev	20.5

B1L001S, U16-ch112, adc0

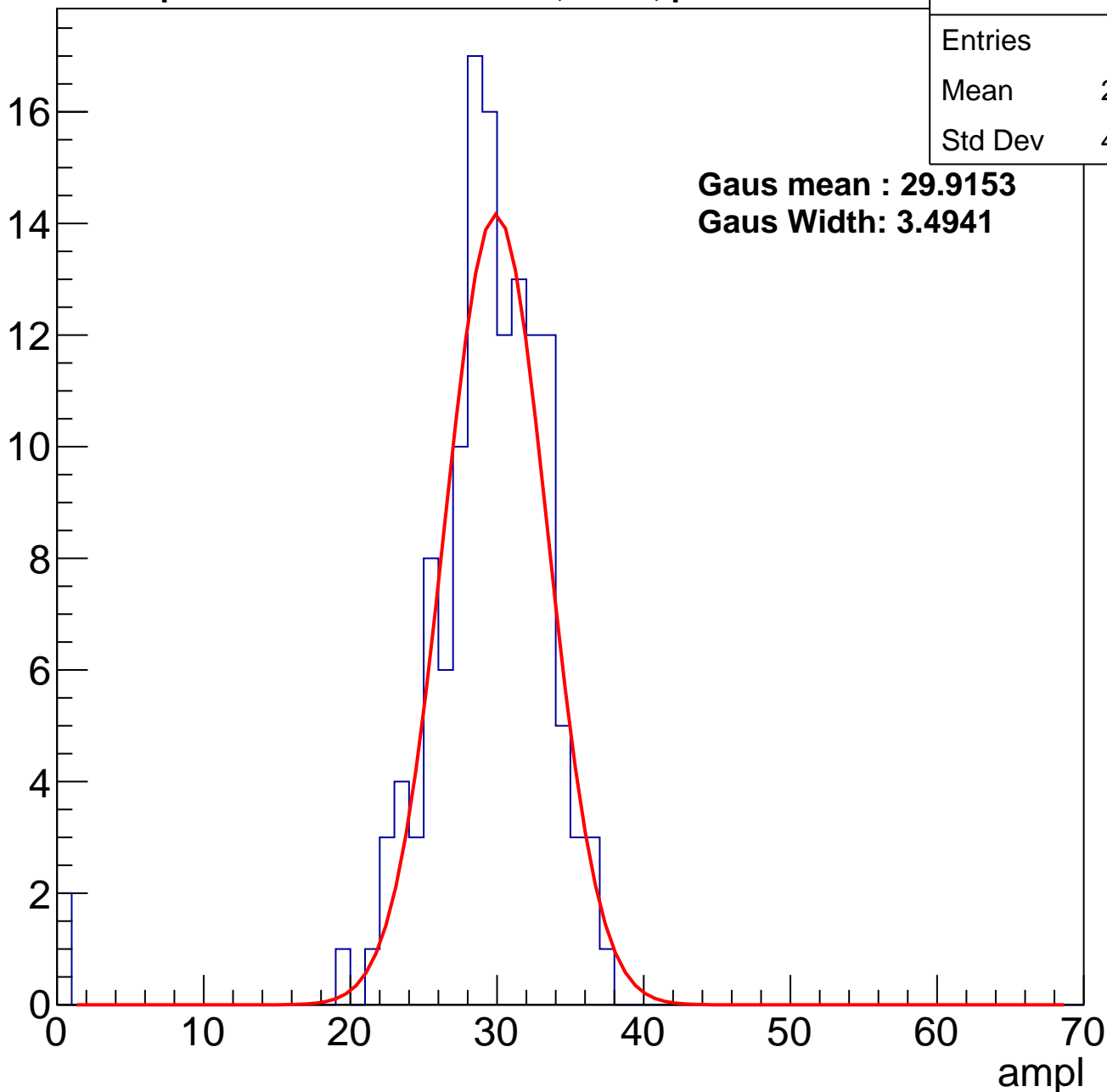
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	28.78
Std Dev	4.964

Gaus mean : 29.9153

Gaus Width: 3.4941

Entry



B1L001S, U16-ch112, adc1

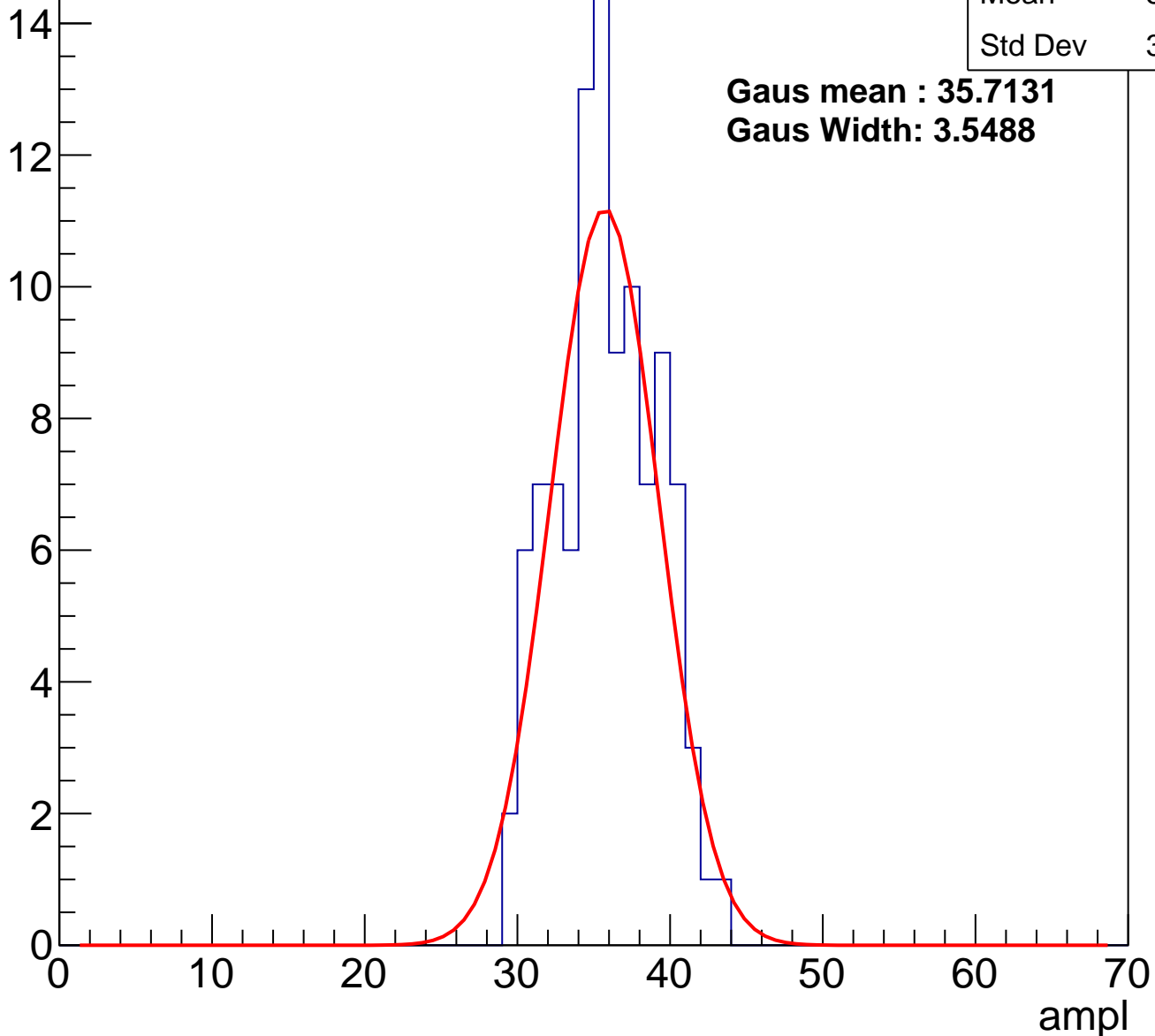
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	35.37
Std Dev	3.226

Gaus mean : 35.7131

Gaus Width: 3.5488

Entry



B1L001S, U16-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	41.28
Std Dev	3.636

Gaus mean : 41.7507

Gaus Width: 3.6342

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

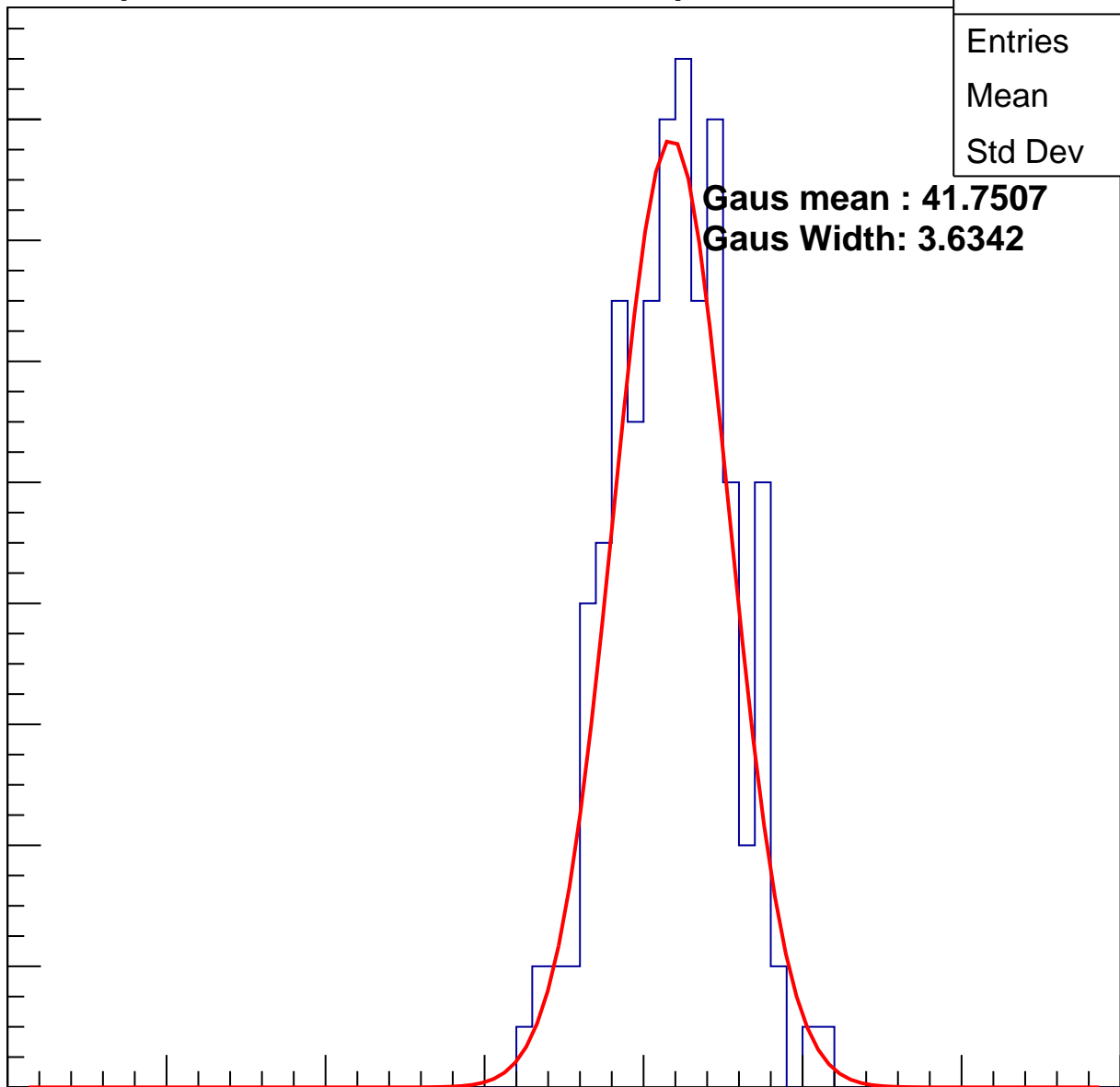
40

50

60

70

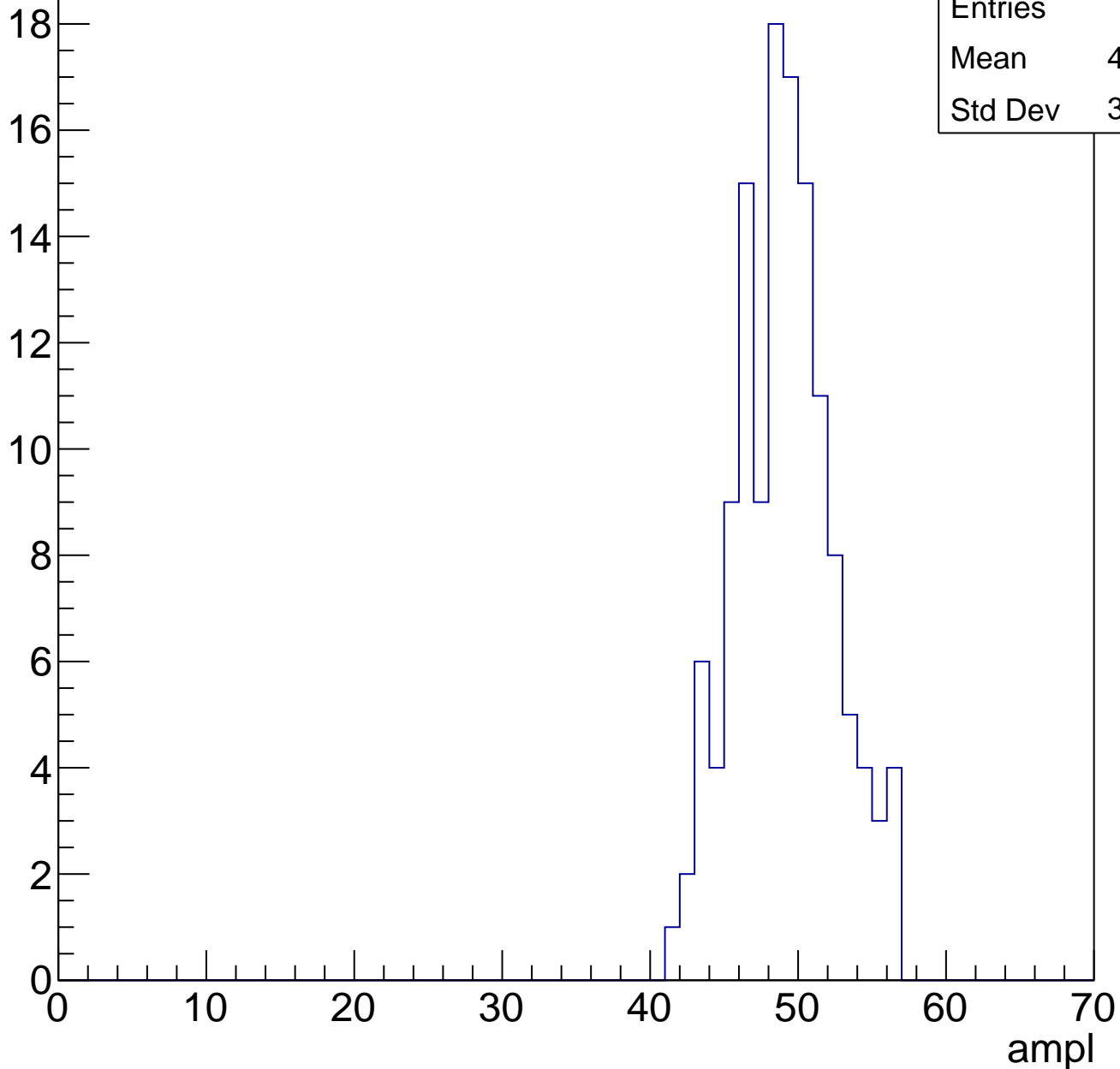
ampl



B1L001S, U16-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	131
Mean	48.63
Std Dev	3.279

B1L001S, U16-ch112, adc4

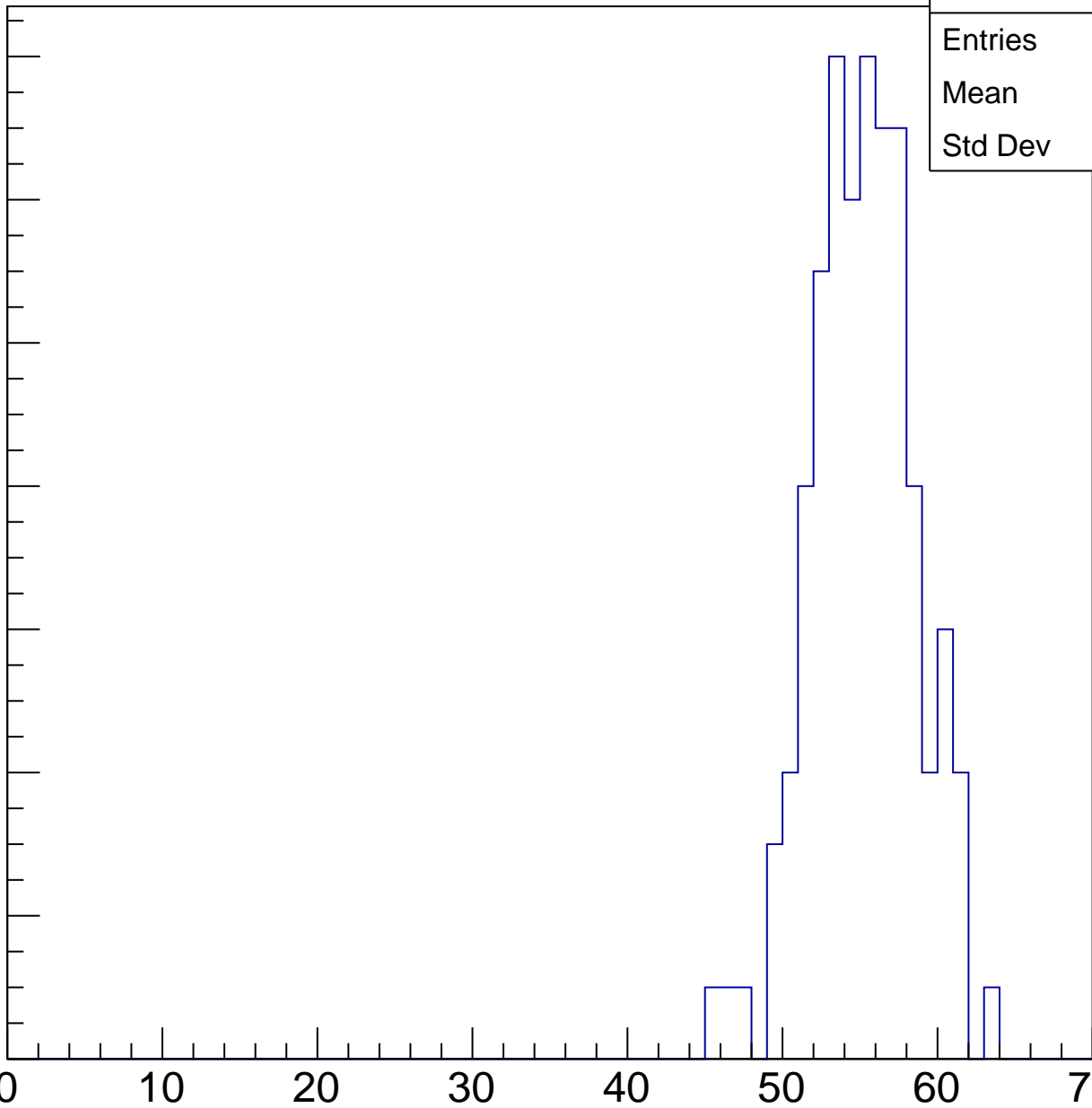
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	54.75
Std Dev	3.319

ampl



B1L001S, U16-ch112, adc5

calib_packv5_042523_0143.root, FC#2, port C2

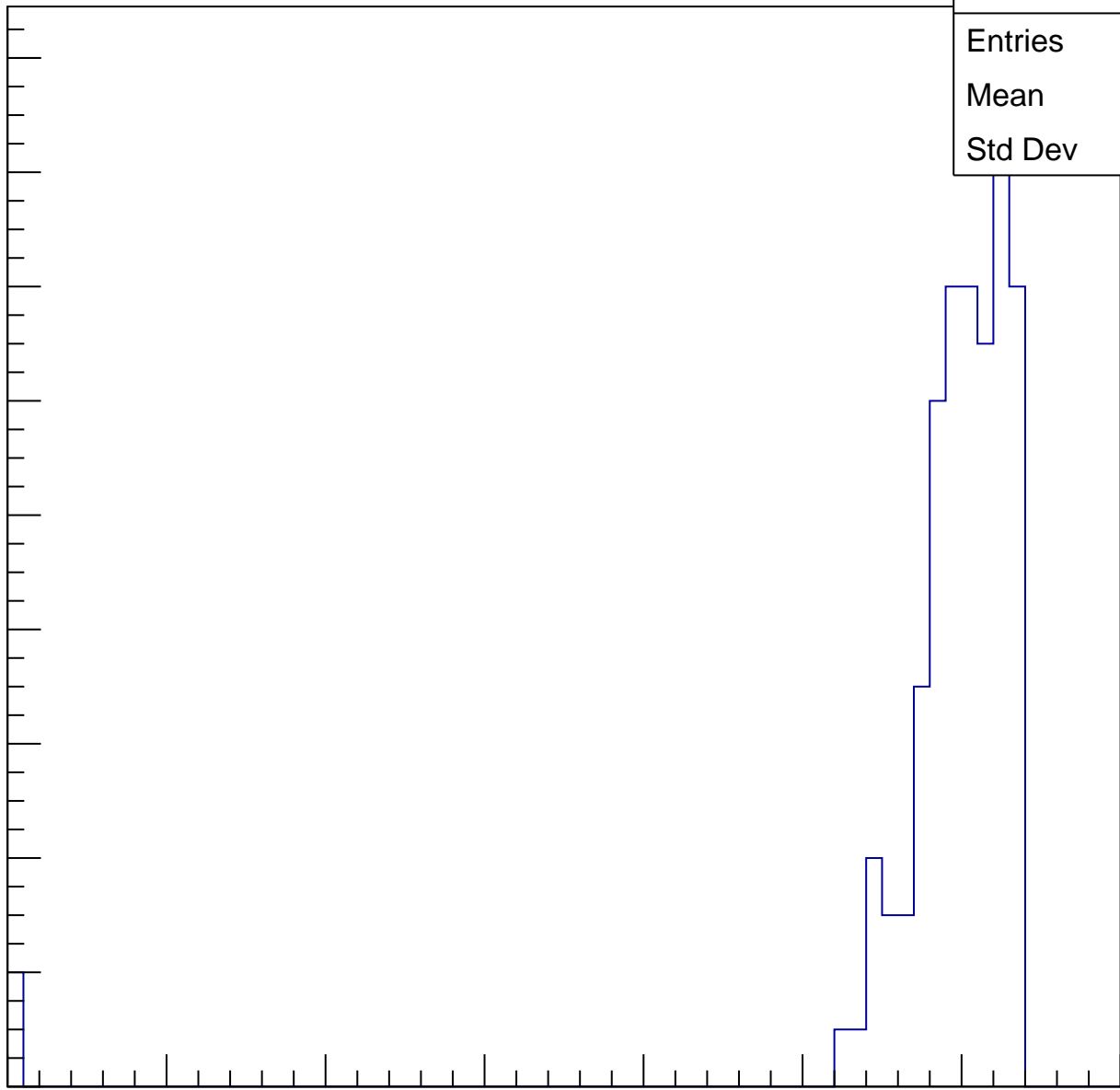
Entries	106
Mean	58.55
Std Dev	8.519

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

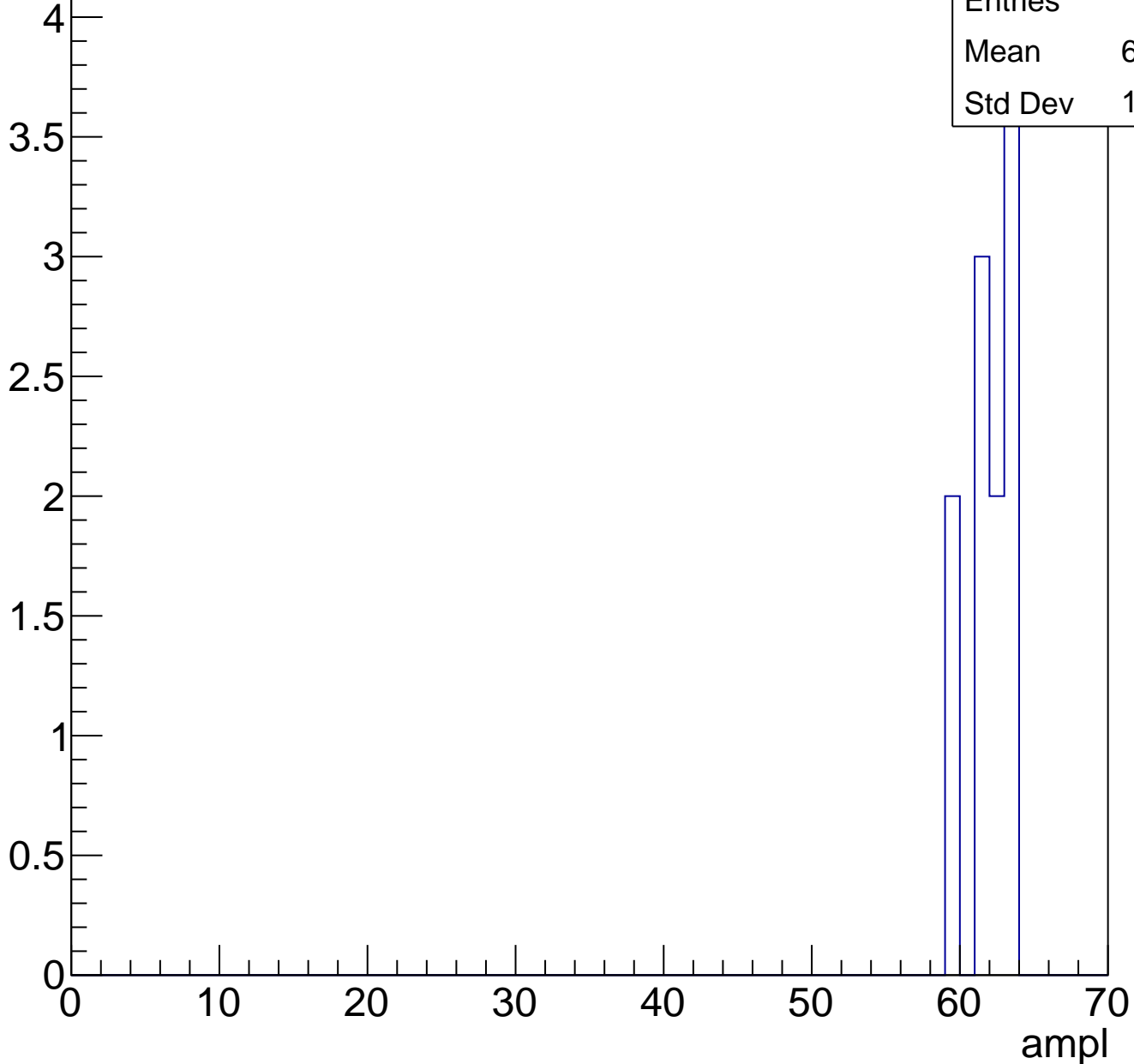
ampl



B1L001S, U16-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

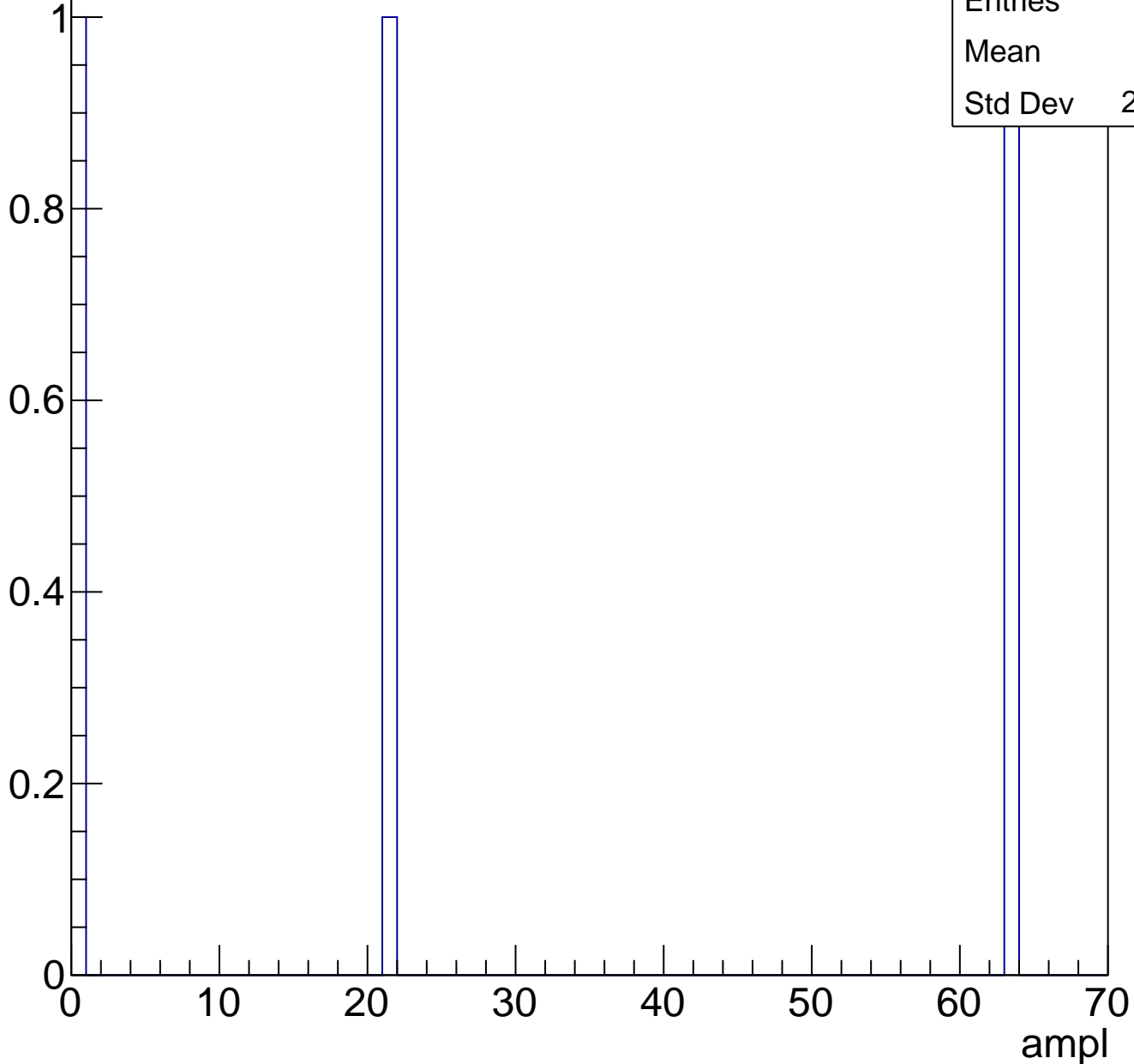


Entries	11
Mean	61.55
Std Dev	1.437

B1L001S, U16-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L001S, U16-ch113, adc0

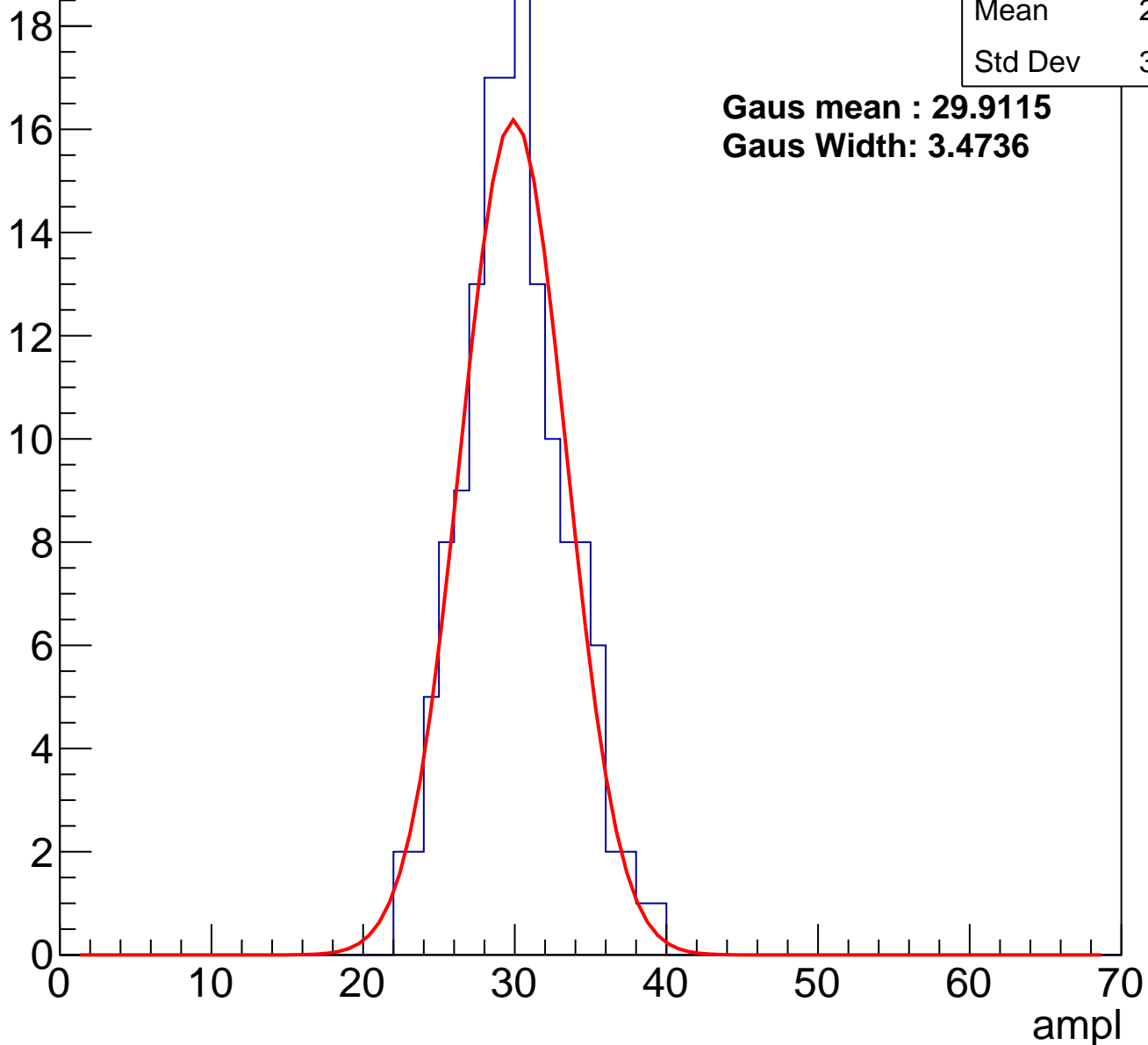
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.55
Std Dev	3.388

Gaus mean : 29.9115

Gaus Width: 3.4736

Entry



B1L001S, U16-ch113, adc1

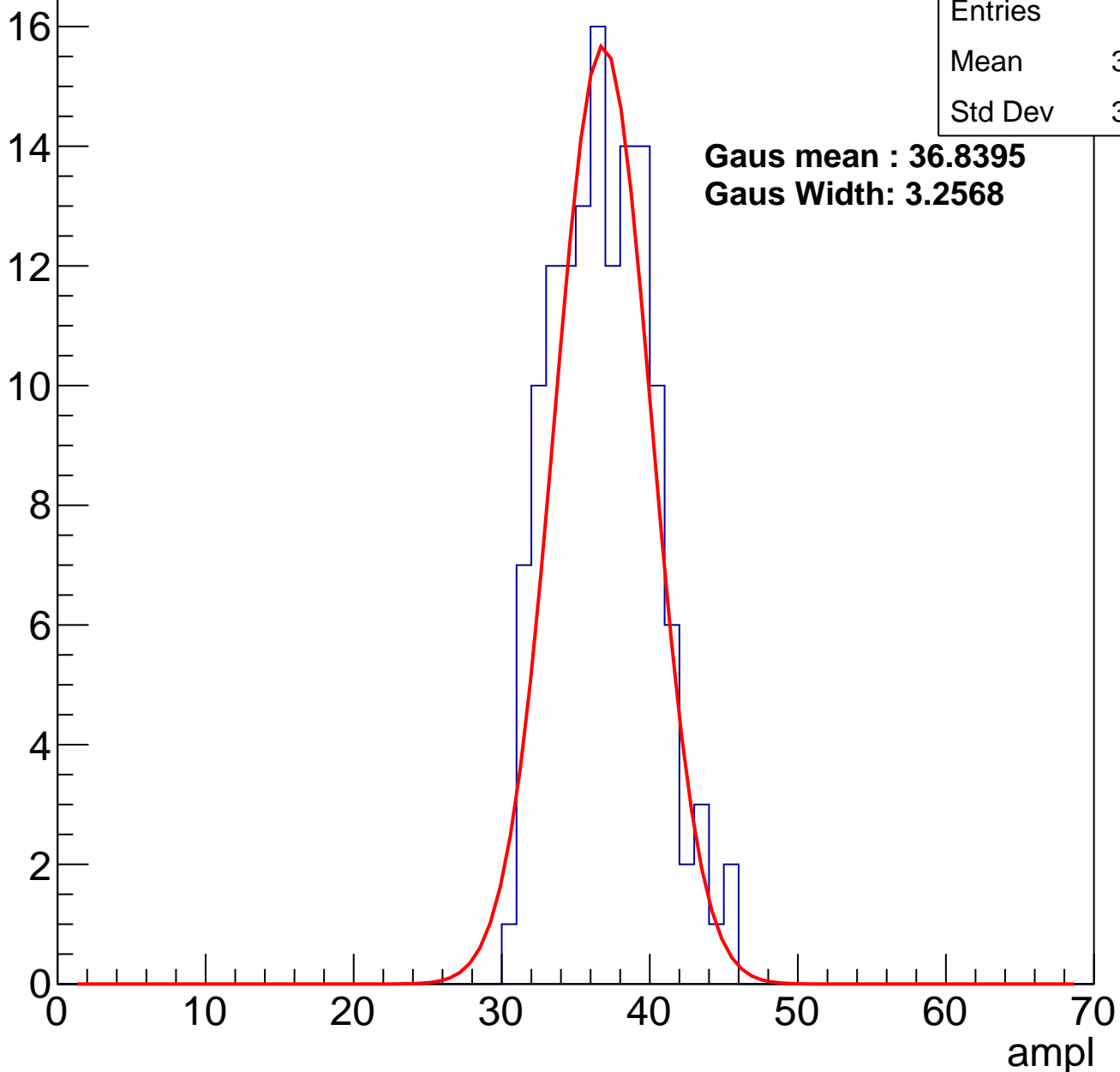
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	36.42
Std Dev	3.294

Gaus mean : 36.8395

Gaus Width: 3.2568

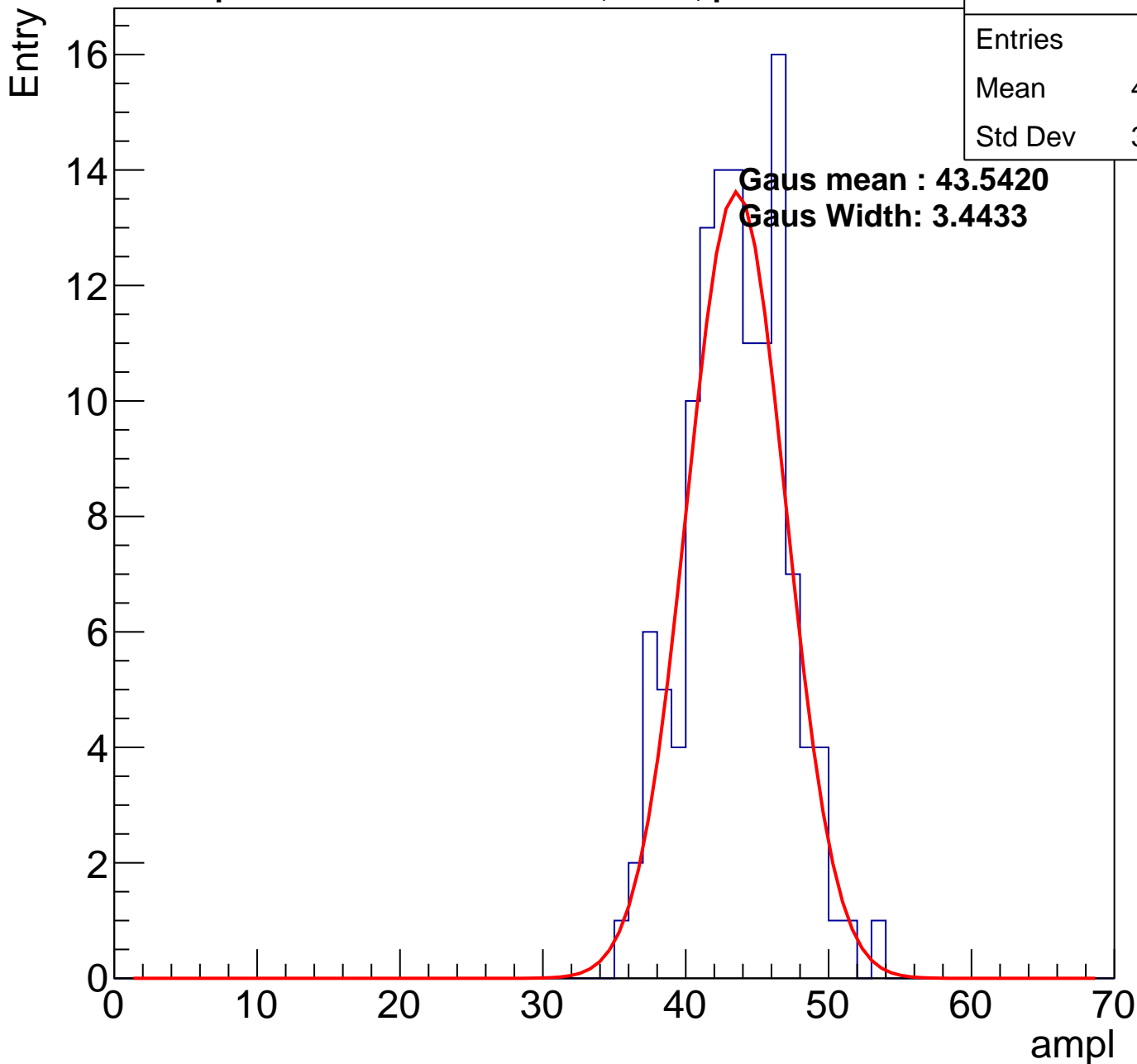
Entry



B1L001S, U16-ch113, adc2

calib_packv5_042523_0143.root, FC#2, port C2

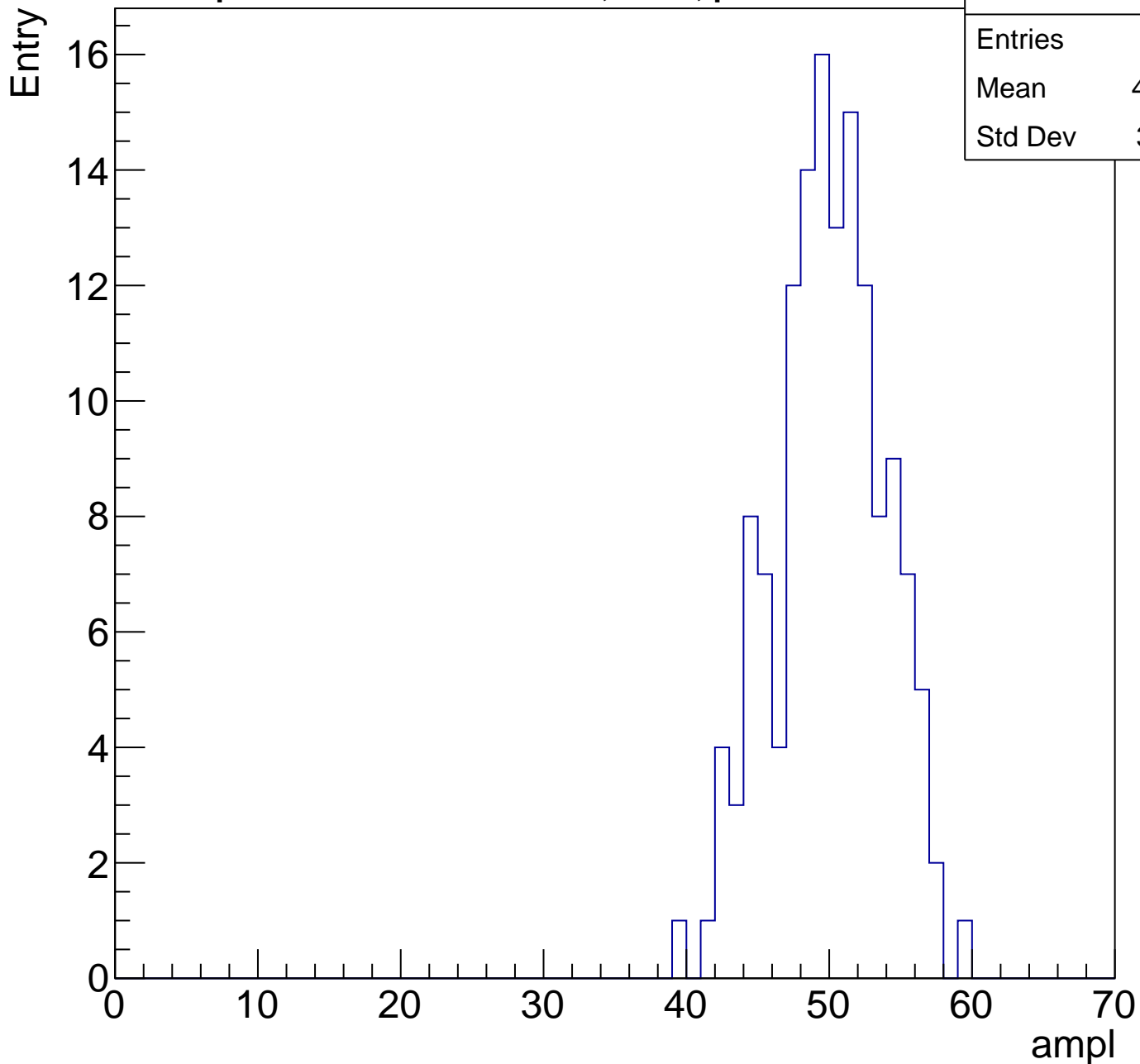
Entries	125
Mean	43.07
Std Dev	3.453



B1L001S, U16-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

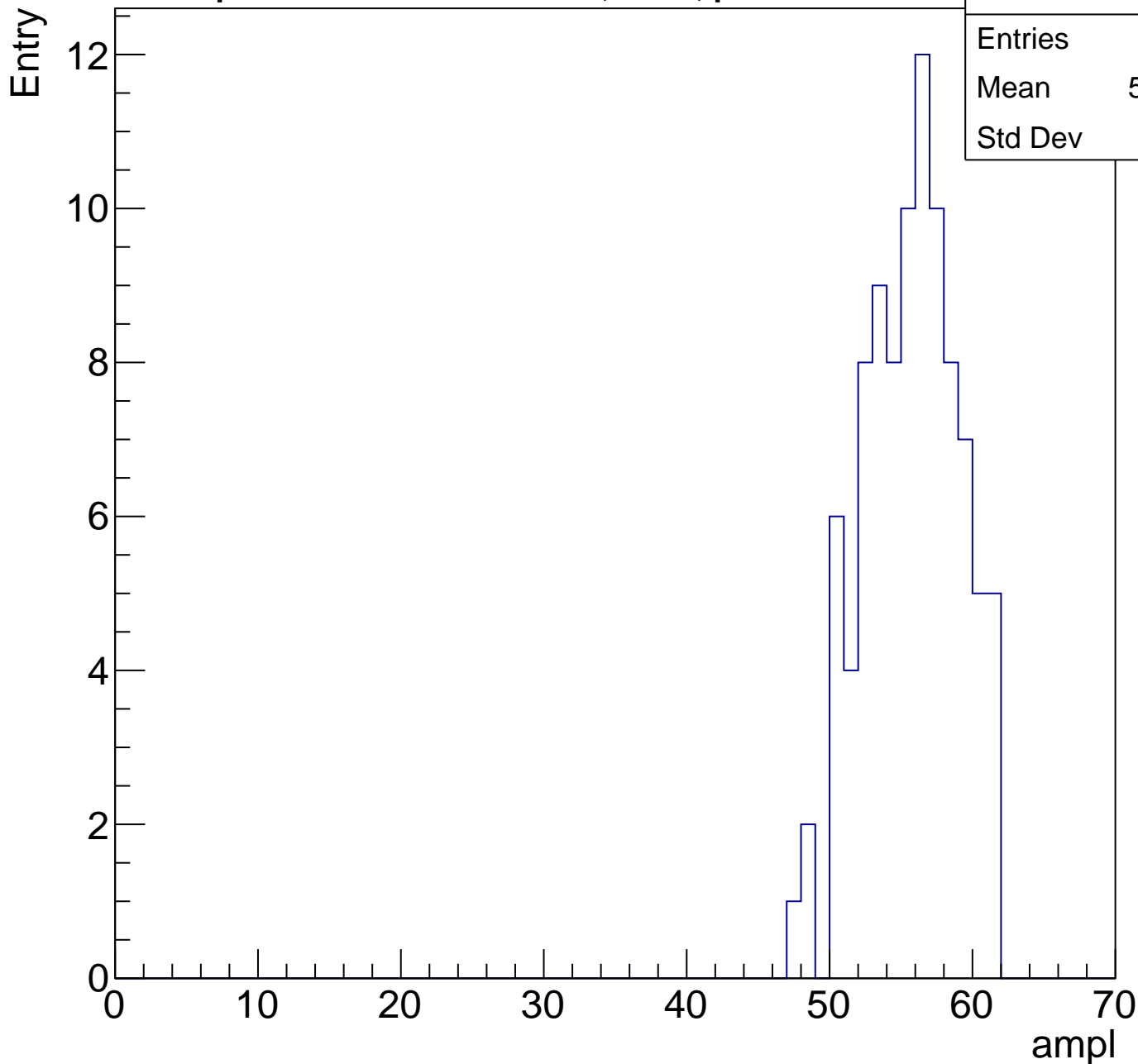
Entries	142
Mean	49.54
Std Dev	3.841



B1L001S, U16-ch113, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	95
Mean	55.22
Std Dev	3.29



B1L001S, U16-ch113, adc5

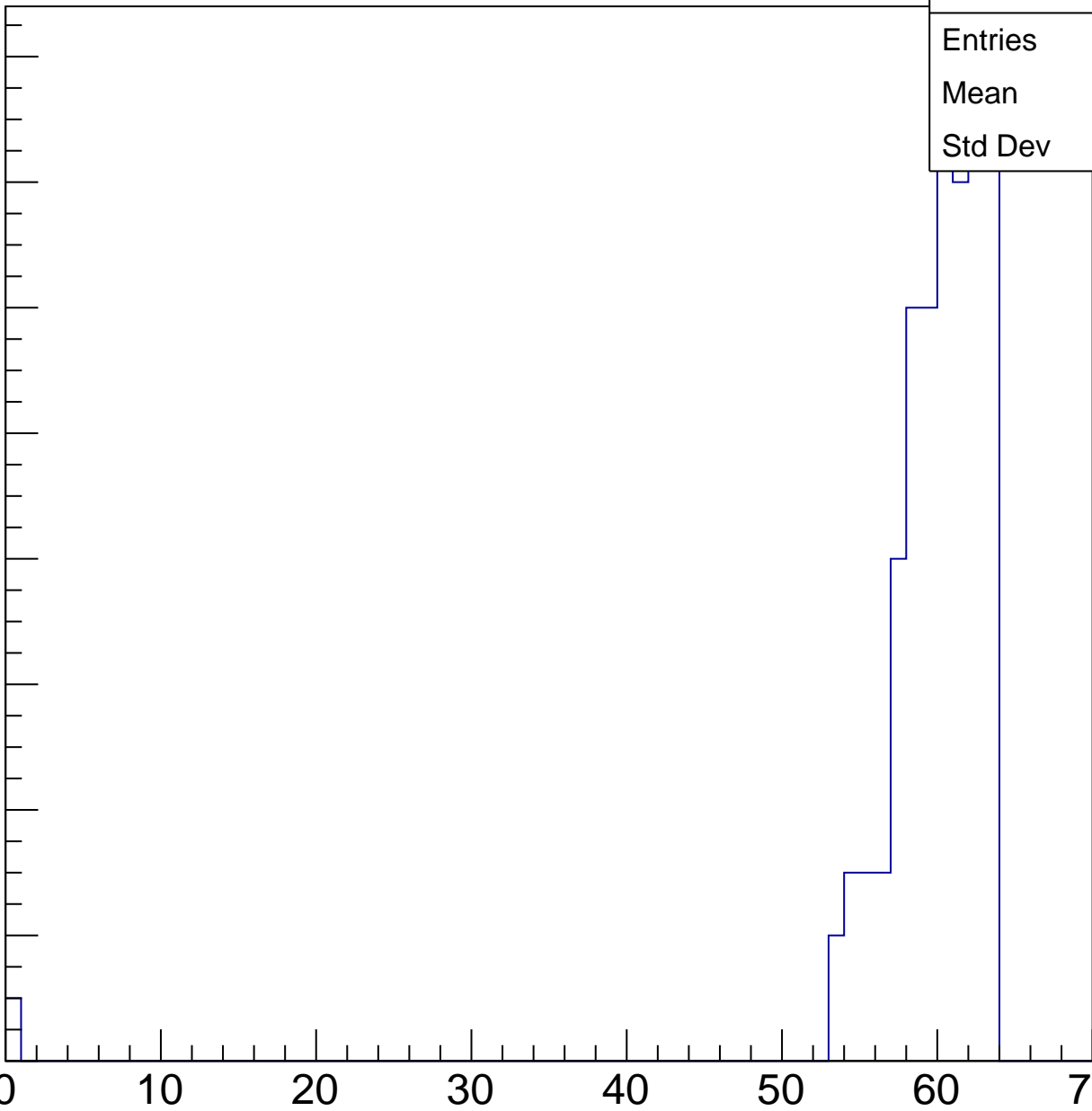
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	59.19
Std Dev	6.335

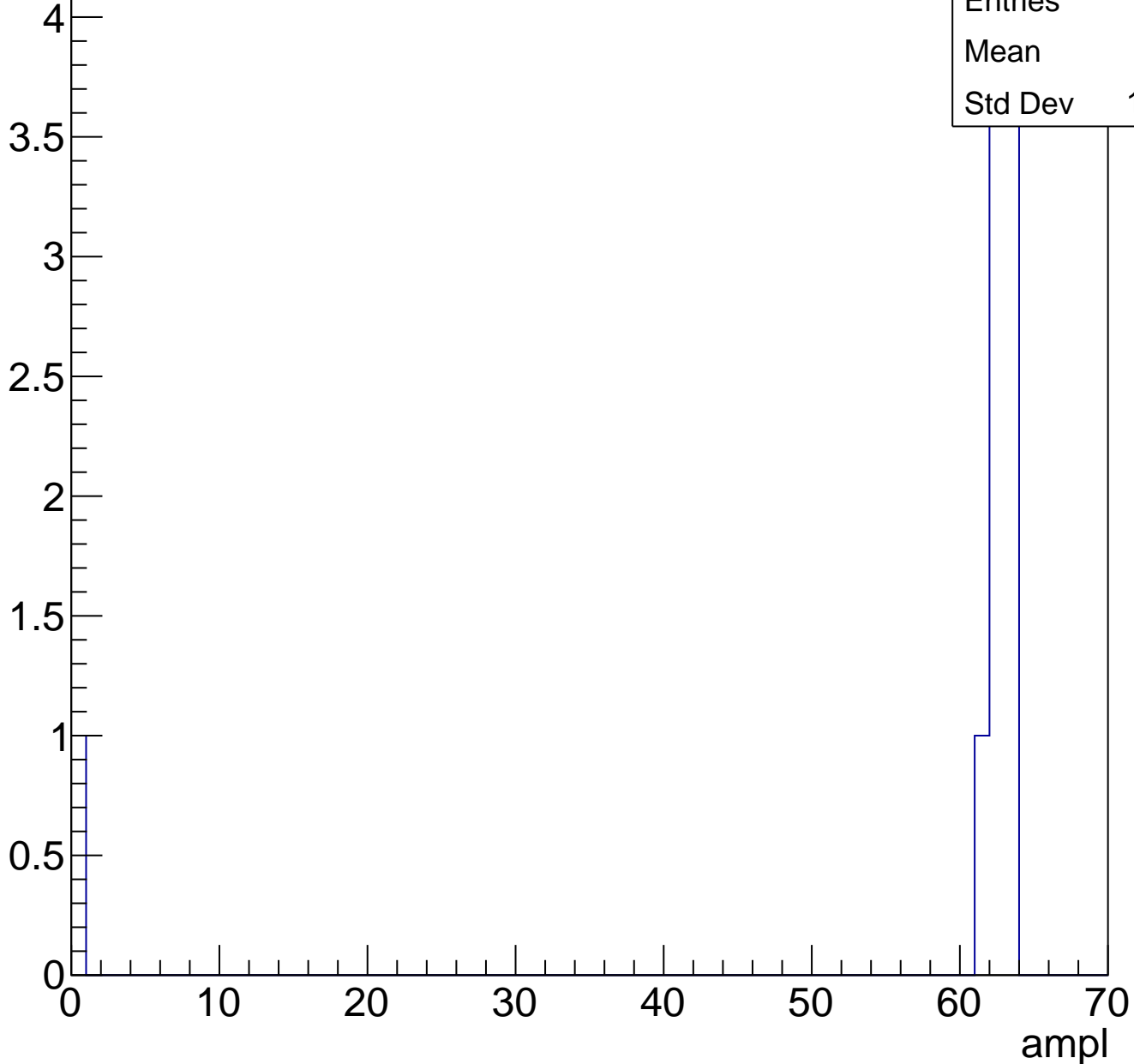
ampl



B1L001S, U16-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U16-ch114, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.64
Std Dev	3.444

Gaus mean : 30.0751

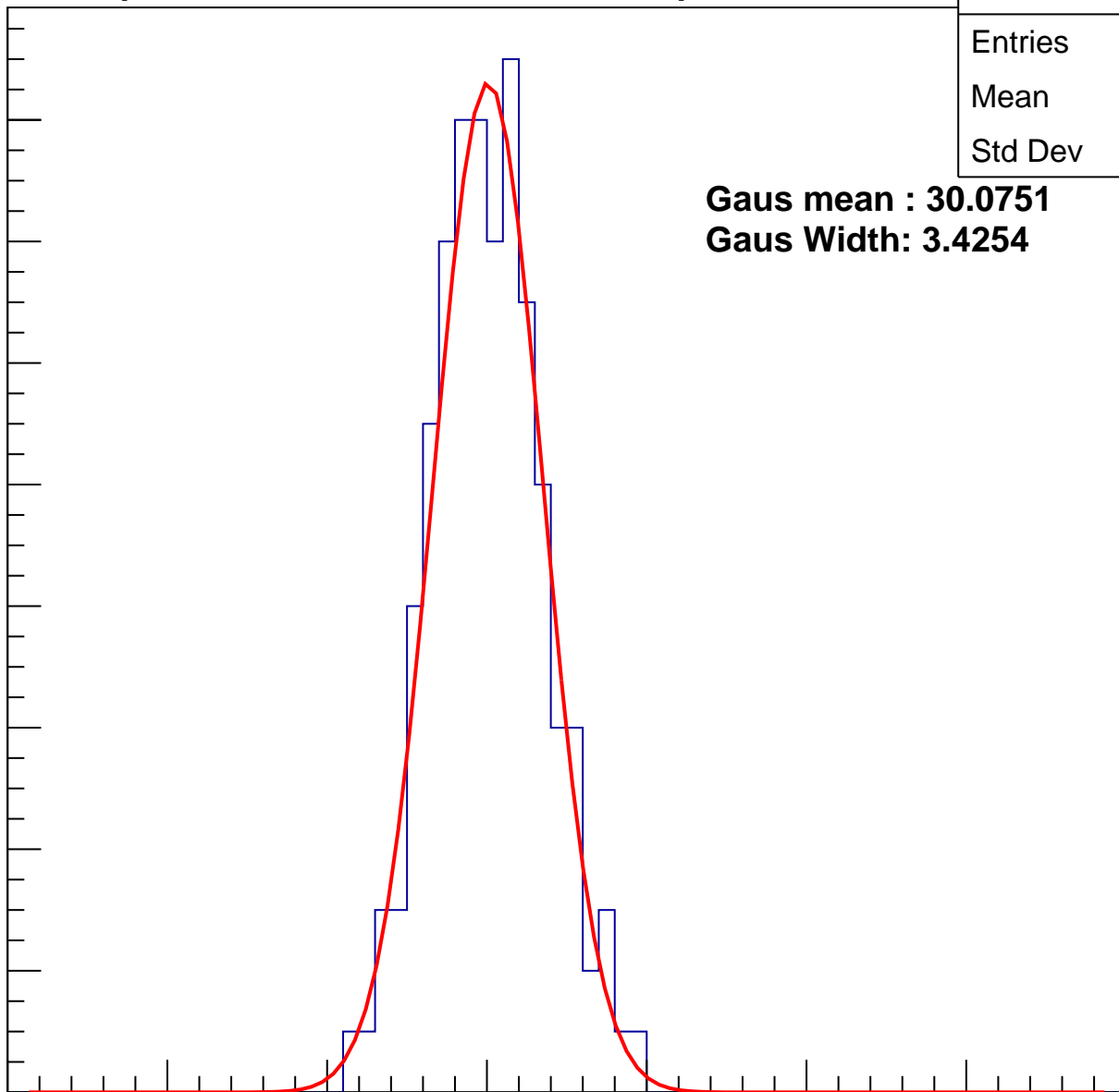
Gaus Width: 3.4254

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch114, adc1

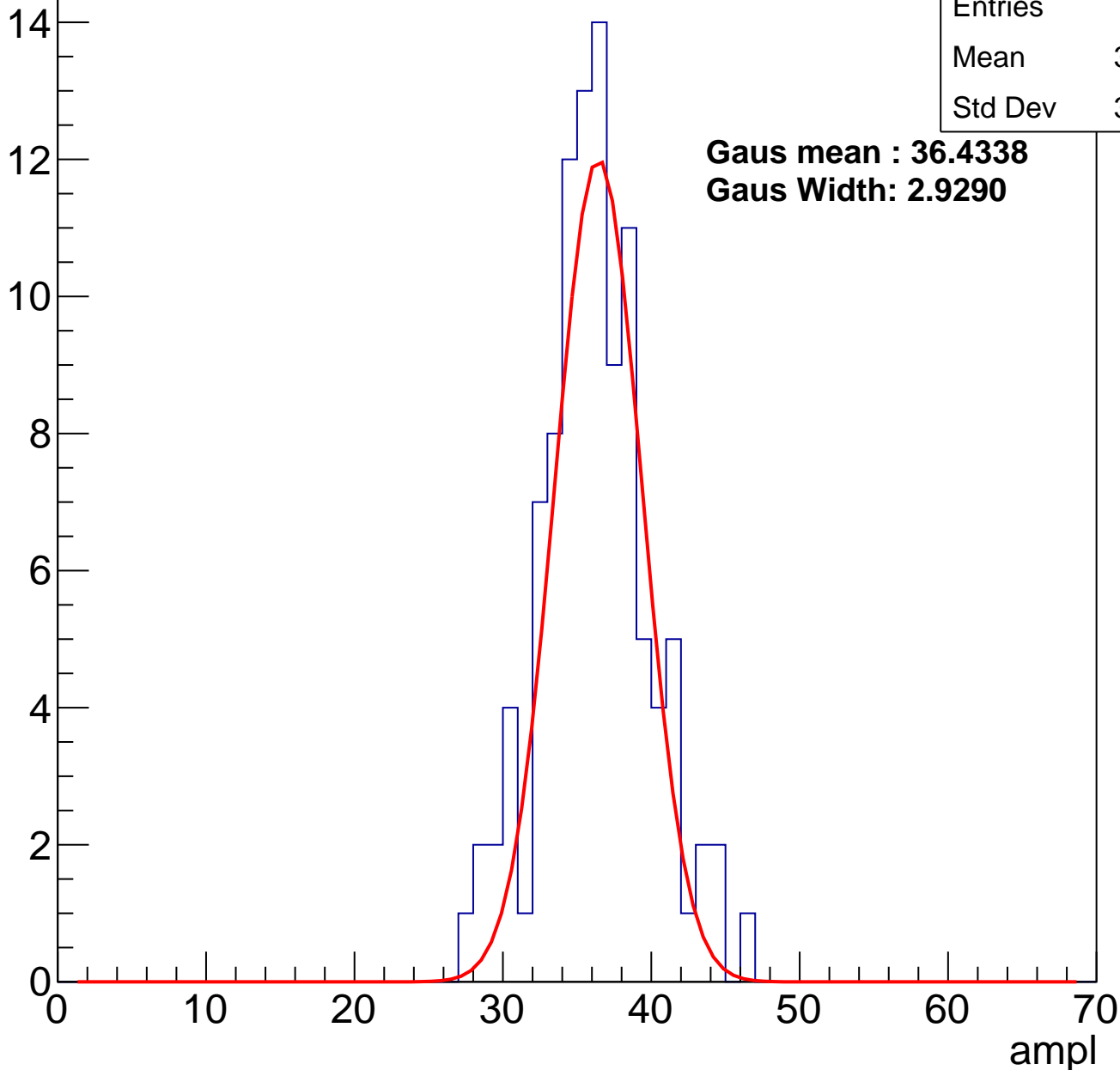
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	35.77
Std Dev	3.622

Gaus mean : 36.4338

Gaus Width: 2.9290

Entry



B1L001S, U16-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	41.84
Std Dev	3.38

Gaus mean : 42.4627

Gaus Width: 3.3372

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

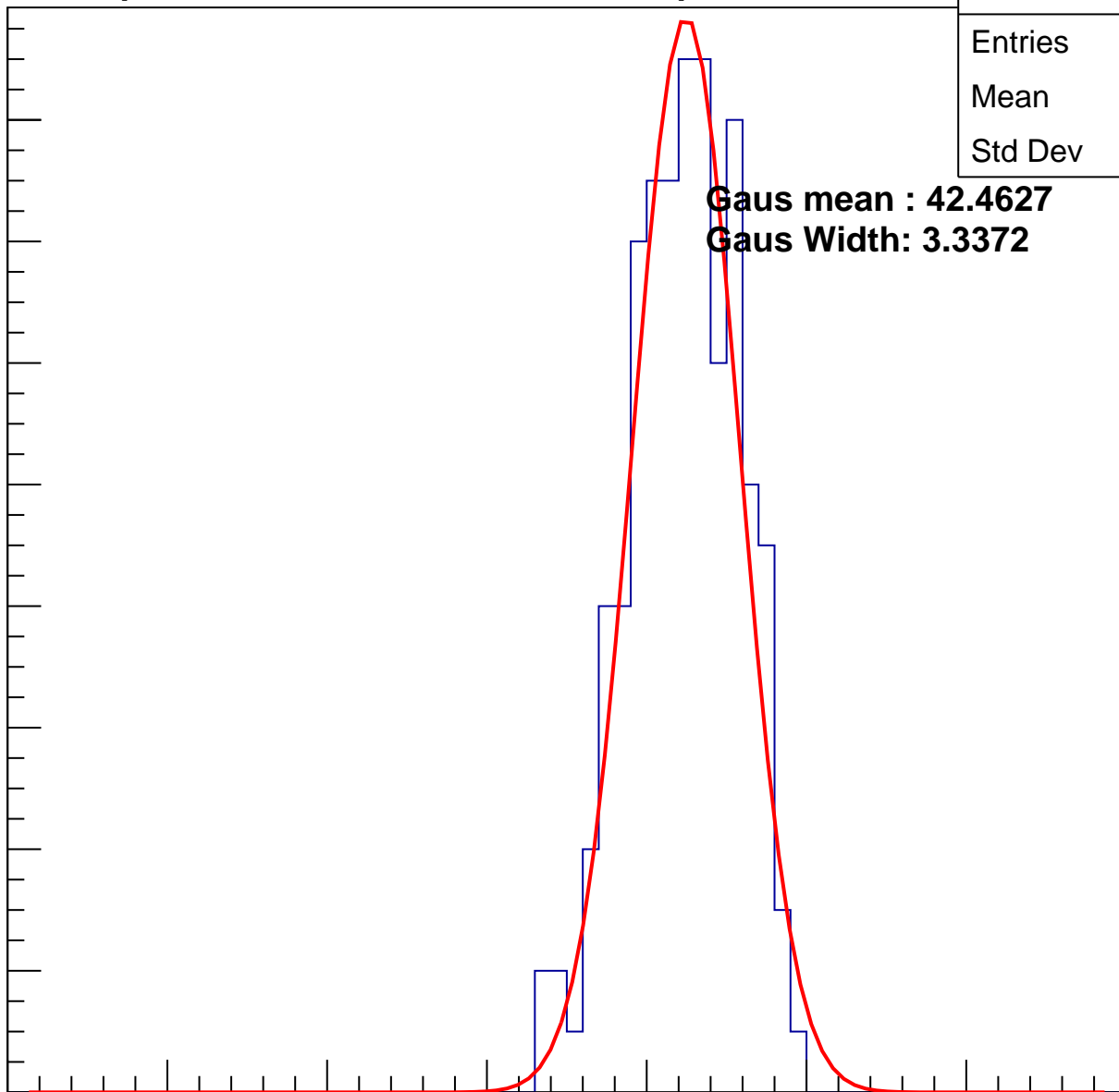
40

50

60

70

ampl

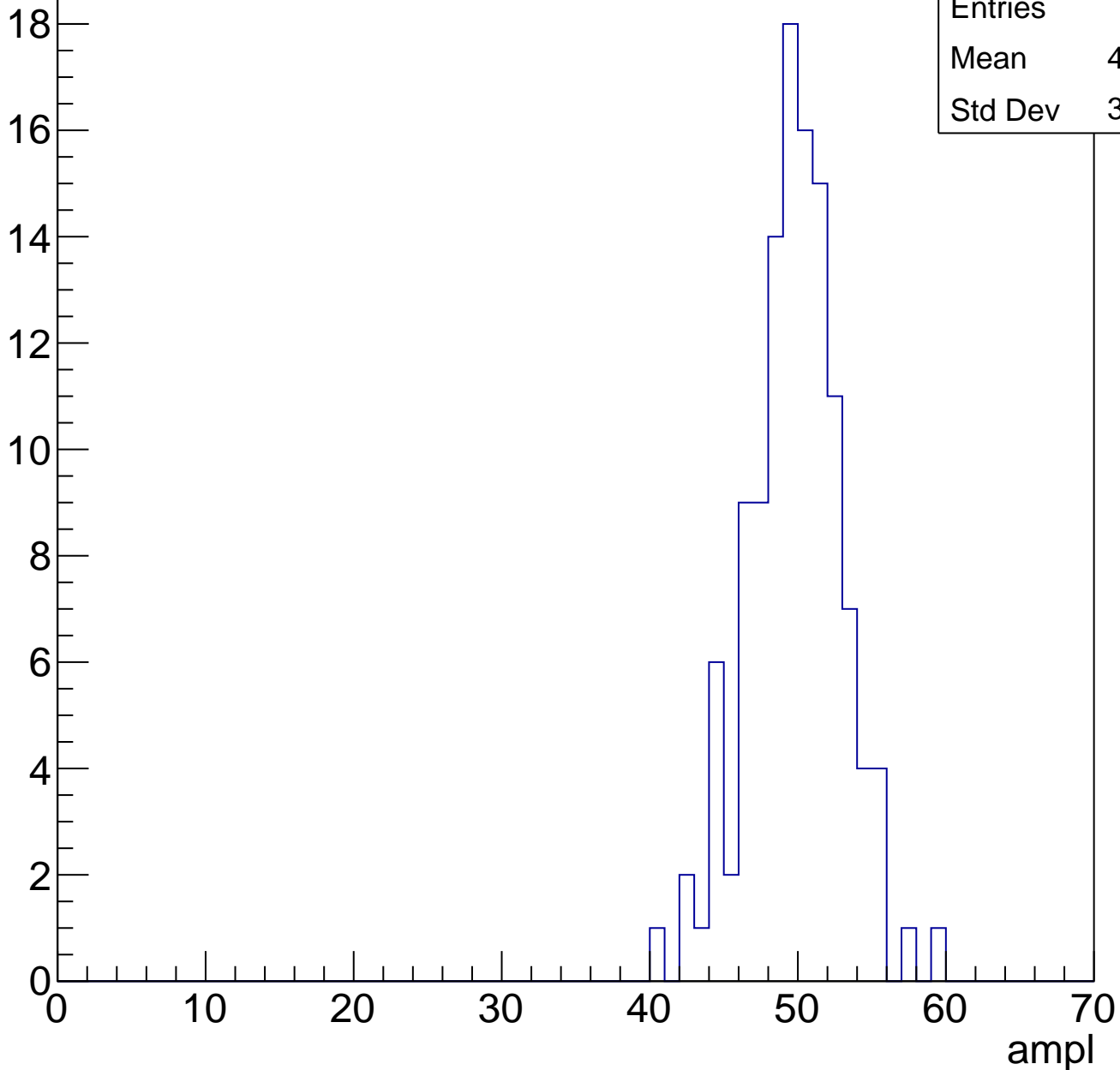


B1L001S, U16-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	121
Mean	49.36
Std Dev	3.159



B1L001S, U16-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

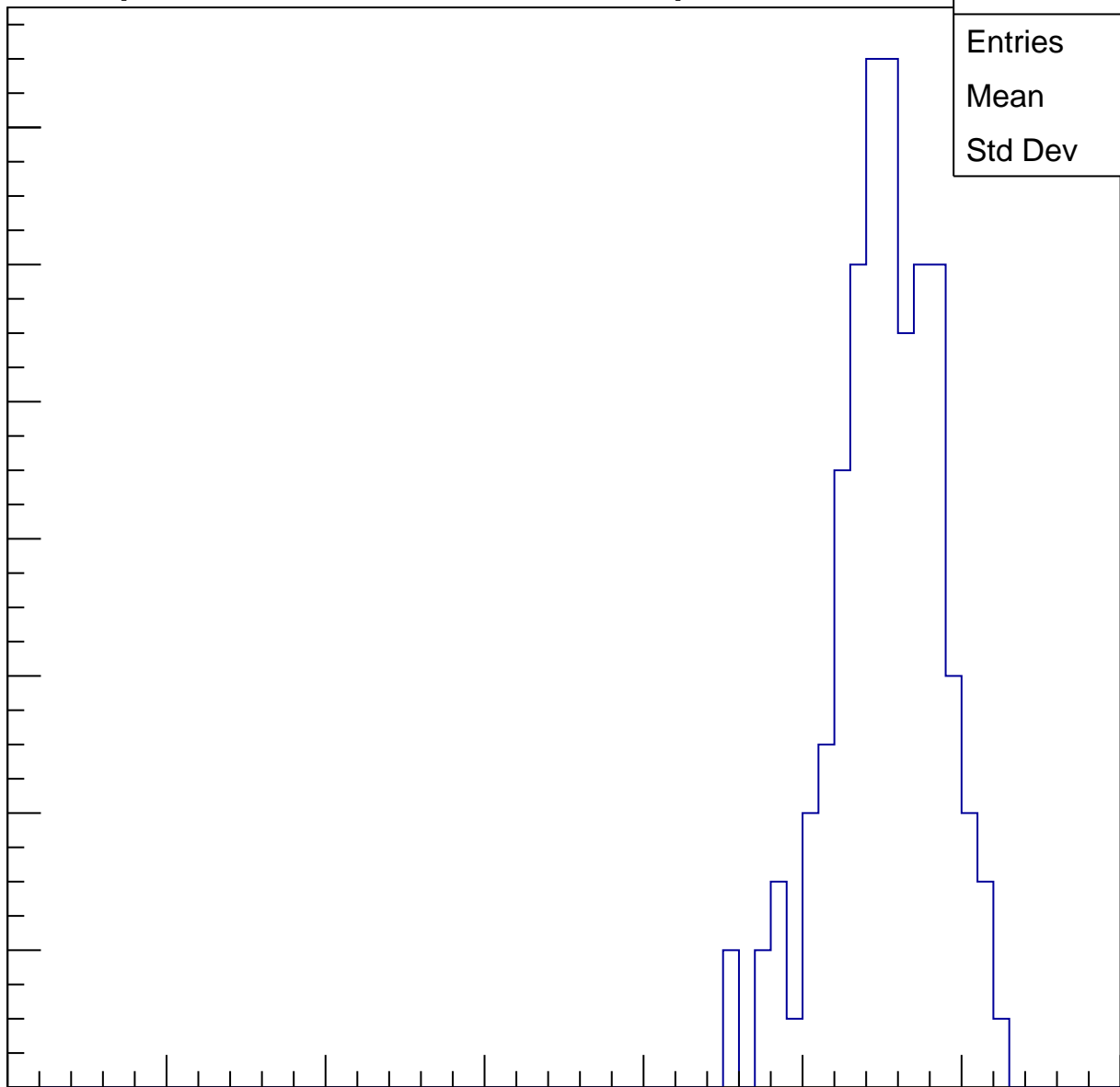
Entries	117
Mean	54.75
Std Dev	3.394

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch114, adc5

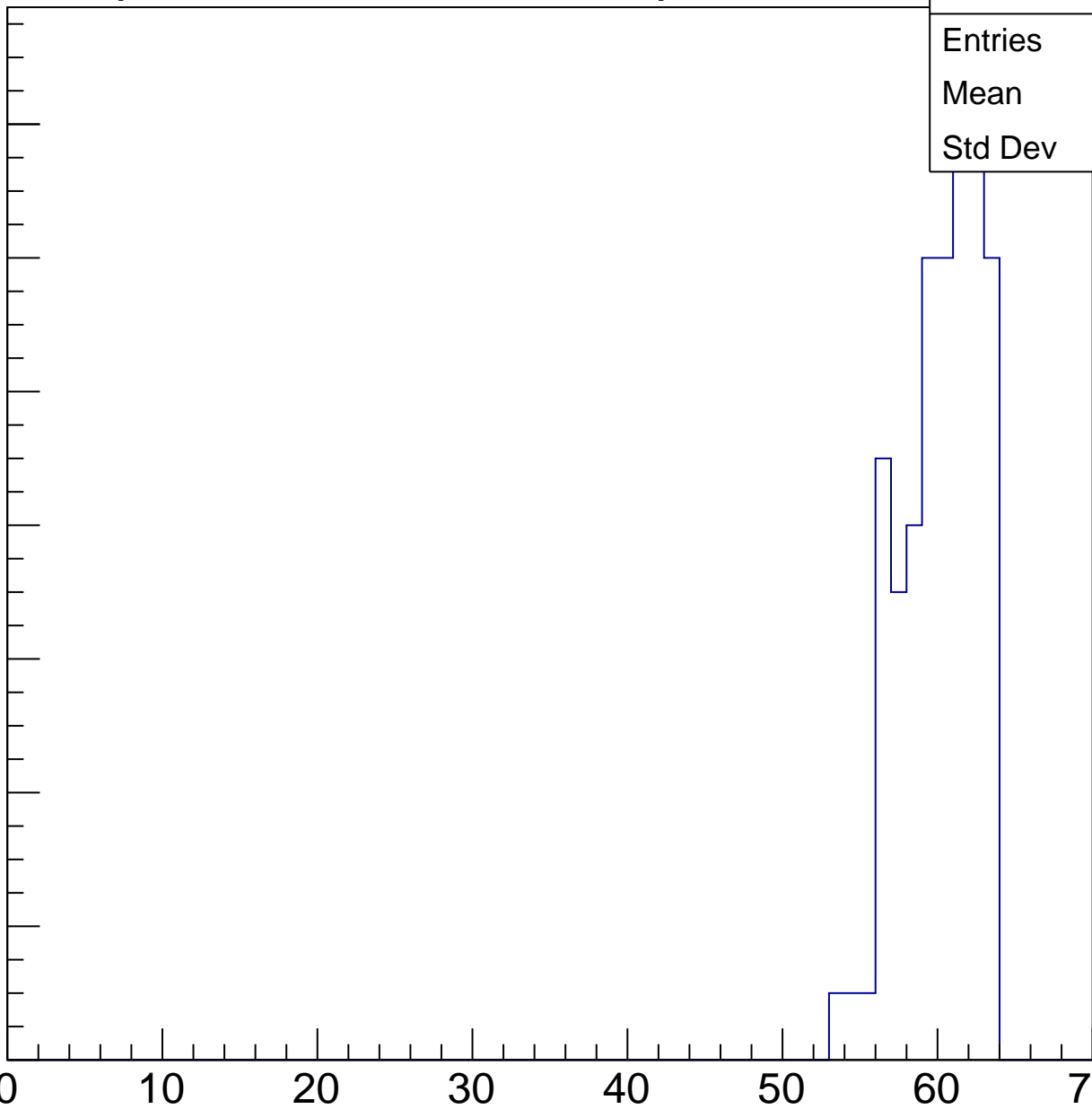
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	59.75
Std Dev	2.417

ampl

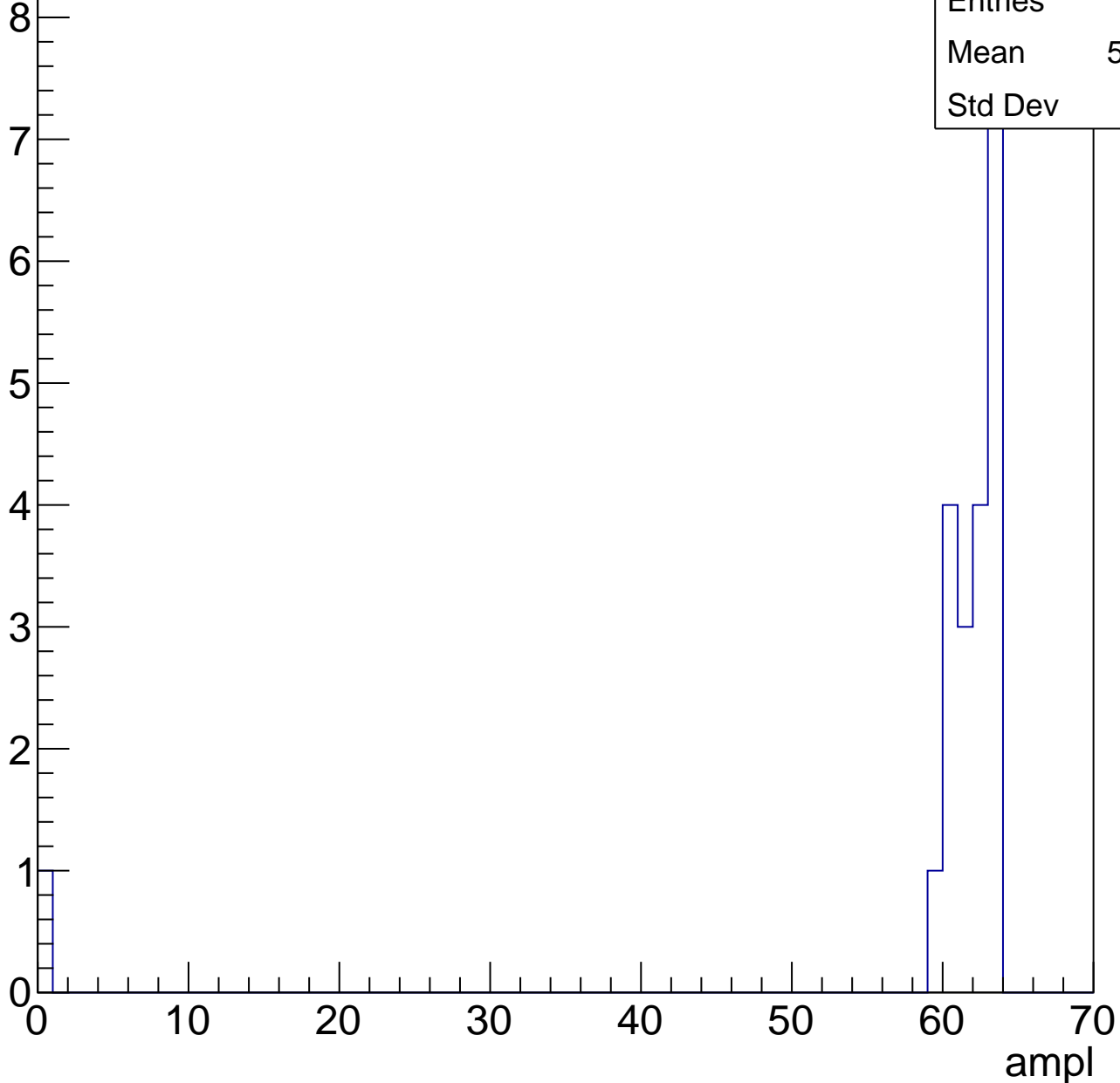


B1L001S, U16-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	58.76
Std Dev	13.2



B1L001S, U16-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

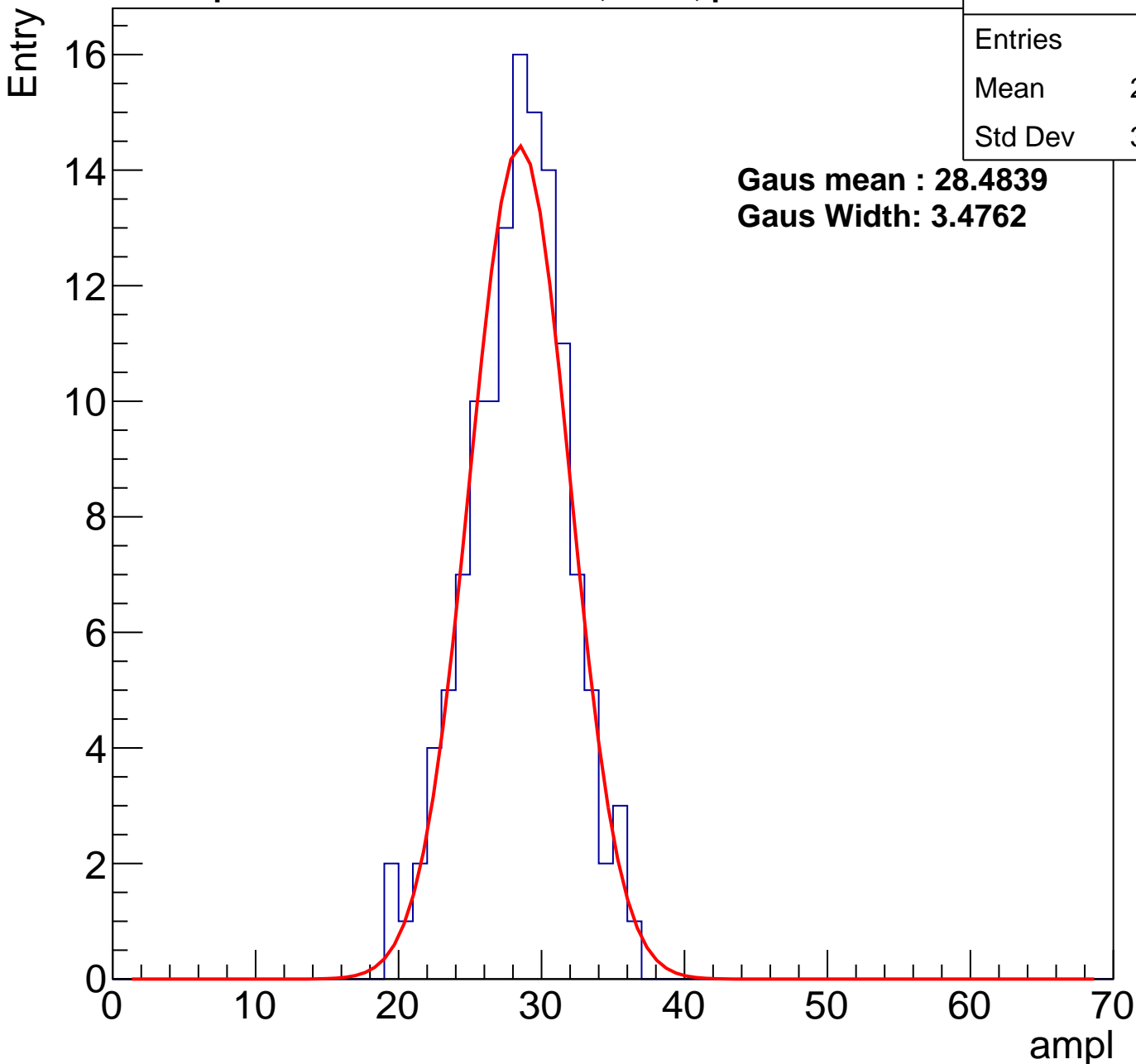
B1L001S, U16-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	27.92
Std Dev	3.465

Gaus mean : 28.4839

Gaus Width: 3.4762



B1L001S, U16-ch115, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	35.02
Std Dev	3.774

Gaus mean : 35.4360

Gaus Width: 3.7577

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

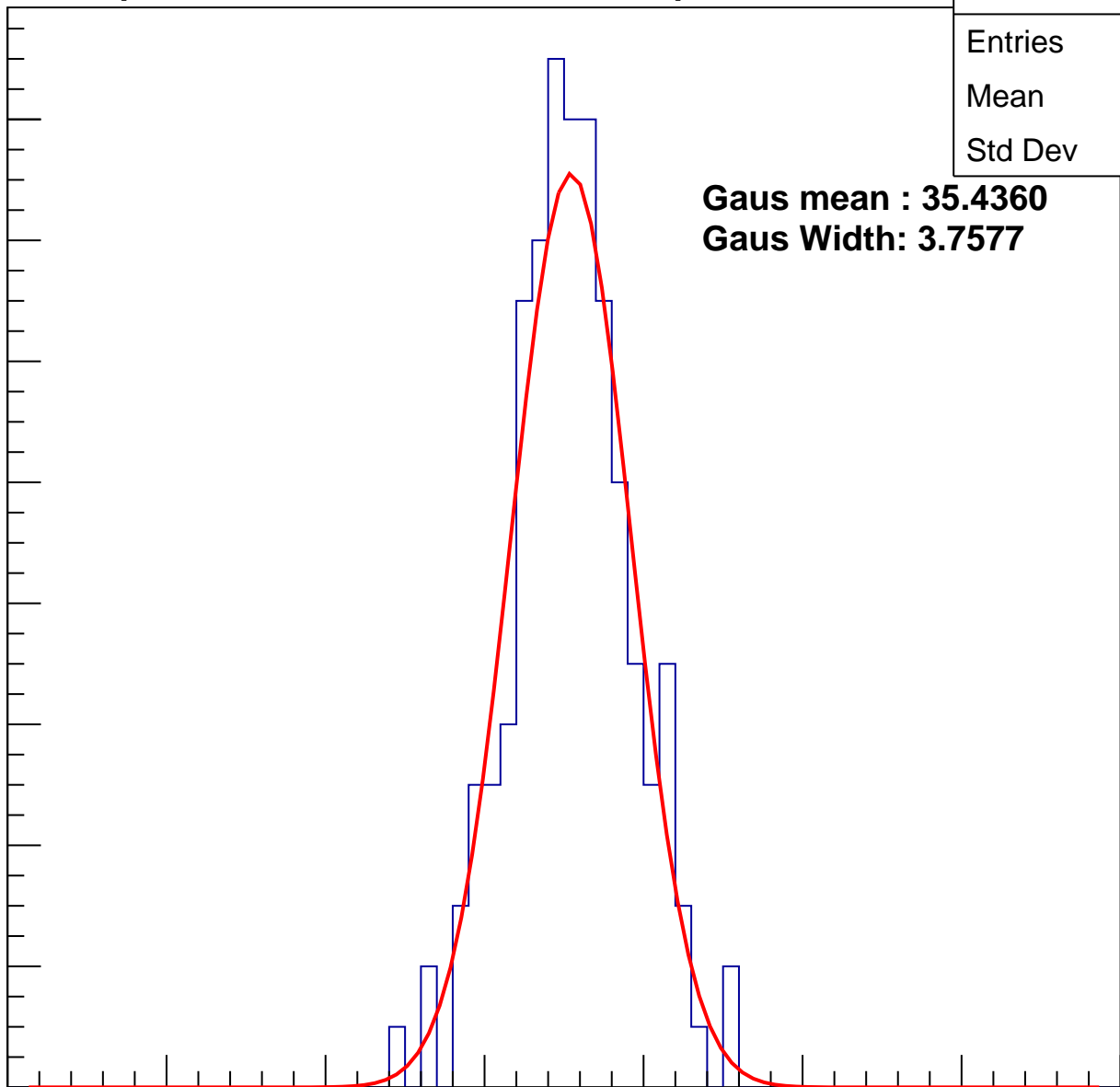
40

50

60

70

ampl



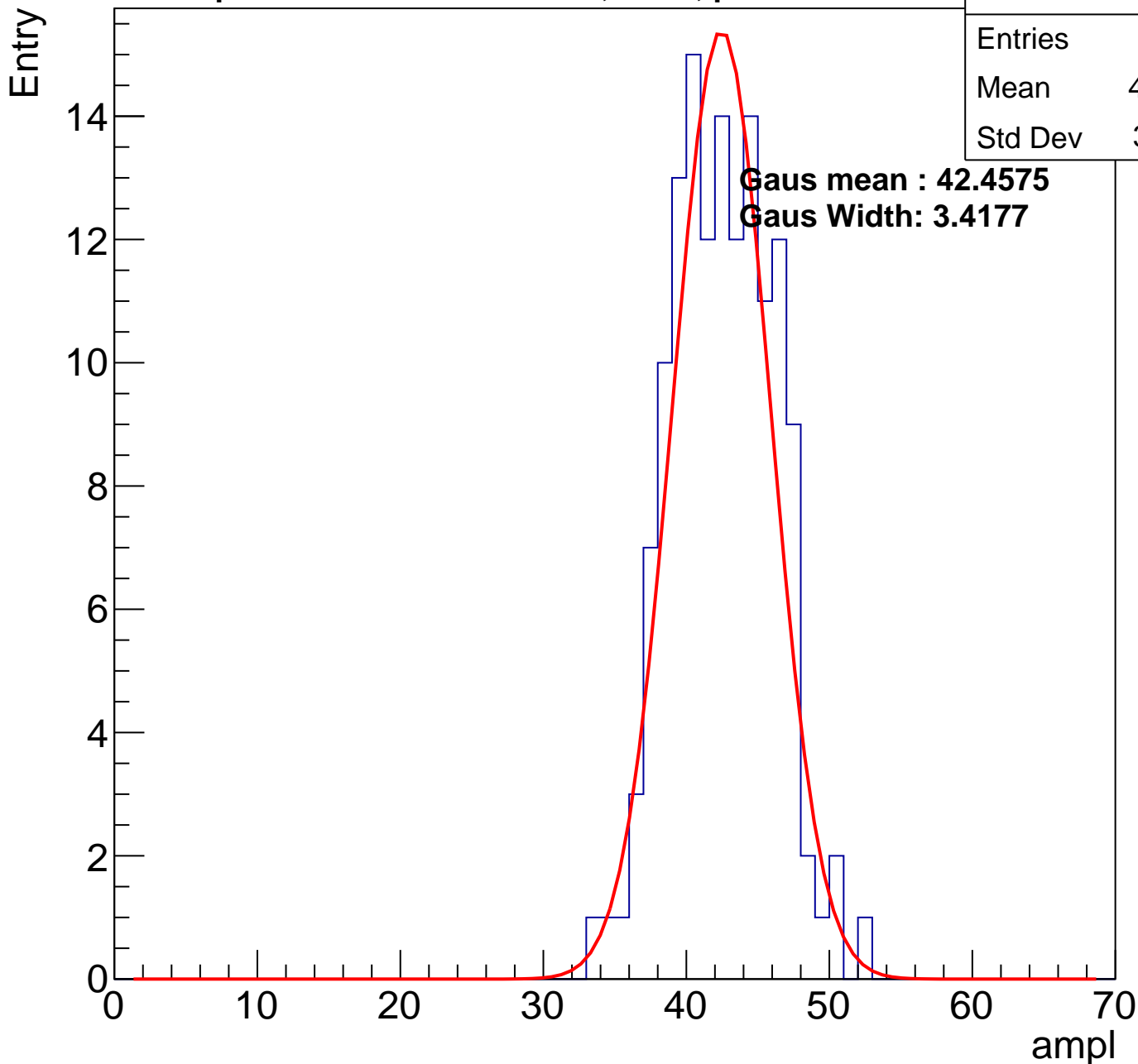
B1L001S, U16-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	42.09
Std Dev	3.531

Gaus mean : 42.4575

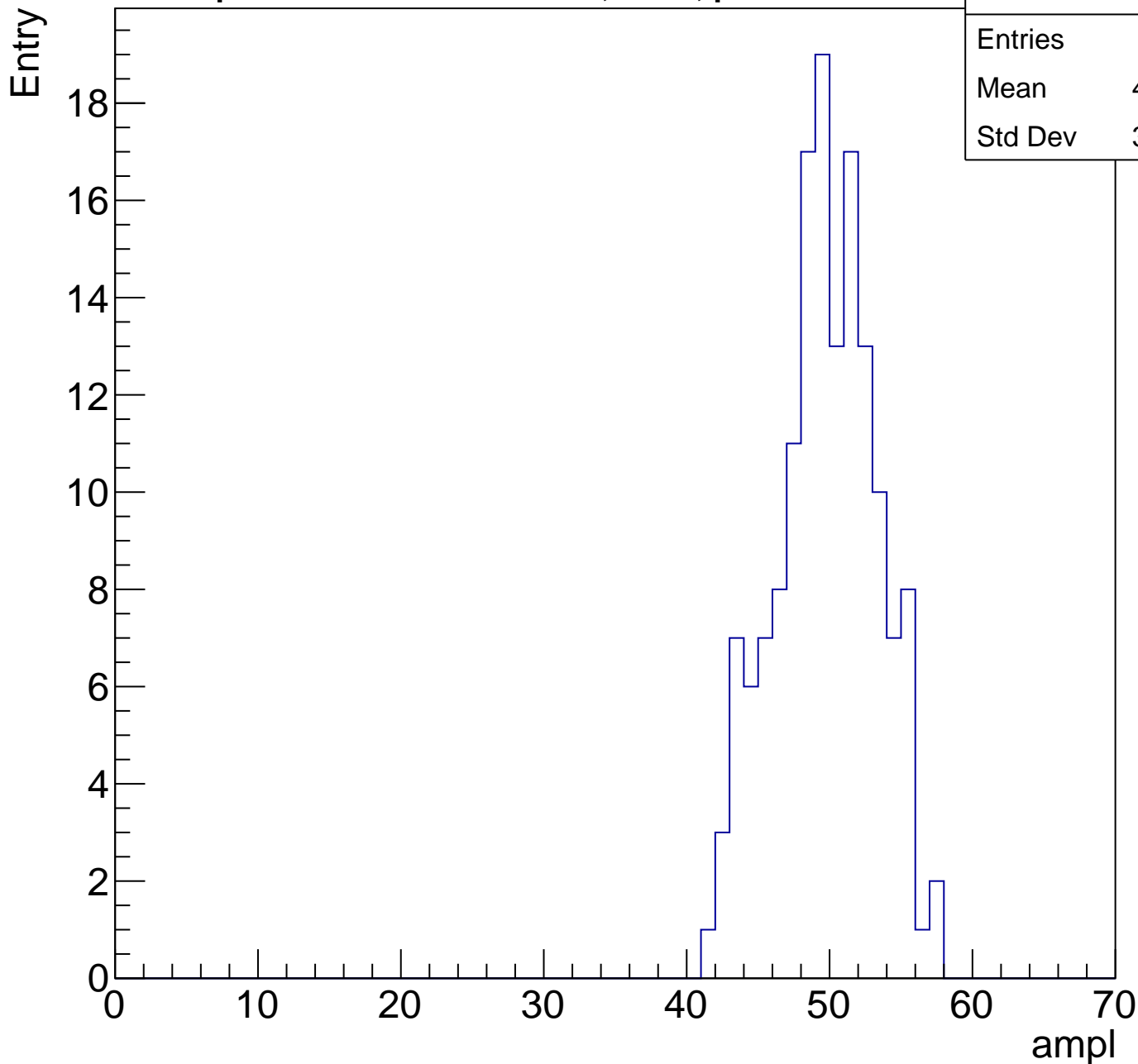
Gaus Width: 3.4177



B1L001S, U16-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

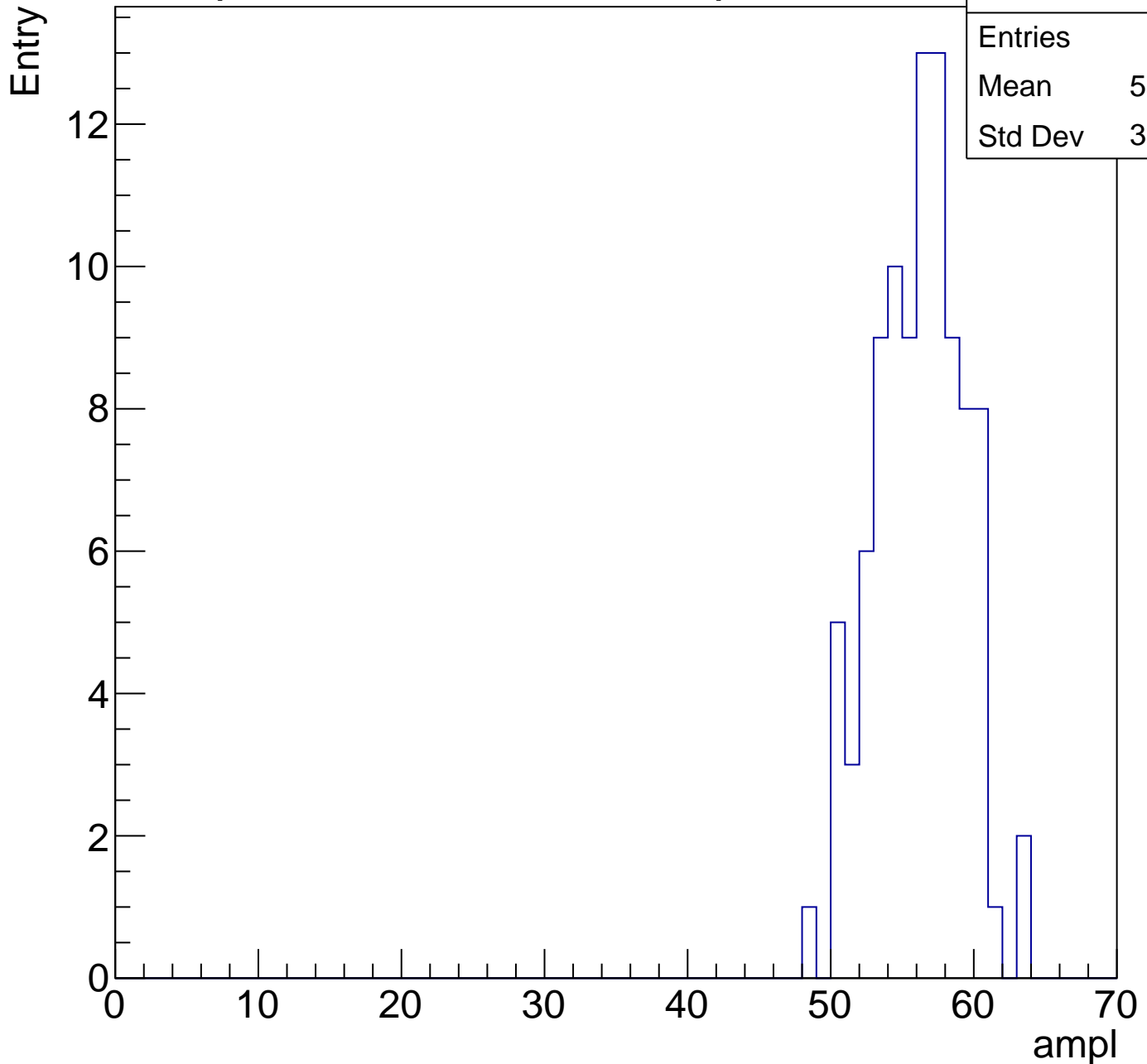
Entries	150
Mean	49.27
Std Dev	3.507



B1L001S, U16-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	55.72
Std Dev	3.062



B1L001S, U16-ch115, adc5

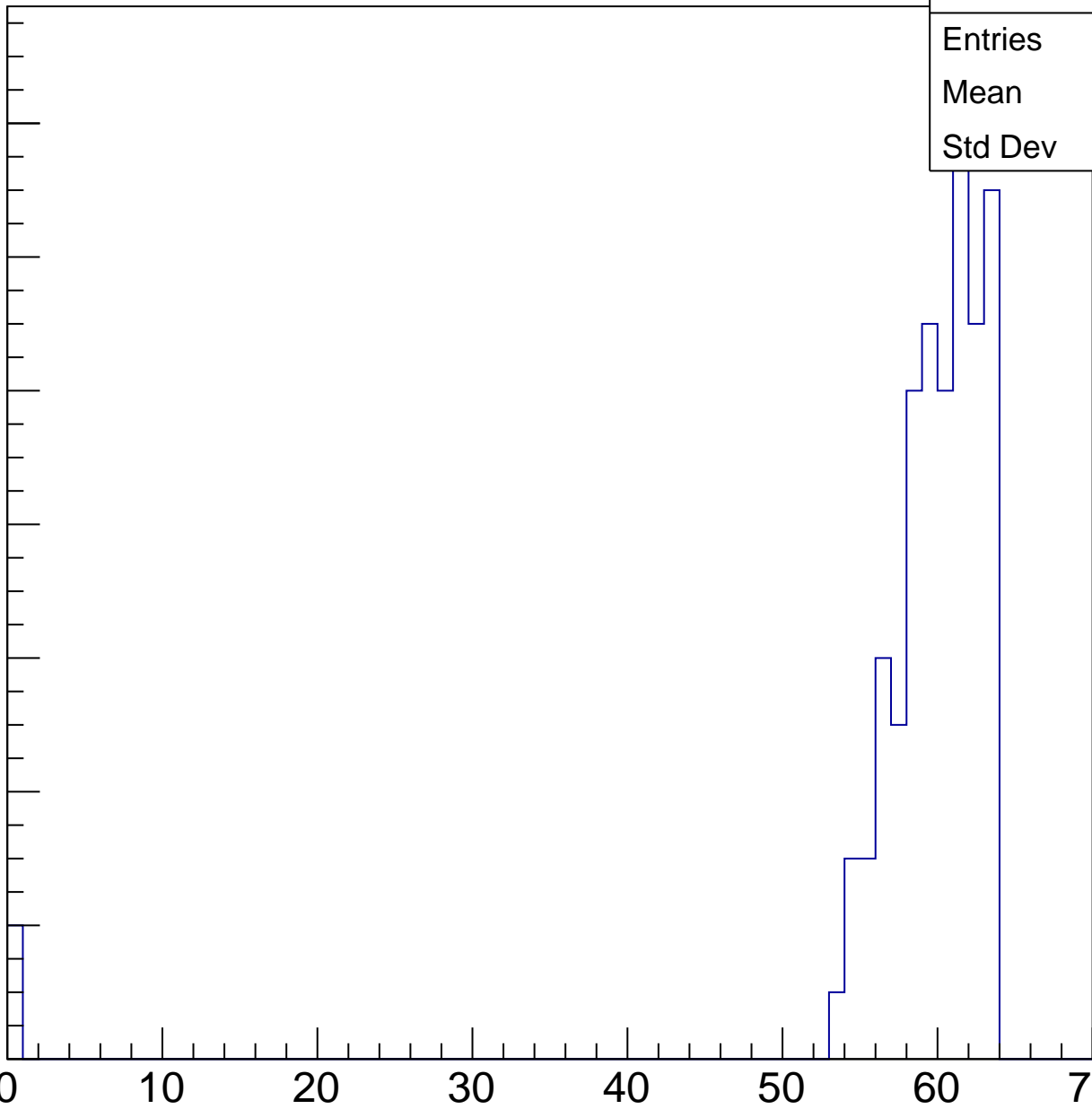
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.29
Std Dev	9.151

ampl

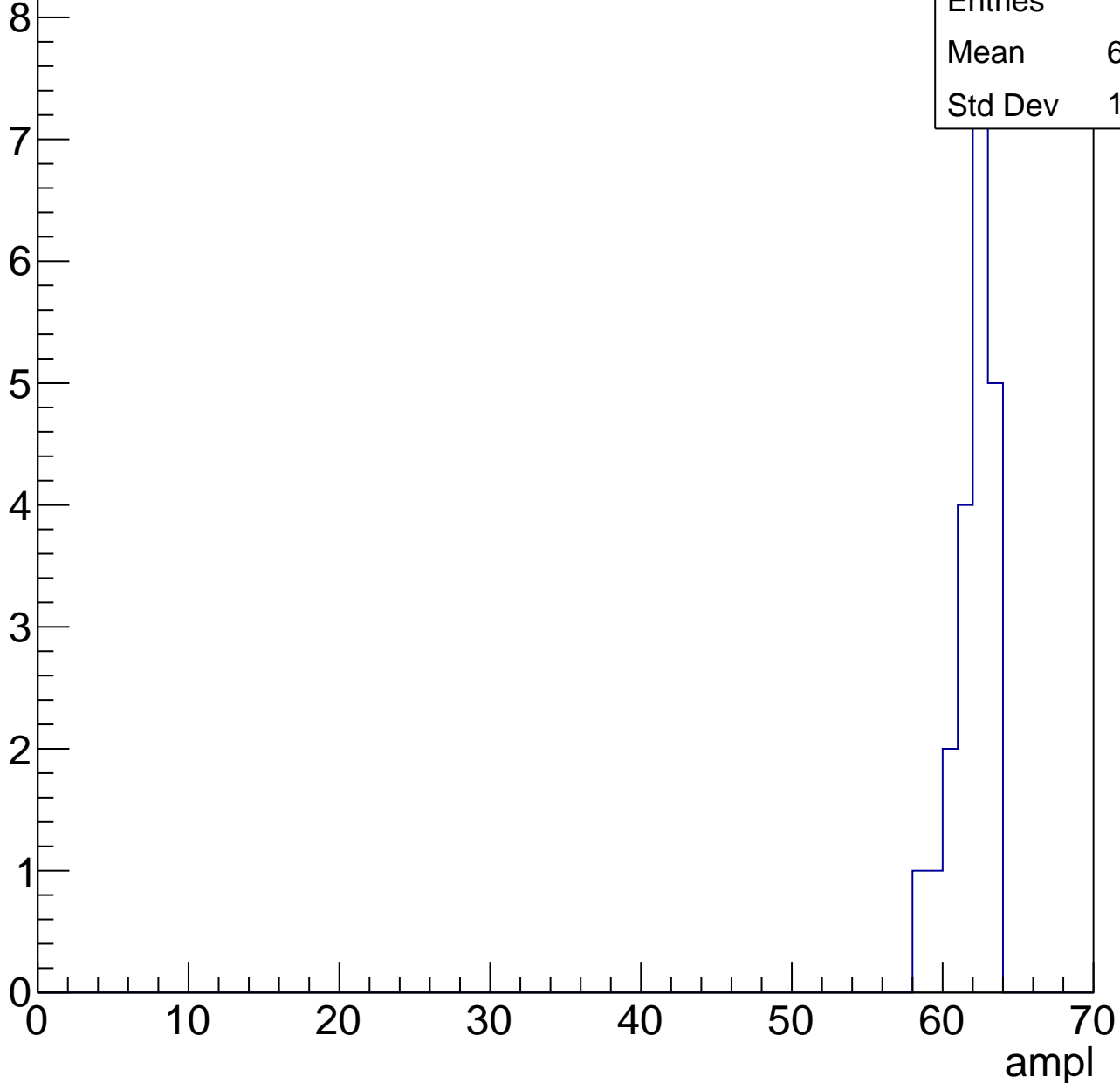


B1L001S, U16-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	61.52
Std Dev	1.332



B1L001S, U16-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch116, adc0

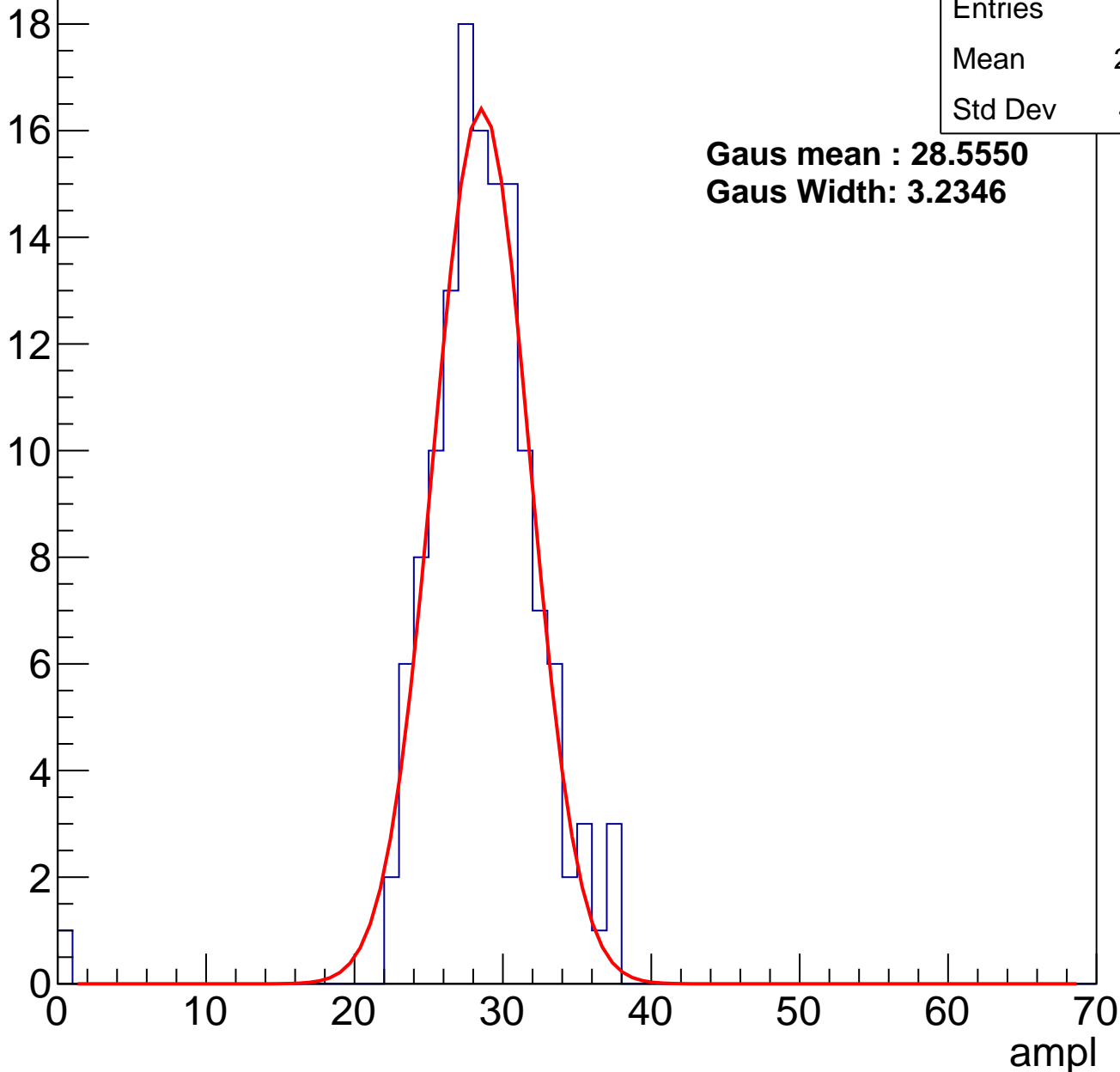
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	28.18
Std Dev	4.061

Gaus mean : 28.5550

Gaus Width: 3.2346

Entry



B1L001S, U16-ch116, adc1

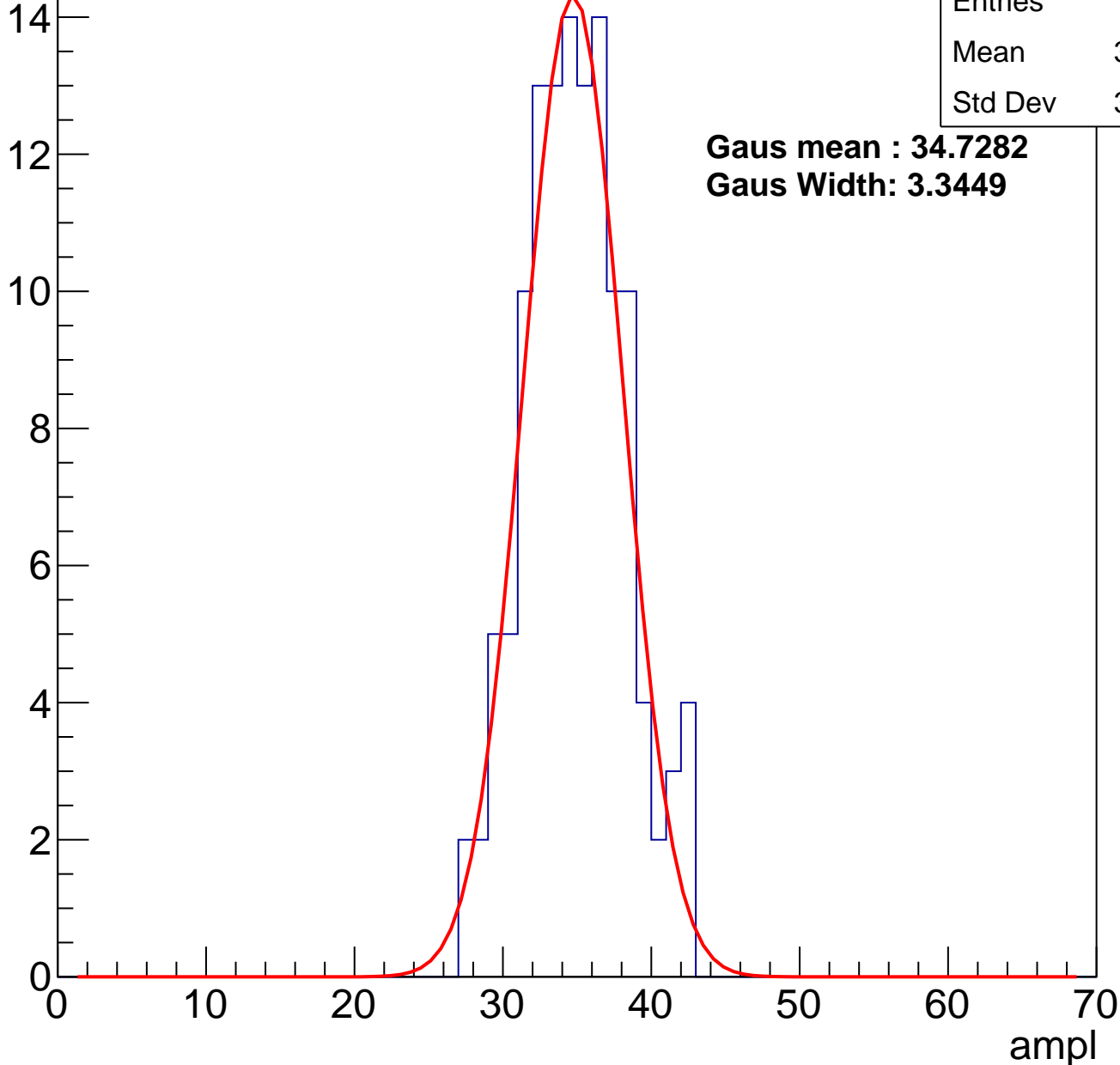
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	34.45
Std Dev	3.359

Gaus mean : 34.7282

Gaus Width: 3.3449

Entry



B1L001S, U16-ch116, adc2

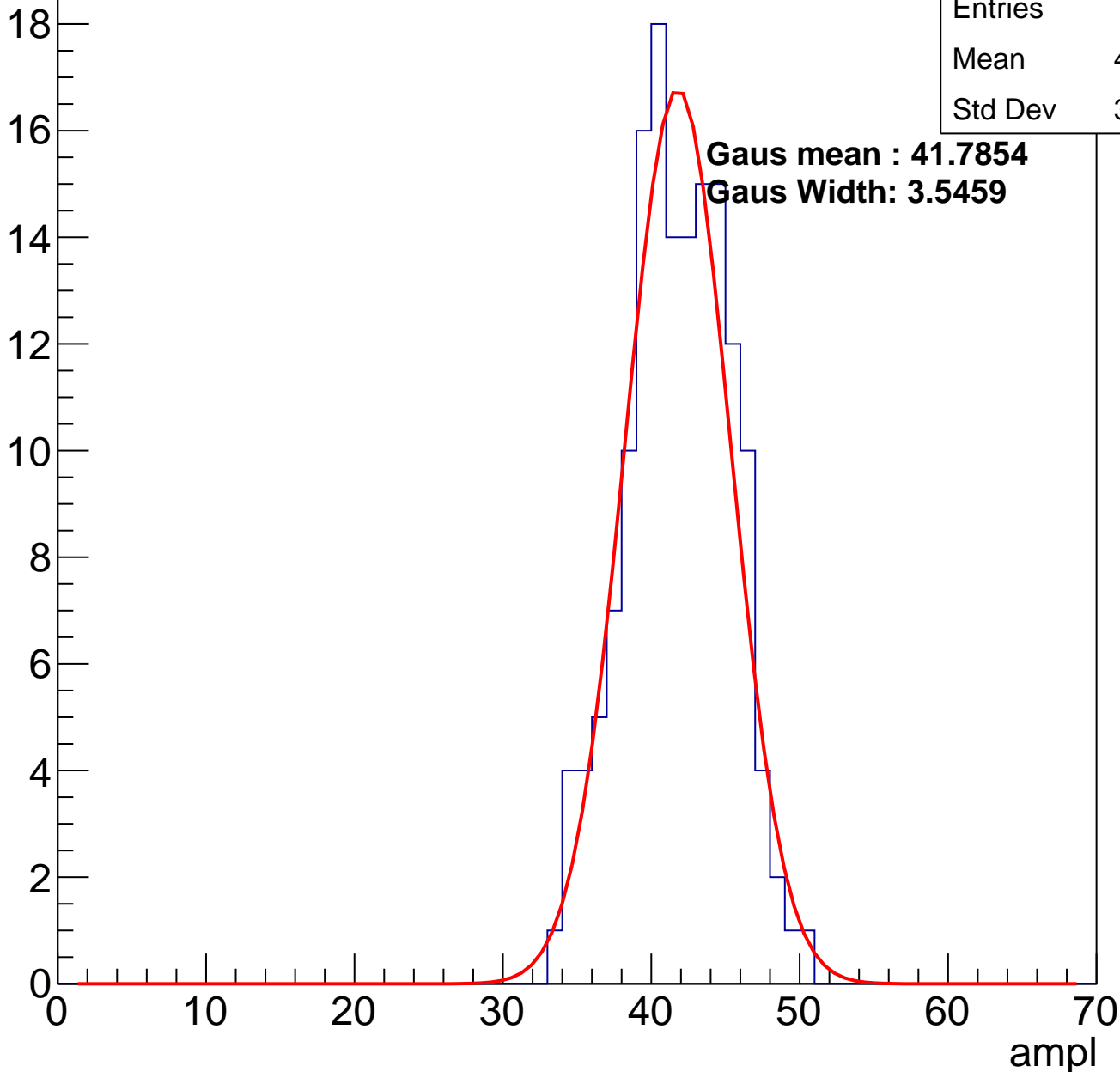
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	41.32
Std Dev	3.463

Gaus mean : 41.7854

Gaus Width: 3.5459

Entry



B1L001S, U16-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

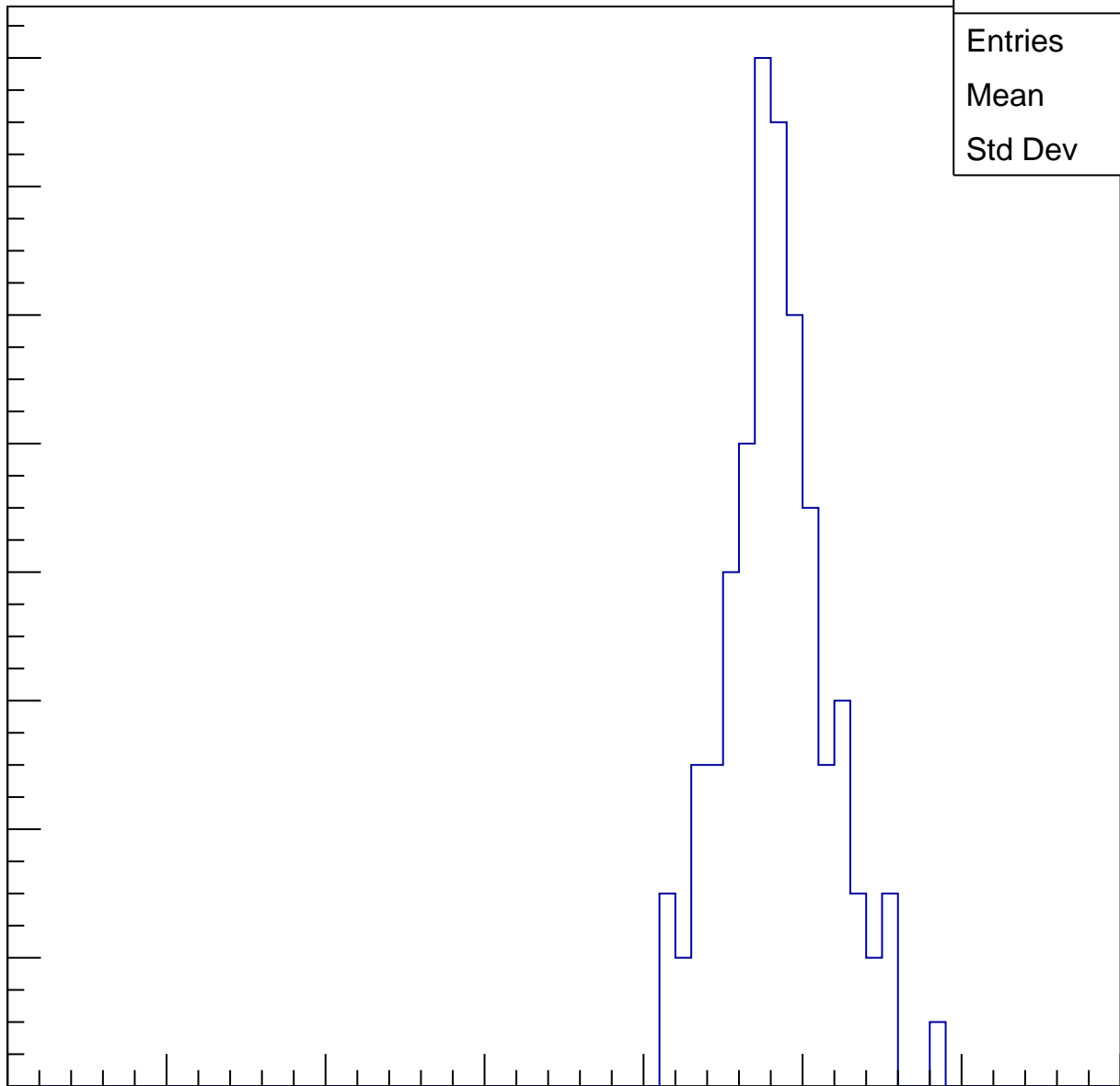
Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	47.9
Std Dev	3.286

ampl

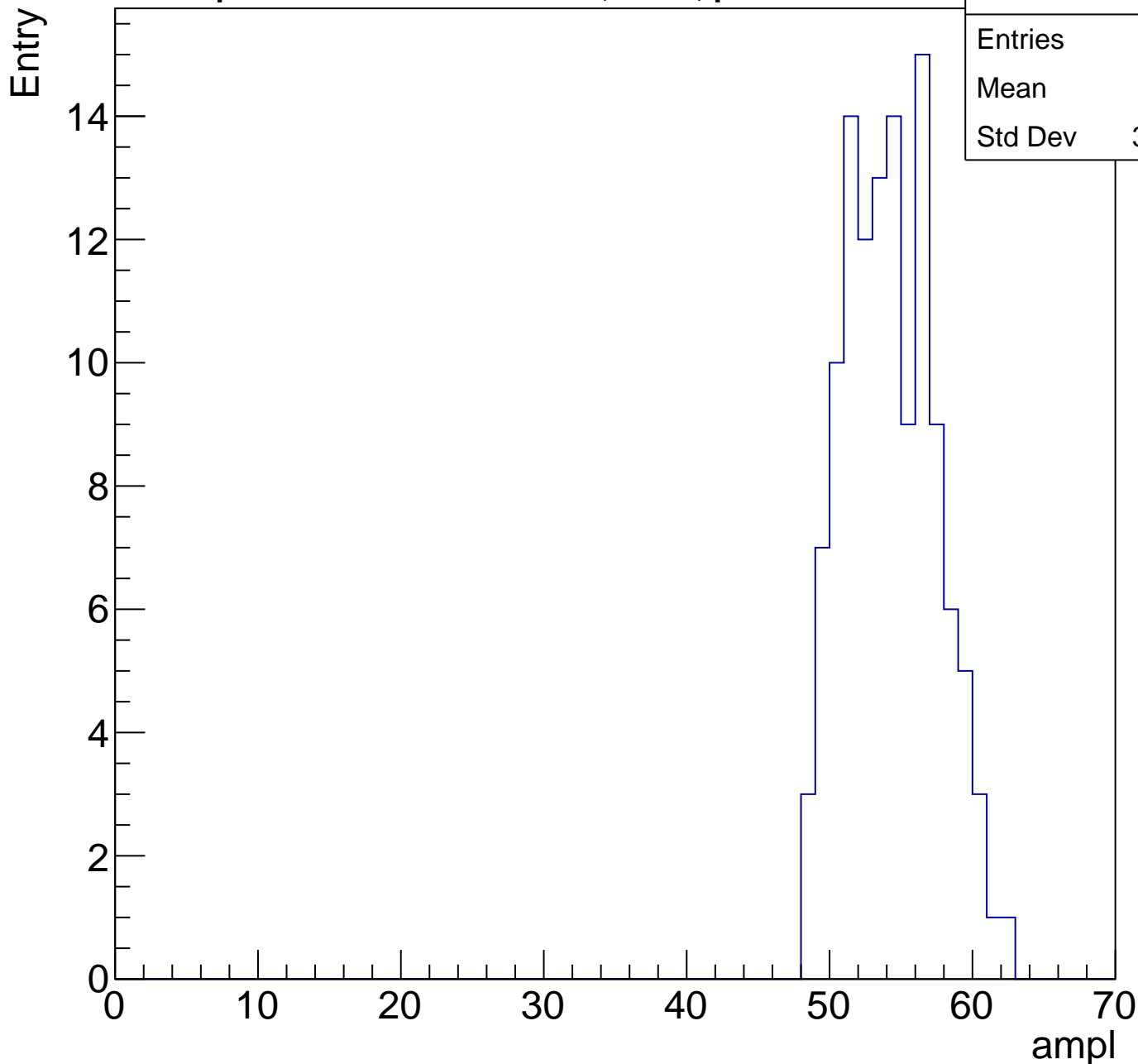
0 10 20 30 40 50 60 70



B1L001S, U16-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

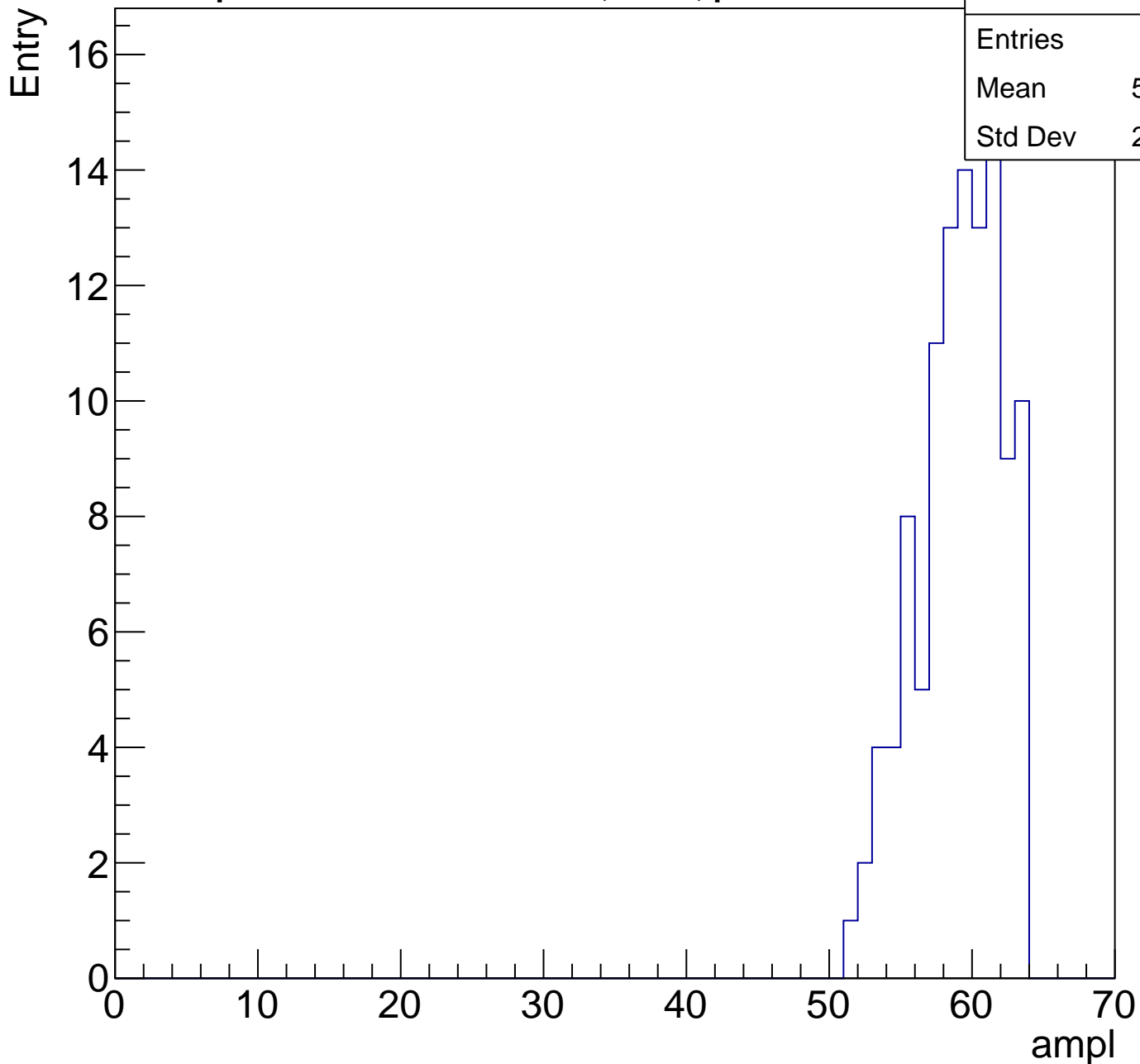
Entries	122
Mean	53.8
Std Dev	3.159



B1L001S, U16-ch116, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	58.67
Std Dev	2.927

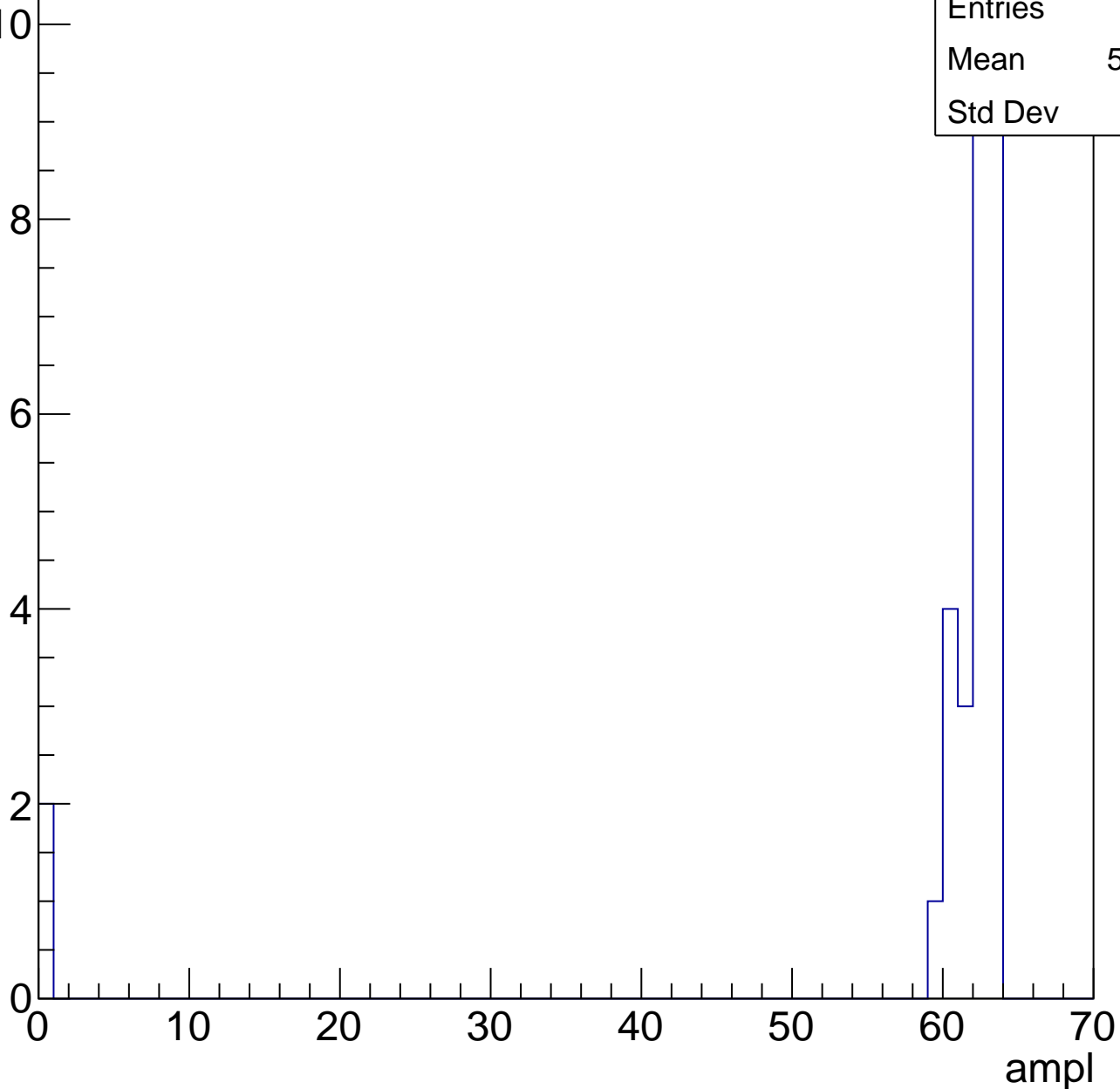


B1L001S, U16-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	29
Mean	57.55
Std Dev	15.7



B1L001S, U16-ch116, adc7

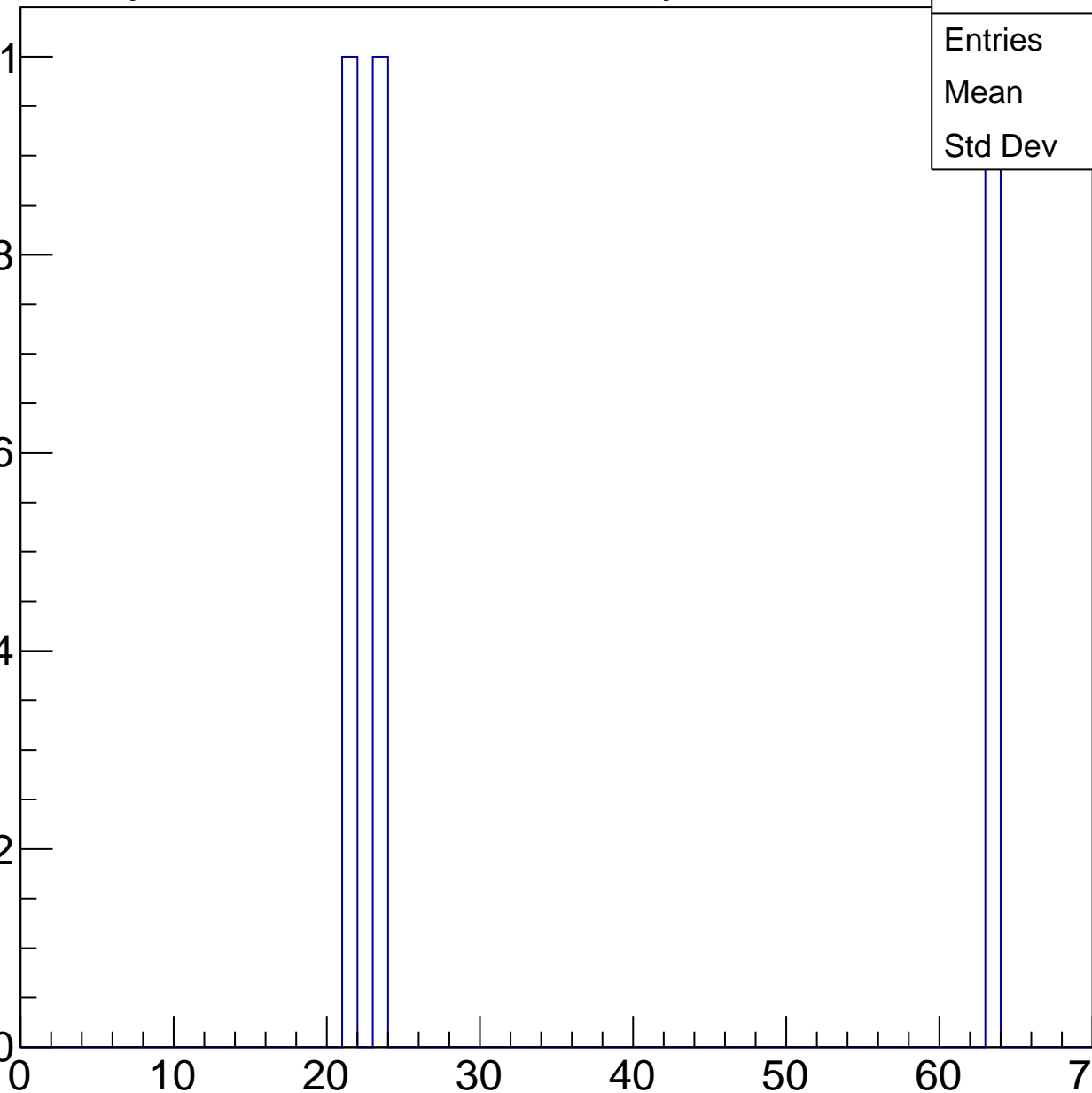
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	35.67
Std Dev	19.34

ampl



B1L001S, U16-ch117, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	30.1
Std Dev	3.425

Gaus mean : 30.2678

Gaus Width: 4.1106

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch117, adc1

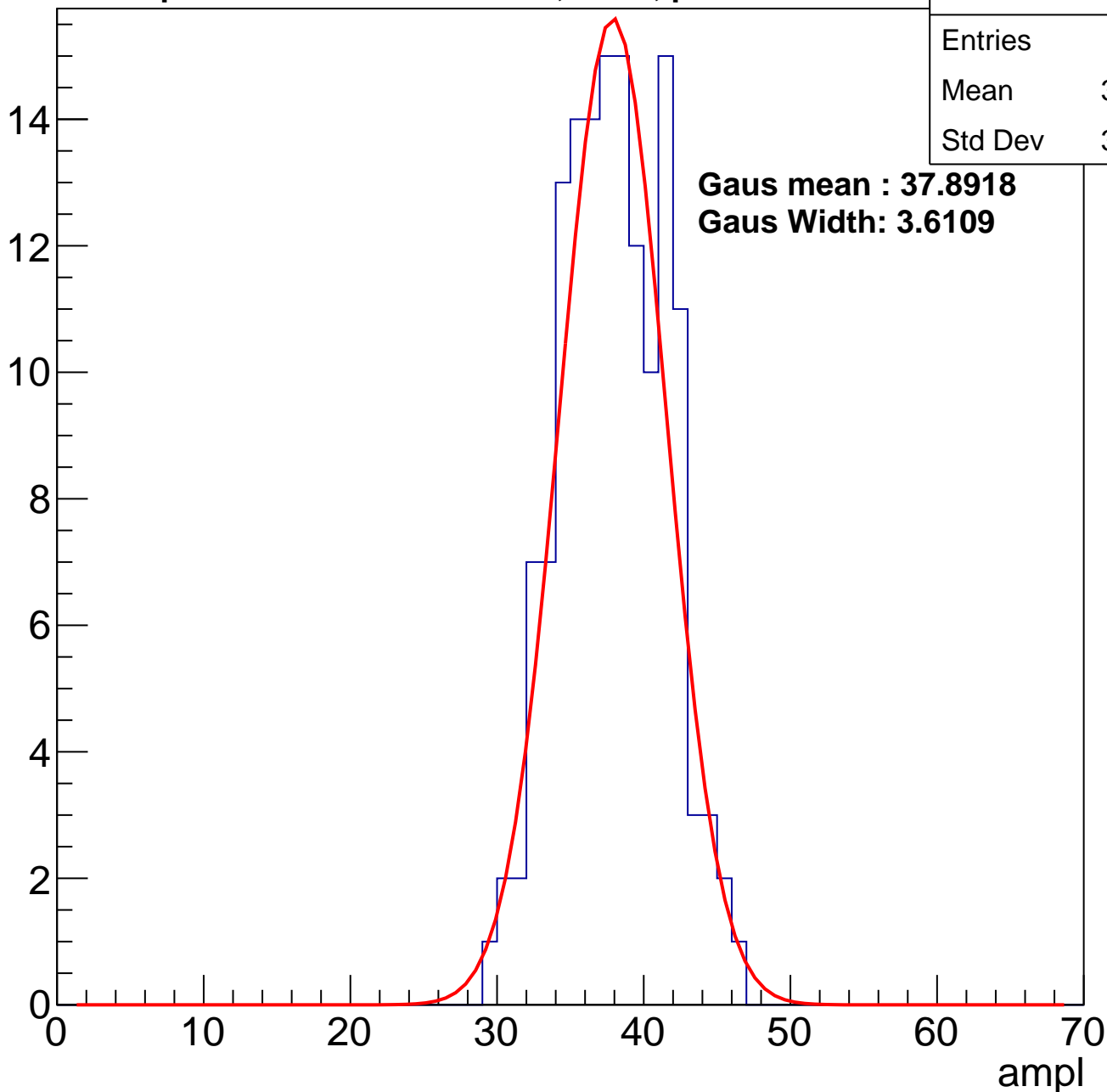
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.48
Std Dev	3.506

Gaus mean : 37.8918

Gaus Width: 3.6109

Entry



B1L001S, U16-ch117, adc2

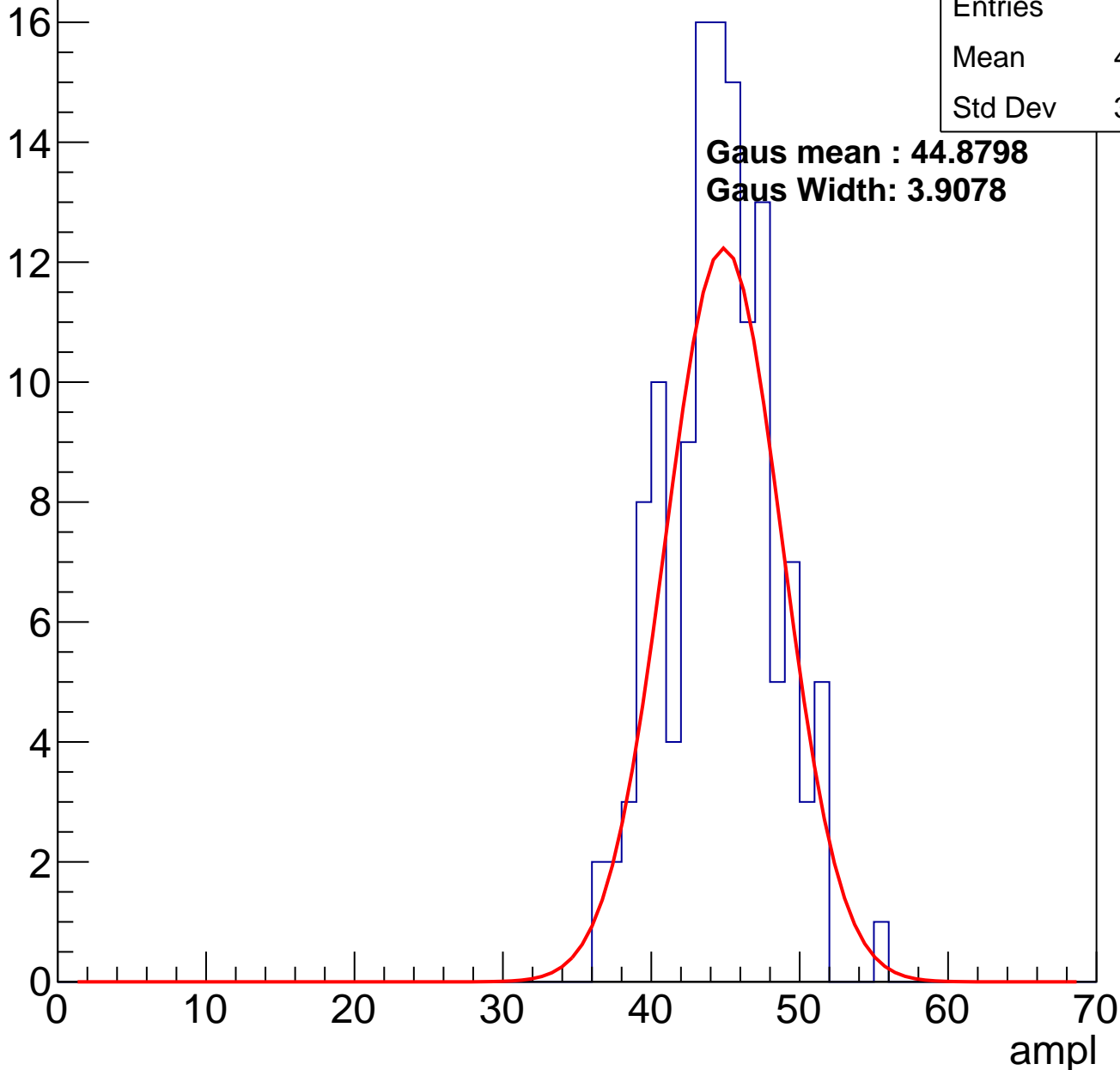
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.16
Std Dev	3.599

Gaus mean : 44.8798

Gaus Width: 3.9078

Entry



B1L001S, U16-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

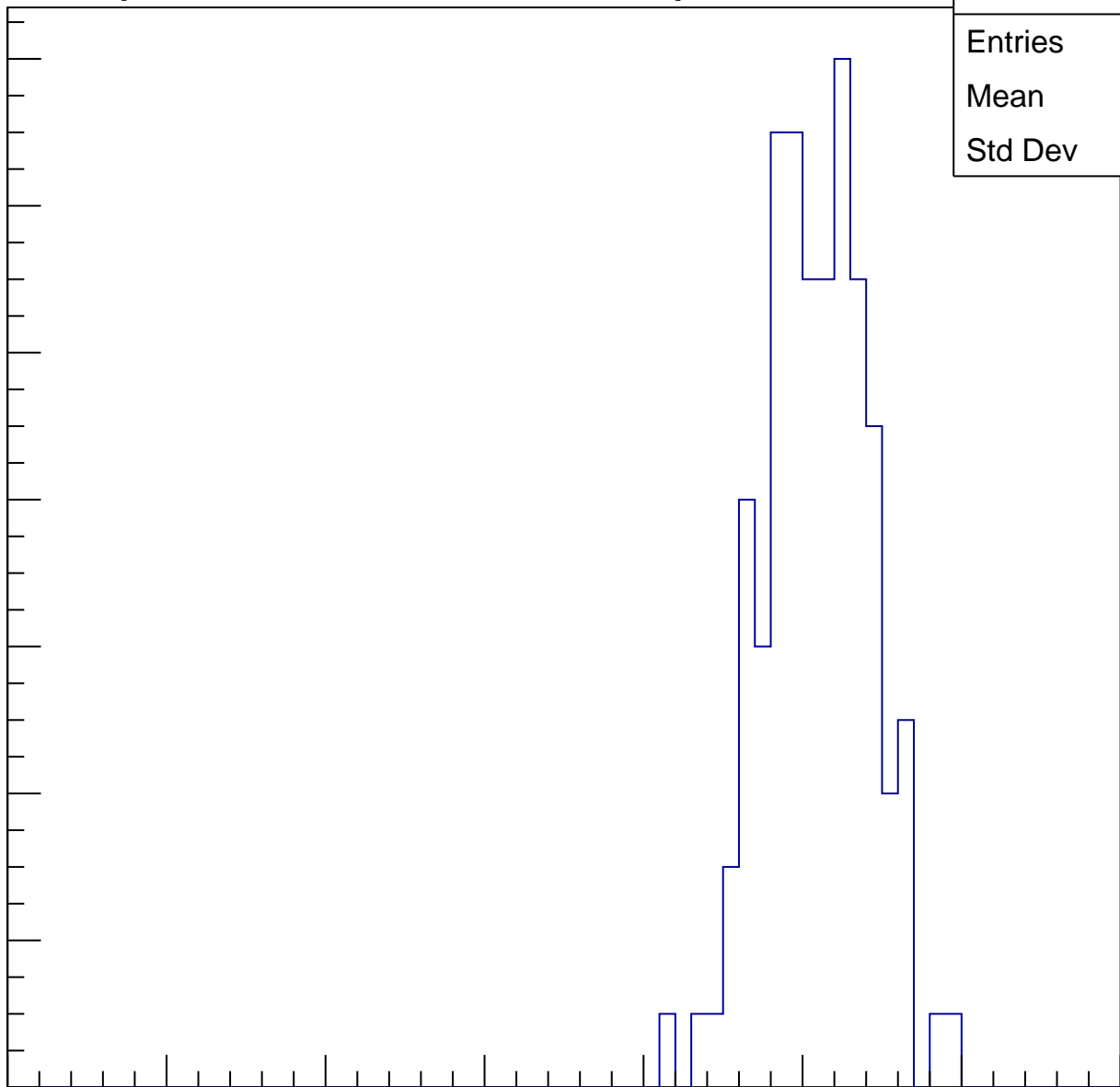
Entries	113
Mean	50.43
Std Dev	3.261

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	55.52
Std Dev	3.314

Entry

12

10

8

6

4

2

0

0

10

20

30

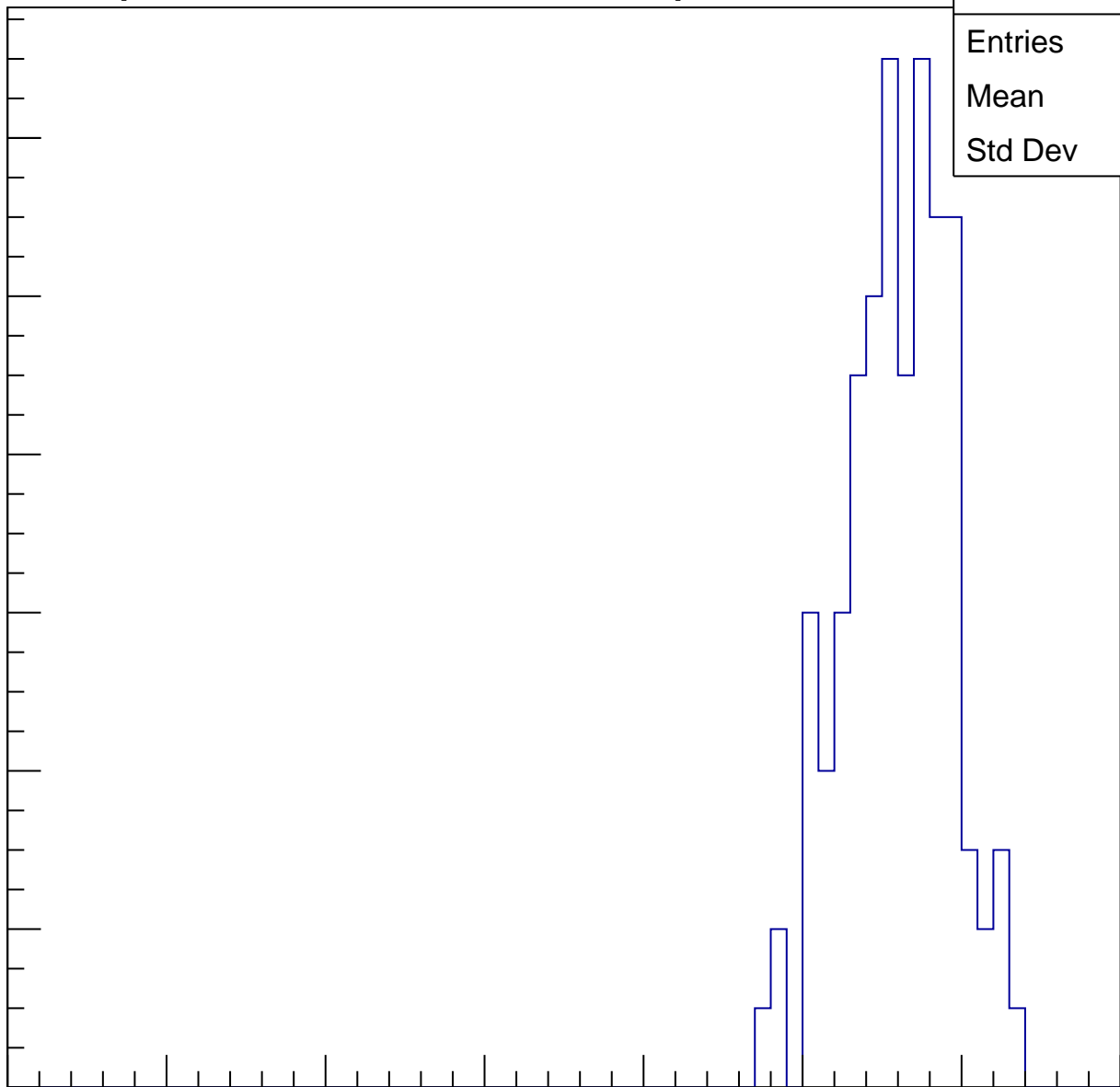
40

50

60

70

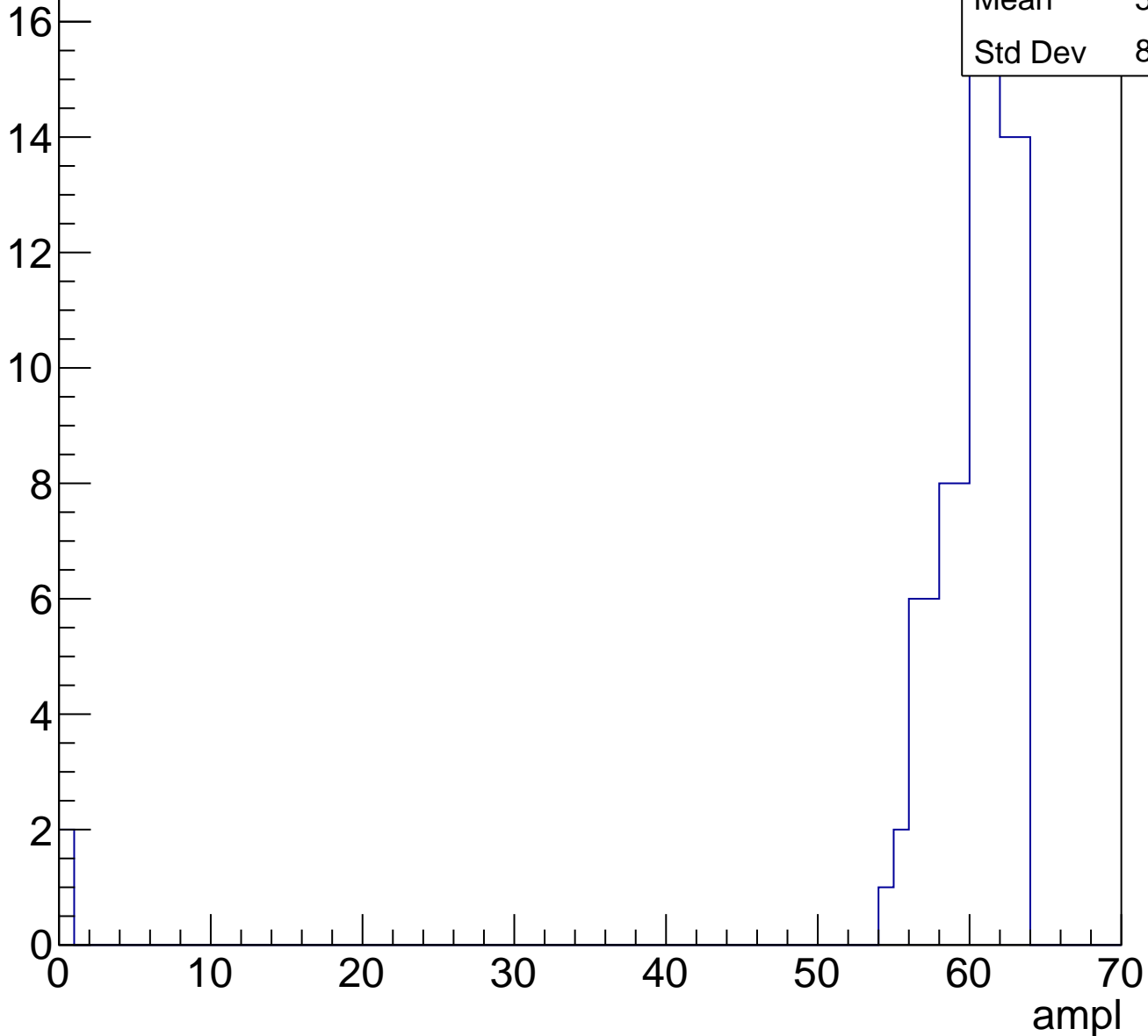
ampl



B1L001S, U16-ch117, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

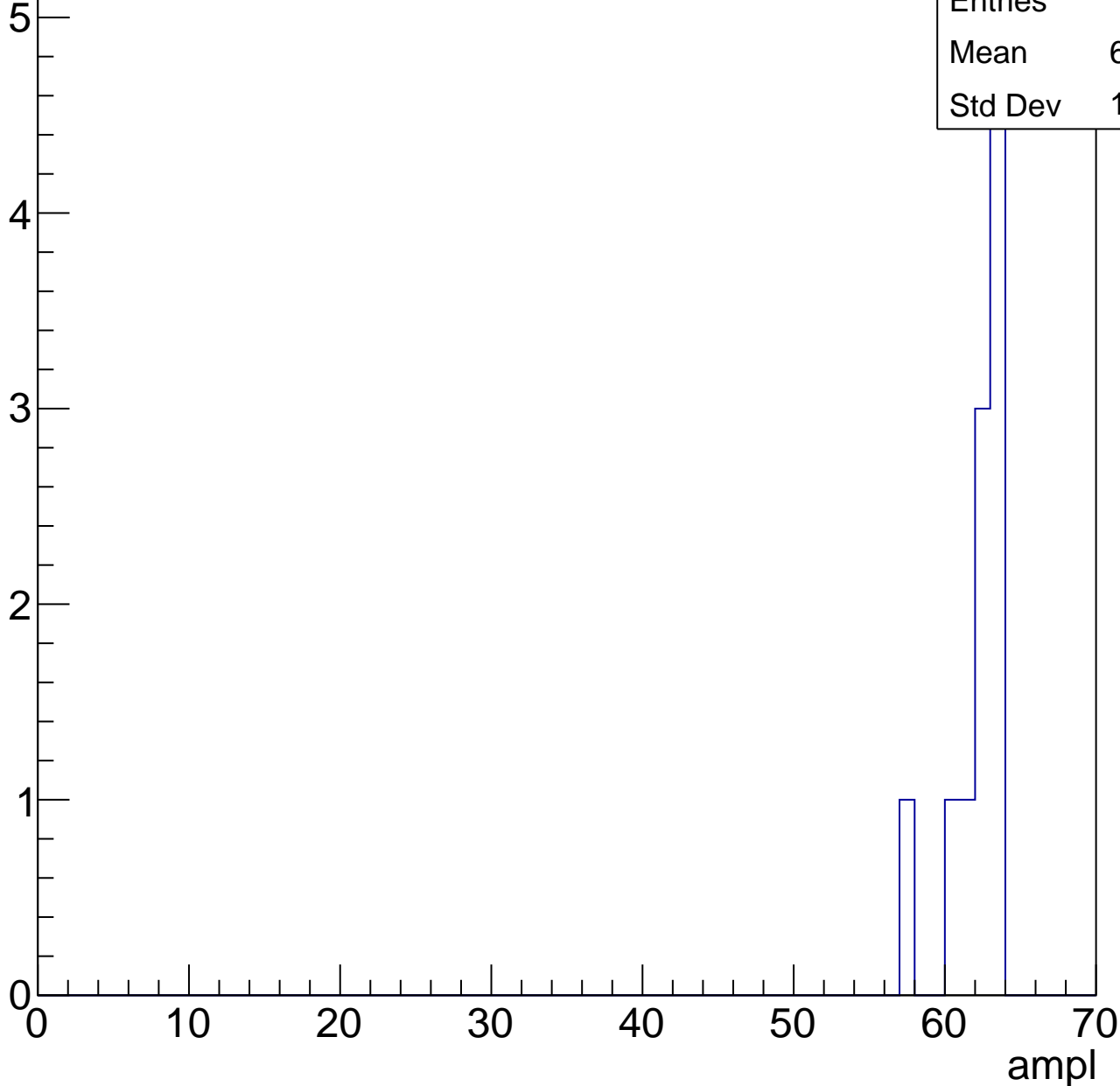


B1L001S, U16-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	61.73
Std Dev	1.763



B1L001S, U16-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch118, adc0

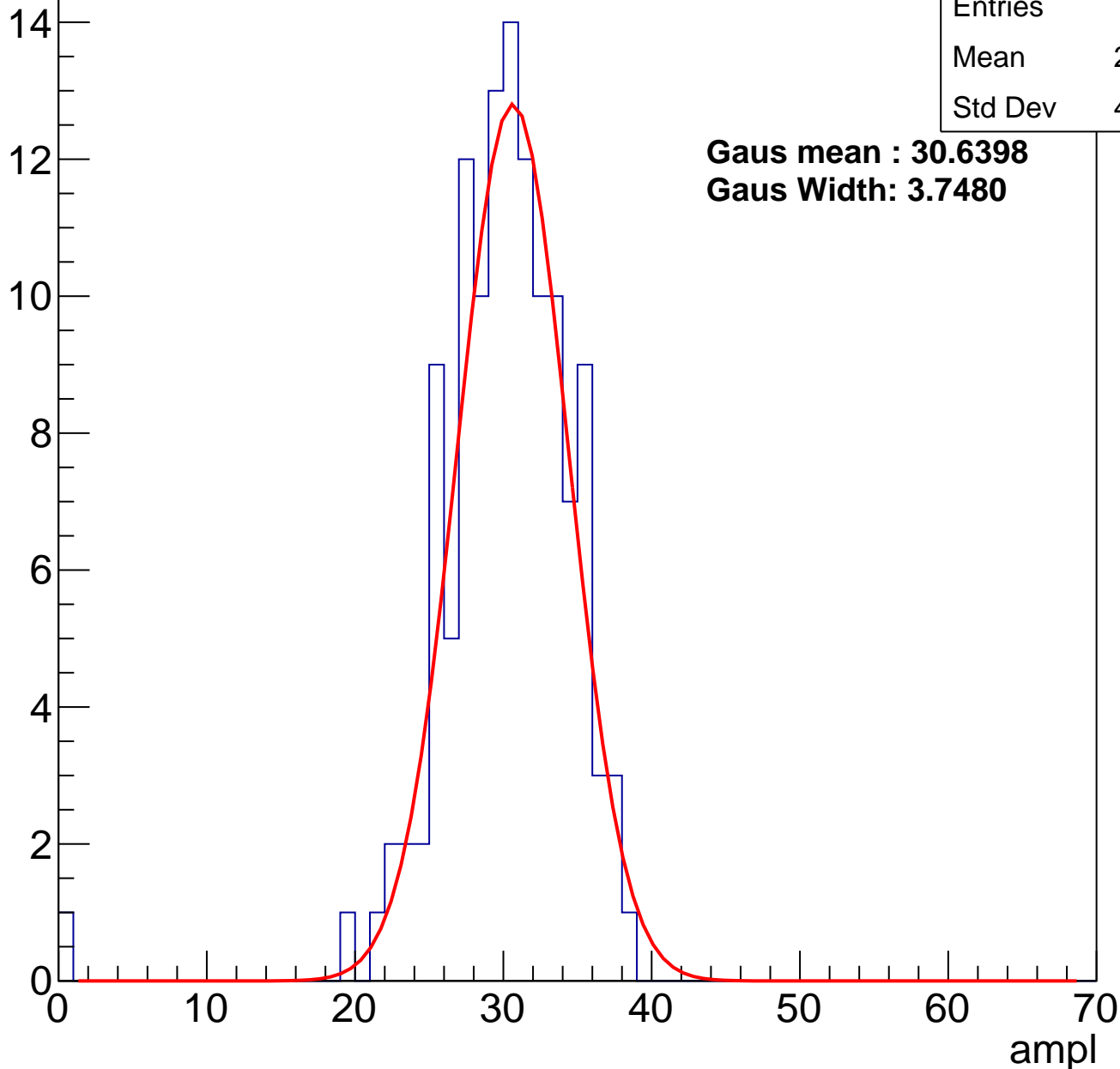
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	29.65
Std Dev	4.567

Gaus mean : 30.6398

Gaus Width: 3.7480

Entry



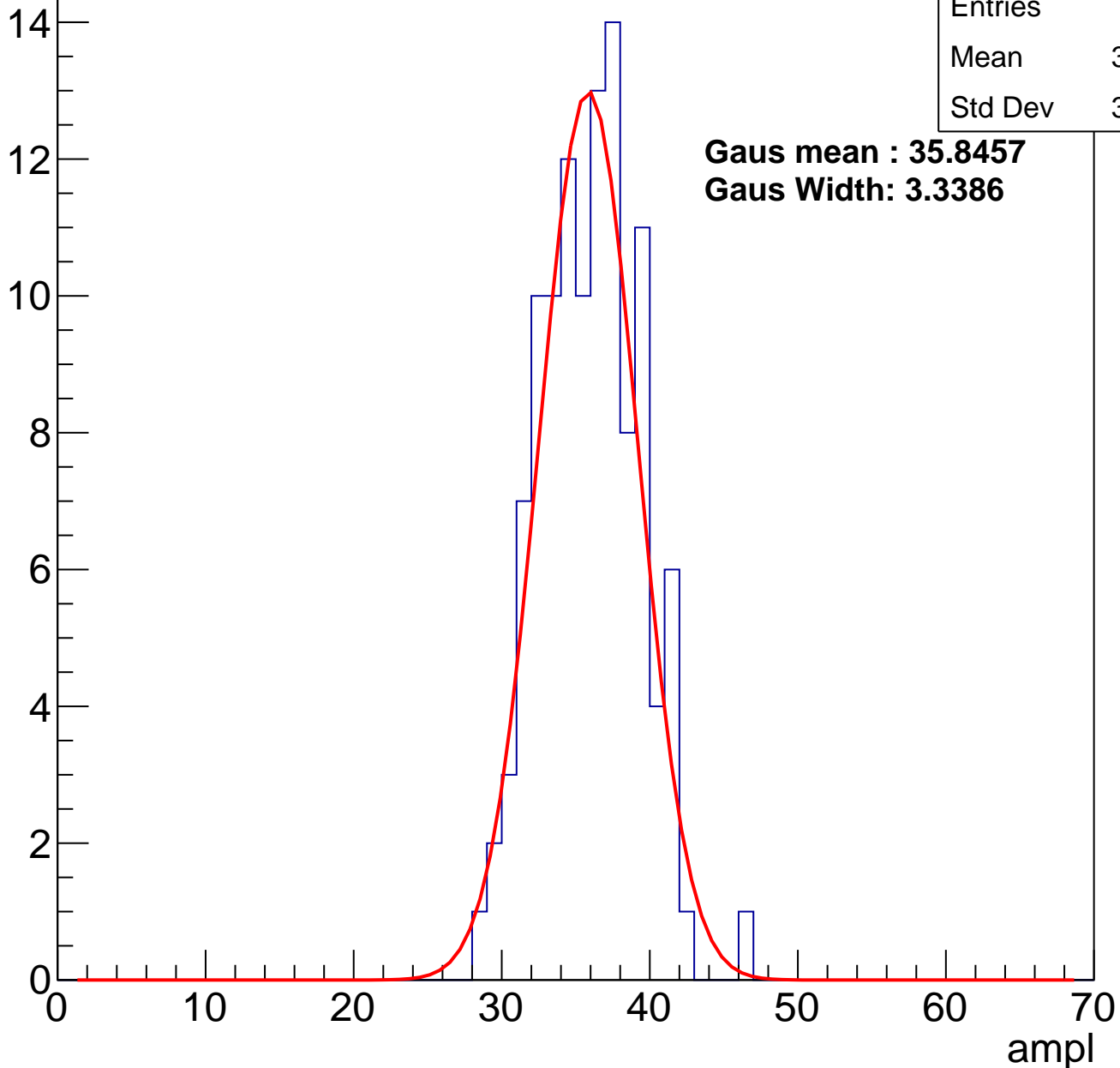
B1L001S, U16-ch118, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	35.52
Std Dev	3.289

Gaus mean : 35.8457
Gaus Width: 3.3386

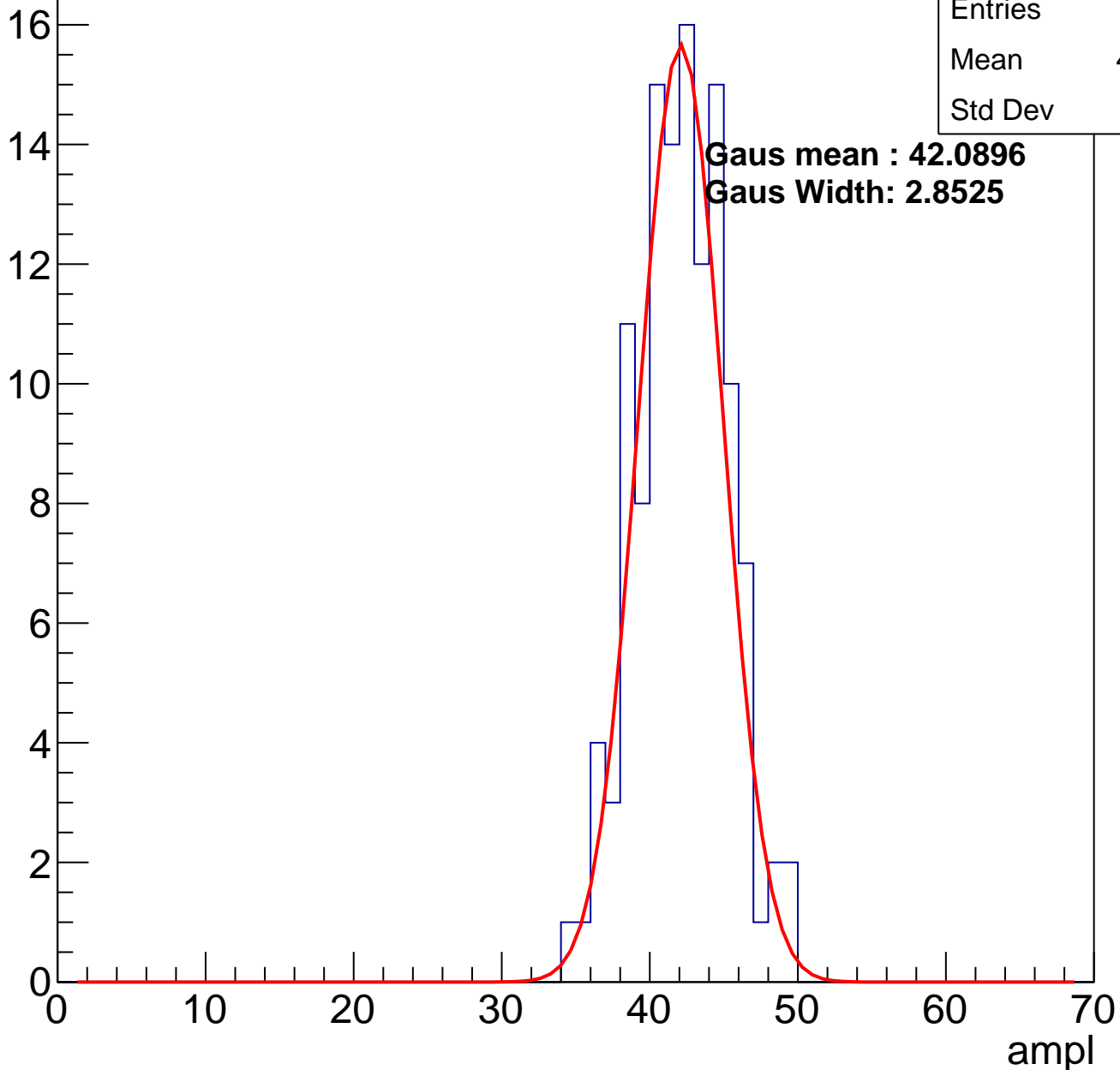
Entry



B1L001S, U16-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

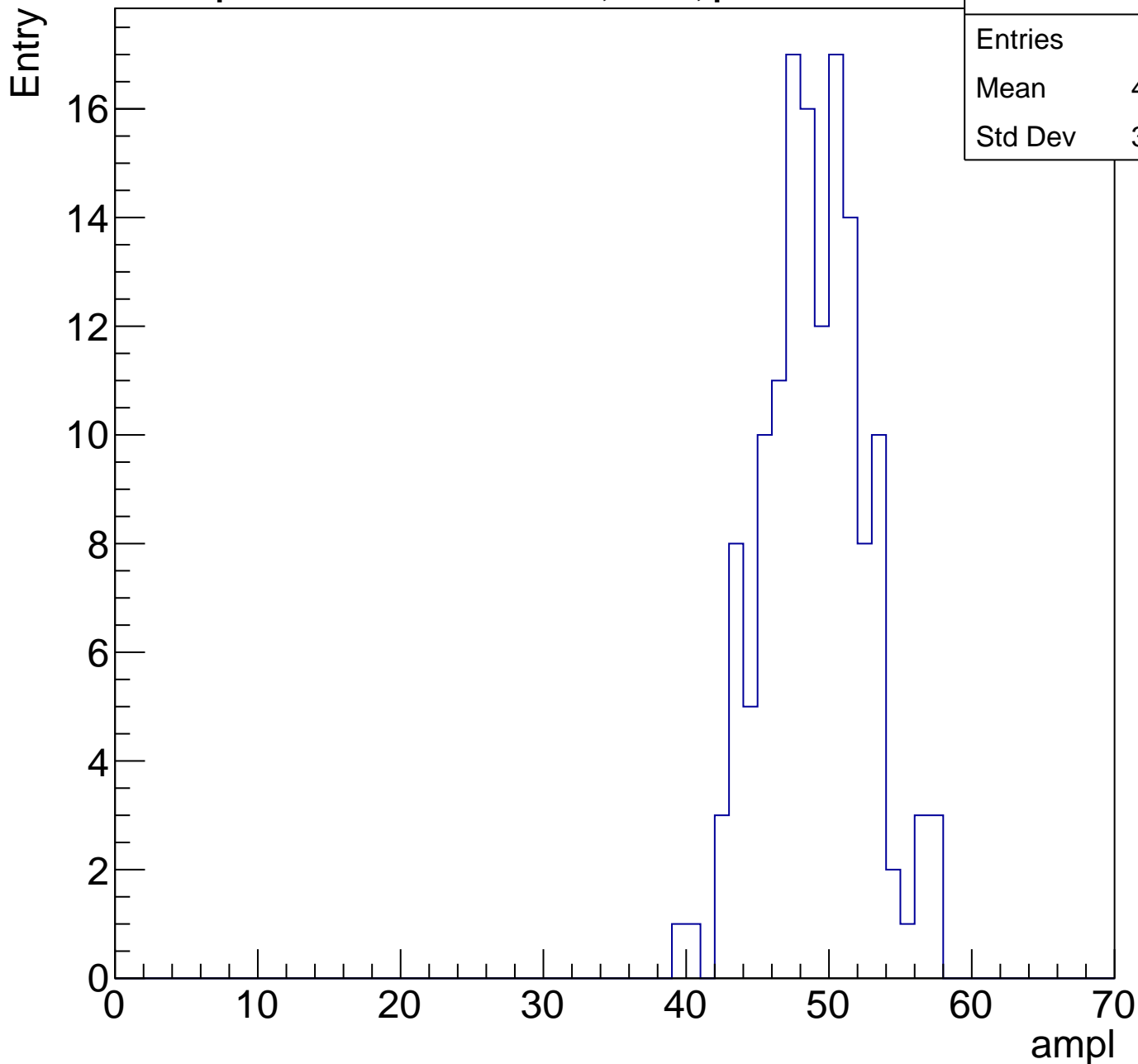
Entry



B1L001S, U16-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

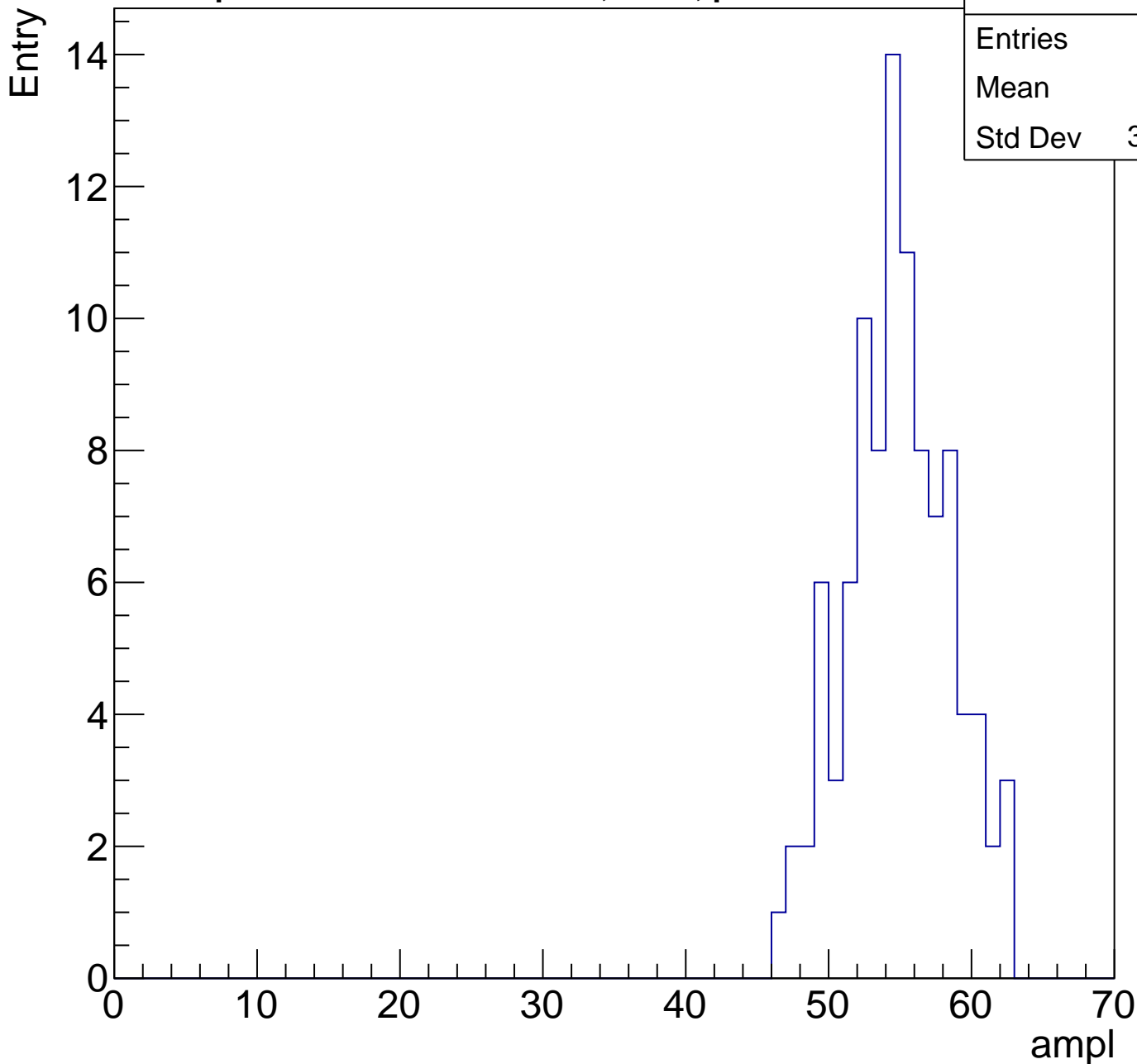
Entries	142
Mean	48.54
Std Dev	3.544



B1L001S, U16-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	54.4
Std Dev	3.604



B1L001S, U16-ch118, adc5

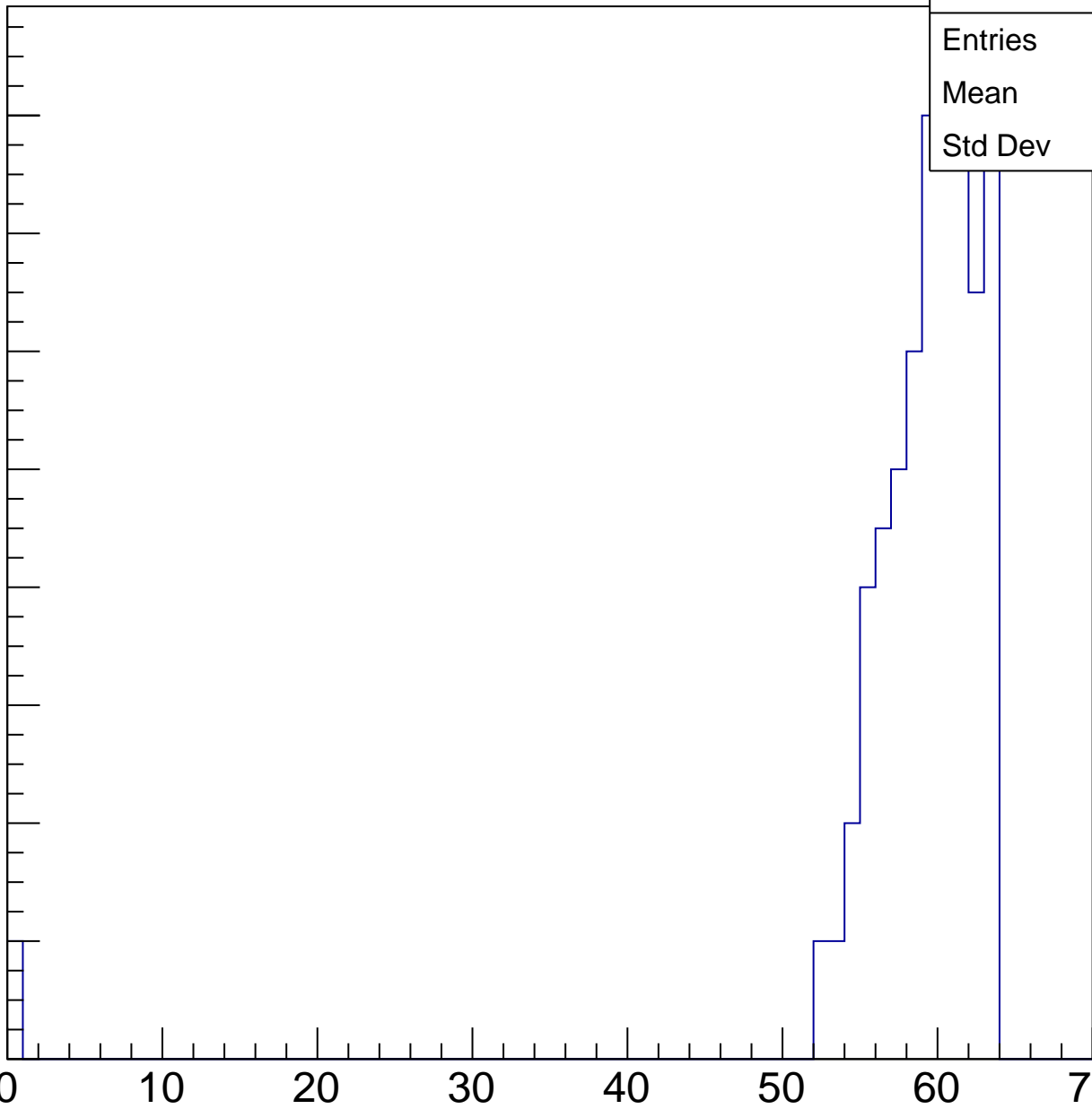
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	58.21
Std Dev	7.879

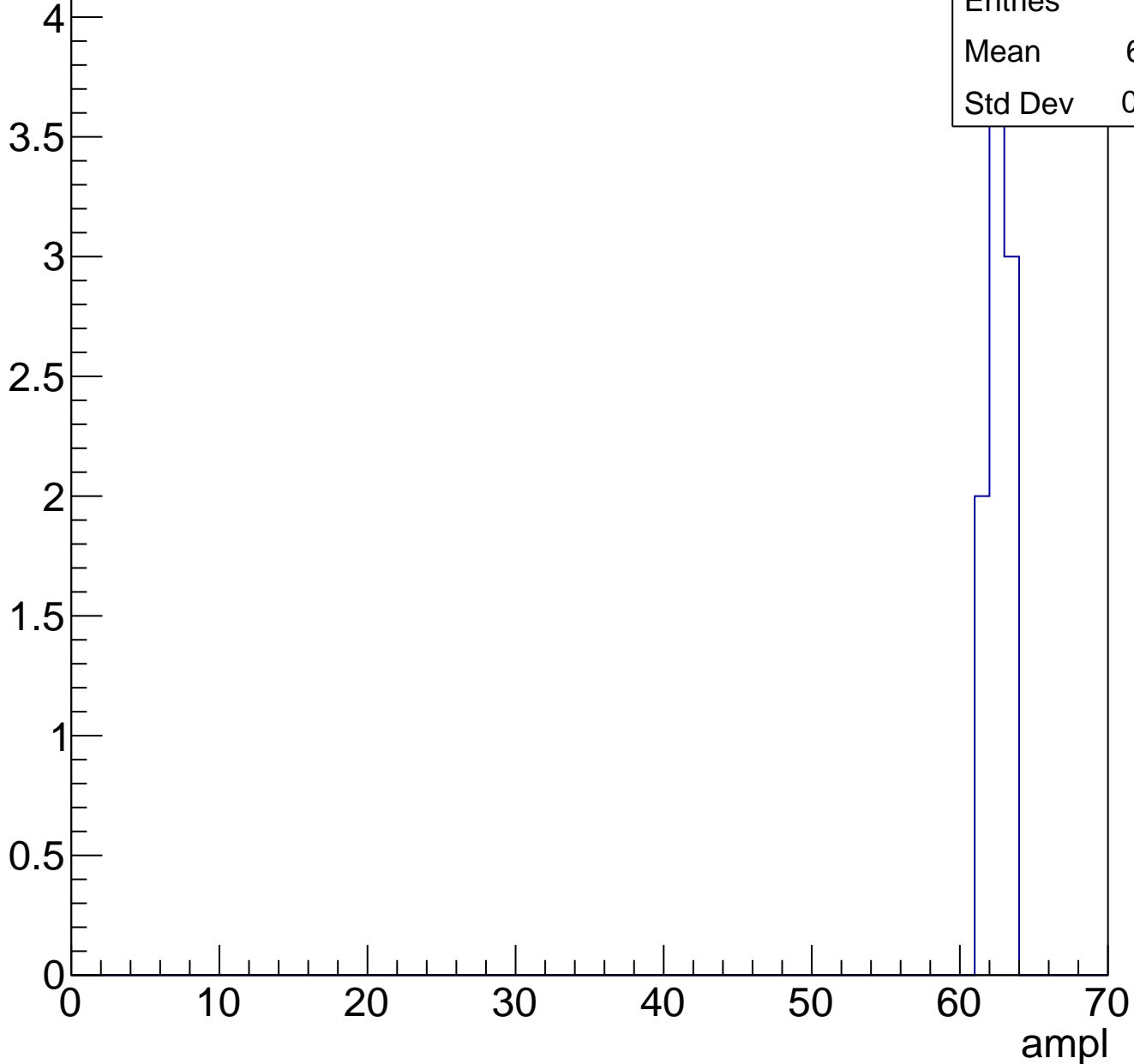
ampl



B1L001S, U16-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

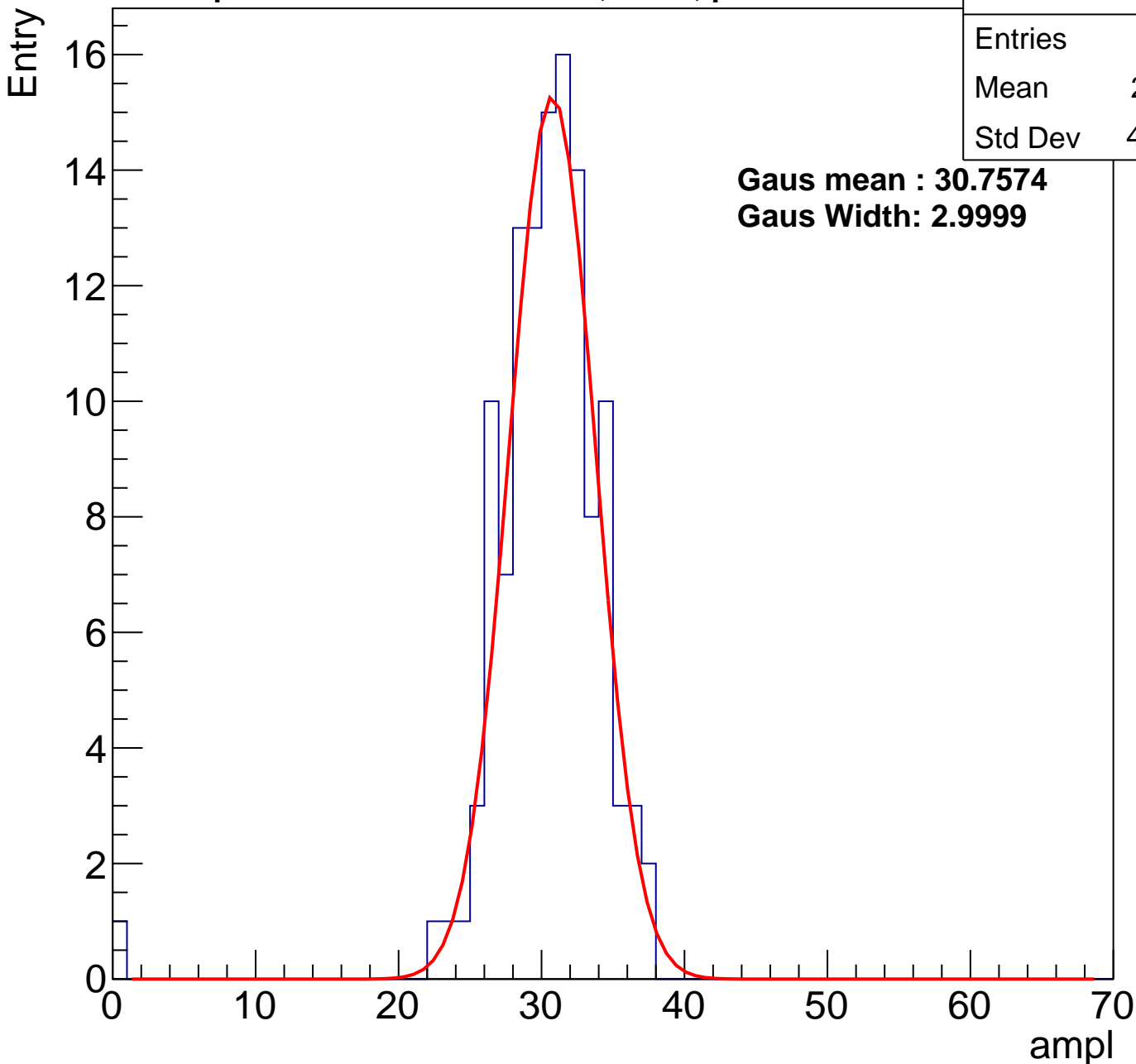
B1L001S, U16-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	29.91
Std Dev	4.062

Gaus mean : 30.7574

Gaus Width: 2.9999



B1L001S, U16-ch119, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

116

Mean

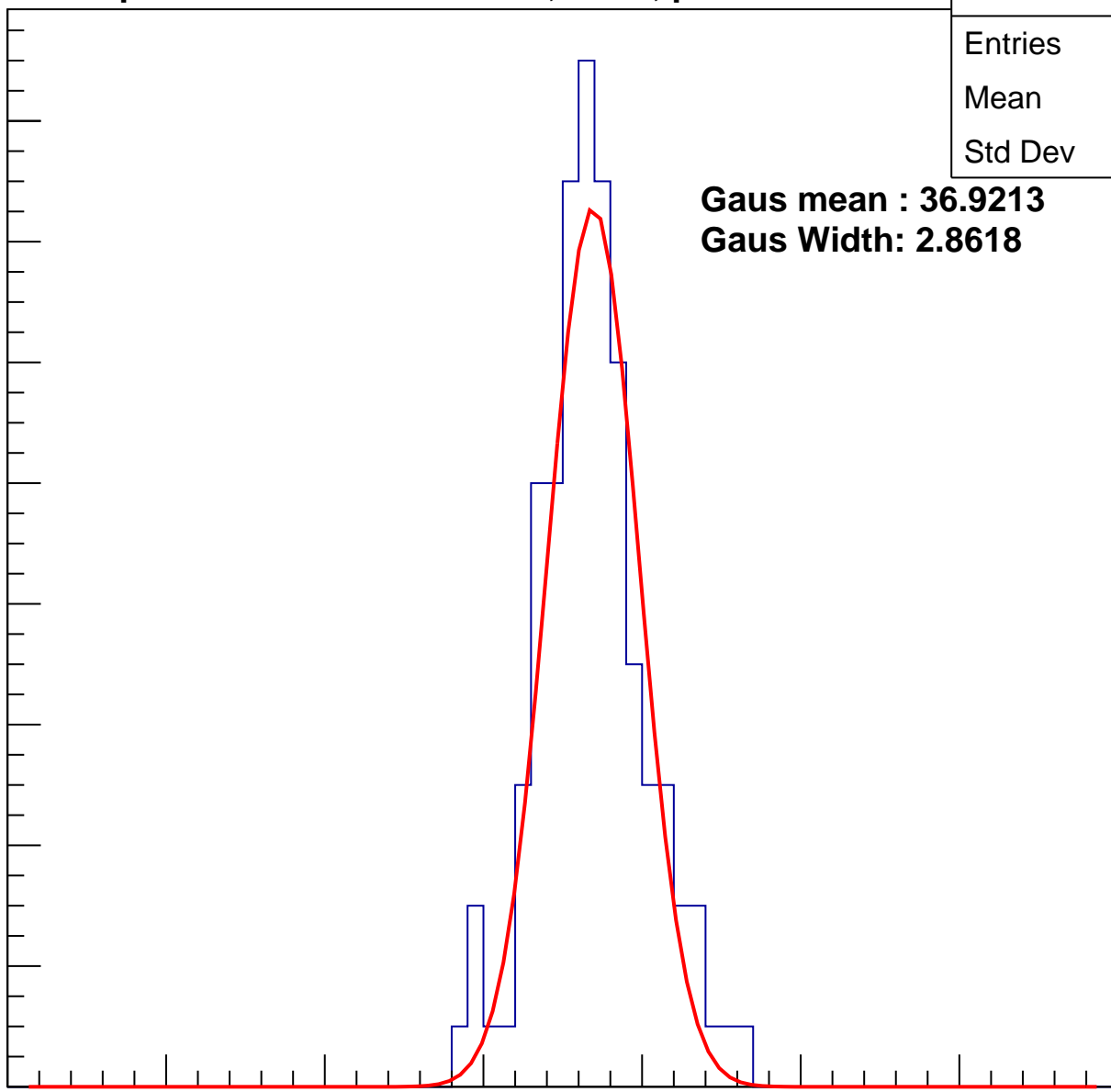
36.4

Std Dev

3.36

Gaus mean : 36.9213

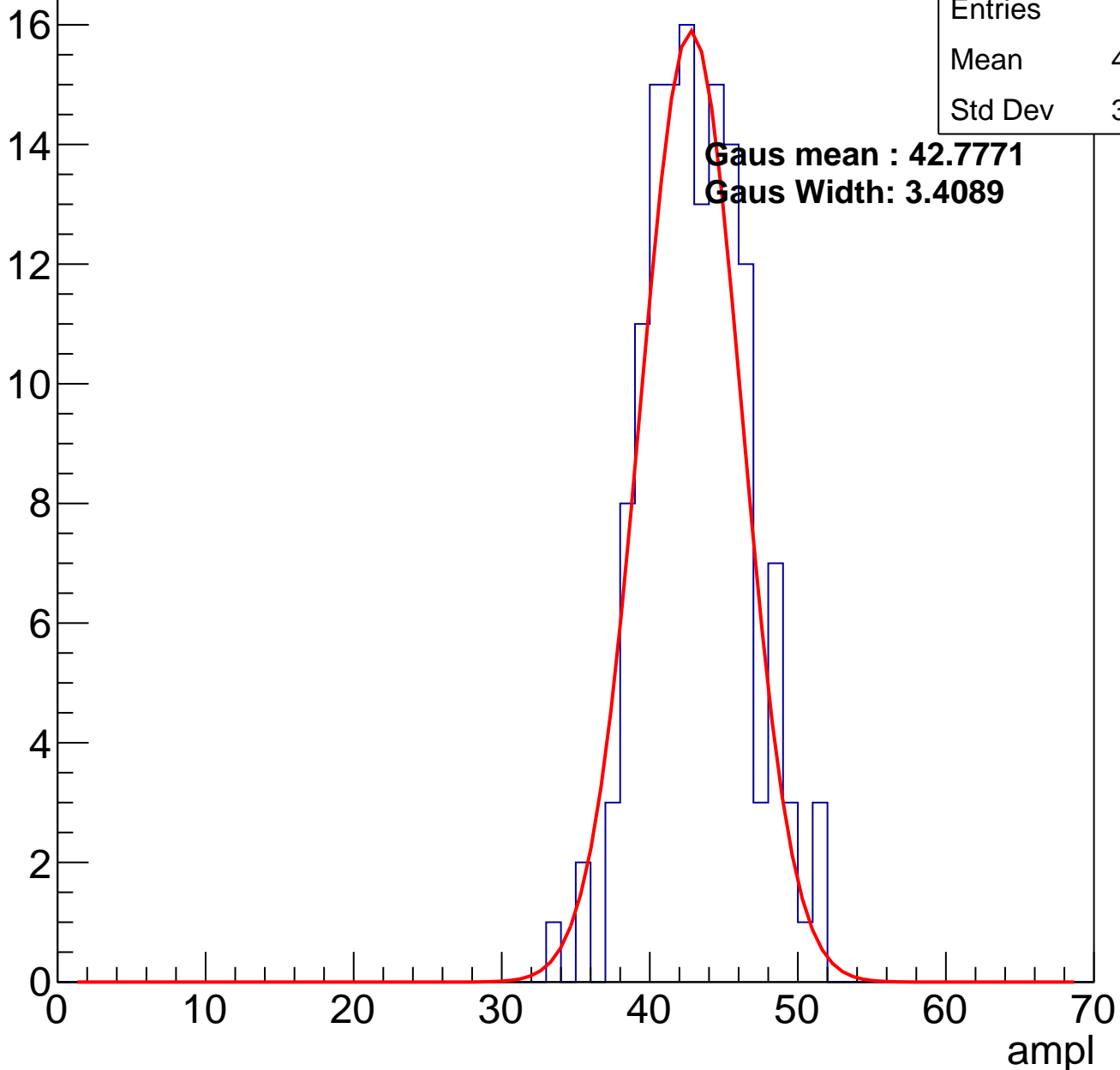
Gaus Width: 2.8618



B1L001S, U16-ch119, adc2

calib_packv5_042523_0143.root, FC#2, port C2

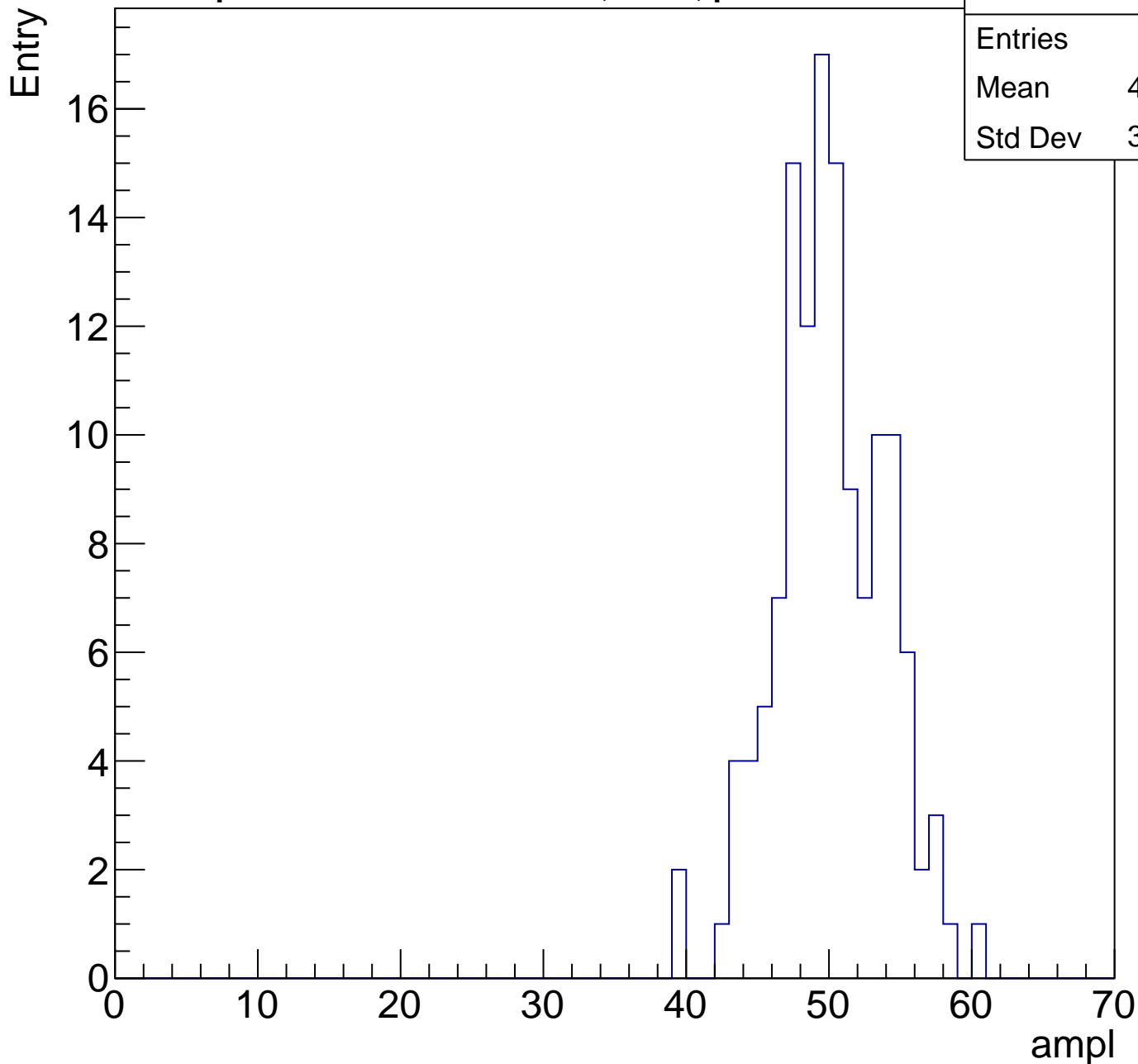
Entry



B1L001S, U16-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	49.64
Std Dev	3.794

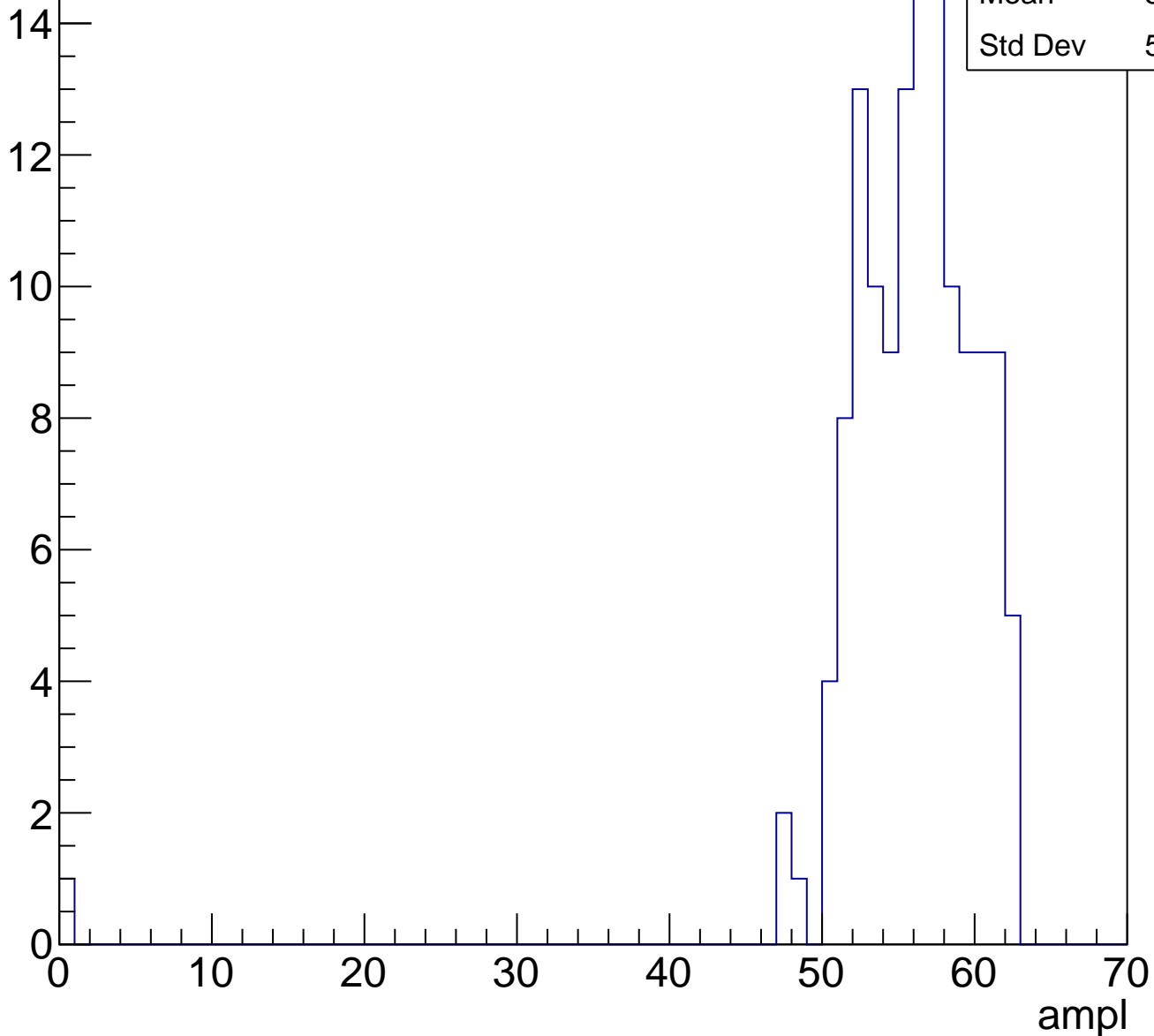


B1L001S, U16-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	55.35
Std Dev	5.933

Entry



B1L001S, U16-ch119, adc5

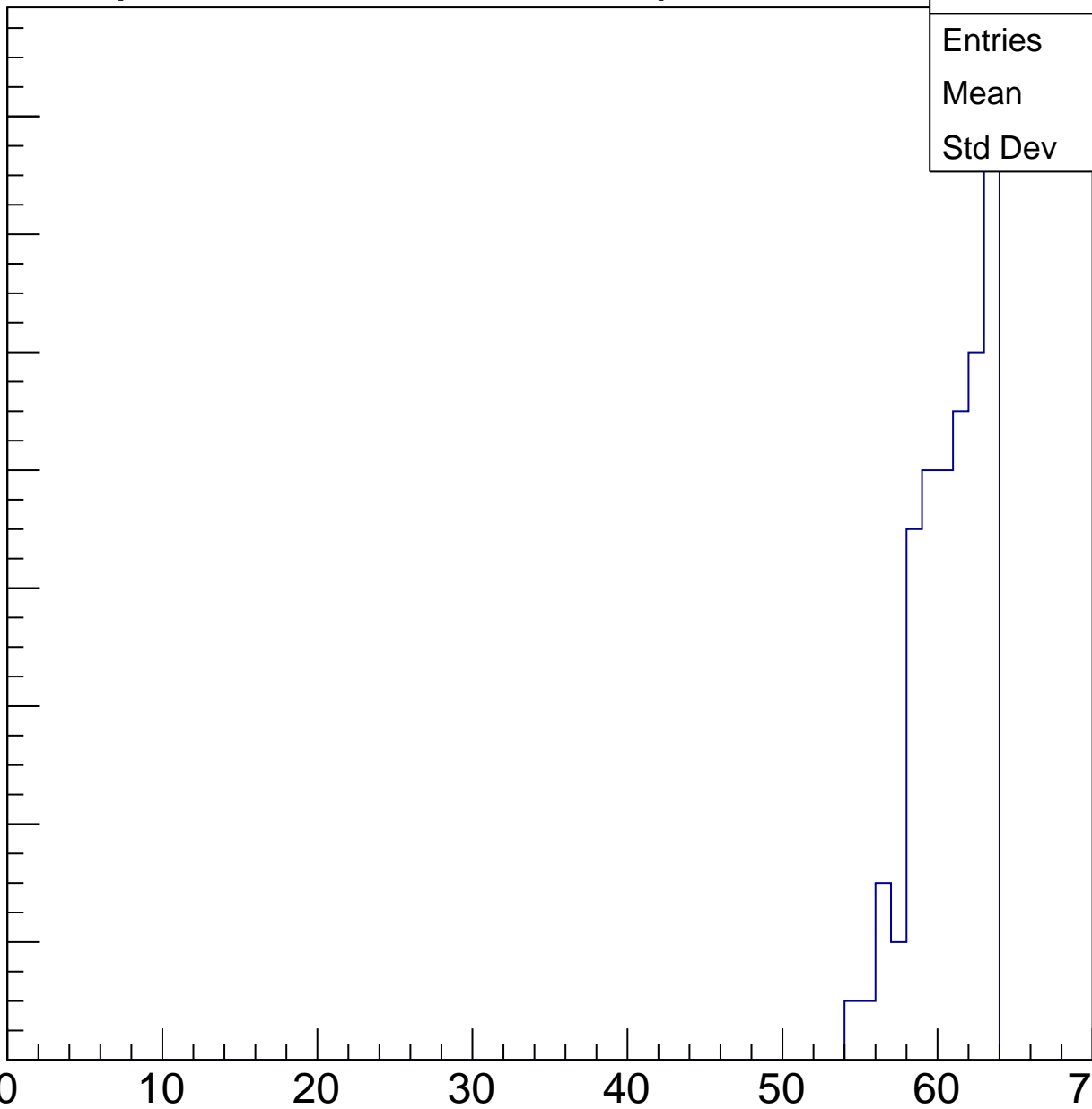
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	60.38
Std Dev	2.218

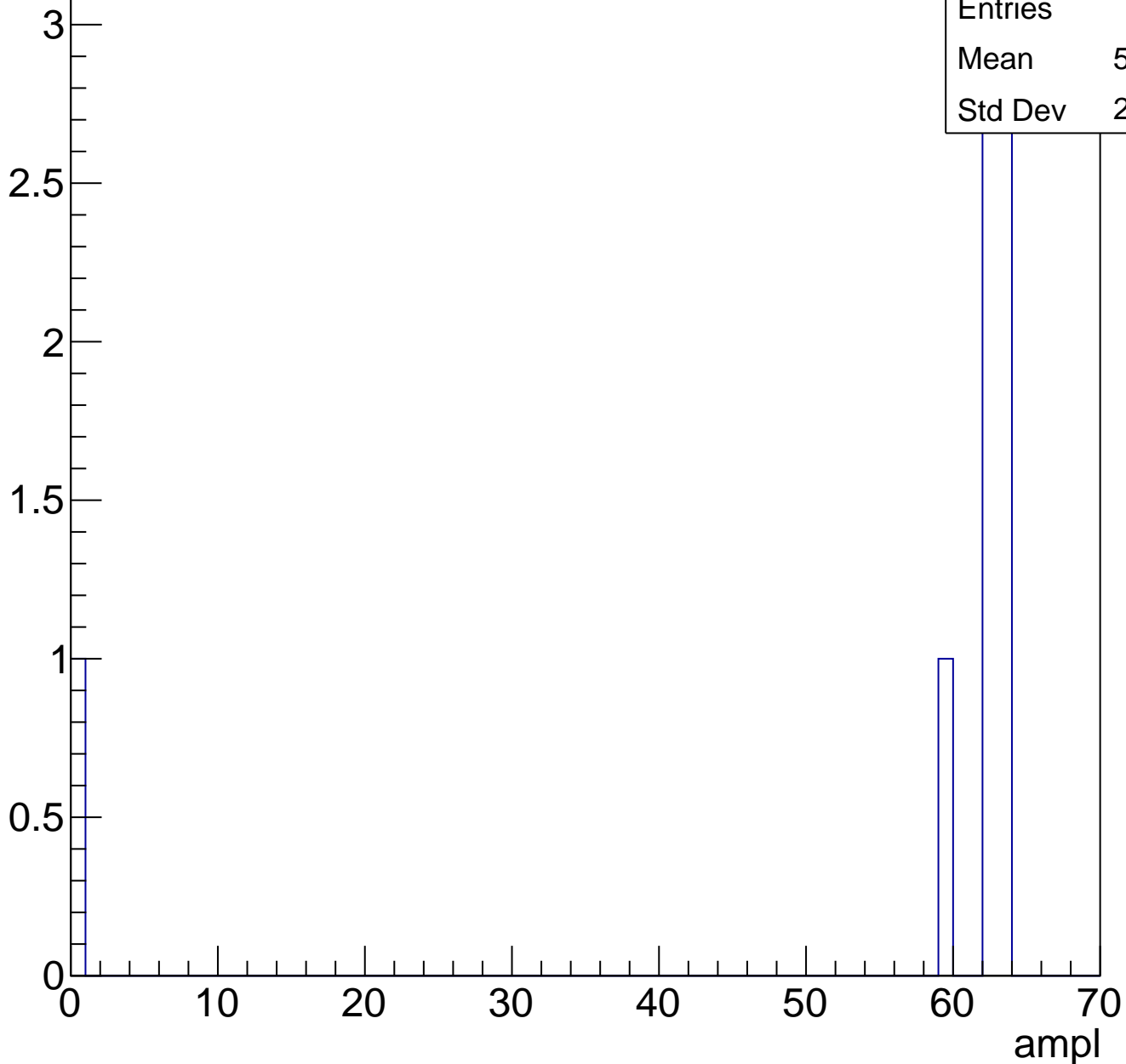
ampl



B1L001S, U16-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

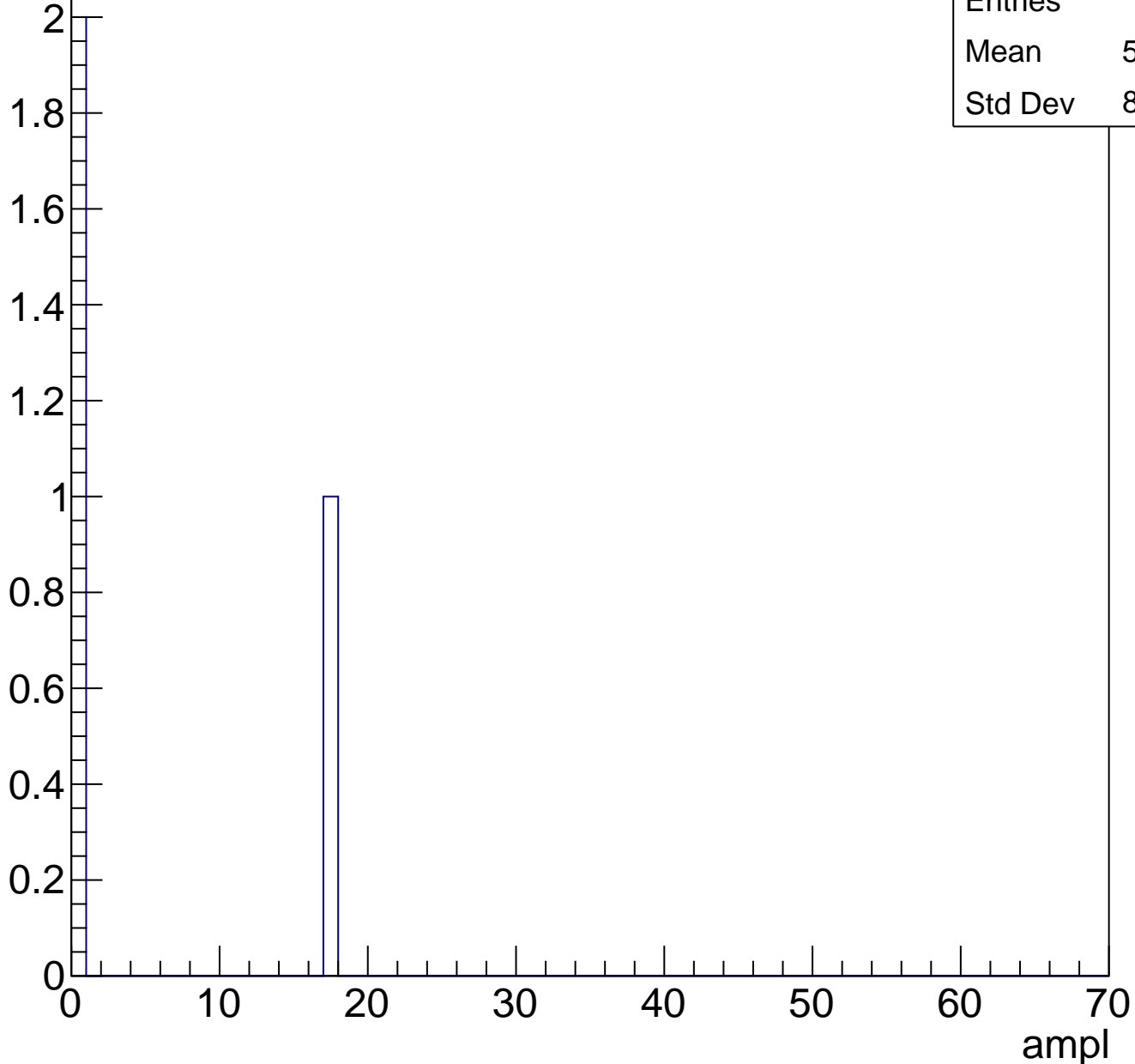
Entry



B1L001S, U16-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	5.667
Std Dev	8.014

B1L001S, U16-ch120, adc0

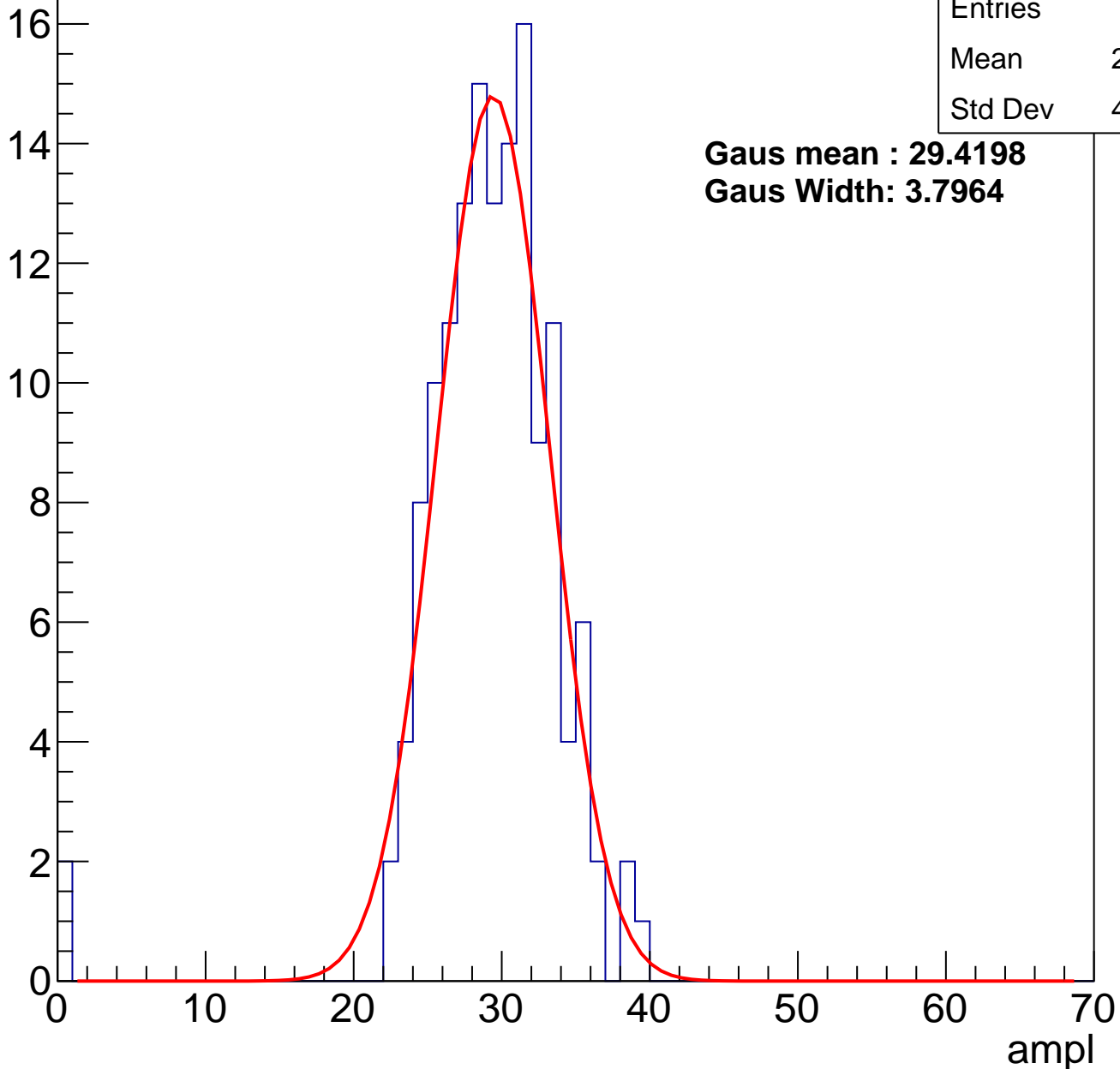
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	28.76
Std Dev	4.914

Gaus mean : 29.4198

Gaus Width: 3.7964

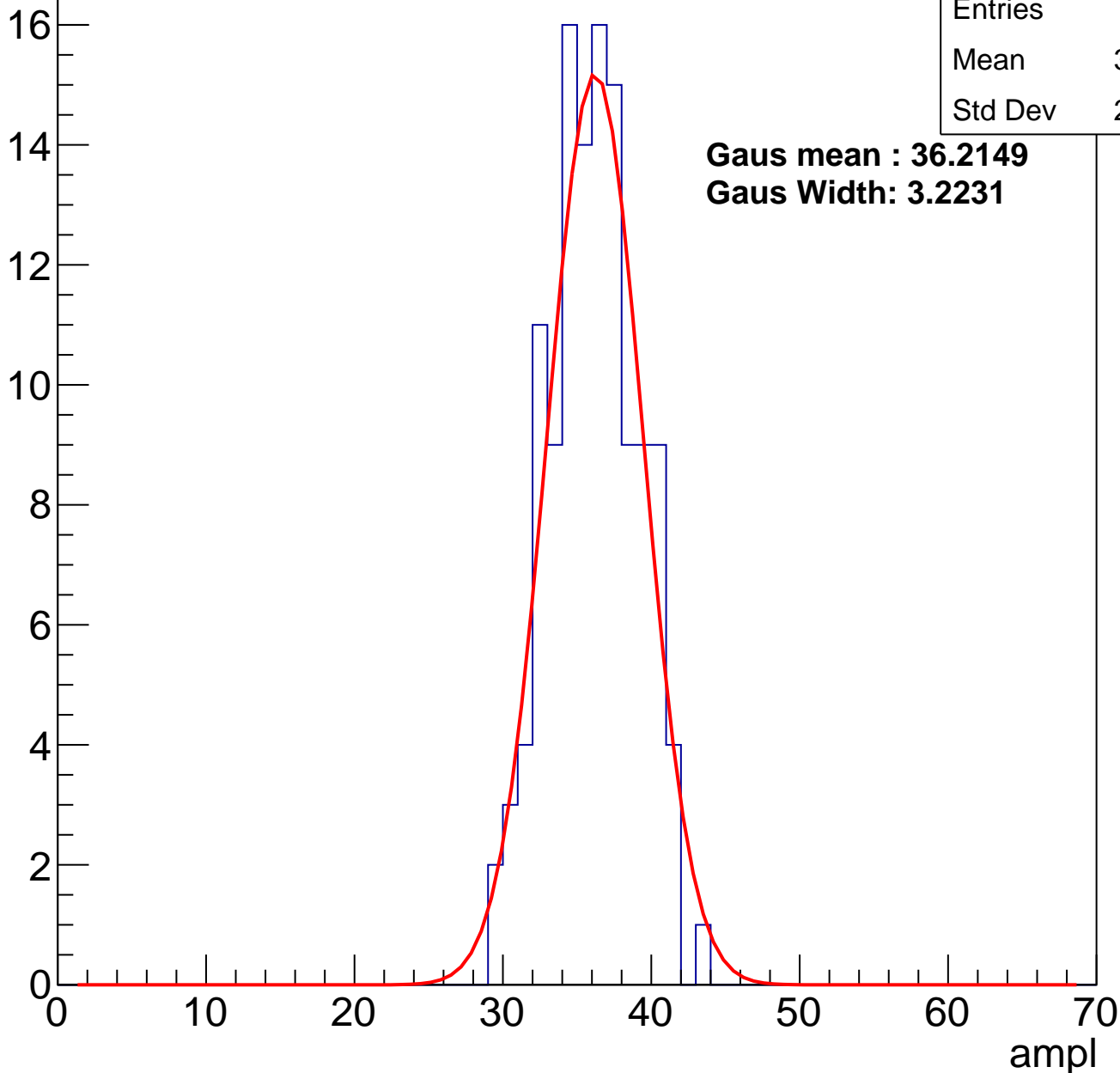
Entry



B1L001S, U16-ch120, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch120, adc2

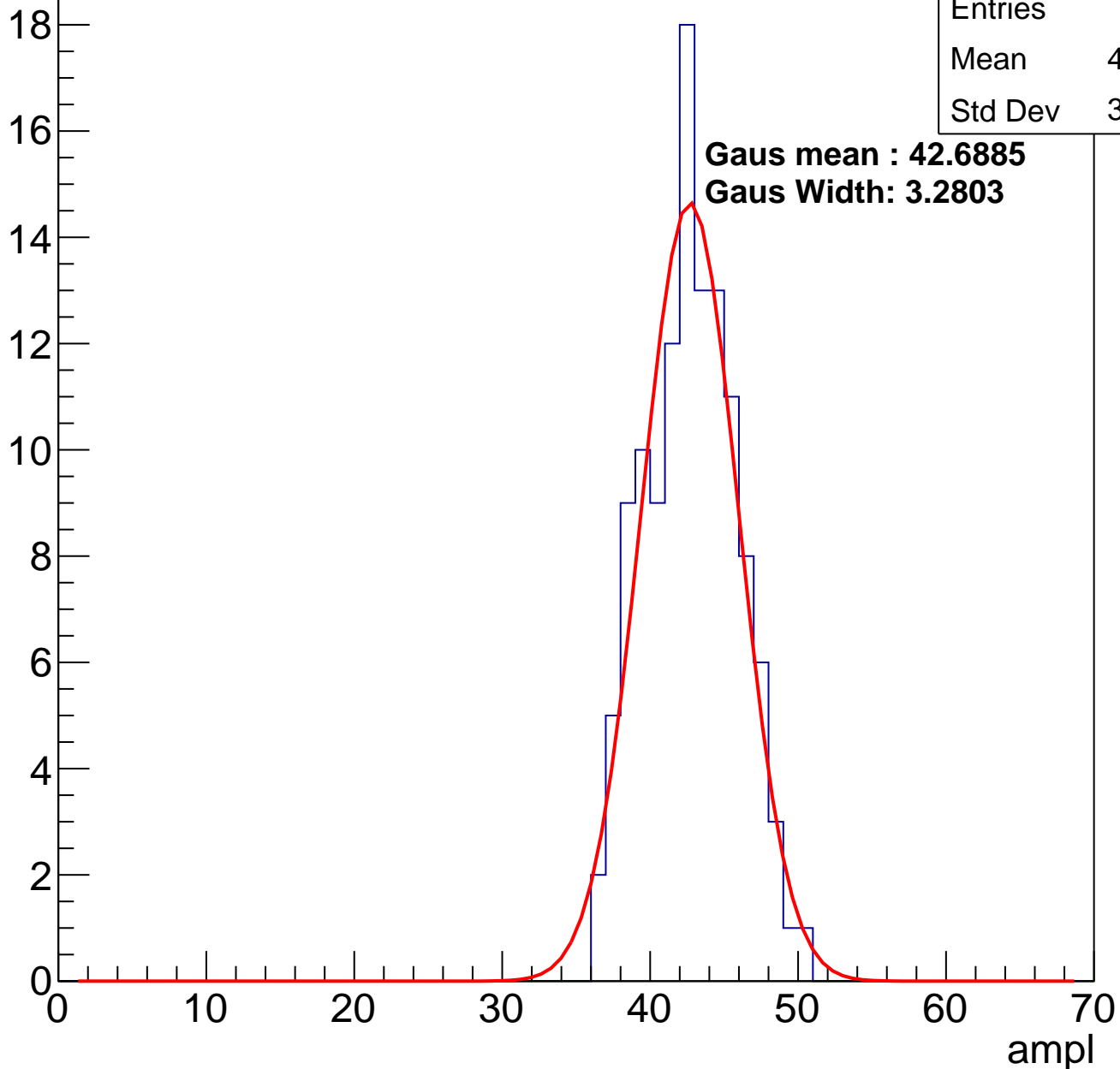
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.28
Std Dev	3.059

Gaus mean : 42.6885

Gaus Width: 3.2803

Entry



B1L001S, U16-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

48.33

Std Dev

3.54

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch120, adc4

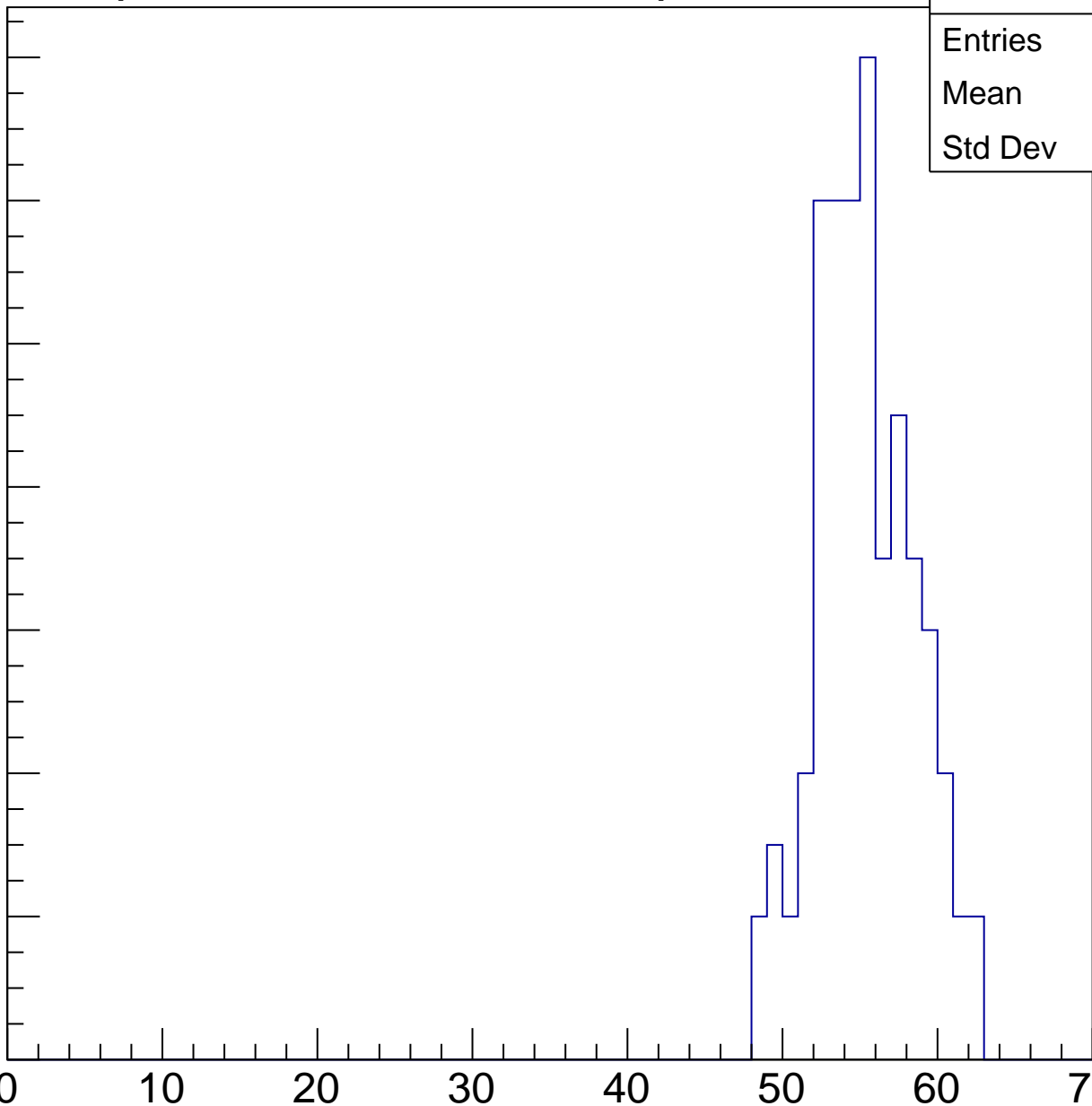
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	54.86
Std Dev	3.127

ampl



B1L001S, U16-ch120, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

99

Mean

57.7

Std Dev

8.704

ampl

0

10

20

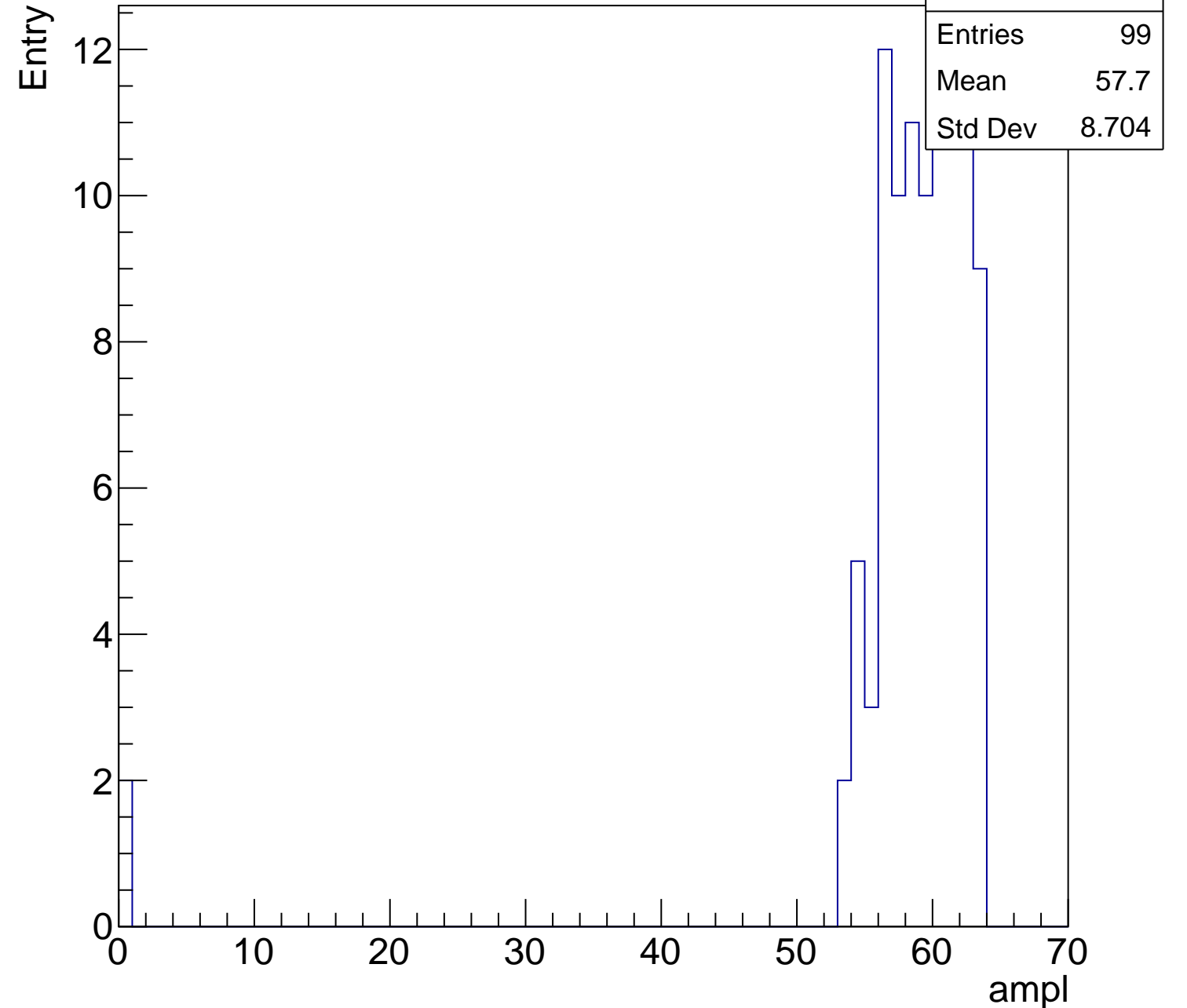
30

40

50

60

70



B1L001S, U16-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	33
Mean	61.42
Std Dev	1.498

B1L001S, U16-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L001S, U16-ch121, adc0

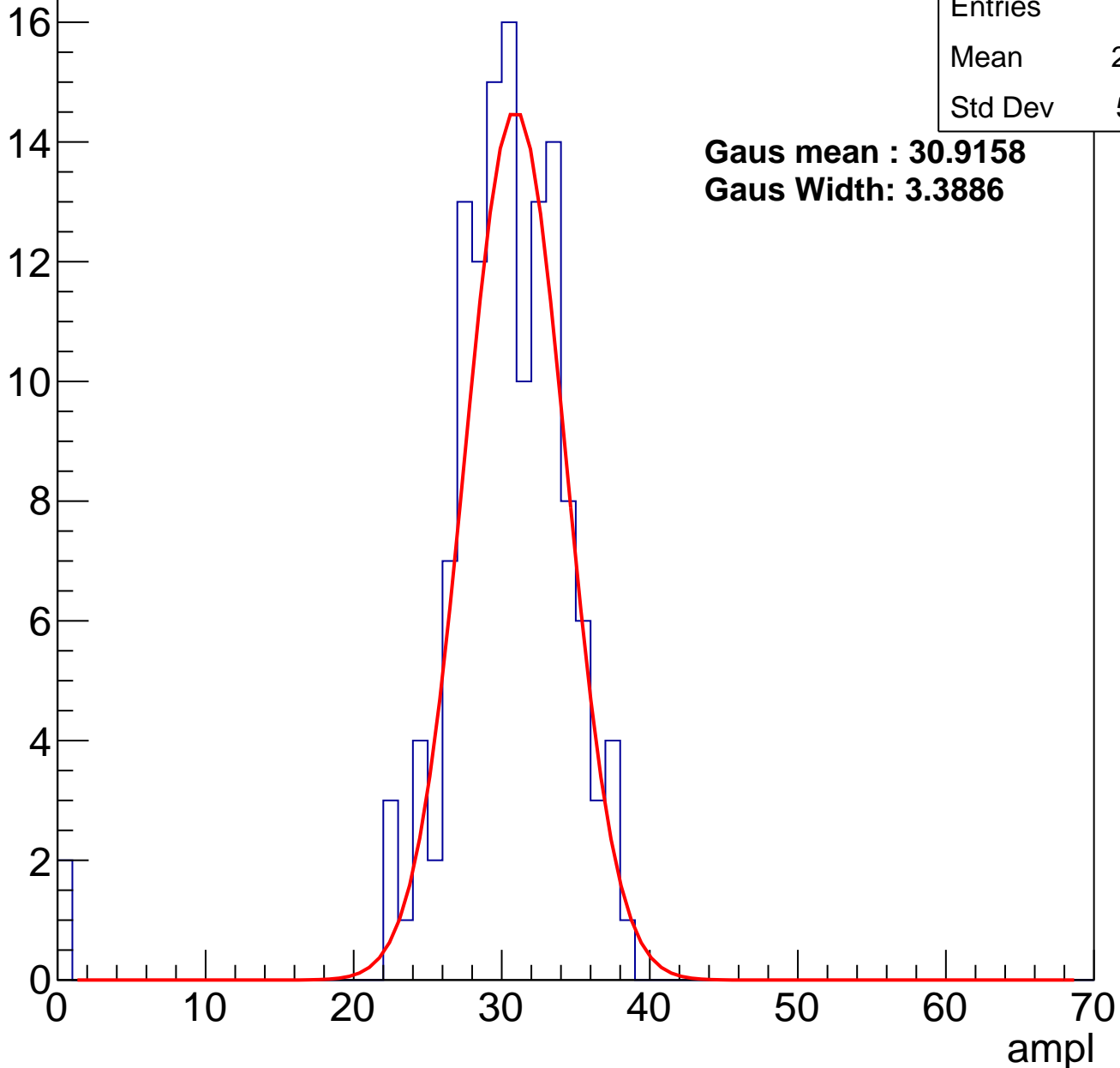
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	29.72
Std Dev	5.001

Gaus mean : 30.9158

Gaus Width: 3.3886

Entry



B1L001S, U16-ch121, adc1

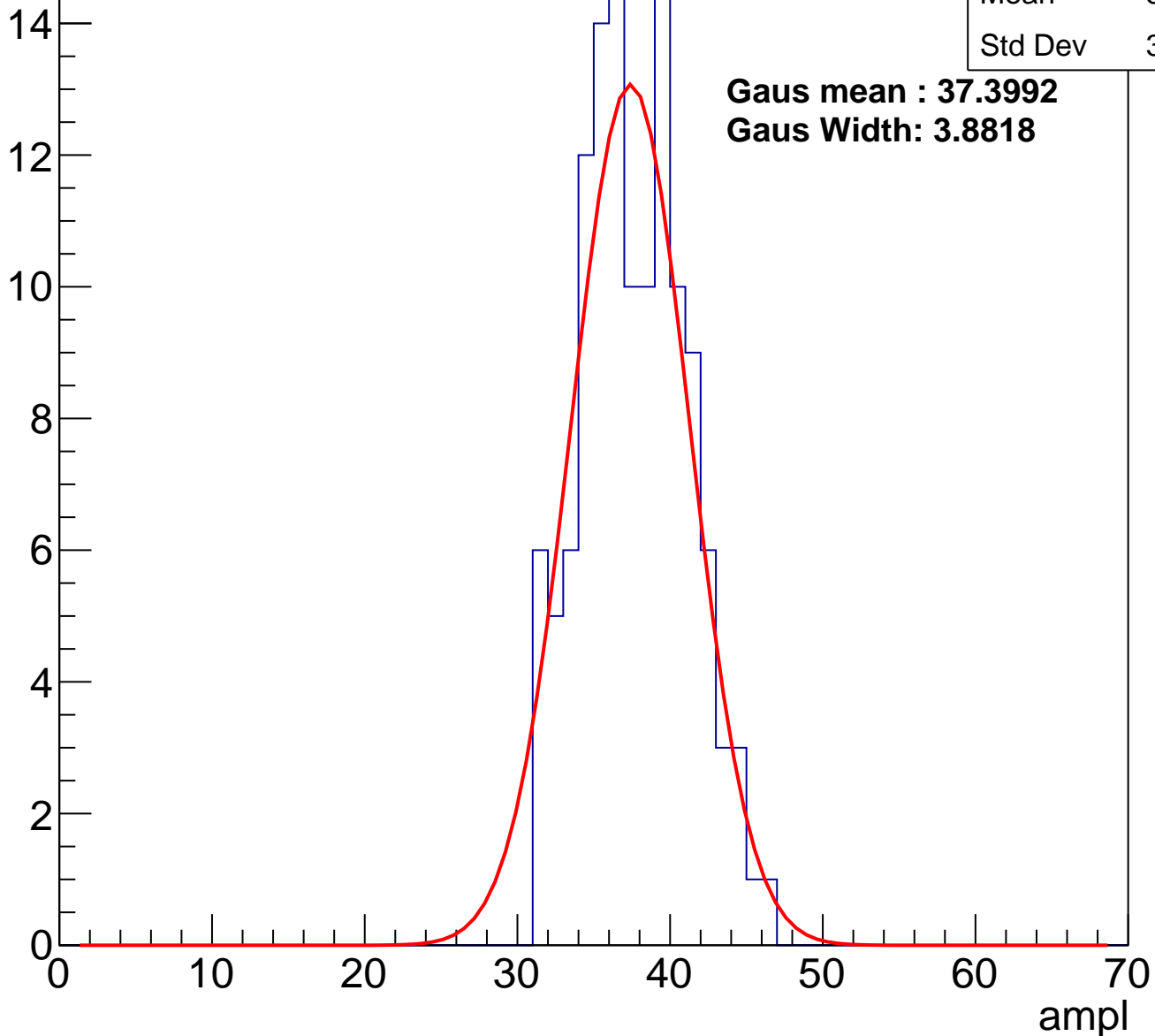
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	37.22
Std Dev	3.408

Gaus mean : 37.3992

Gaus Width: 3.8818

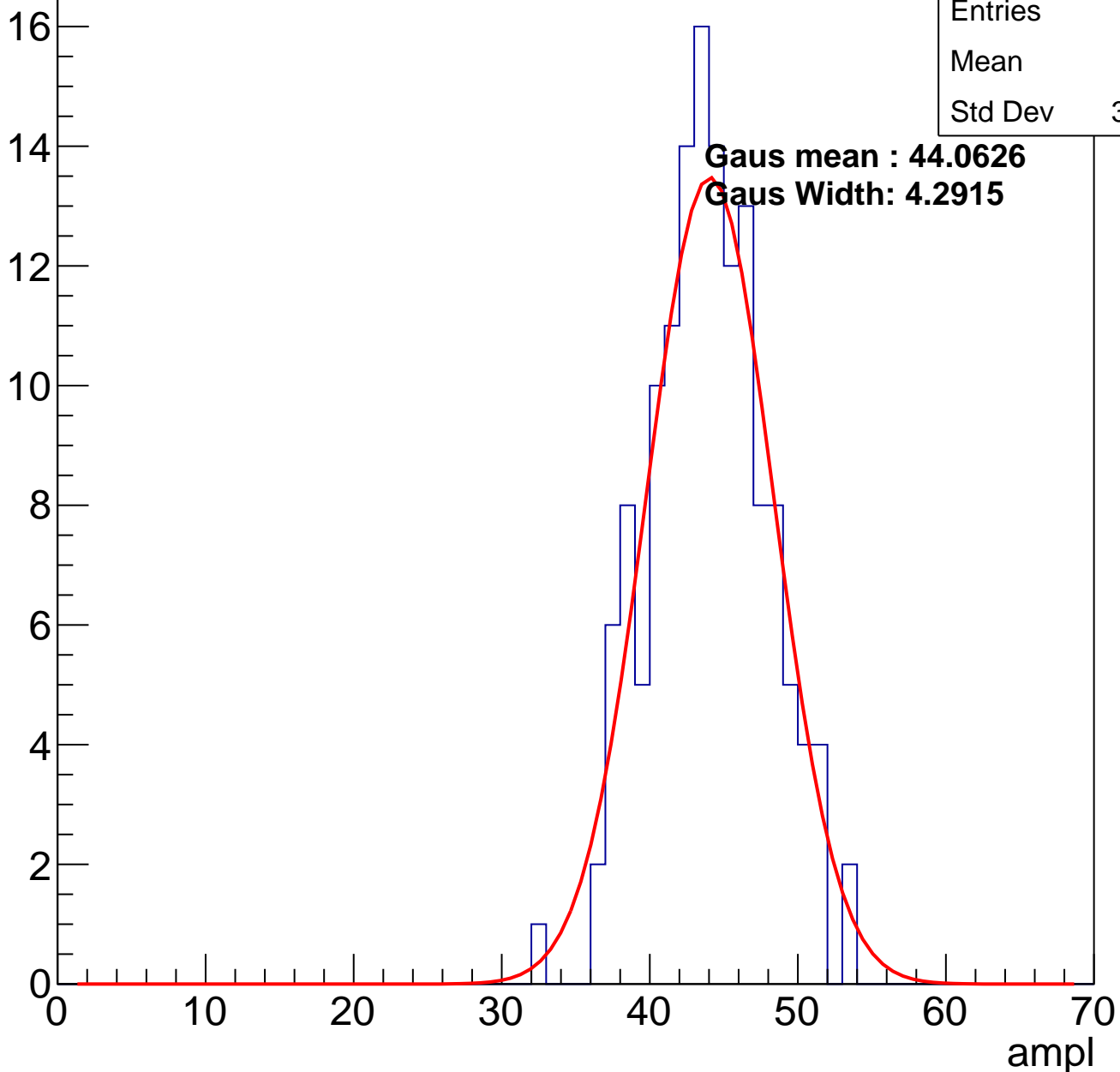
Entry



B1L001S, U16-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

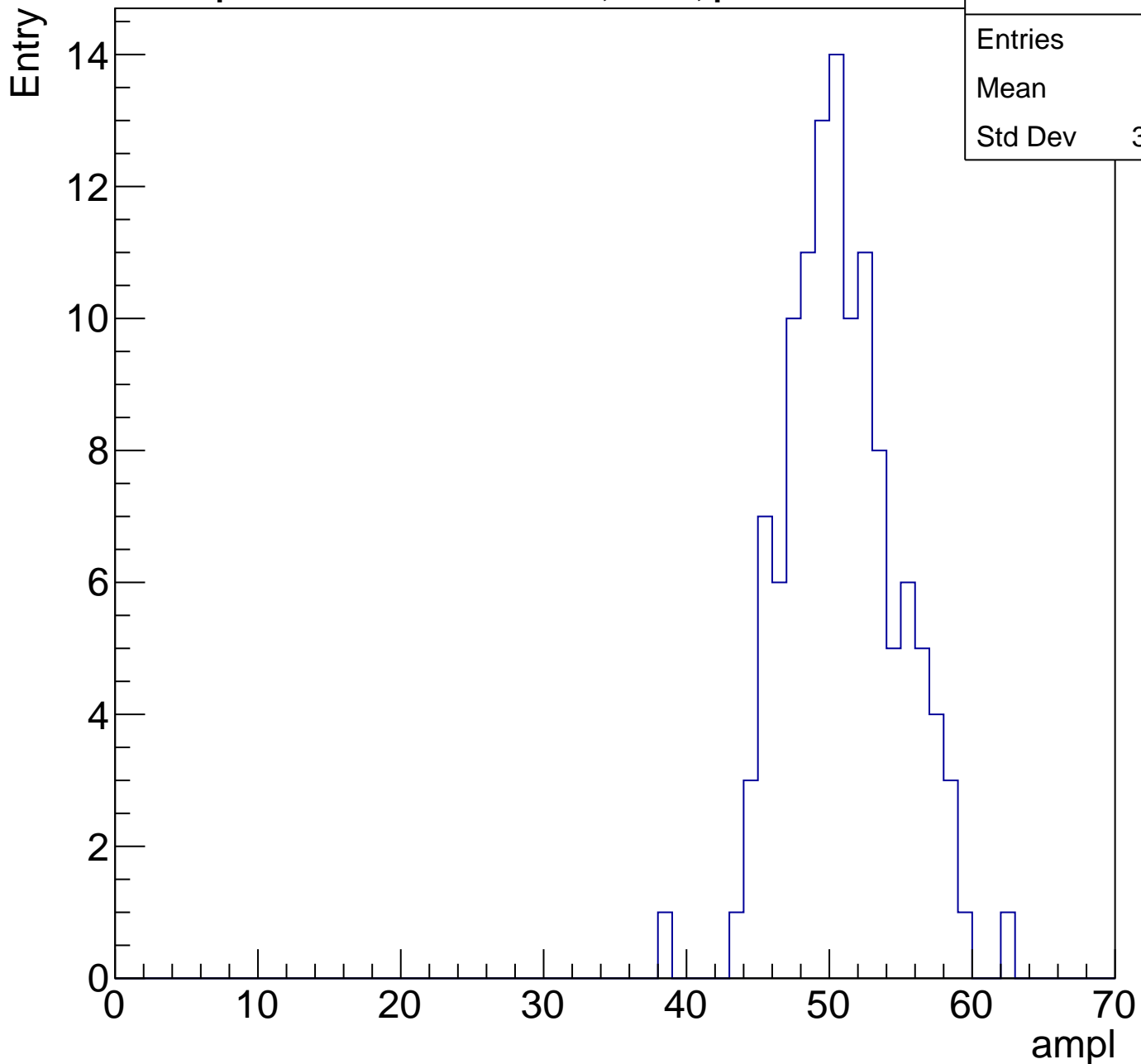
Entry



B1L001S, U16-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

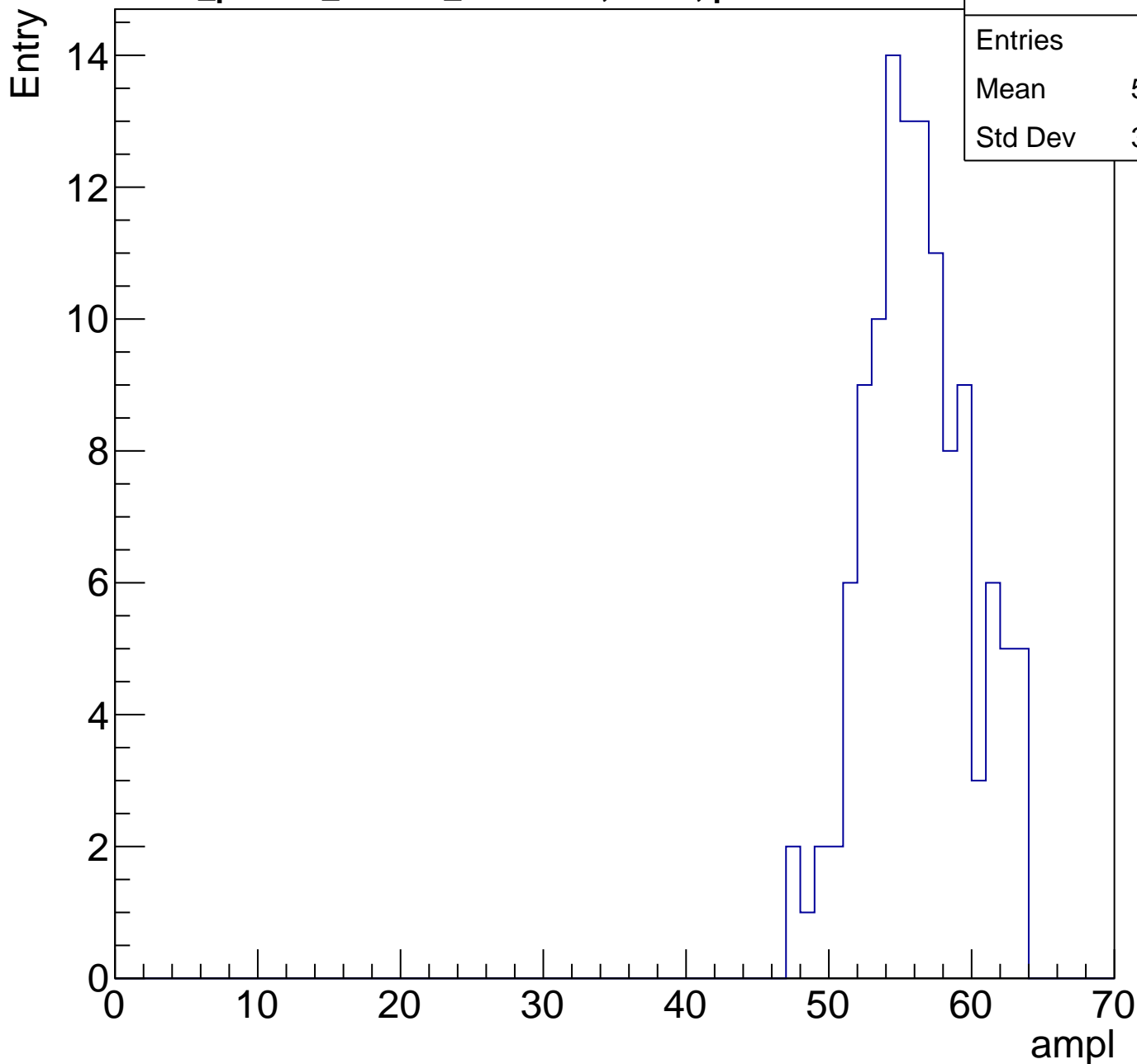
Entries	120
Mean	50.4
Std Dev	3.925



B1L001S, U16-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.76
Std Dev	3.655



B1L001S, U16-ch121, adc5

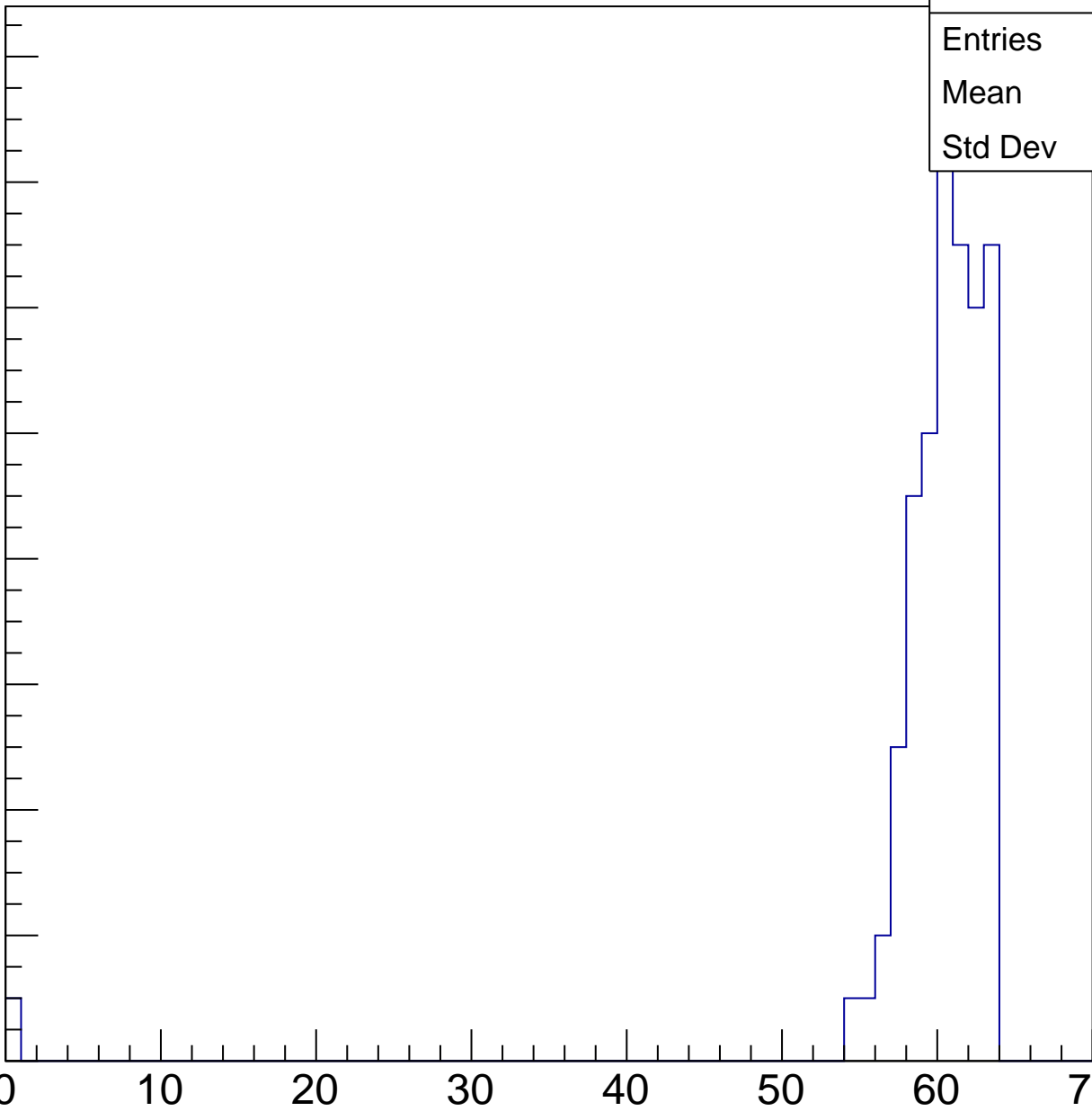
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.45
Std Dev	6.887

ampl



B1L001S, U16-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

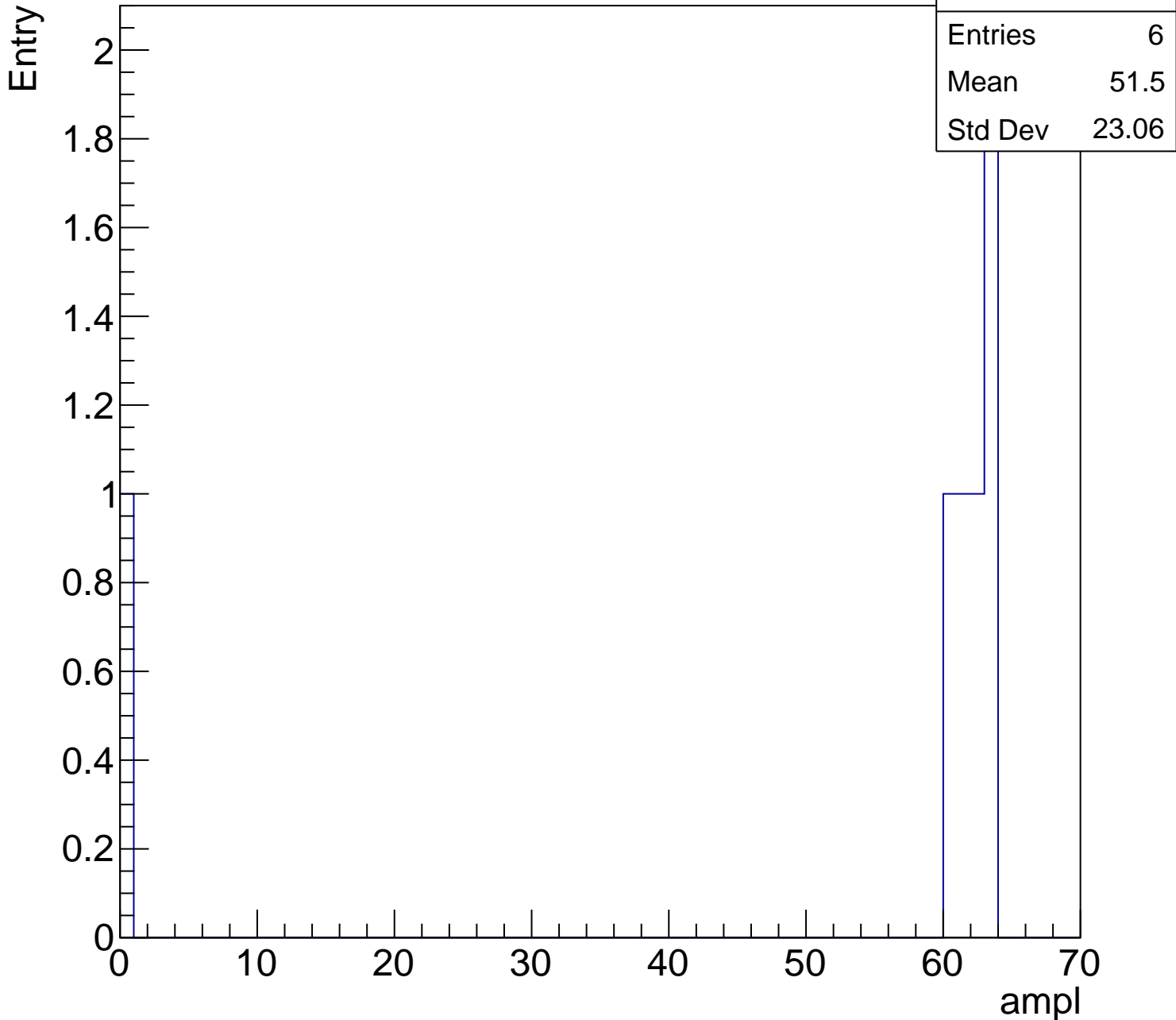
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.06

ampl

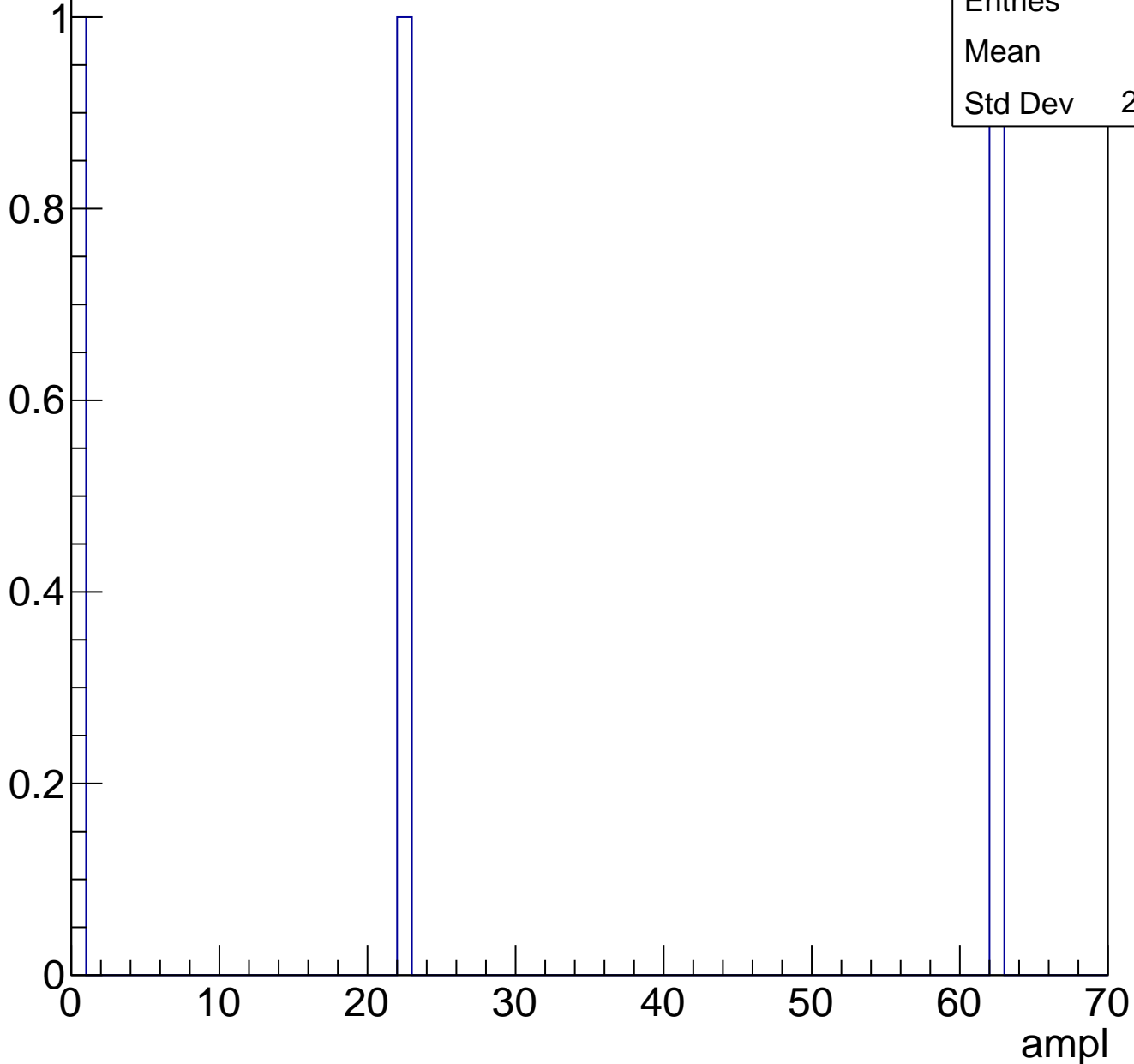
0 10 20 30 40 50 60 70



B1L001S, U16-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	25.66

B1L001S, U16-ch122, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.34
Std Dev	4.816

Gaus mean : 30.7015

Gaus Width: 3.2717

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

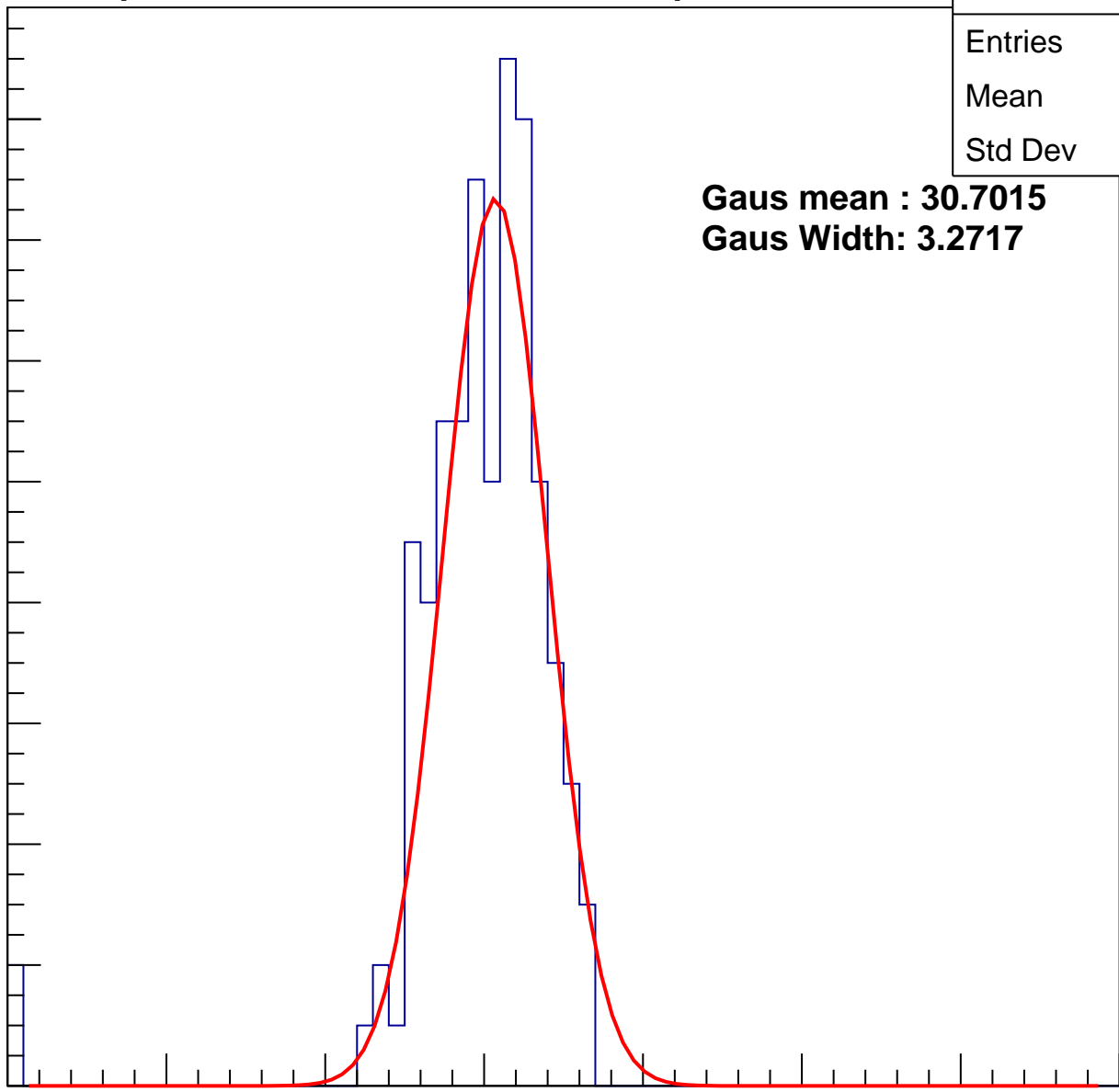
40

50

60

70

ampl



B1L001S, U16-ch122, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.1
Std Dev	4.728

Gaus mean : 36.9963
Gaus Width: 3.3737

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

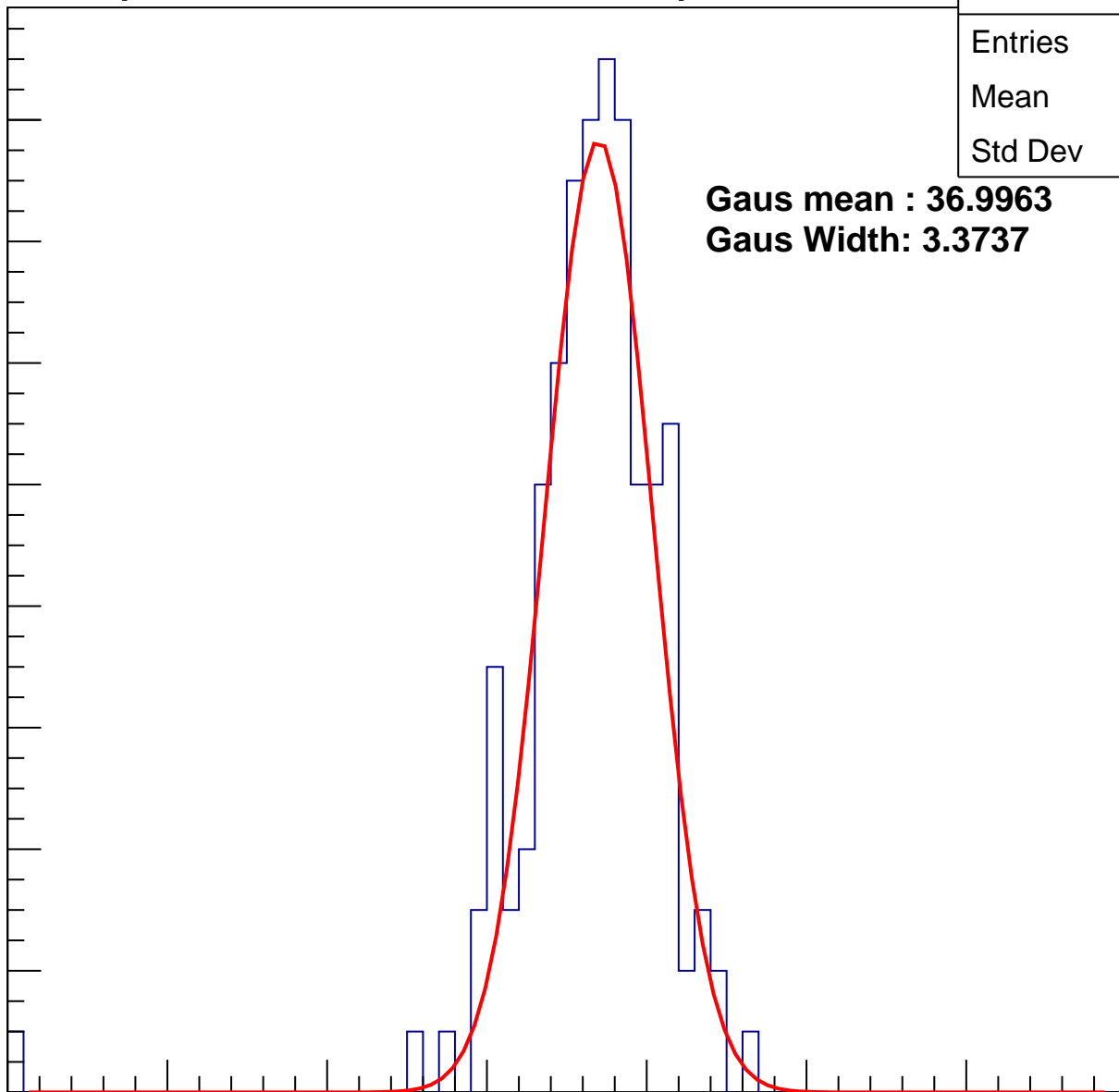
40

50

60

70

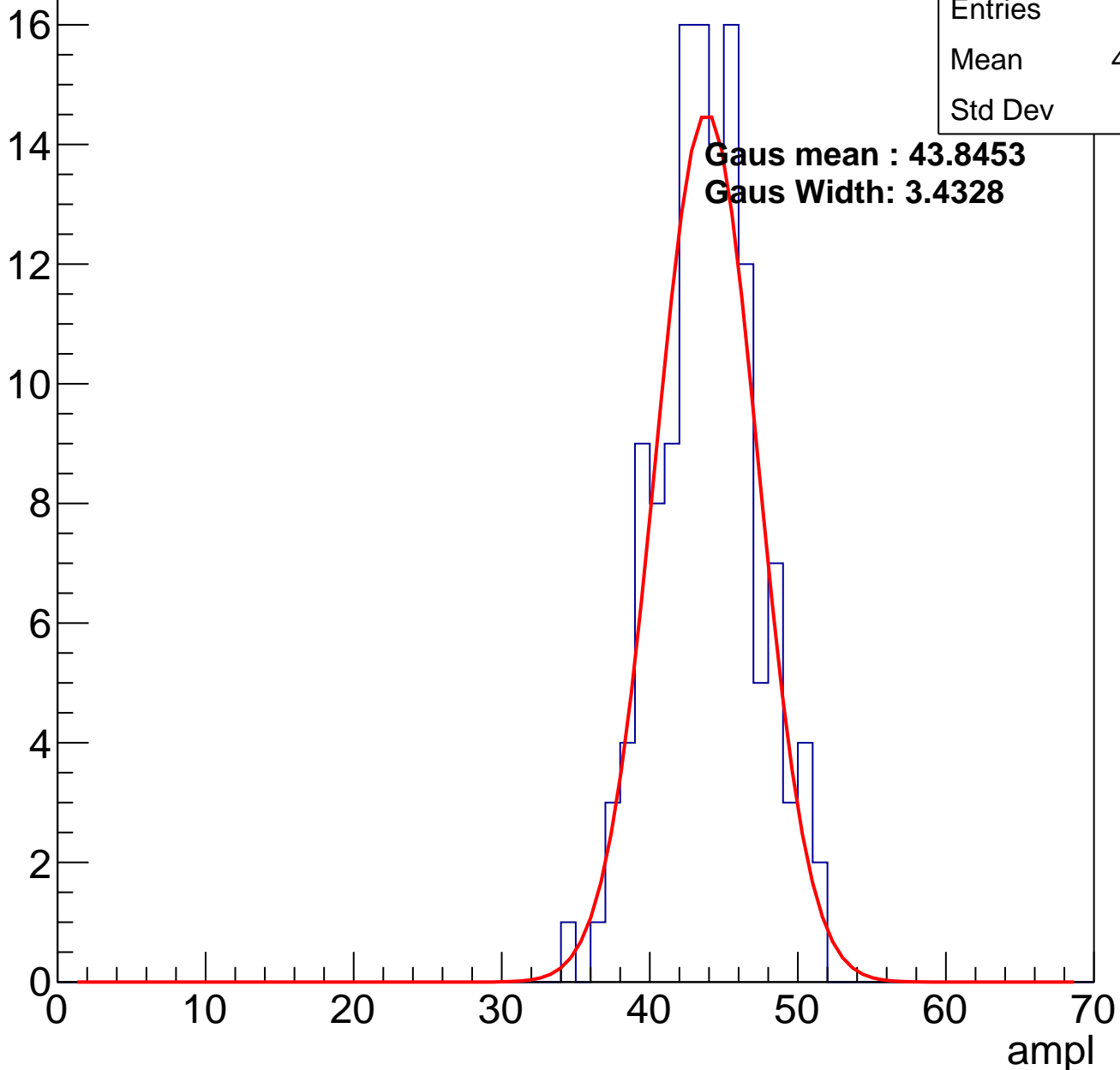
ampl



B1L001S, U16-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

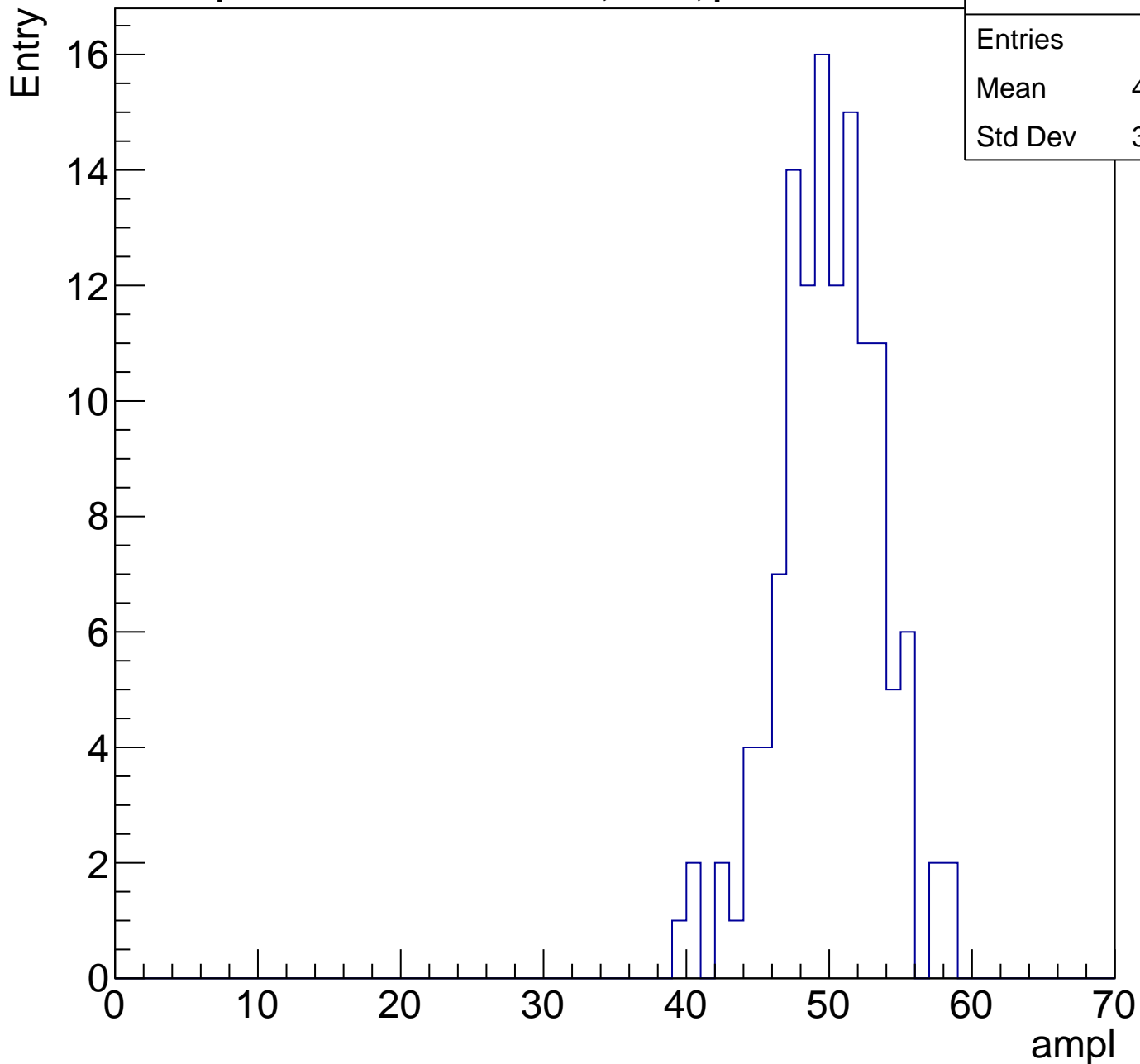
Entry



B1L001S, U16-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	49.54
Std Dev	3.584



B1L001S, U16-ch122, adc4

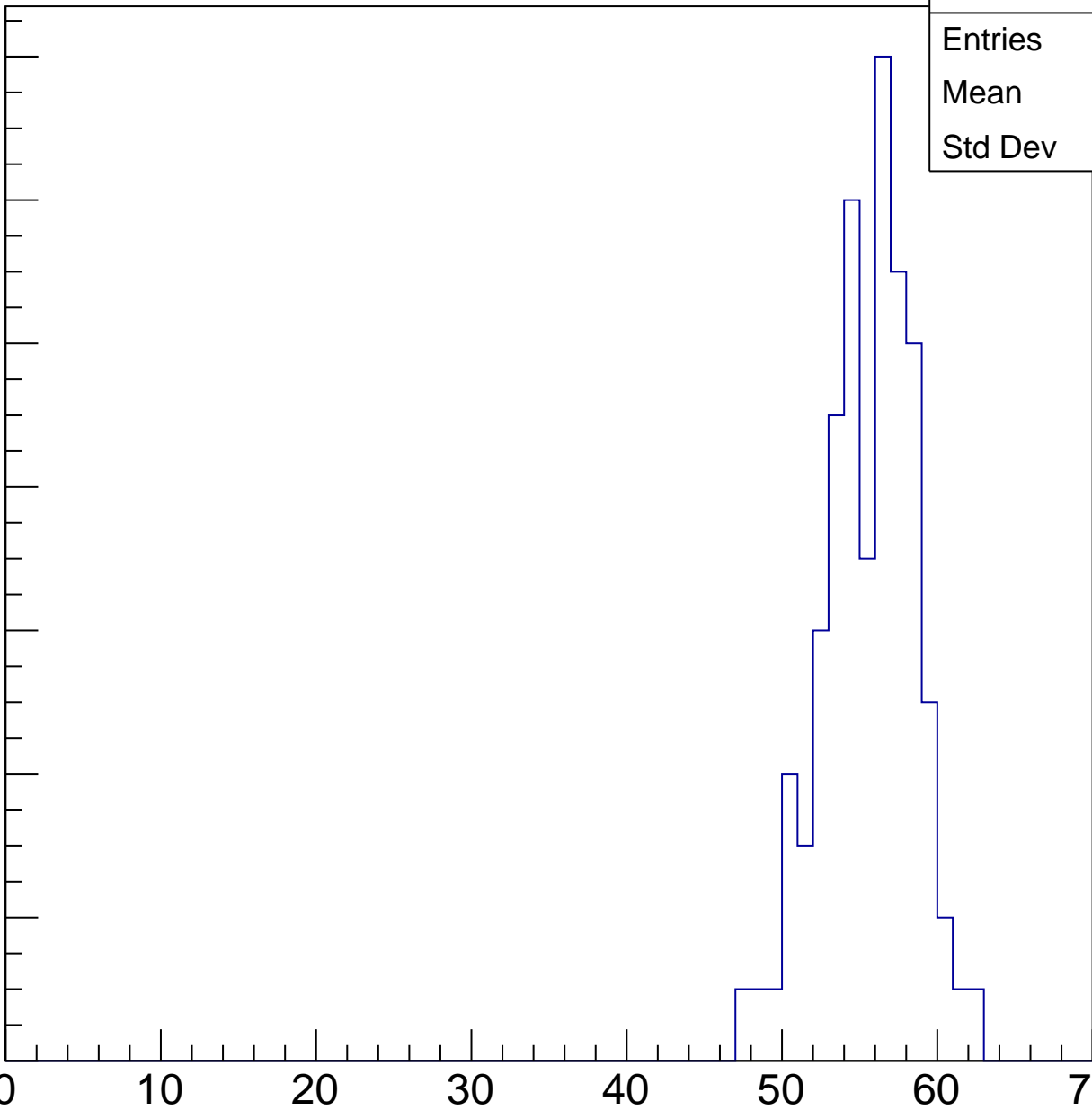
calib_packv5_042523_0143.root, FC#2, port C2

Entries	88
Mean	55.09
Std Dev	2.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U16-ch122, adc5

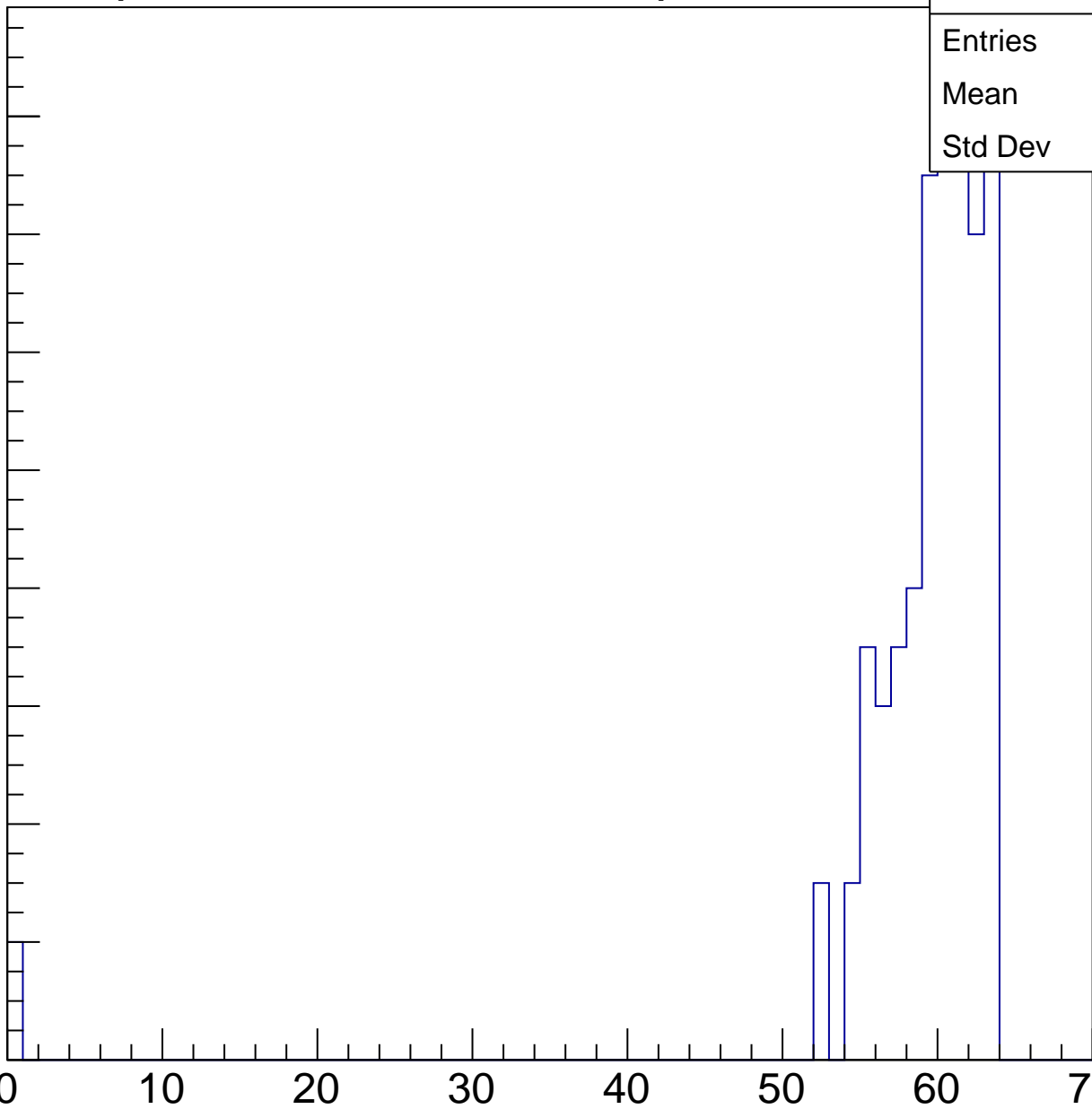
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	58.42
Std Dev	8.279

ampl

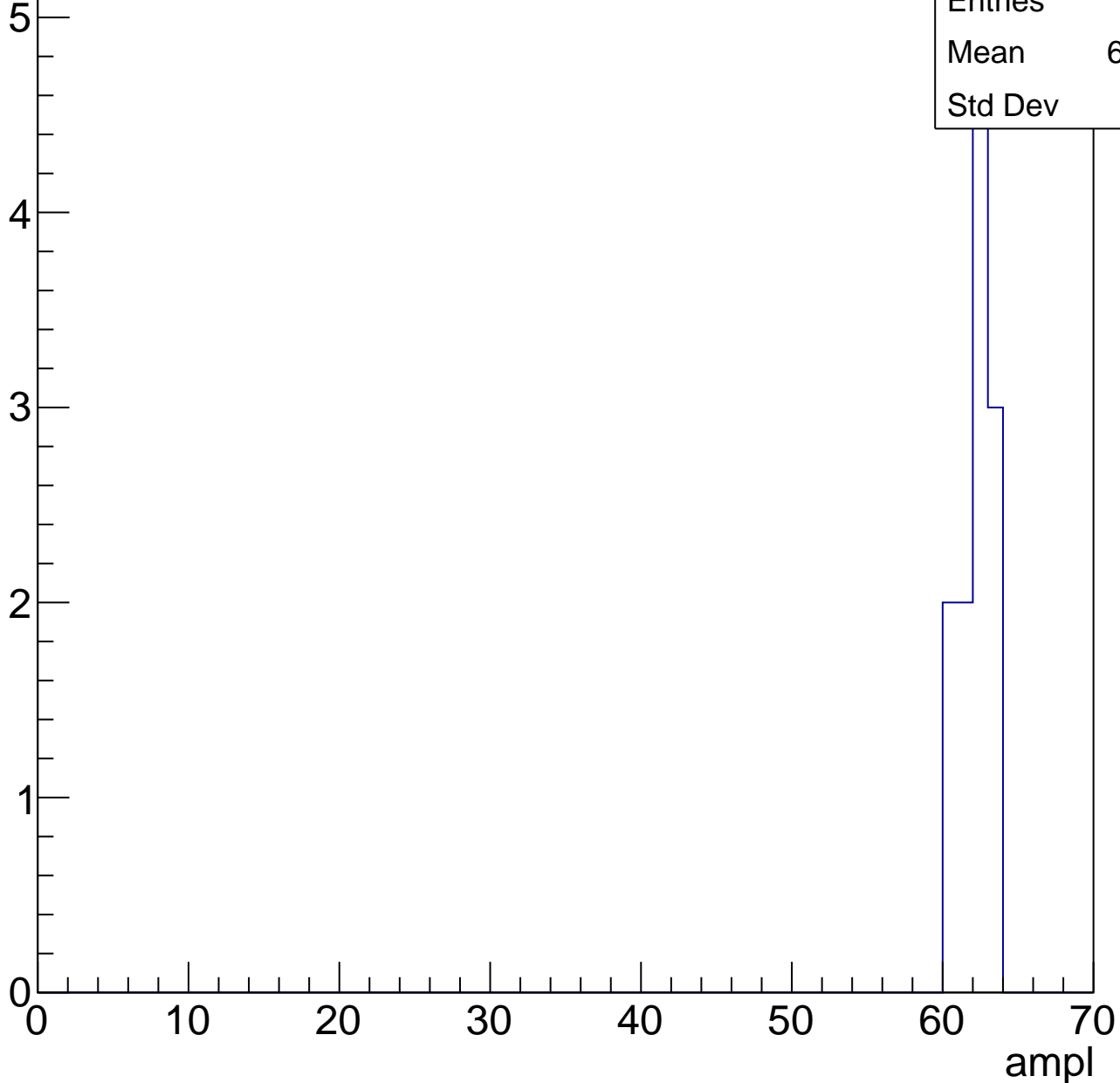


B1L001S, U16-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.01



B1L001S, U16-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch123, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	28.07
Std Dev	3.956

Gaus mean : 28.6969

Gaus Width: 2.8146

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

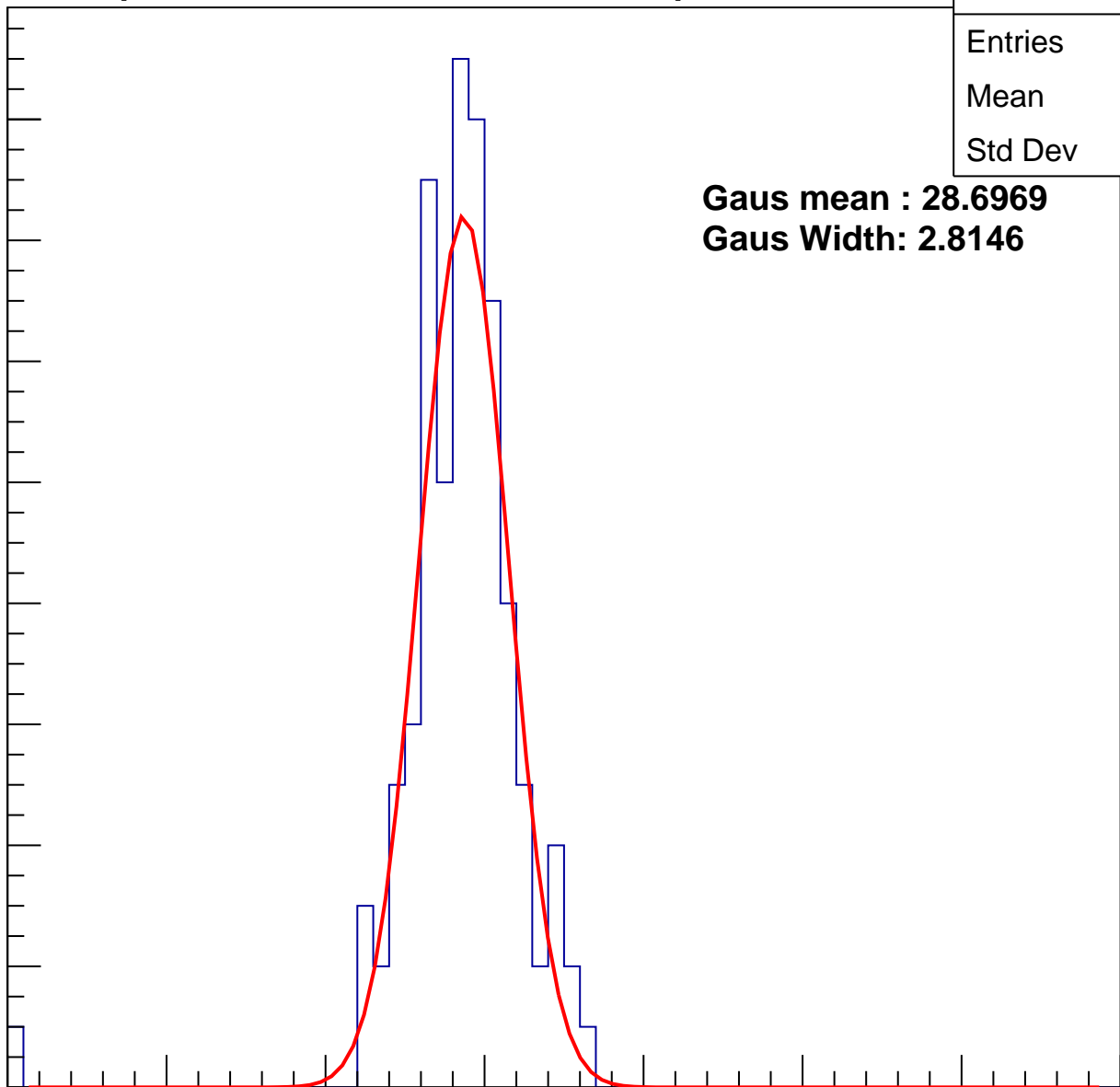
40

50

60

70

ampl



B1L001S, U16-ch123, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	35.17
Std Dev	3.848

Gaus mean : 36.2009

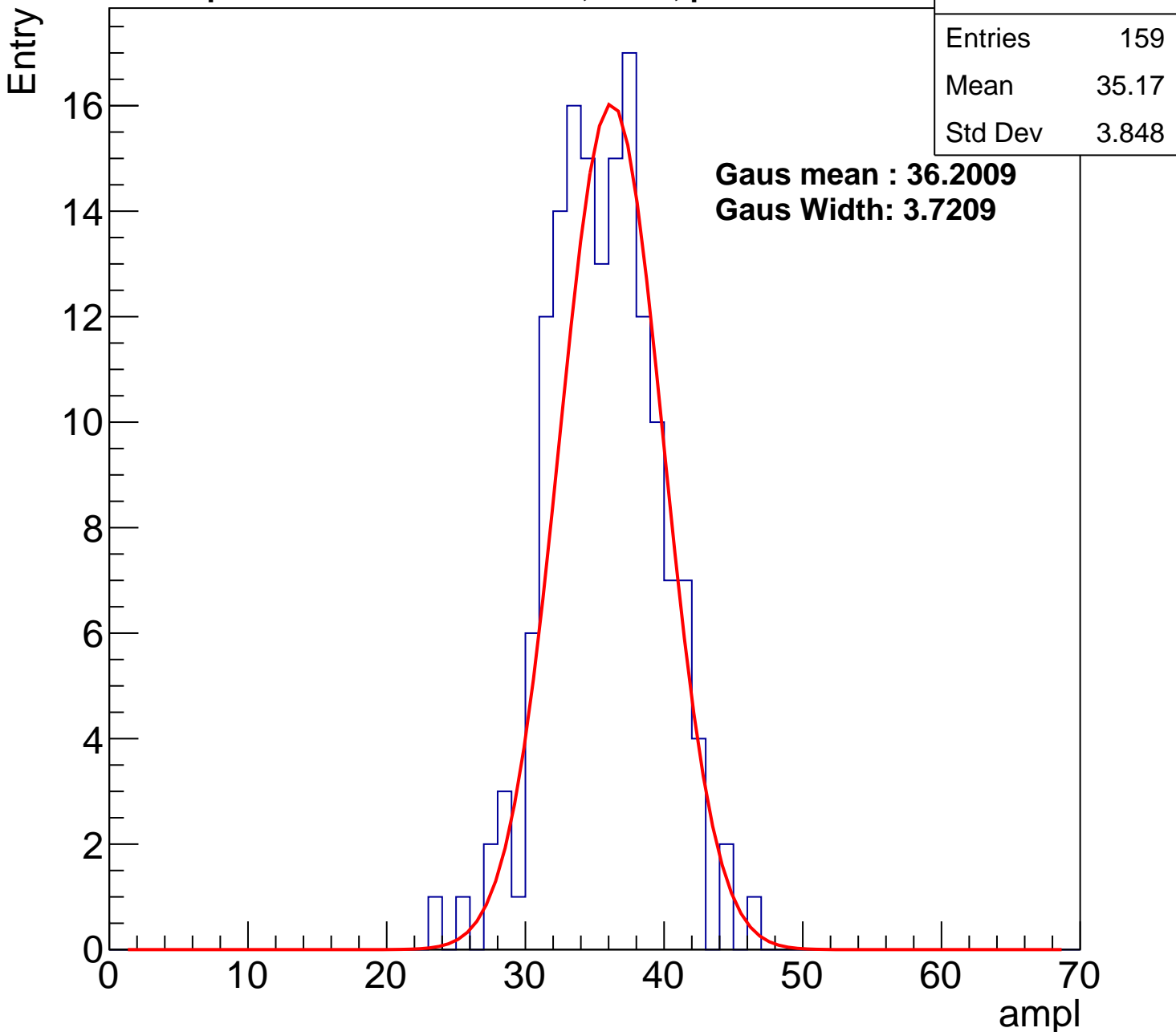
Gaus Width: 3.7209

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

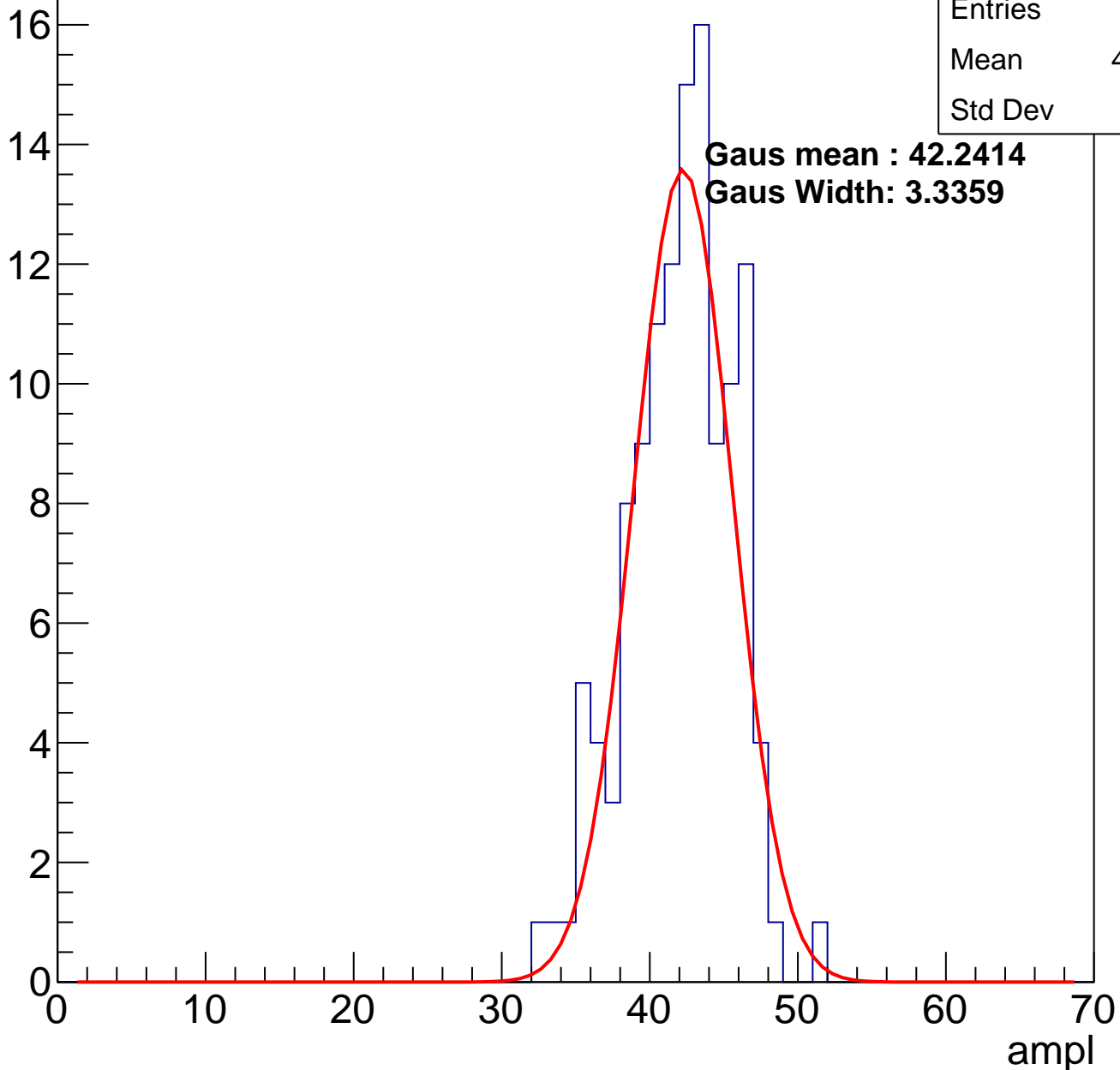
ampl



B1L001S, U16-ch123, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

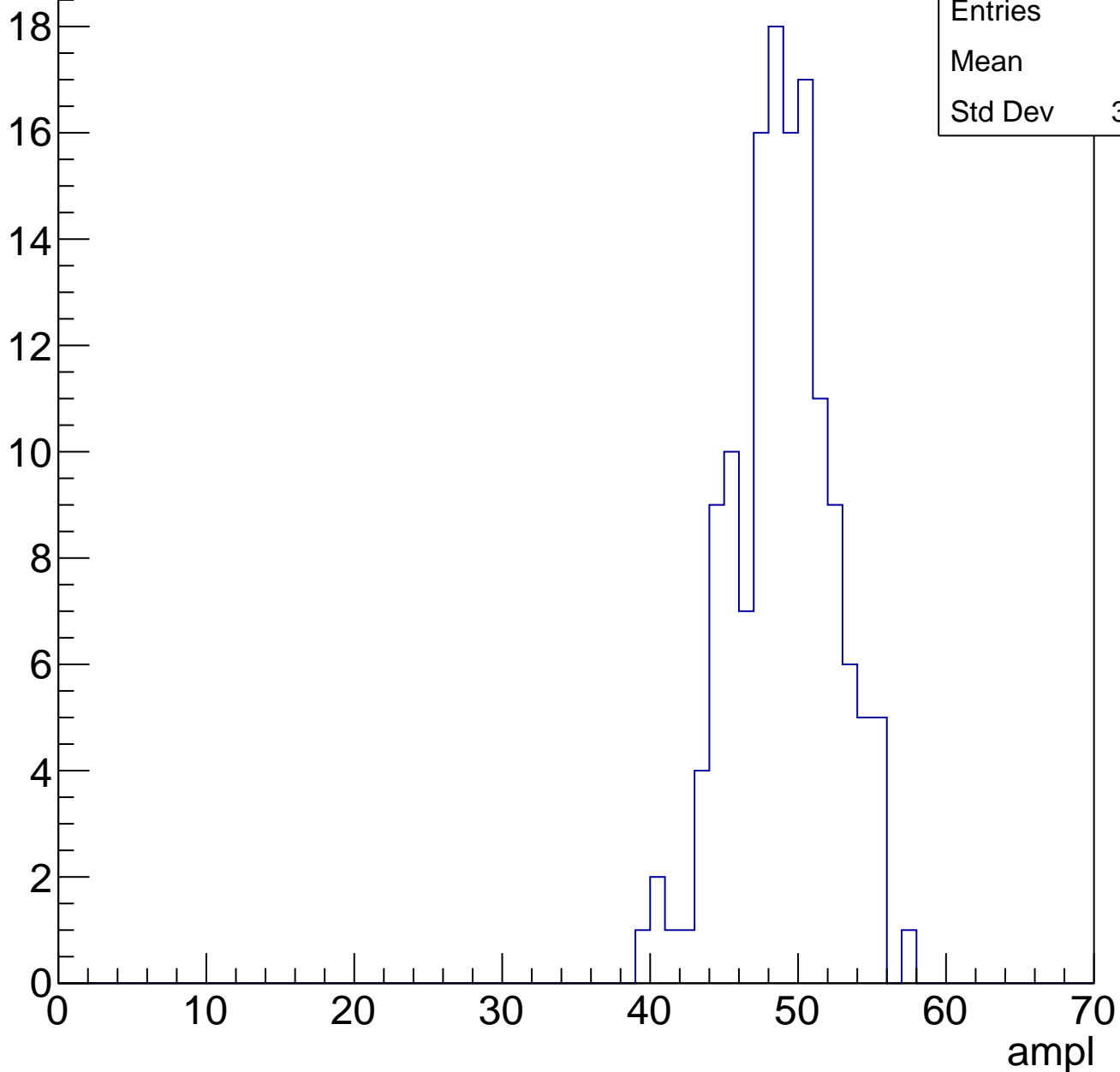


B1L001S, U16-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.5
Std Dev	3.415

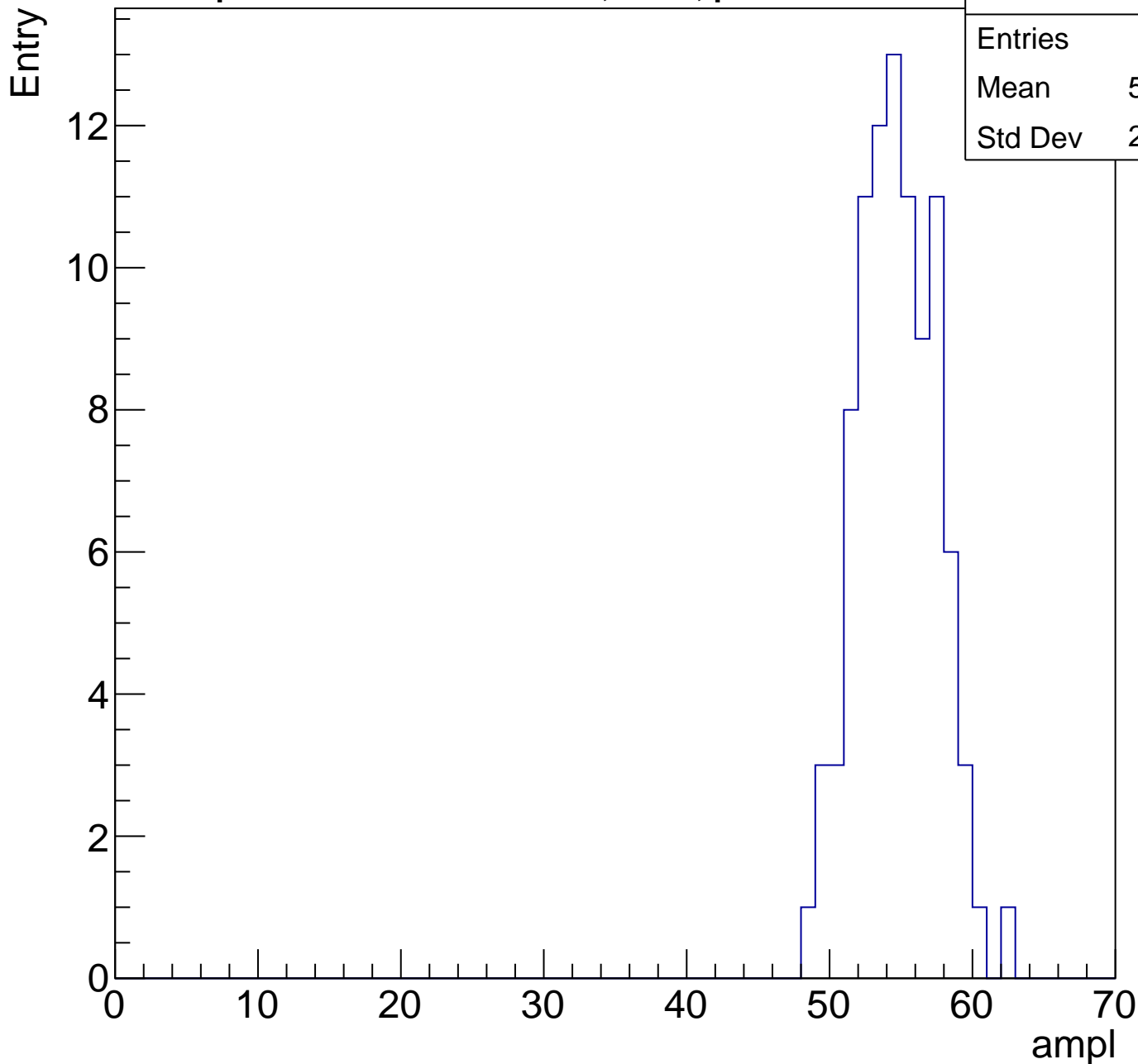
Entry



B1L001S, U16-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

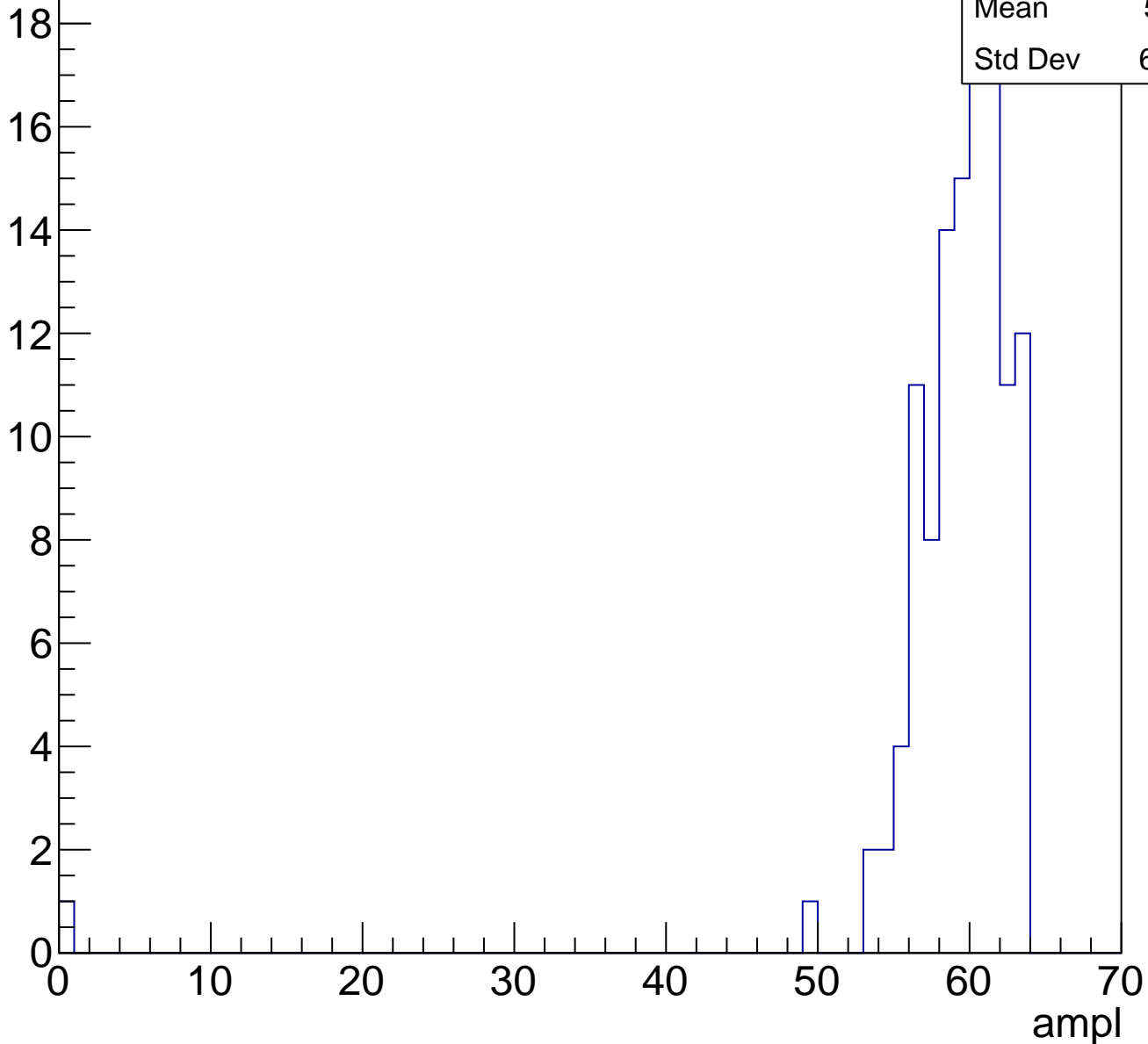
Entries	93
Mean	54.26
Std Dev	2.739



B1L001S, U16-ch123, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

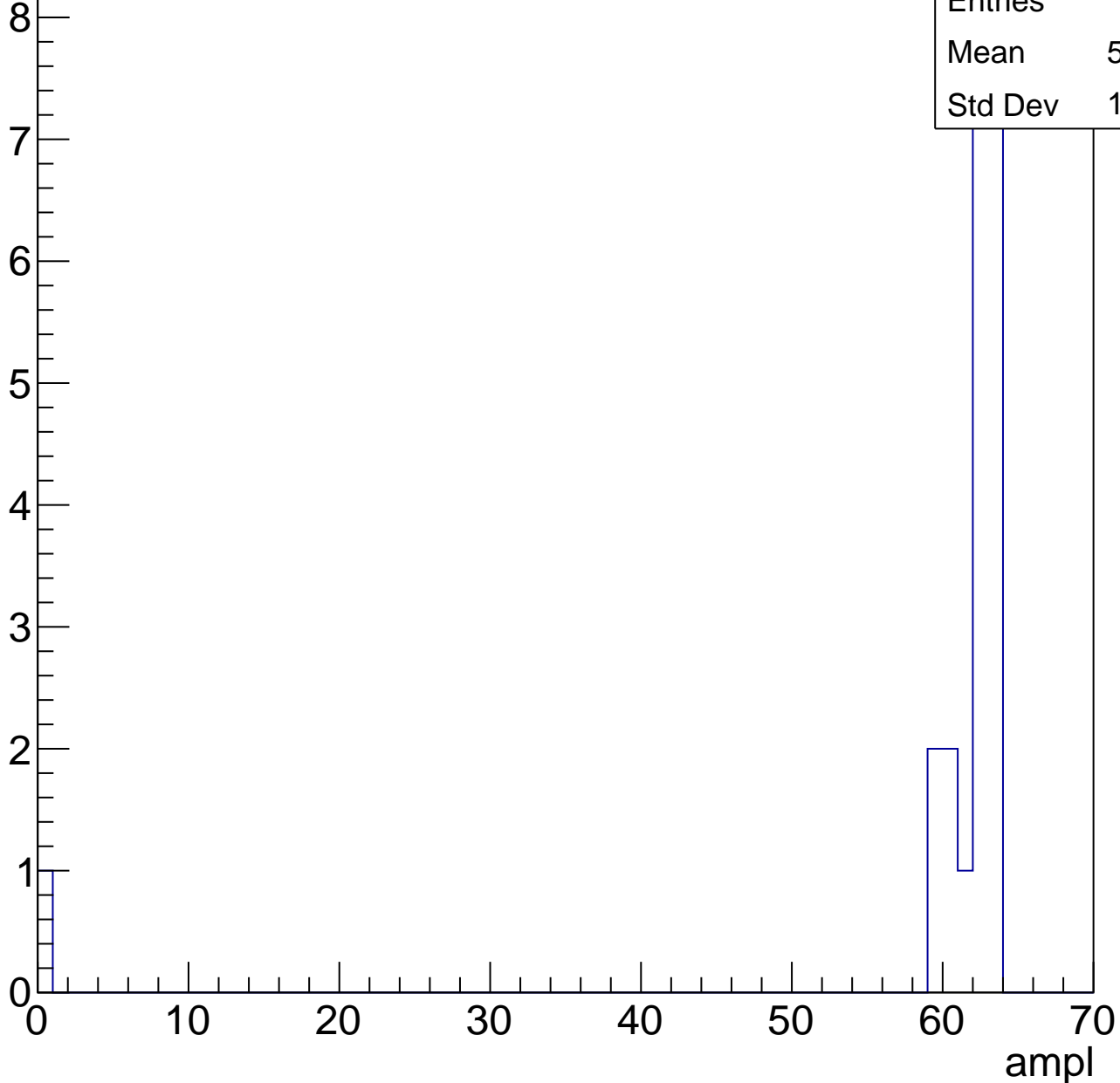


B1L001S, U16-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

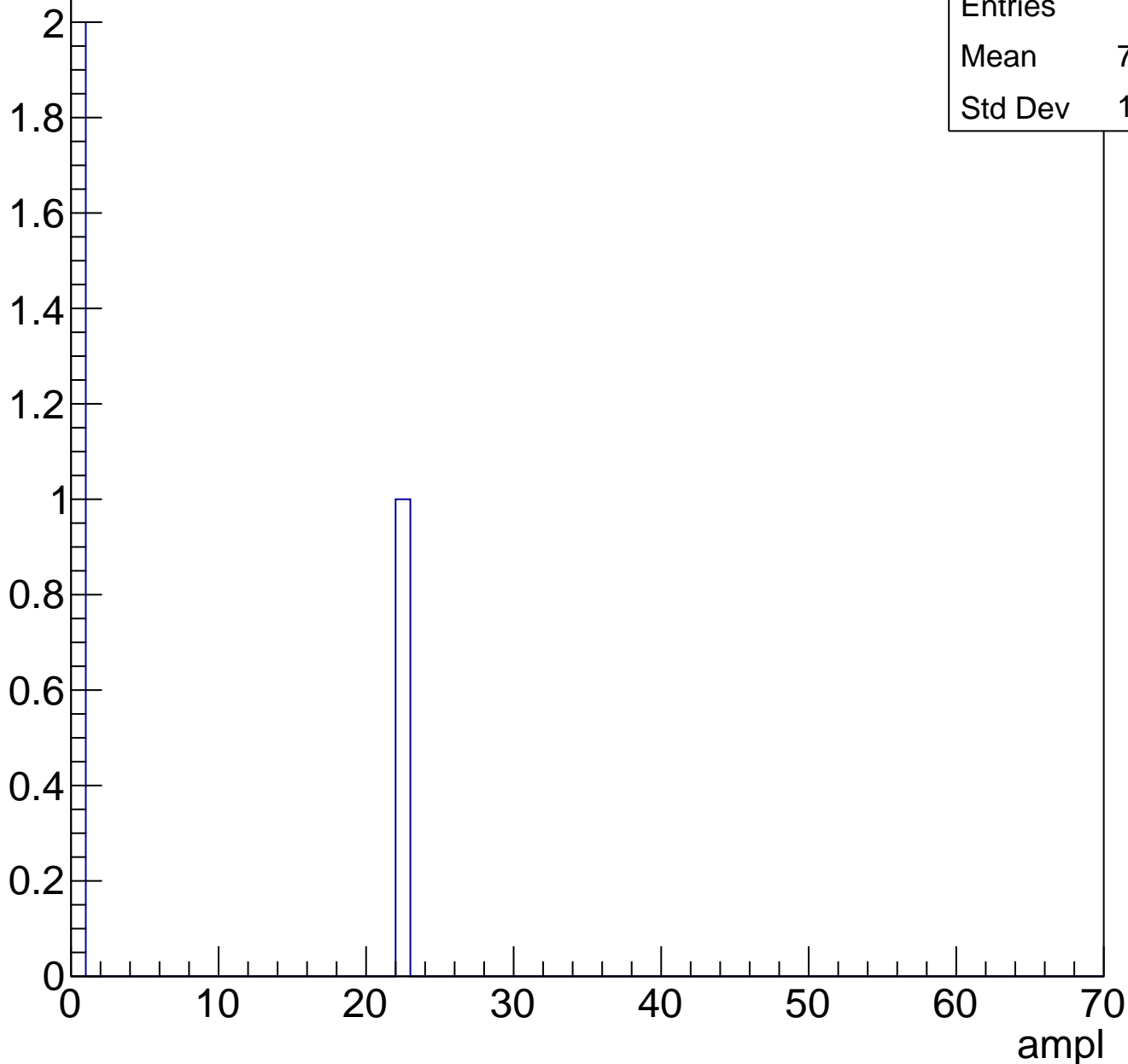
Entries	22
Mean	59.05
Std Dev	12.95



B1L001S, U16-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

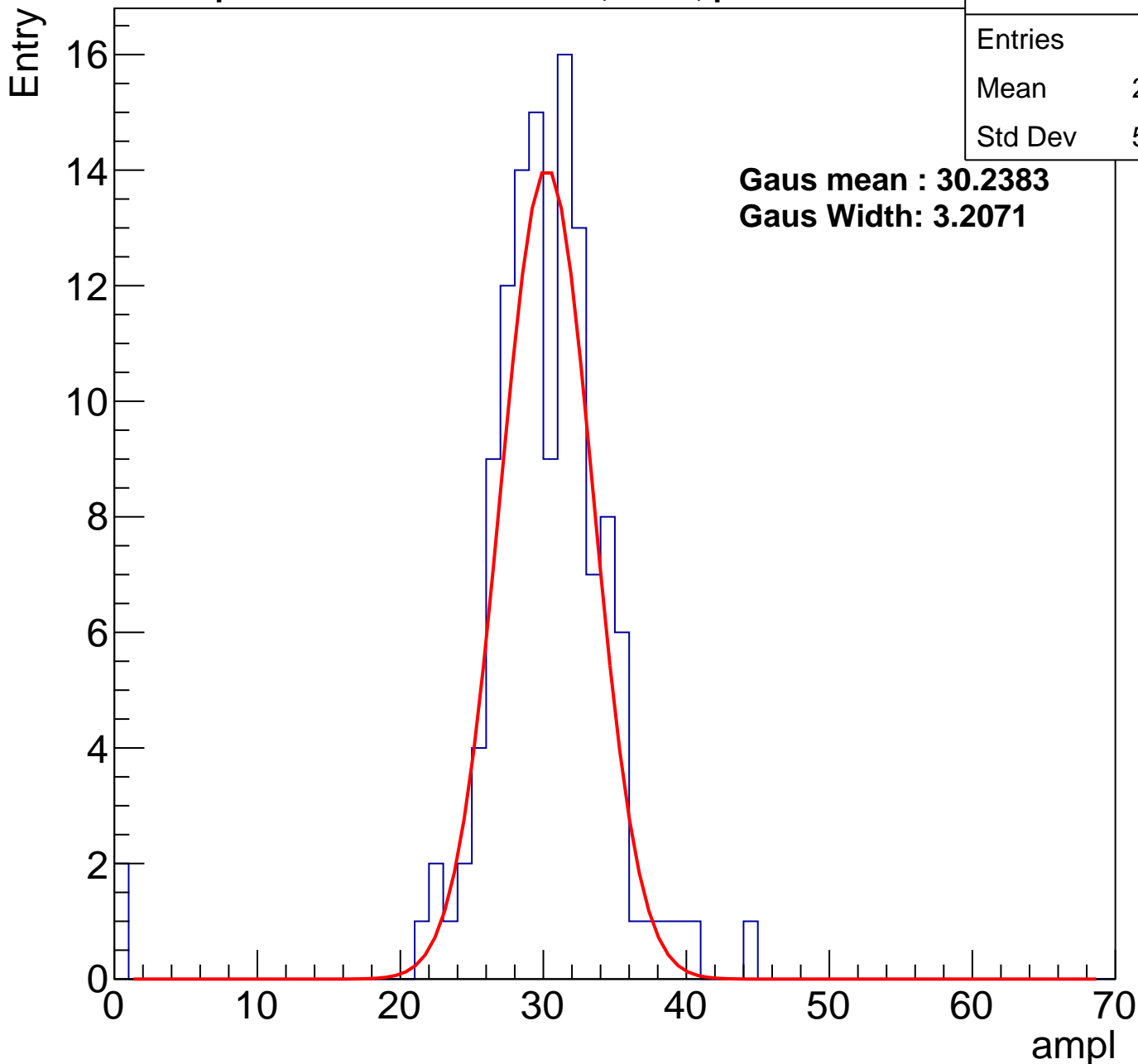
B1L001S, U16-ch124, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	29.53
Std Dev	5.219

Gaus mean : 30.2383

Gaus Width: 3.2071



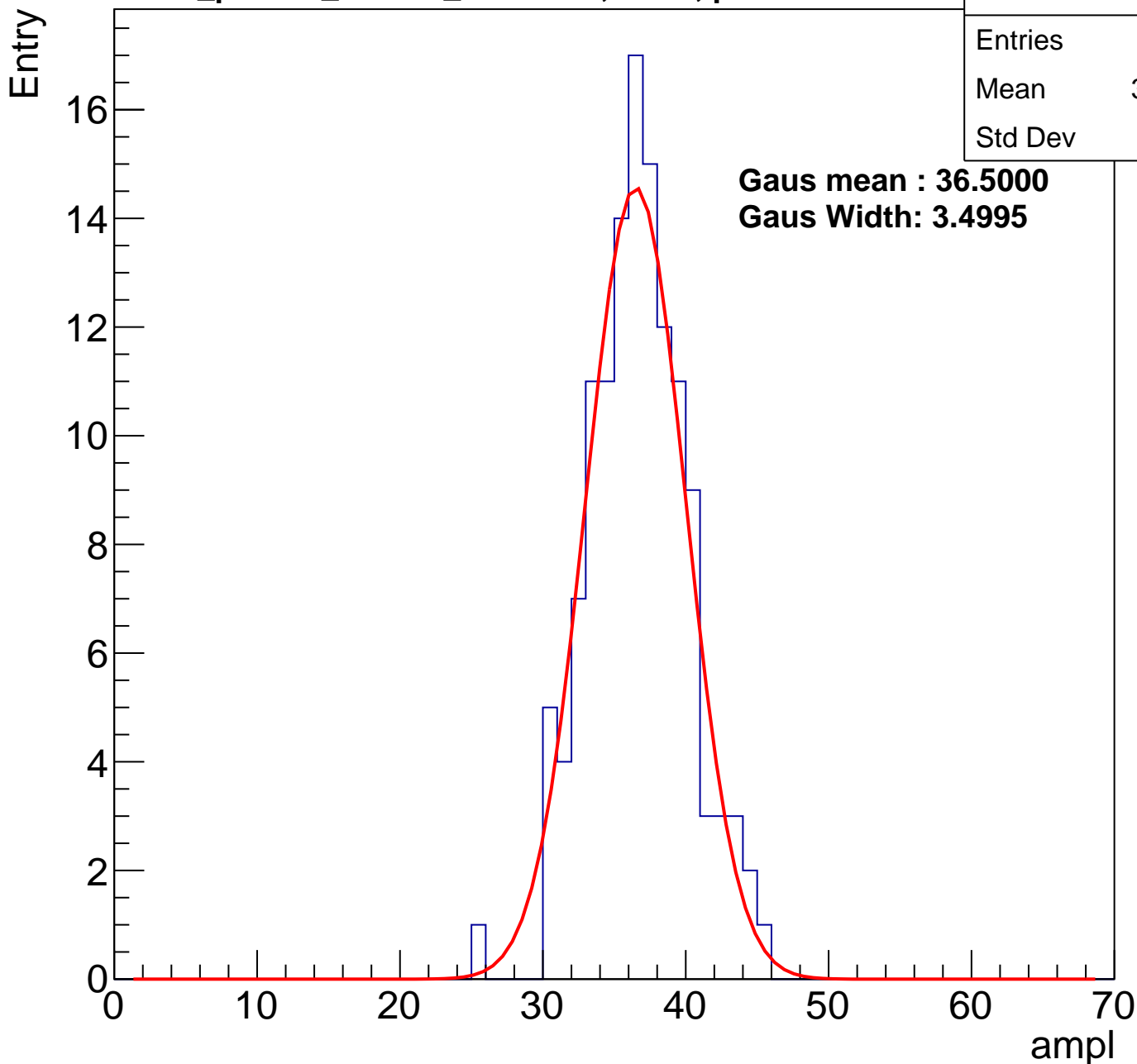
B1L001S, U16-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.22
Std Dev	3.42

Gaus mean : 36.5000

Gaus Width: 3.4995



B1L001S, U16-ch124, adc2

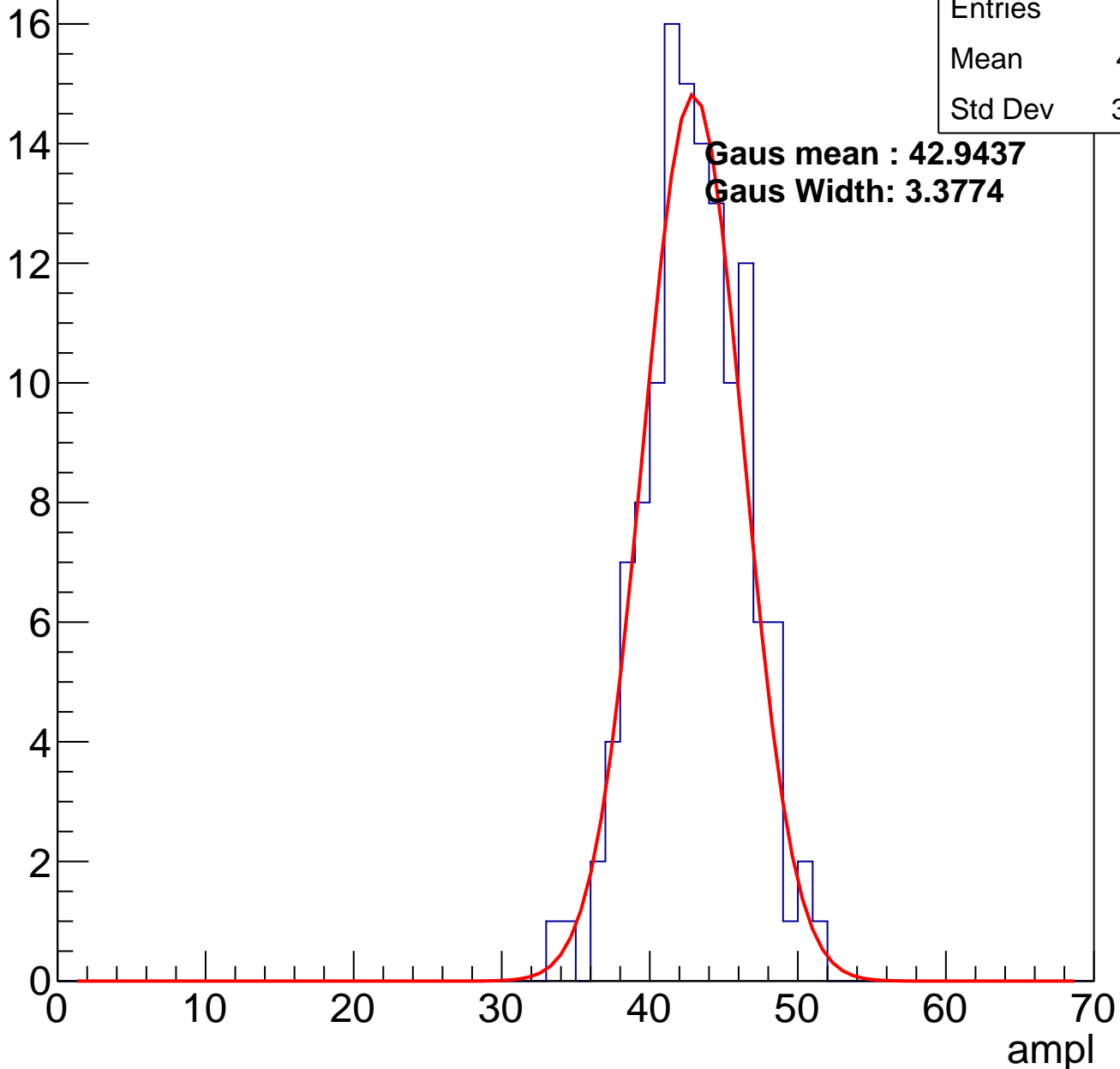
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.61
Std Dev	3.388

Gaus mean : 42.9437

Gaus Width: 3.3774

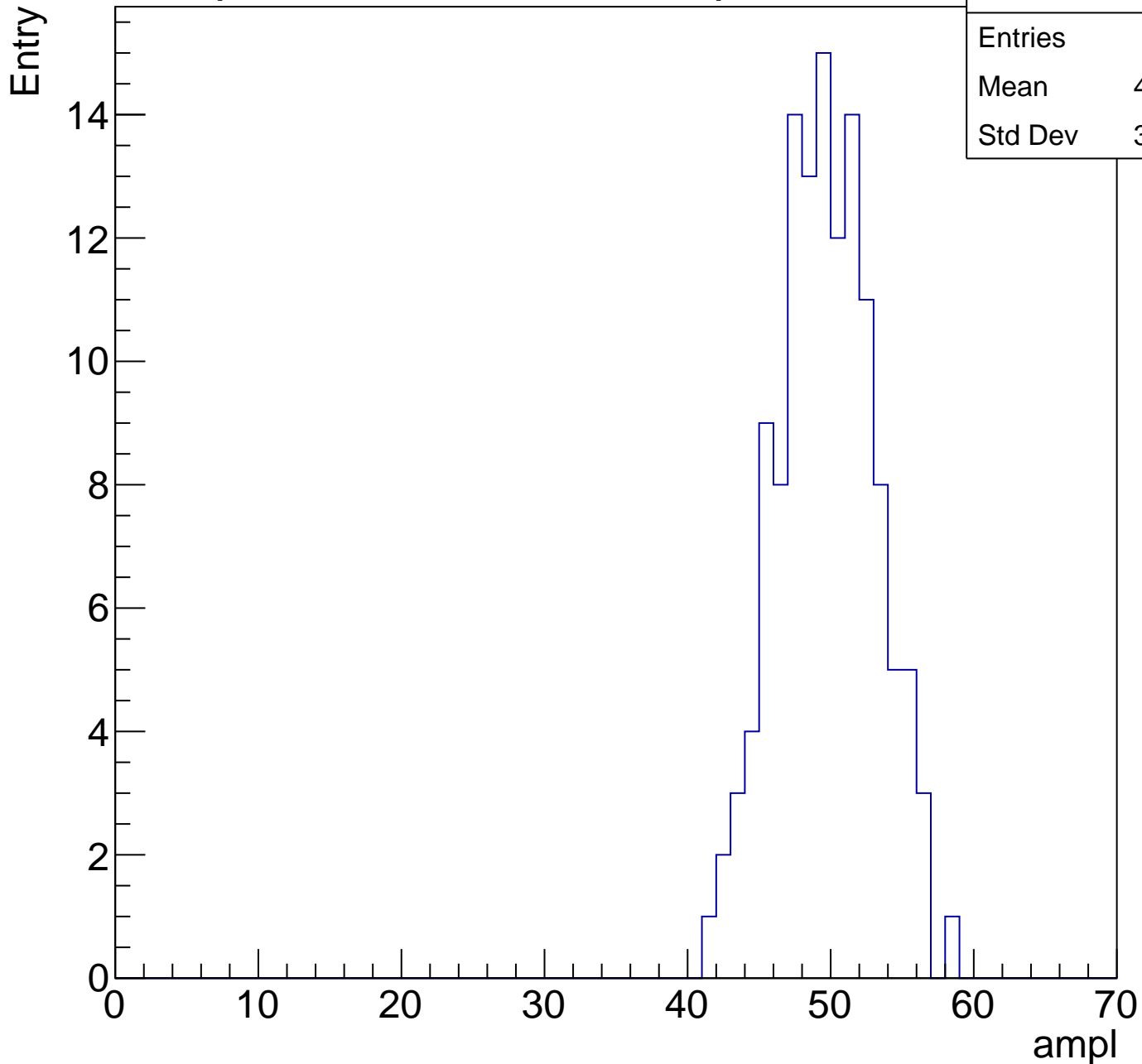
Entry



B1L001S, U16-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	49.23
Std Dev	3.396



B1L001S, U16-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

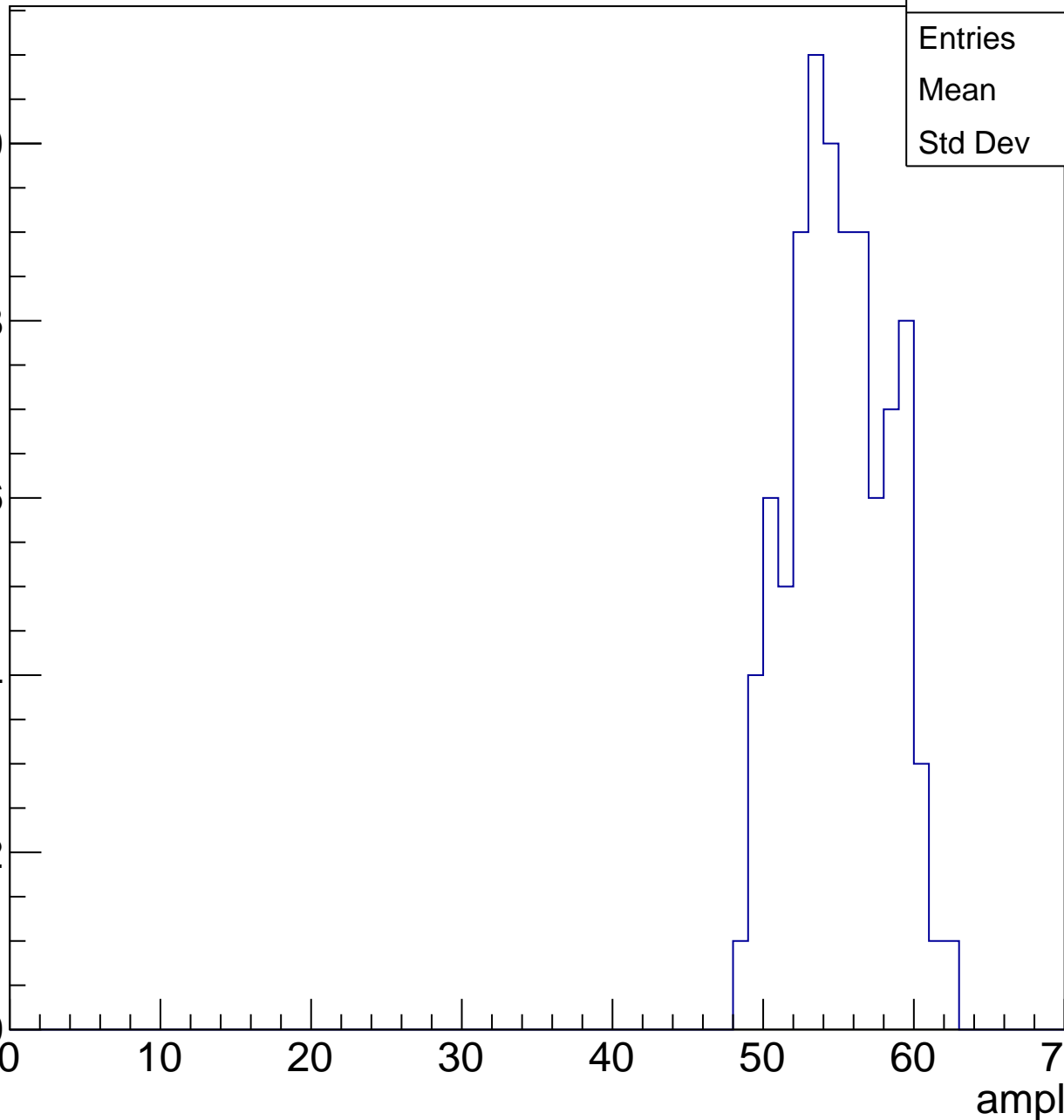
40

50

60

ampl

Entries	90
Mean	54.58
Std Dev	3.208



B1L001S, U16-ch124, adc5

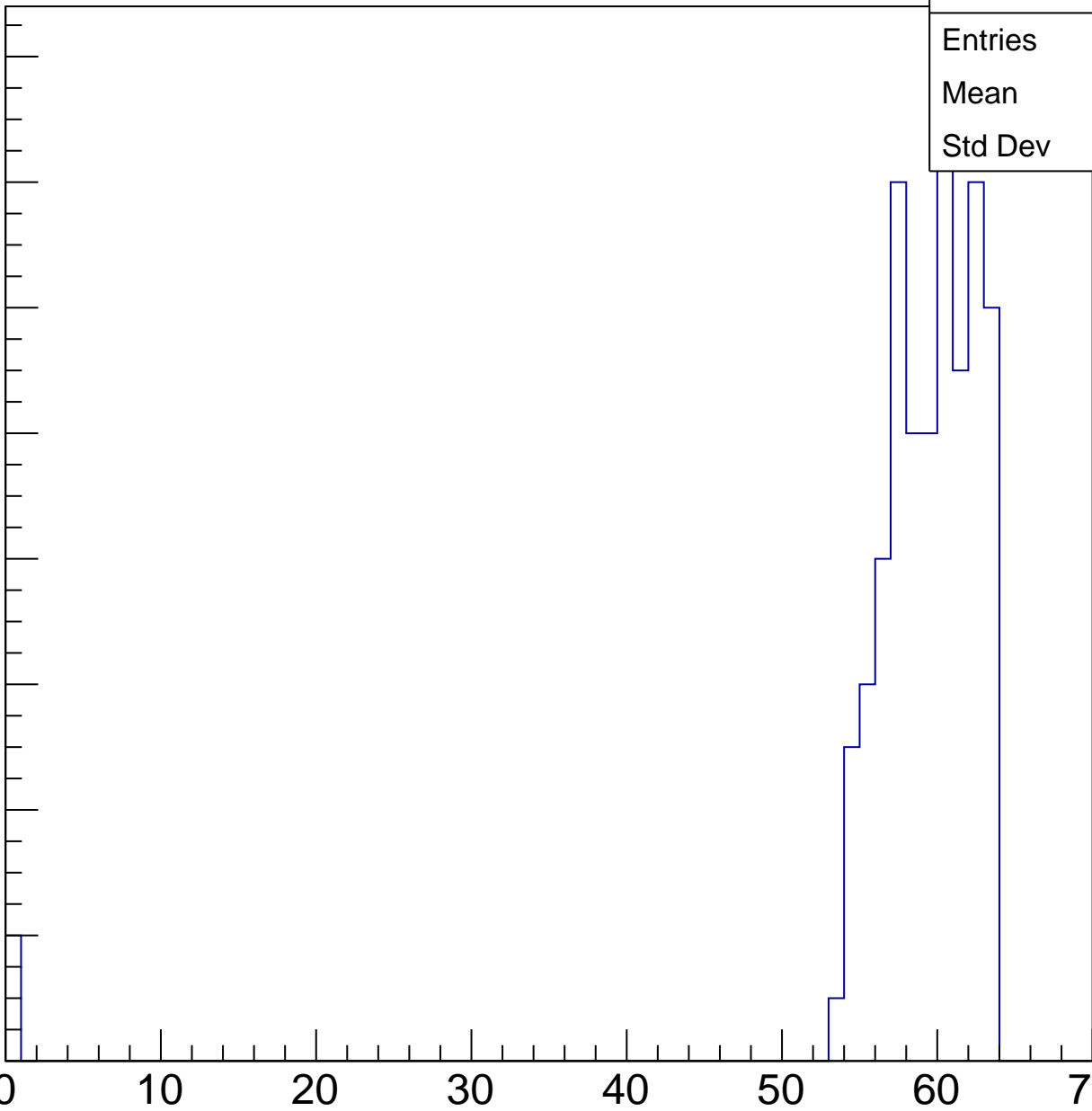
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.02
Std Dev	8.367

ampl

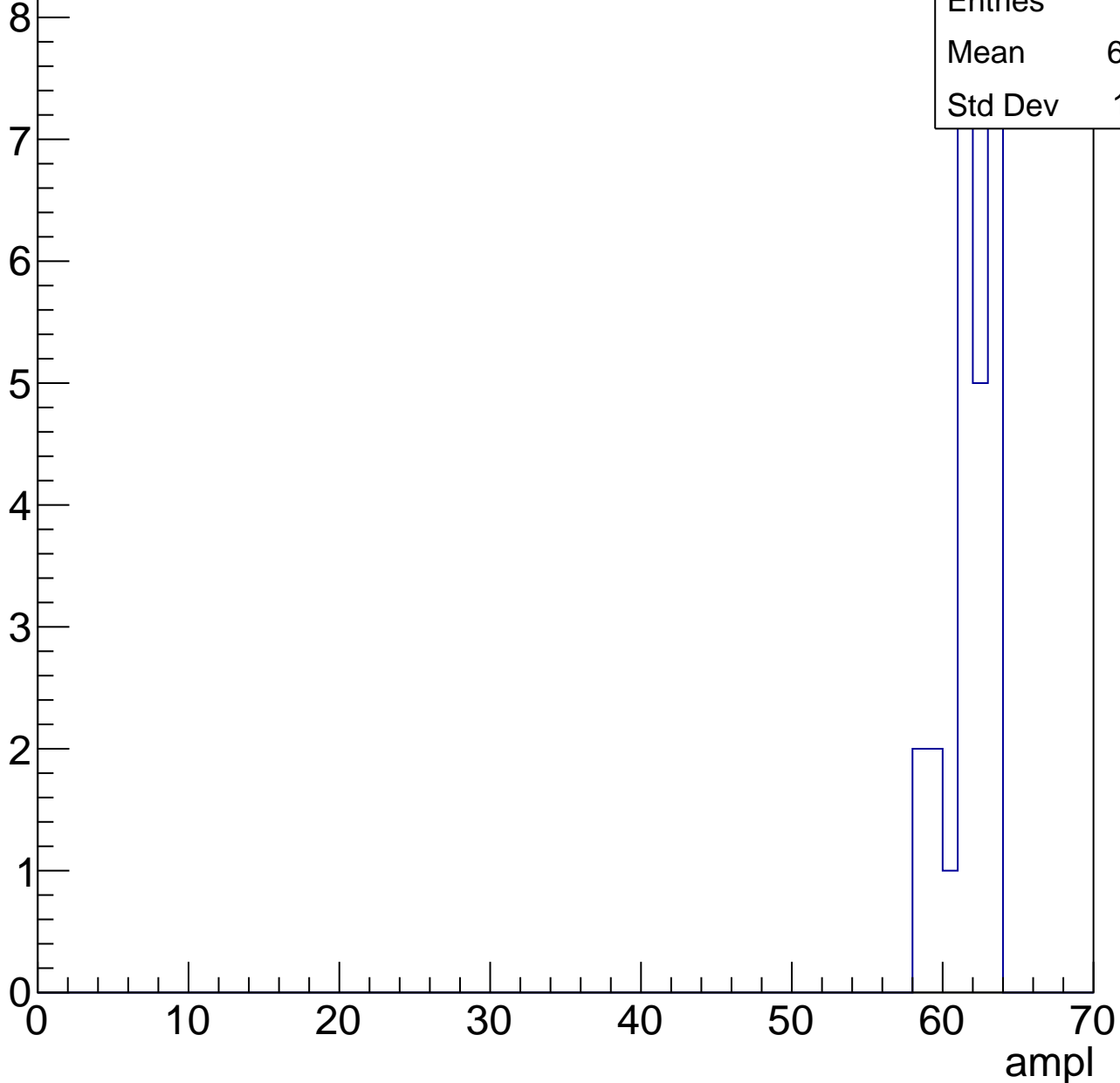


B1L001S, U16-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	61.38
Std Dev	1.521



B1L001S, U16-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L001S, U16-ch125, adc0

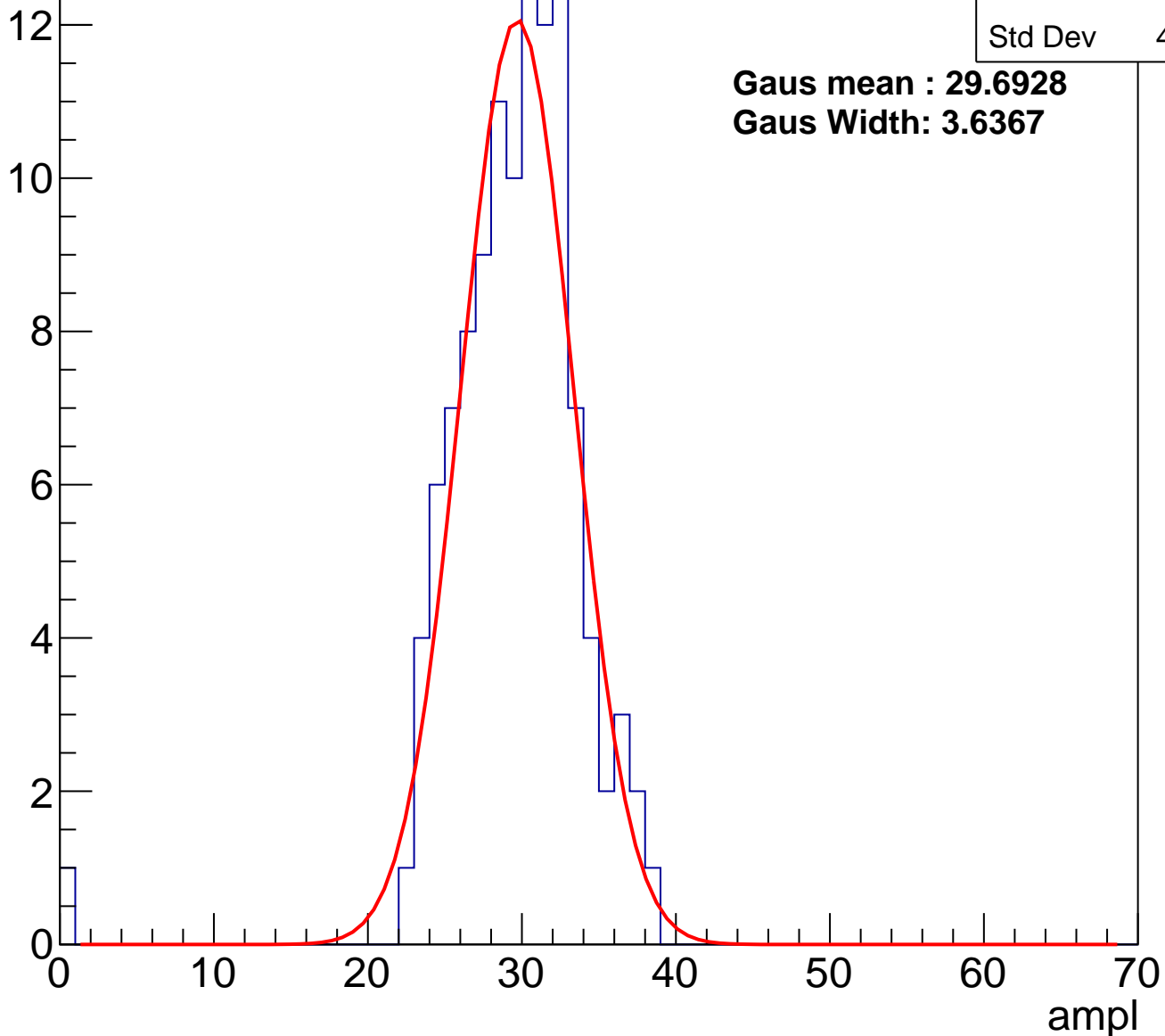
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	29.1
Std Dev	4.425

Gaus mean : 29.6928

Gaus Width: 3.6367

Entry



B1L001S, U16-ch125, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	35.69
Std Dev	3.718

Gaus mean : 36.3212

Gaus Width: 3.8268

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

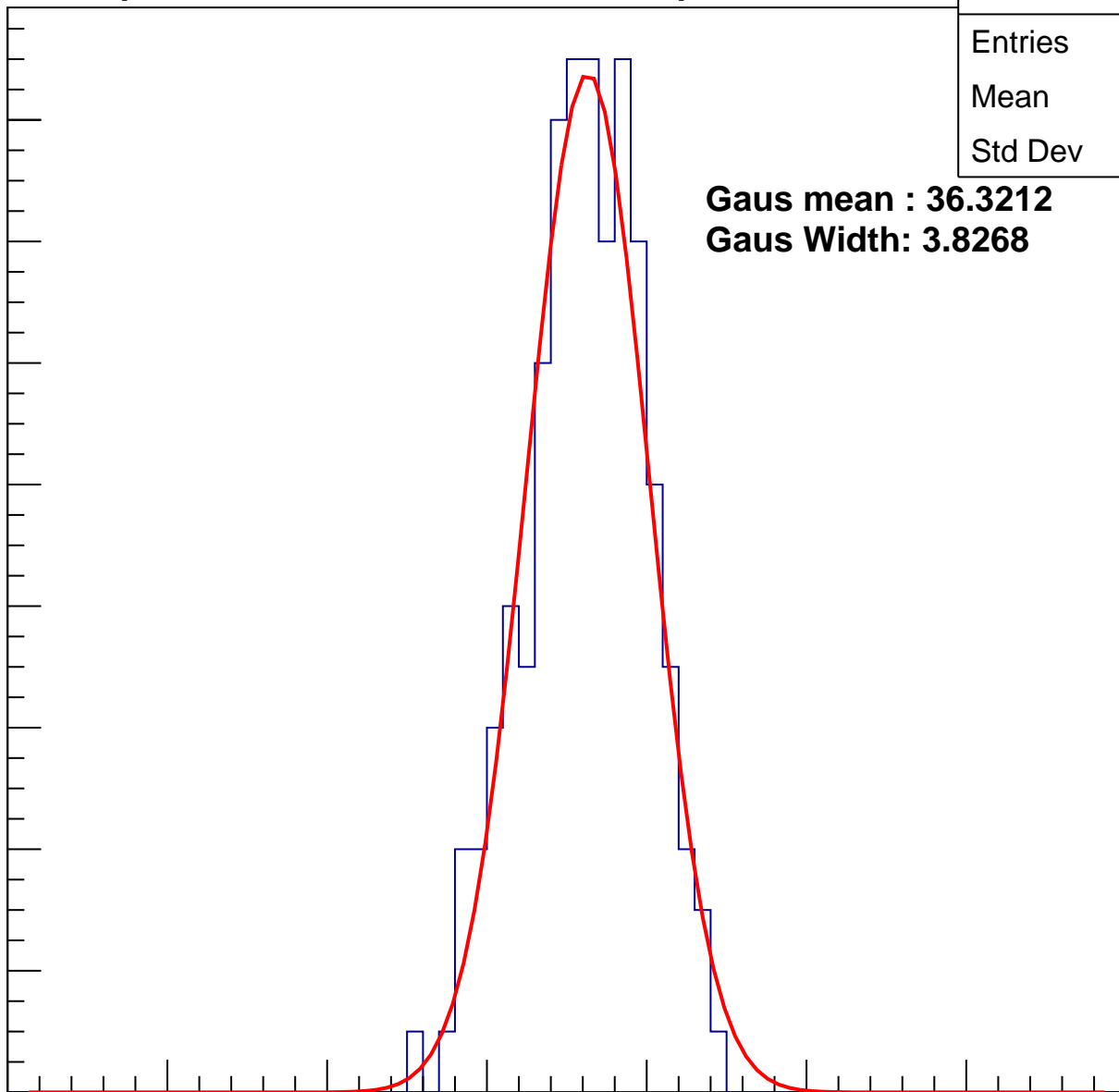
40

50

60

70

ampl



B1L001S, U16-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	96
Mean	42.83
Std Dev	3.051

Gaus mean : 43.5249

Gaus Width: 2.8616

0

2

4

6

8

10

12

B1L001S, U16-ch125, adc3

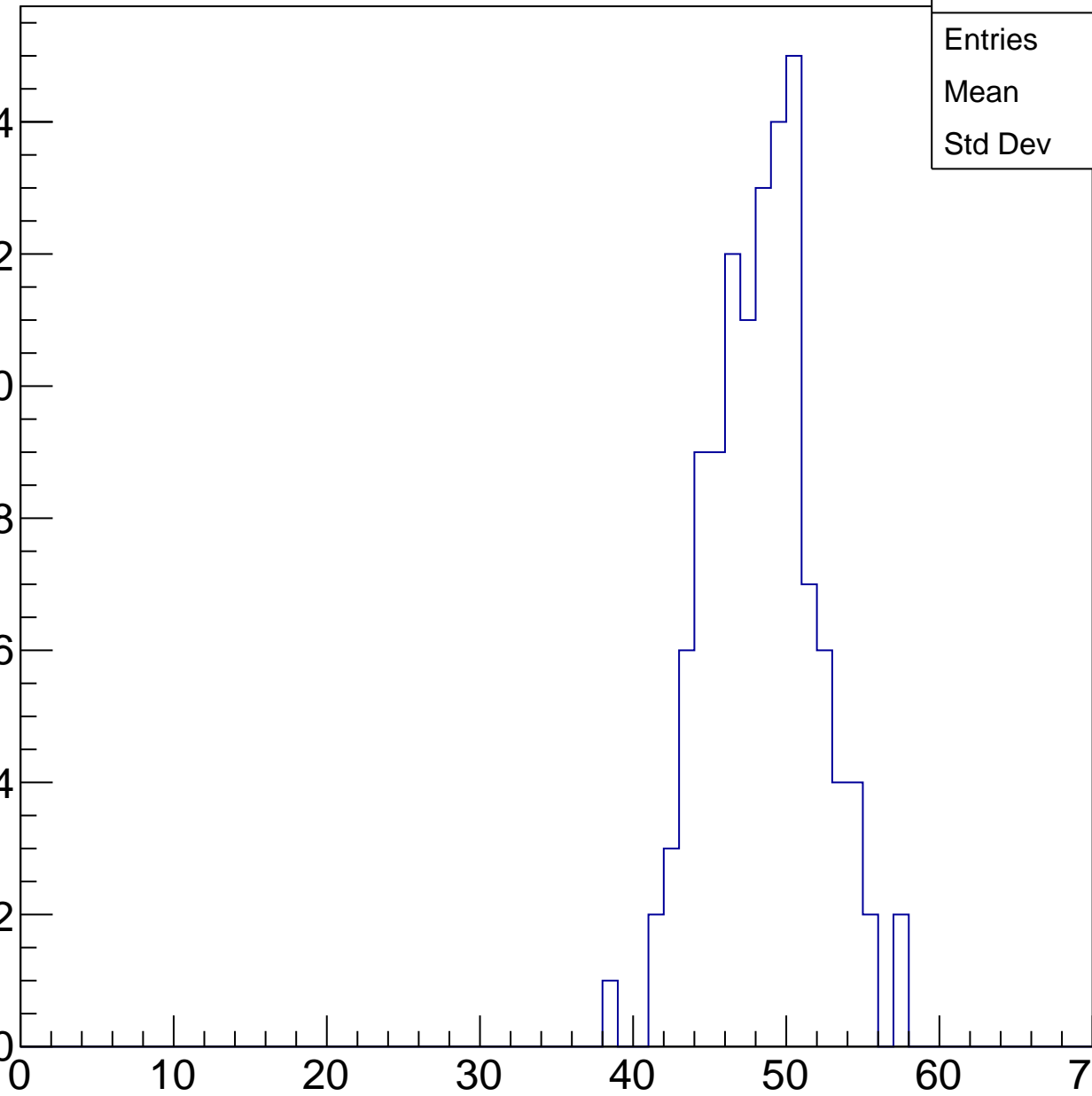
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	47.96
Std Dev	3.508

Entry

14
12
10
8
6
4
2
0

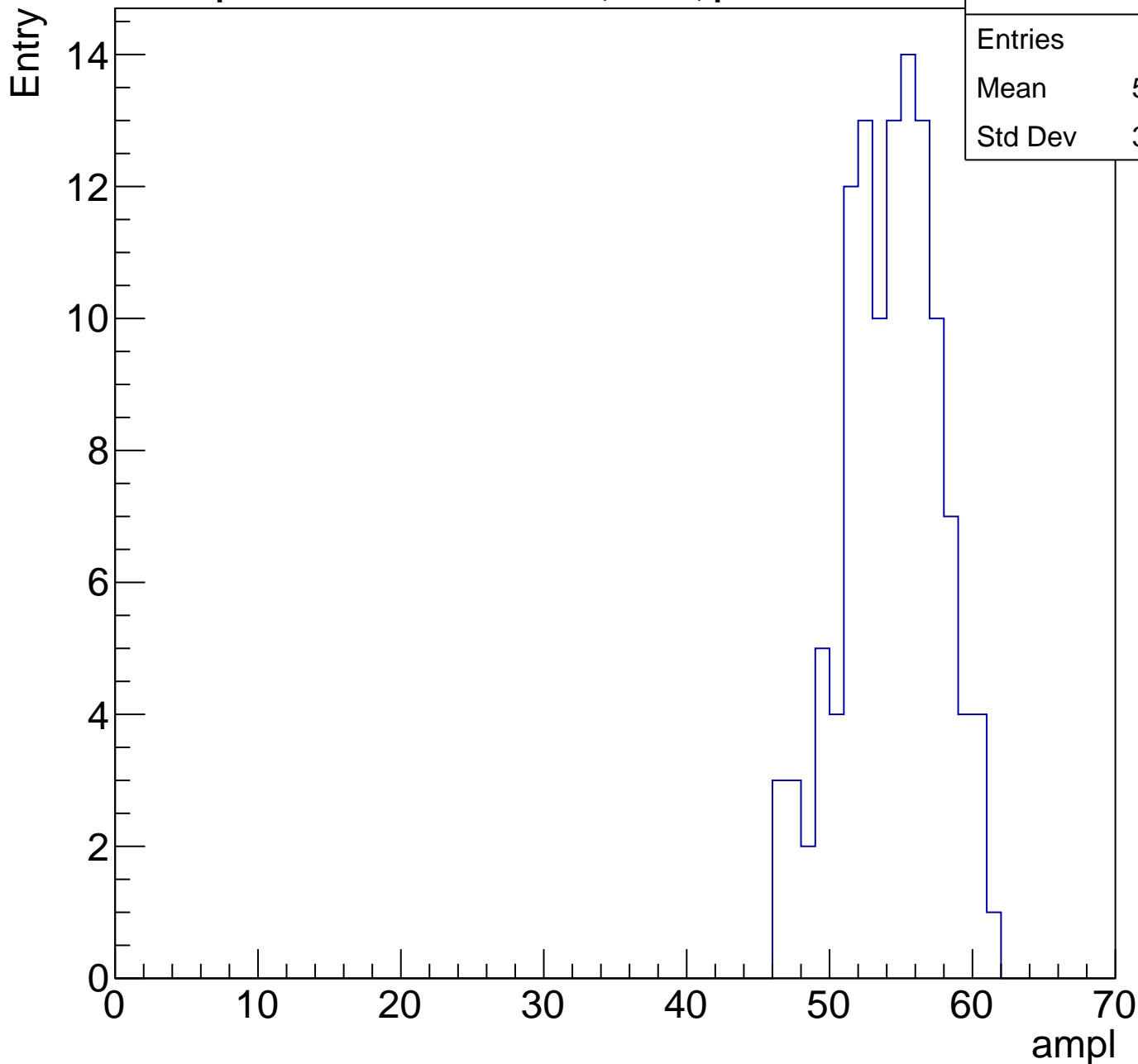
ampl



B1L001S, U16-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	53.82
Std Dev	3.354



B1L001S, U16-ch125, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	58.53
Std Dev	6.319

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

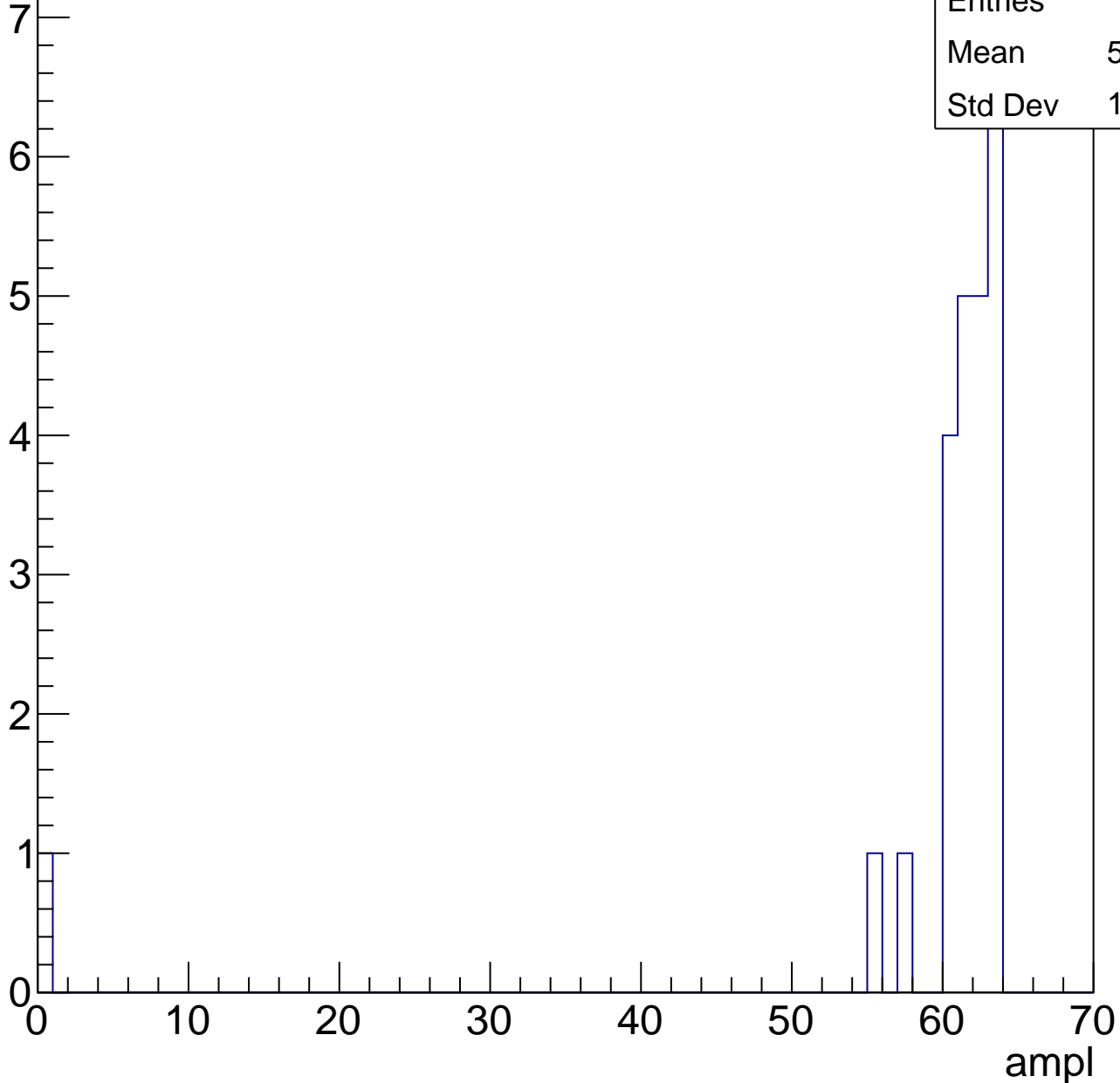
70

B1L001S, U16-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	24
Mean	58.67
Std Dev	12.38



B1L001S, U16-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42
Std Dev	20

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	28.24
Std Dev	3.524

Gaus mean : 28.8084

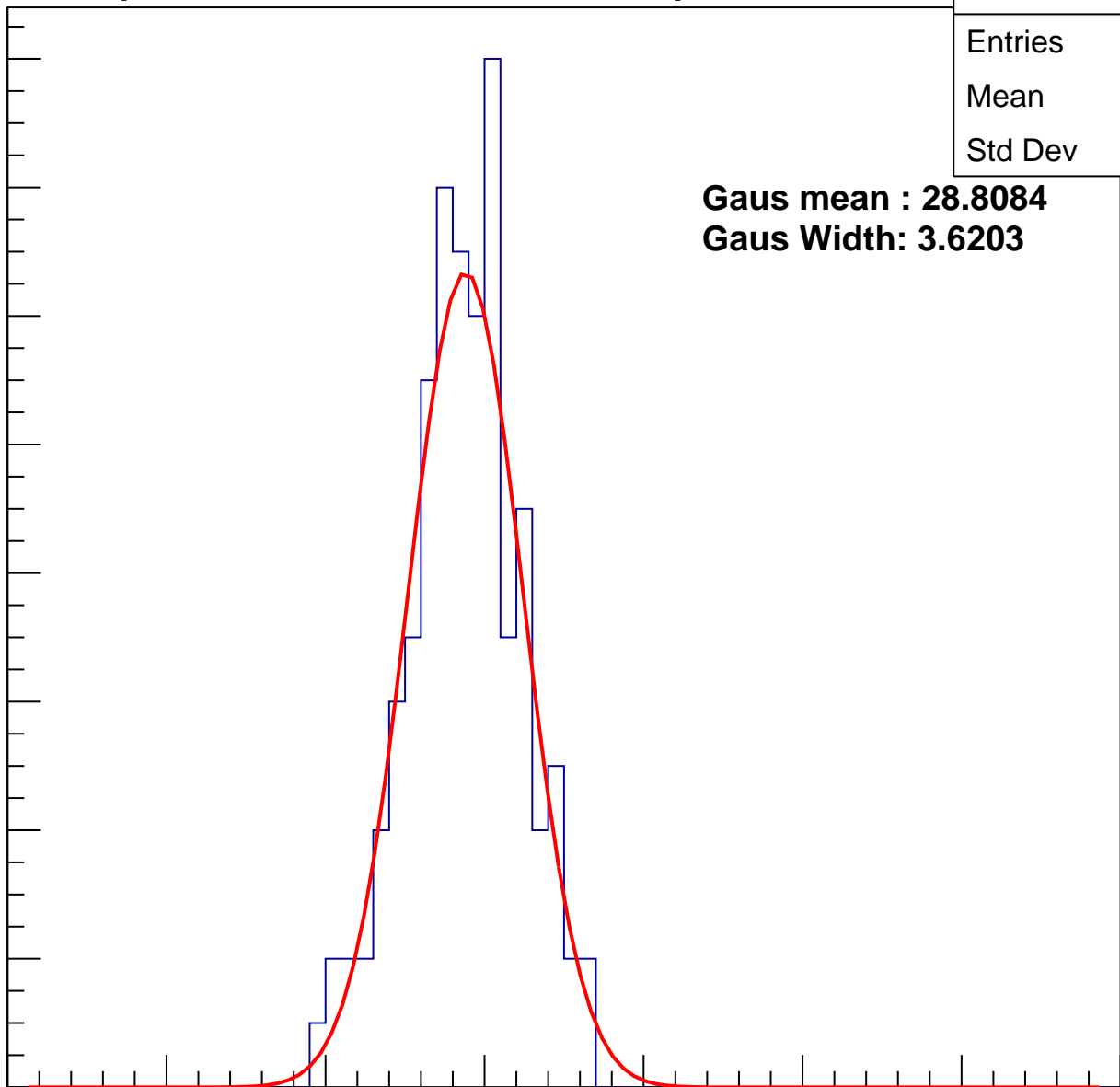
Gaus Width: 3.6203

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U16-ch126, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	34.97
Std Dev	3.501

Gaus mean : 35.3988

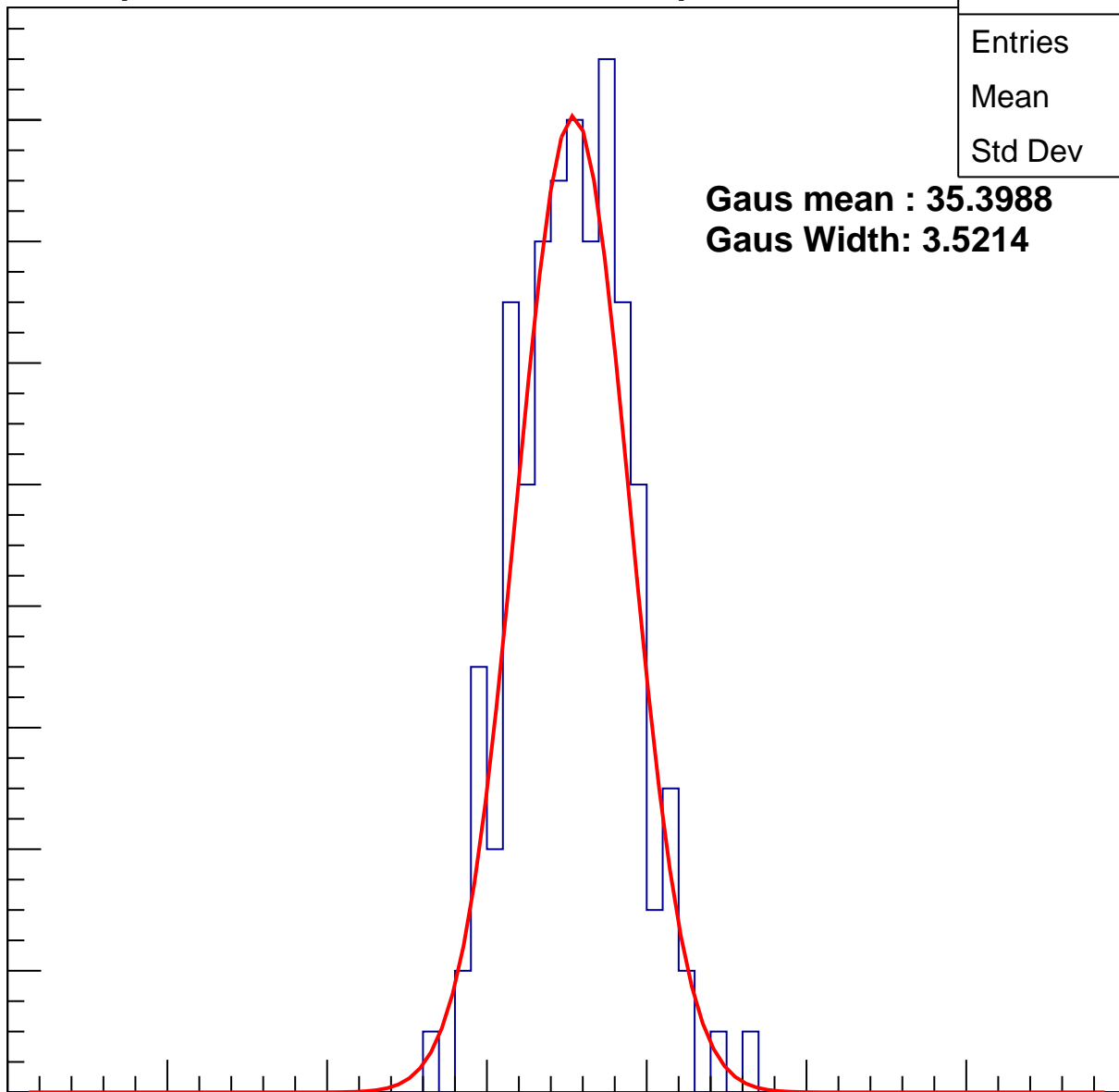
Gaus Width: 3.5214

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

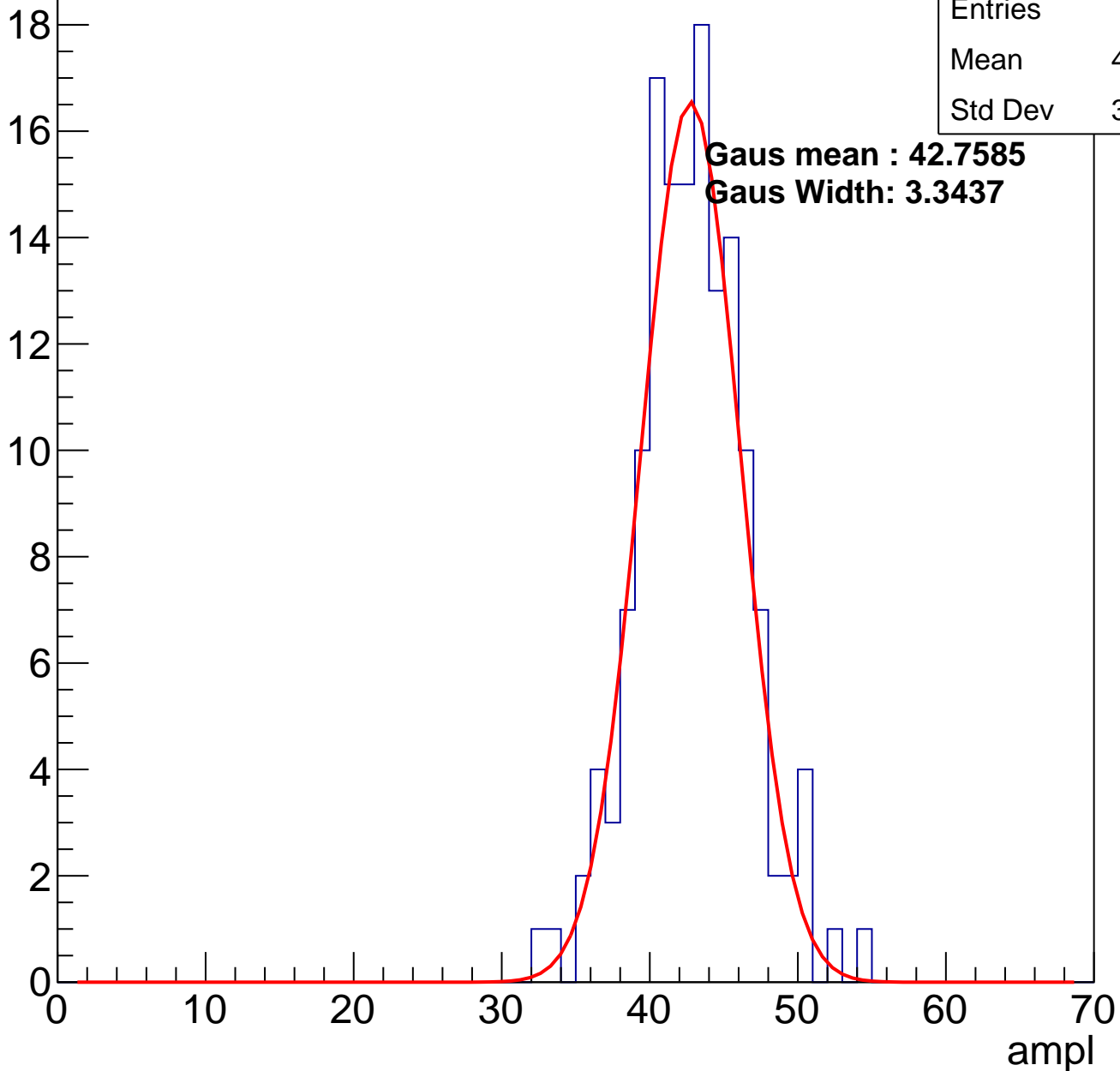
ampl



B1L001S, U16-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

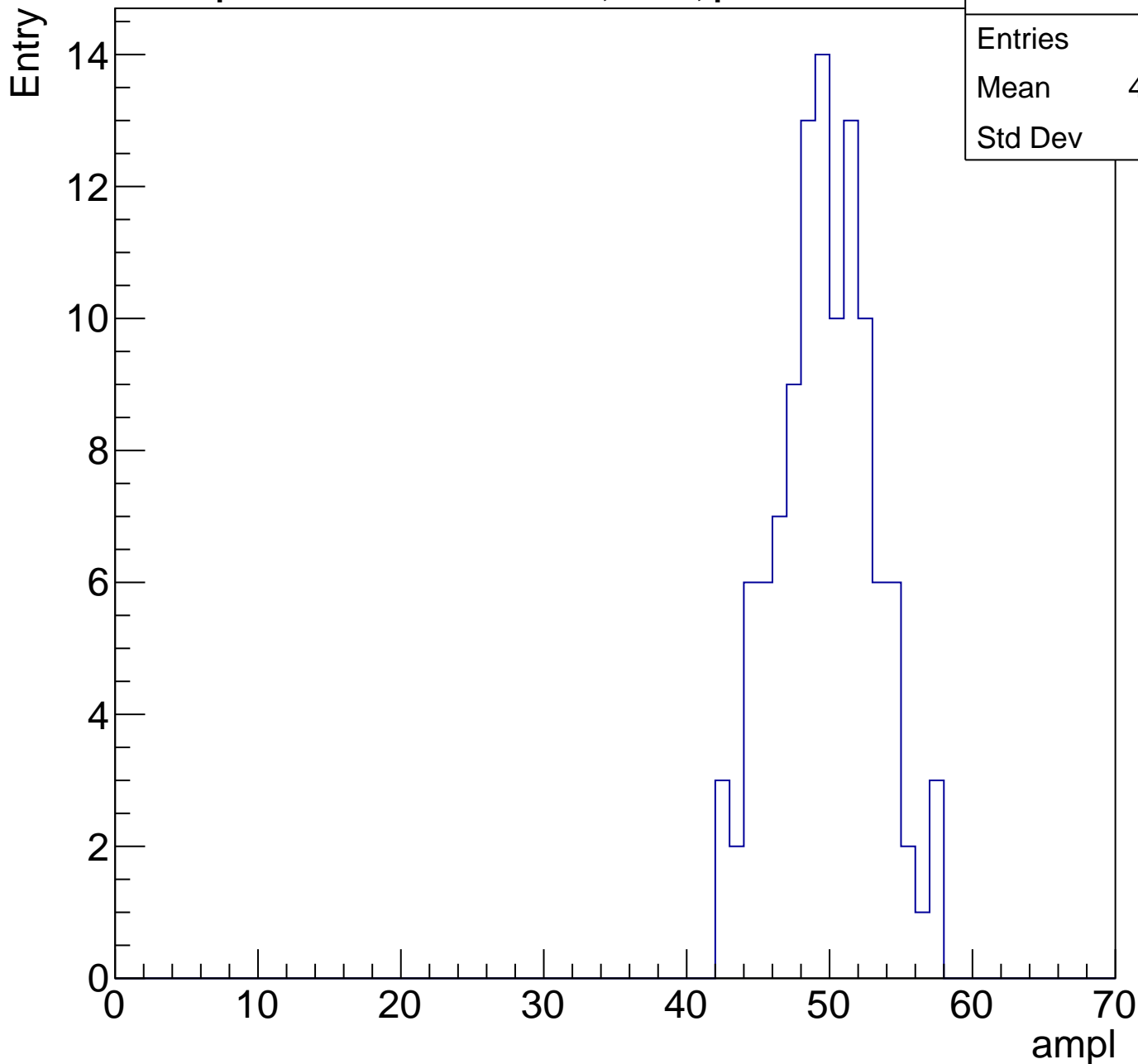
Entry



B1L001S, U16-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

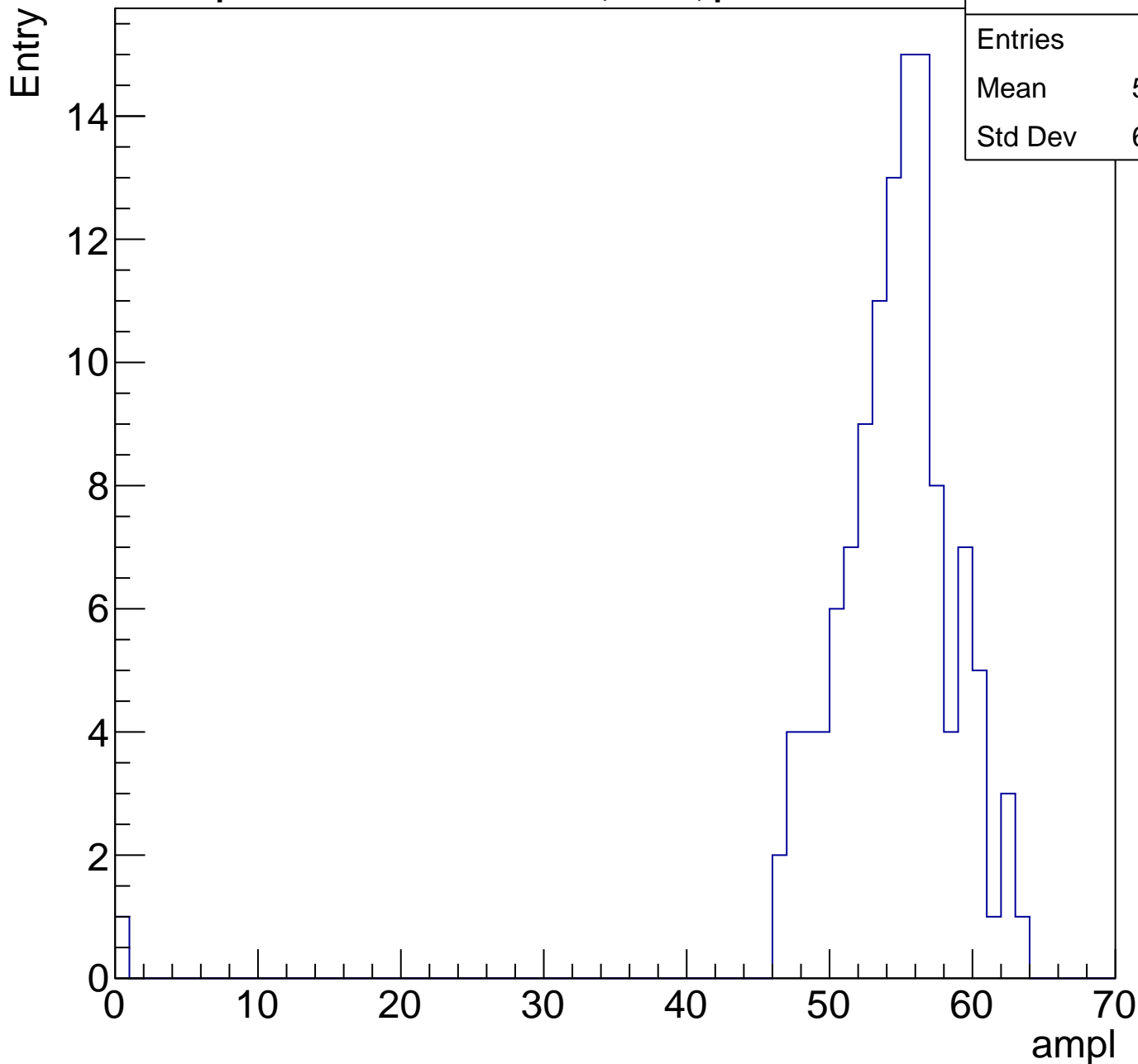
Entries	111
Mean	49.22
Std Dev	3.4



B1L001S, U16-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	53.78
Std Dev	6.173



B1L001S, U16-ch126, adc5

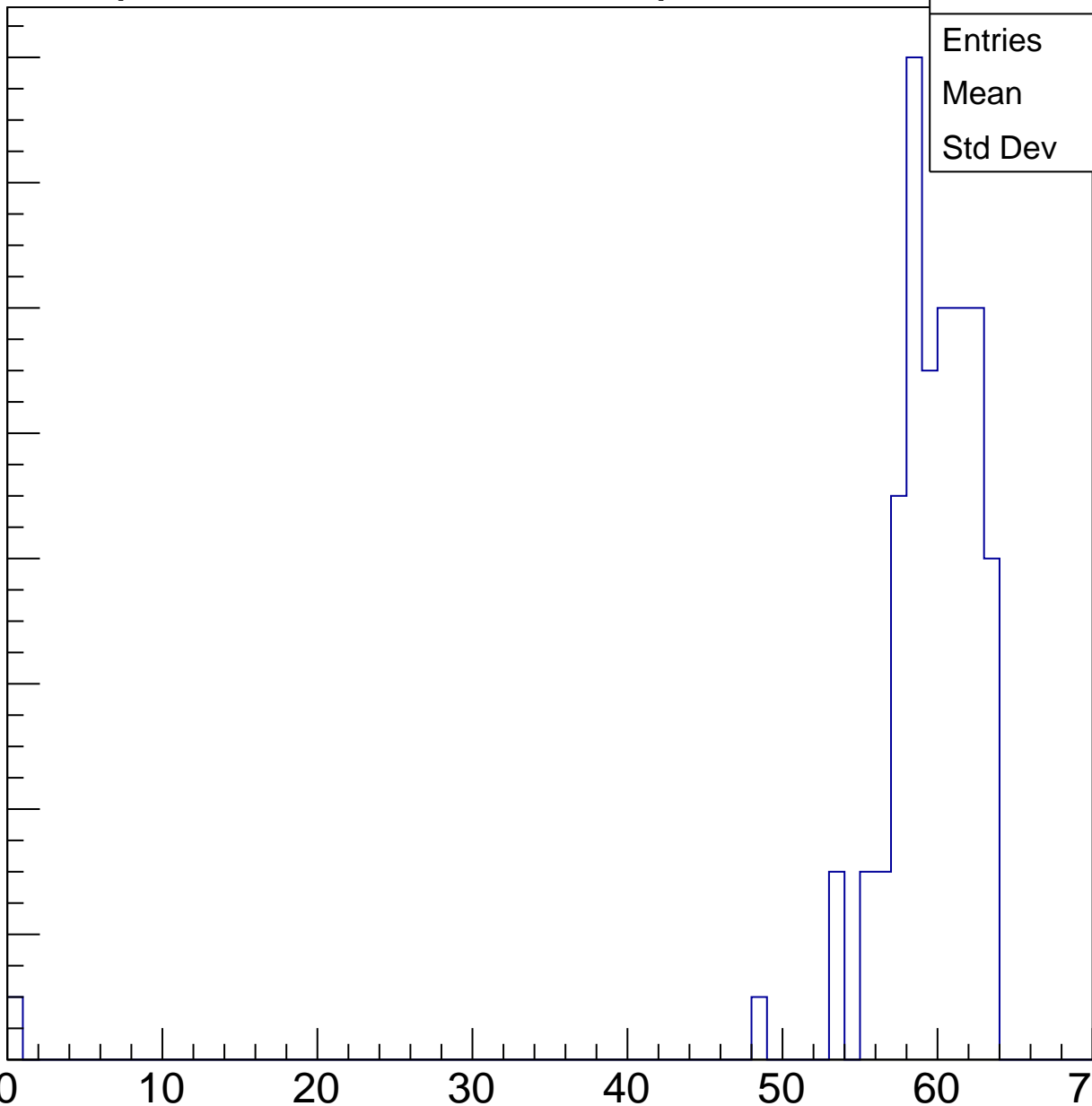
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.57
Std Dev	6.726

ampl

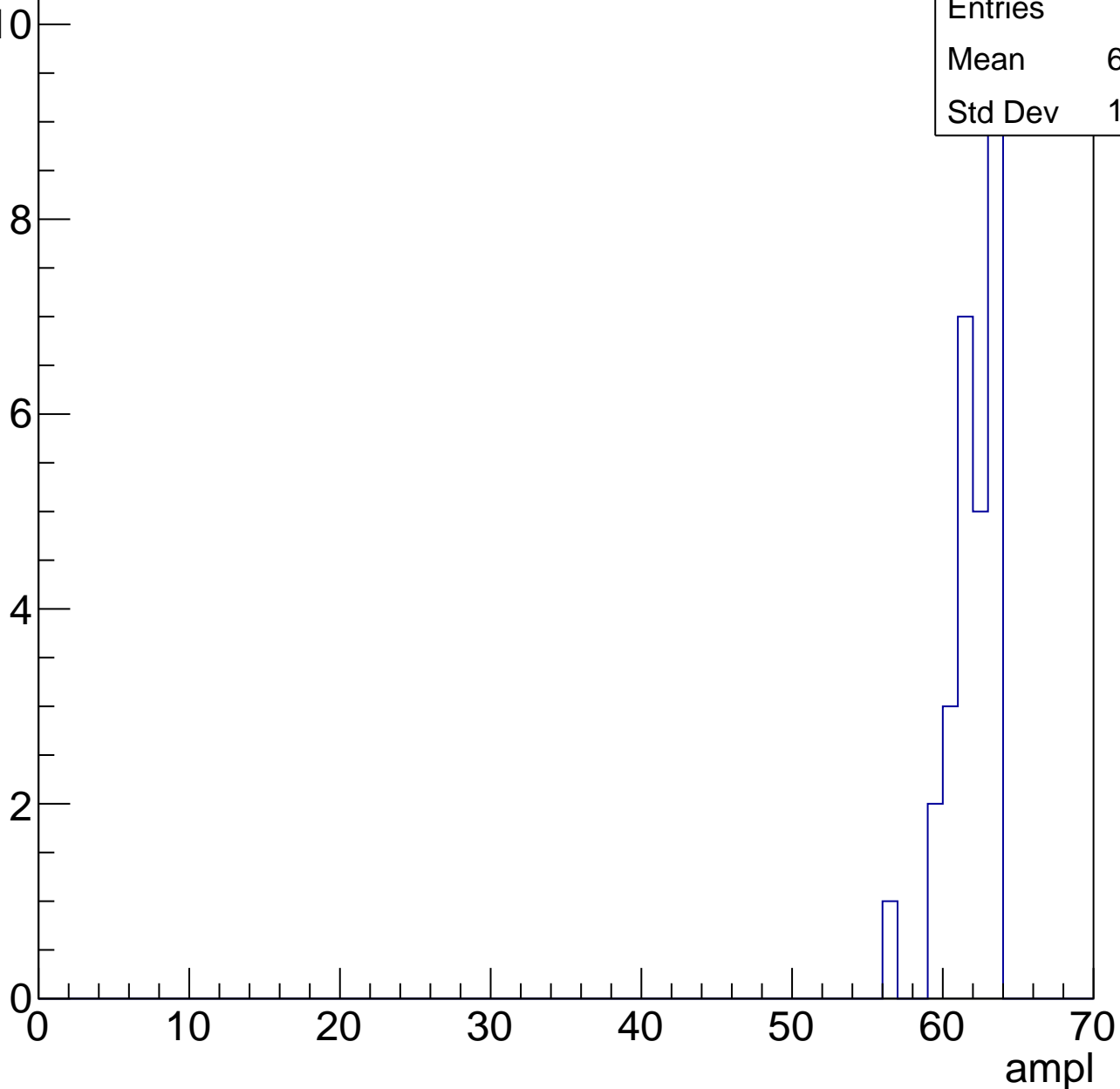


B1L001S, U16-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	28
Mean	61.46
Std Dev	1.636



B1L001S, U16-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch127, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	27.75
Std Dev	4.017

Gaus mean : 28.5519

Gaus Width: 3.2964

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U16-ch127, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	34.93
Std Dev	3.446

Gaus mean : 35.2175

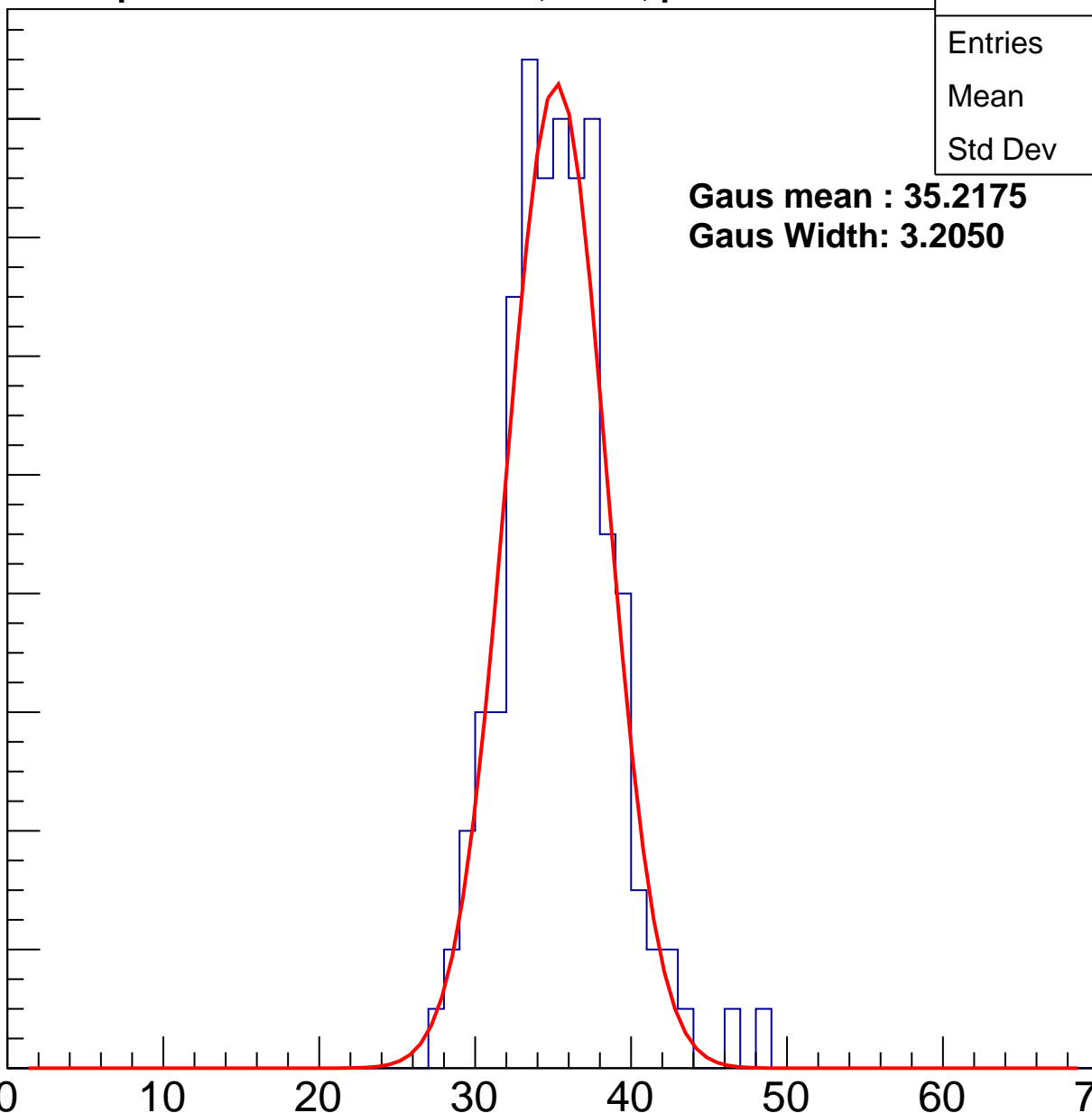
Gaus Width: 3.2050

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U16-ch127, adc2

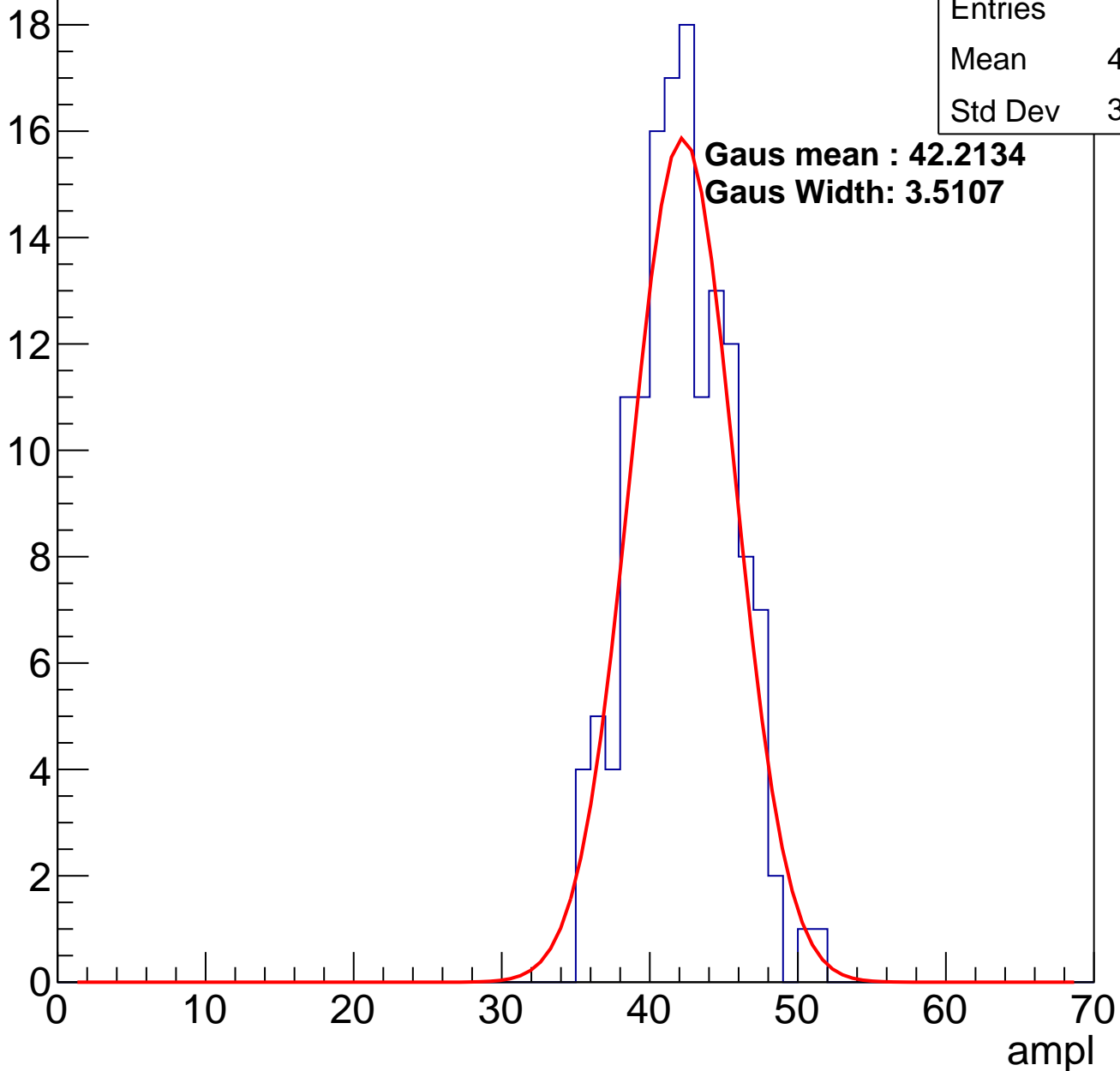
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	41.75
Std Dev	3.277

Gaus mean : 42.2134

Gaus Width: 3.5107

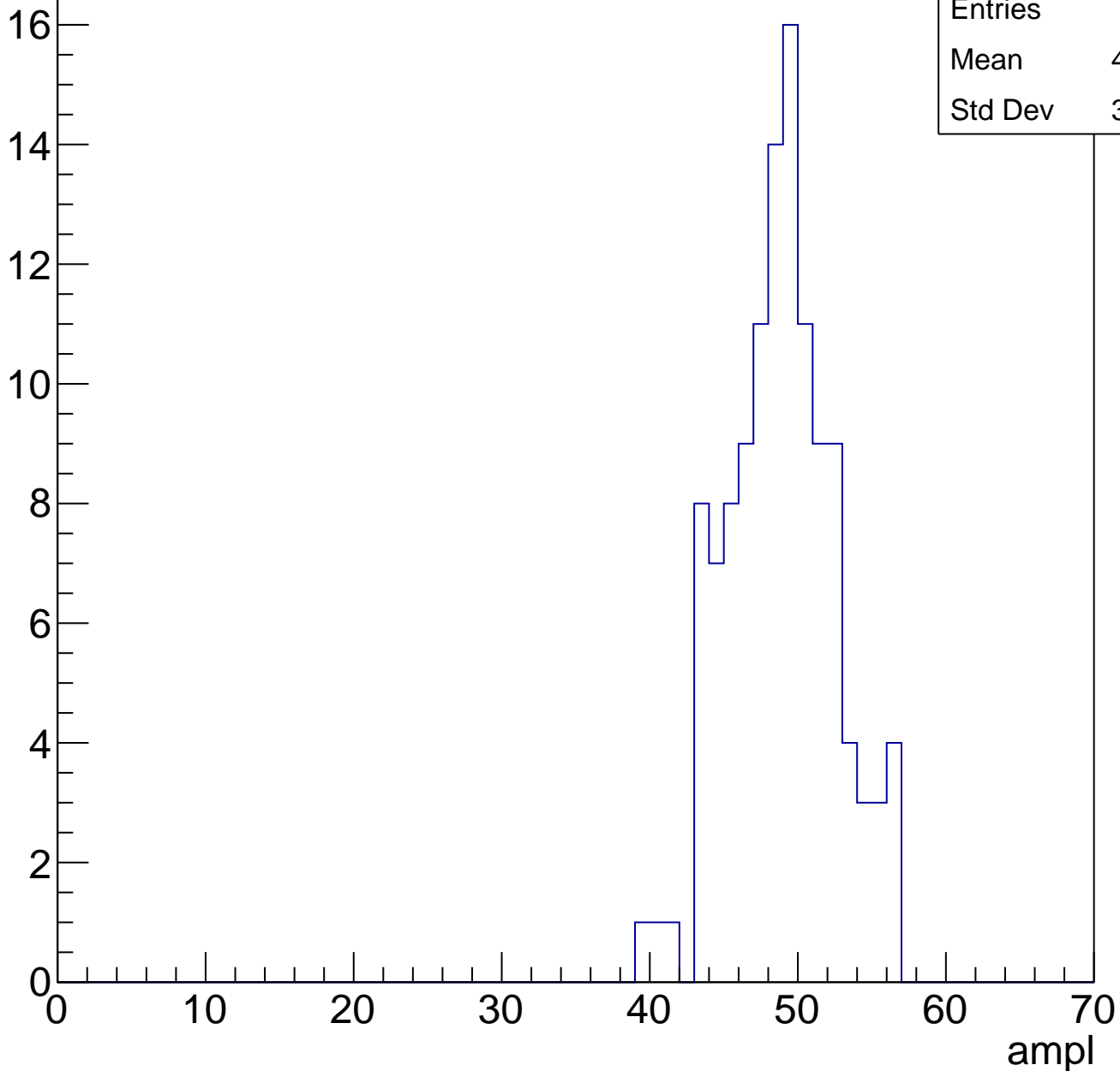
Entry



B1L001S, U16-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

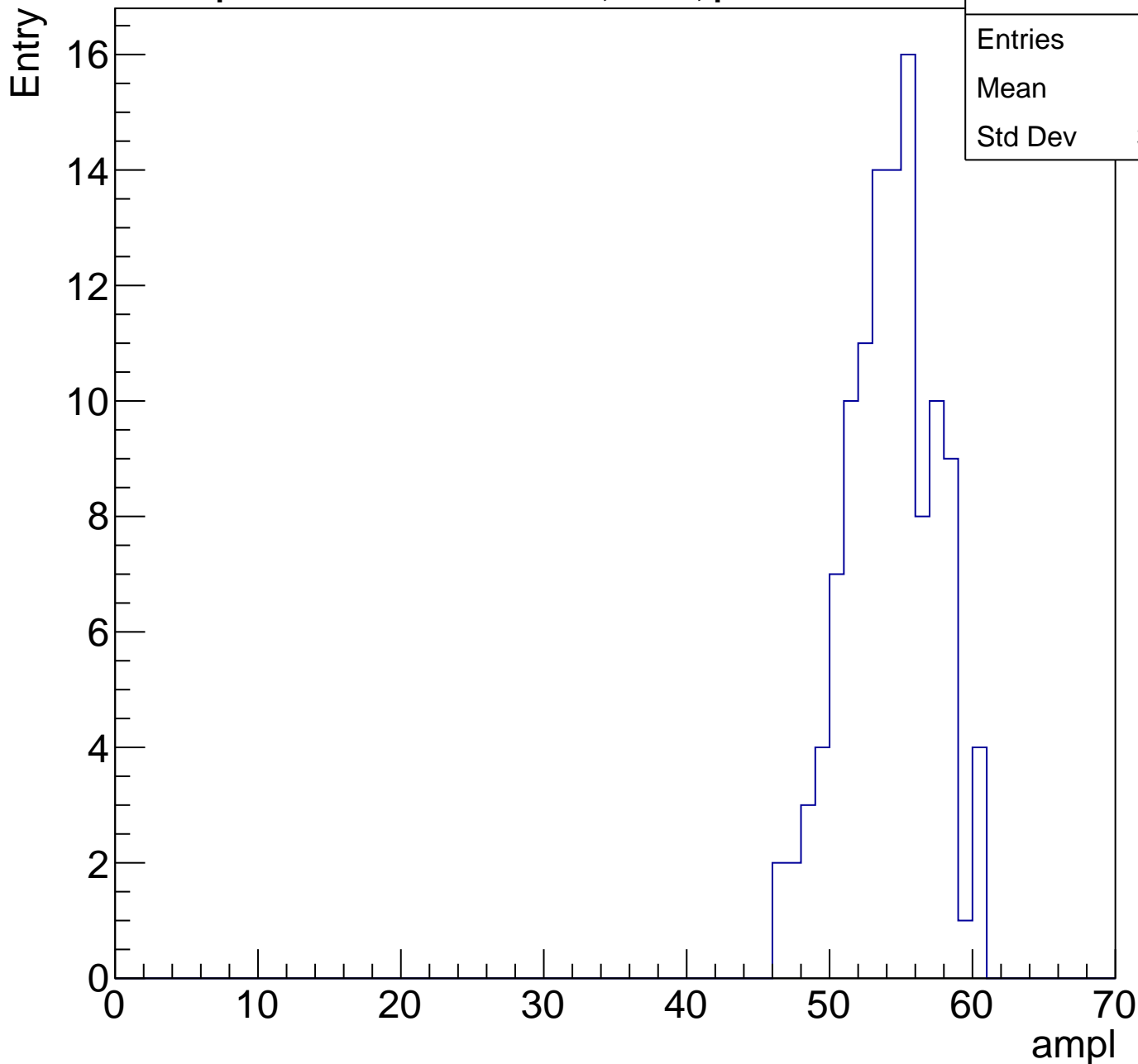


Entries	119
Mean	48.39
Std Dev	3.553

B1L001S, U16-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	53.7
Std Dev	3.141



B1L001S, U16-ch127, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	58.69
Std Dev	6.276

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

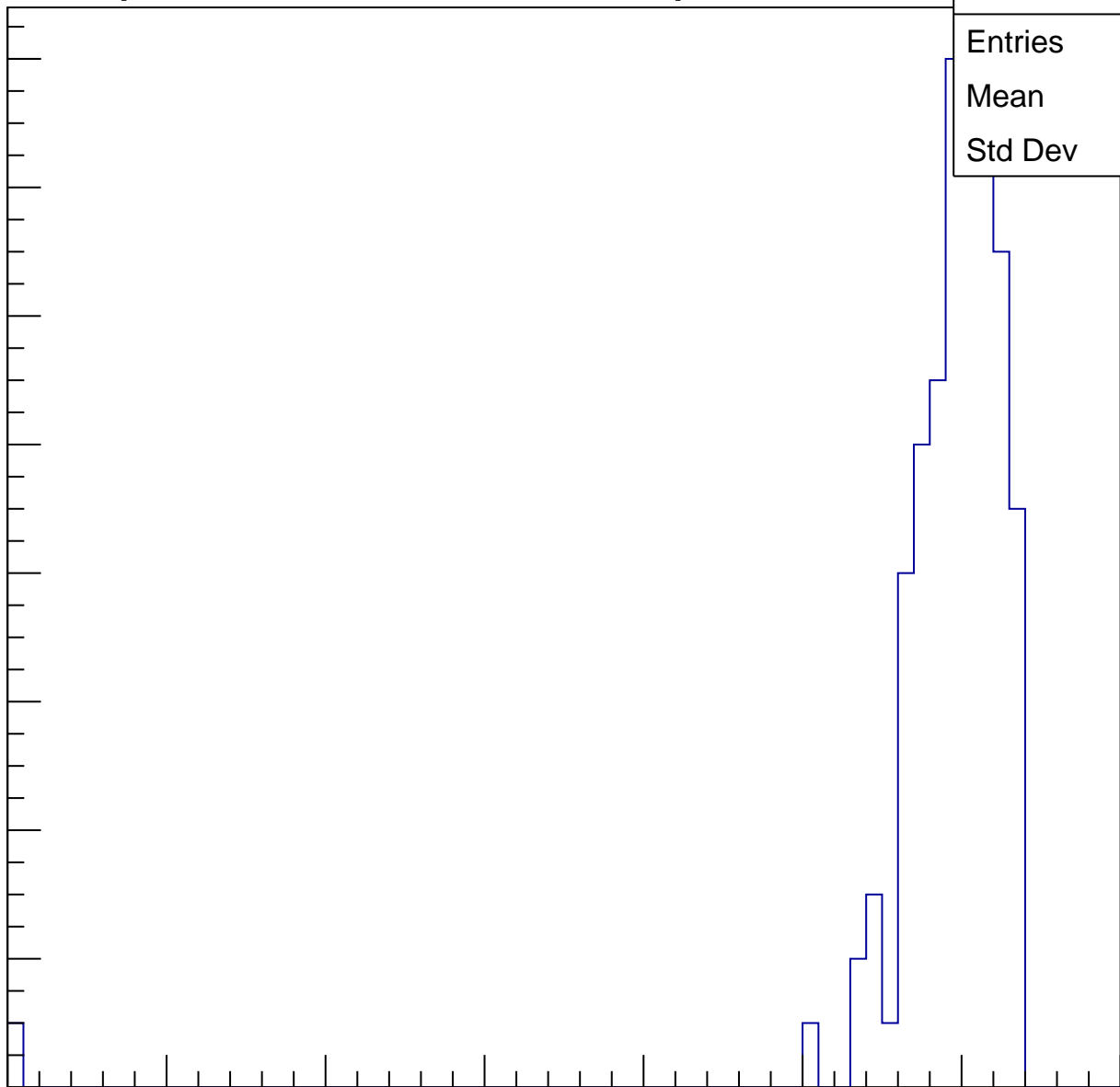
40

50

60

70

ampl



B1L001S, U16-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

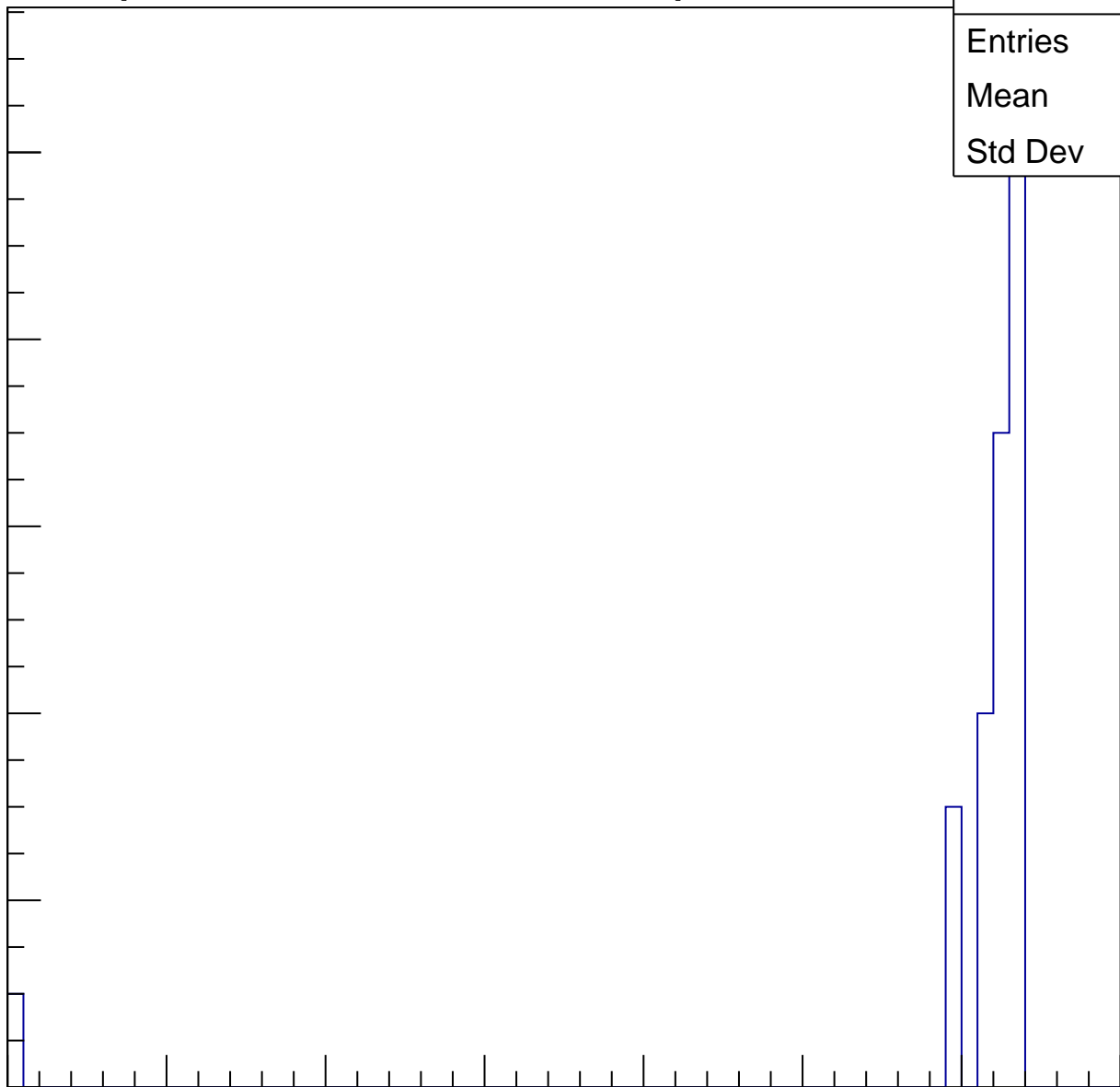
2

0

Entries	26
Mean	59.54
Std Dev	11.98

ampl

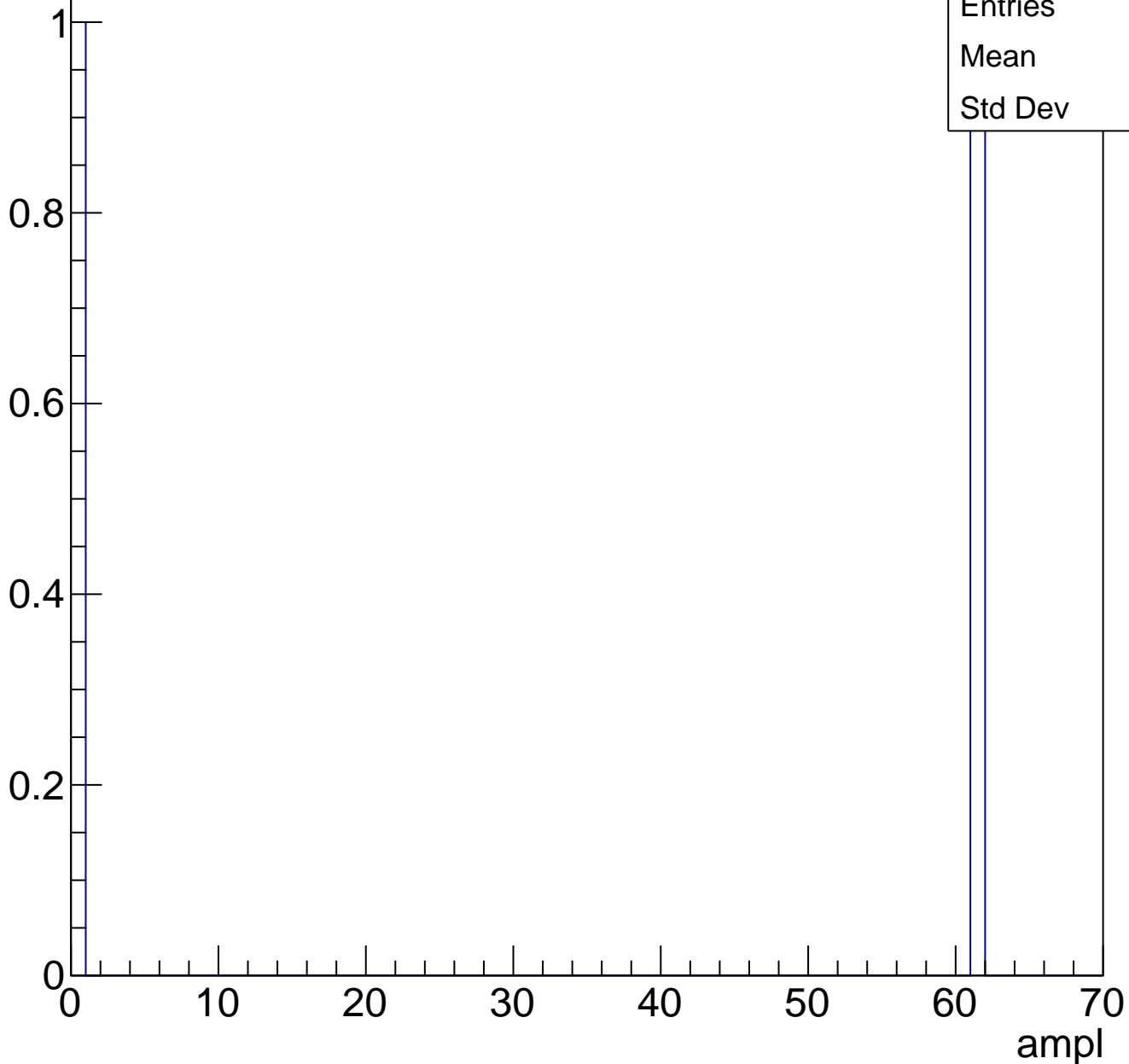
0 10 20 30 40 50 60 70



B1L001S, U16-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U16-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

