

B1L000S, U6-ch0

calib_packv5_042523_0143.root, FC#3, port B3

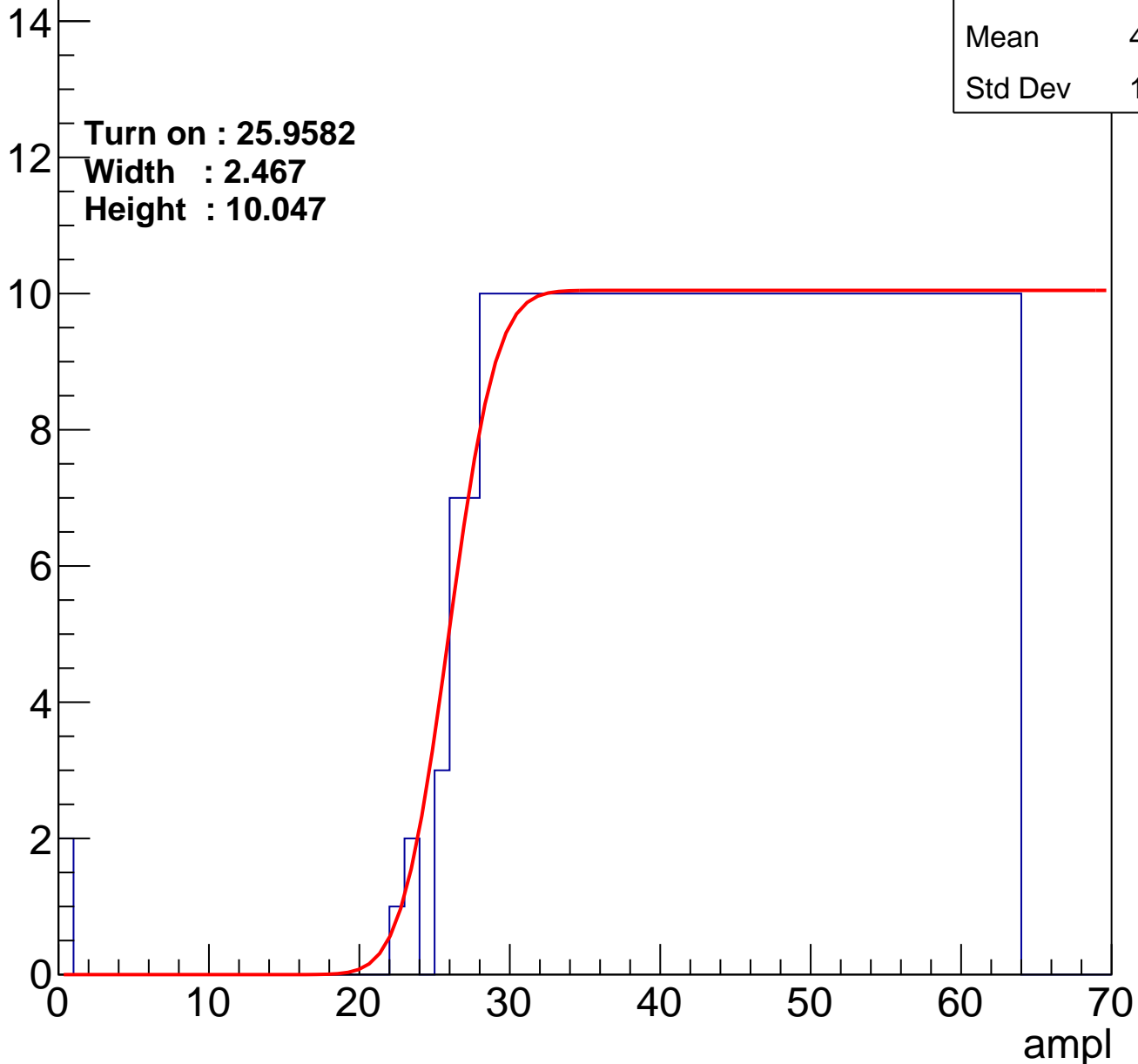
Entry

Entries	382
Mean	44.23
Std Dev	11.47

Turn on : 25.9582

Width : 2.467

Height : 10.047



B1L000S, U6-ch1

calib_packv5_042523_0143.root, FC#3, port B3

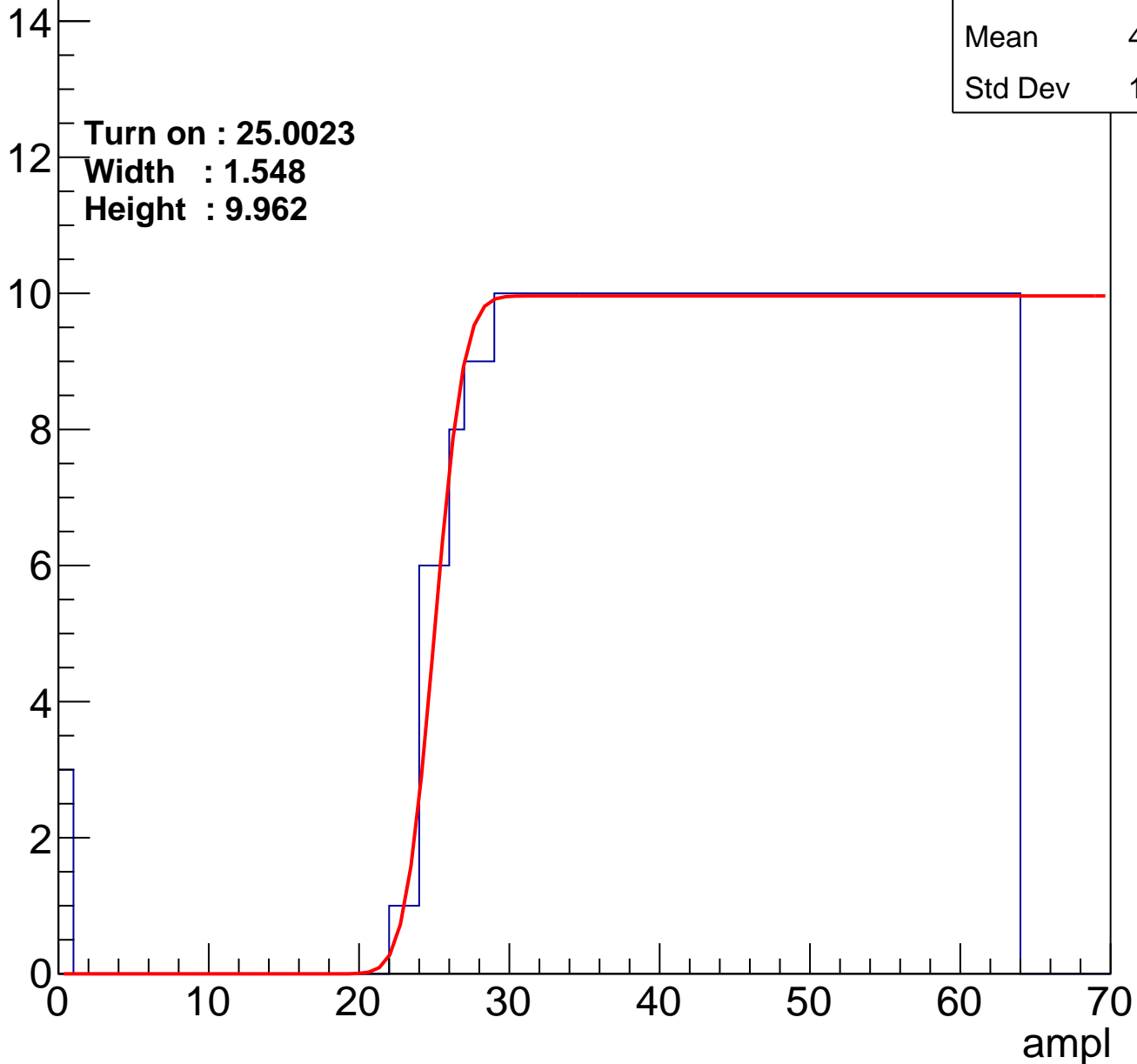
Entries	393
Mean	43.62
Std Dev	11.92

Turn on : 25.0023

Width : 1.548

Height : 9.962

Entry



B1L000S, U6-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.9177

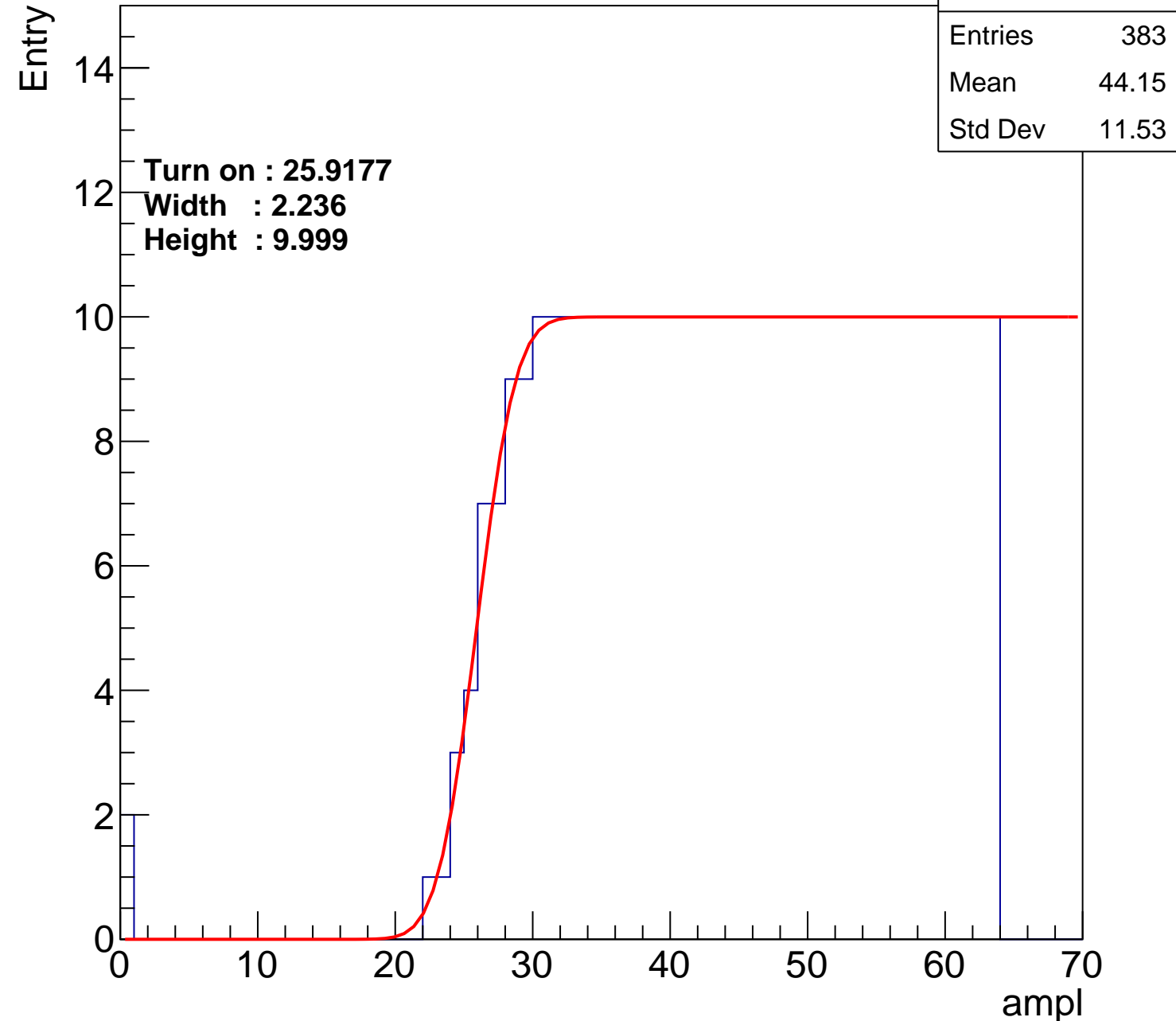
Width : 2.236

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch3

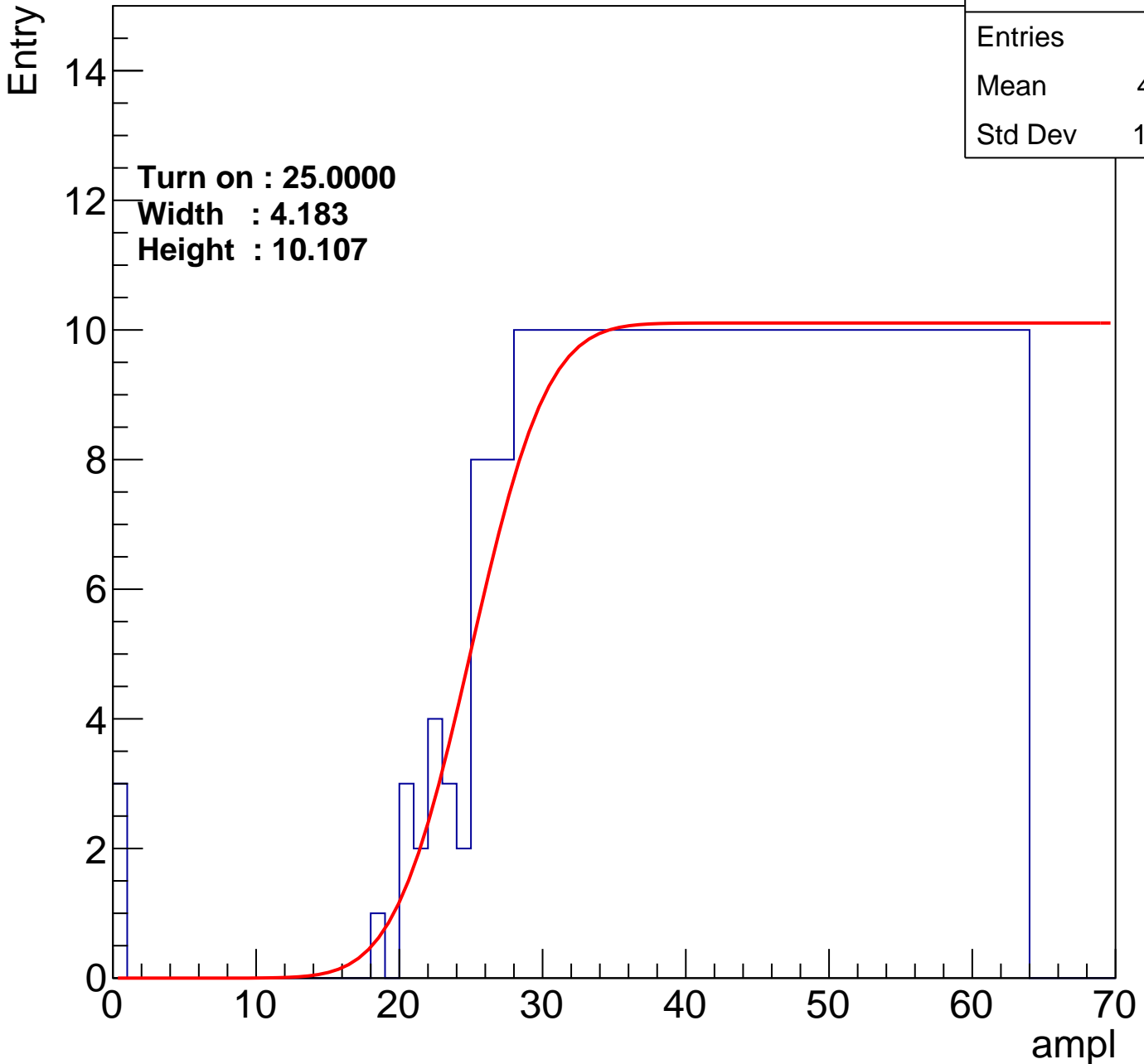
calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.11
Std Dev	12.26

Turn on : 25.0000

Width : 4.183

Height : 10.107



B1L000S, U6-ch4

calib_packv5_042523_0143.root, FC#3, port B3

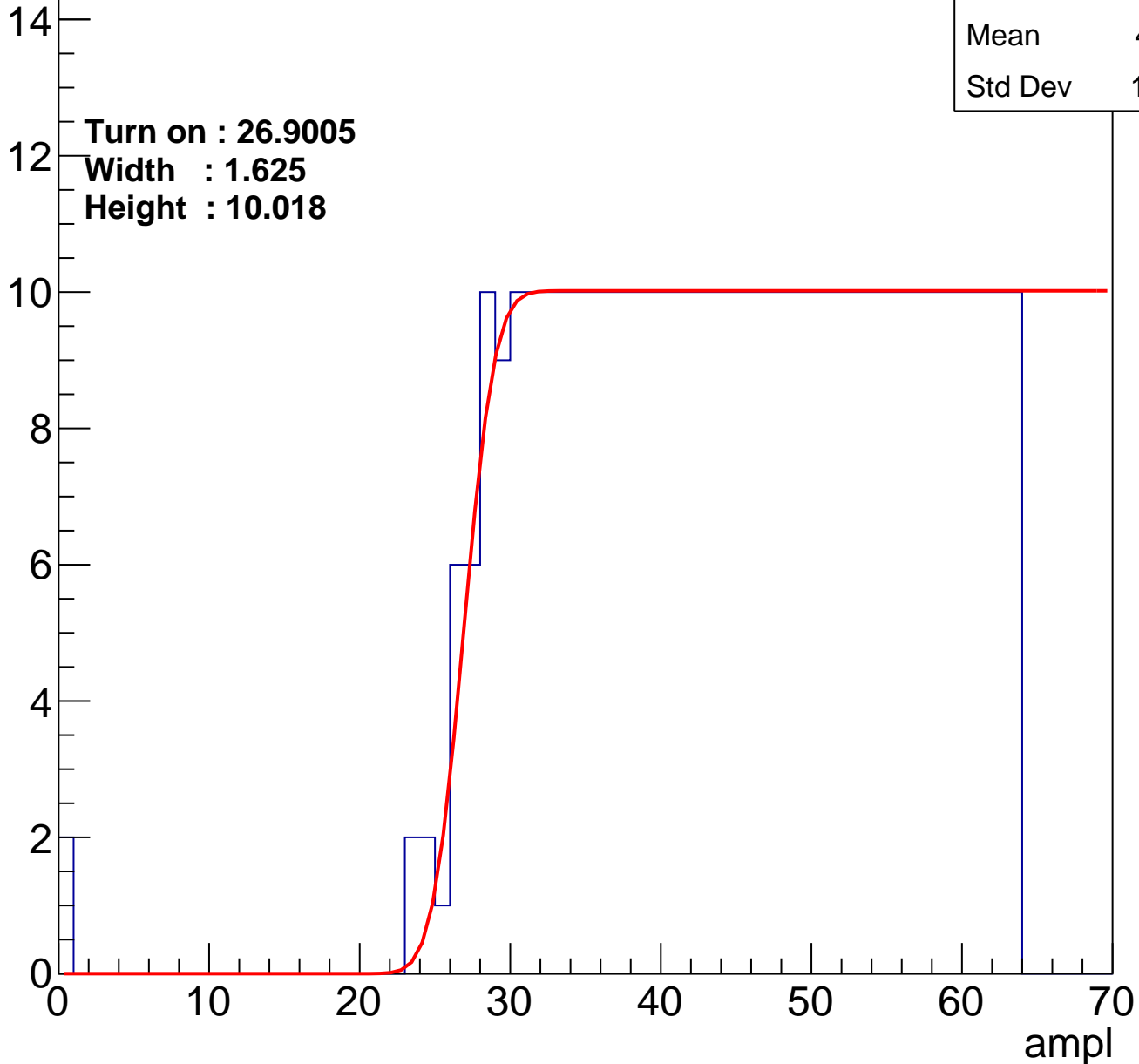
Entries	378
Mean	44.41
Std Dev	11.38

Turn on : 26.9005

Width : 1.625

Height : 10.018

Entry



B1L000S, U6-ch5

calib_packv5_042523_0143.root, FC#3, port B3

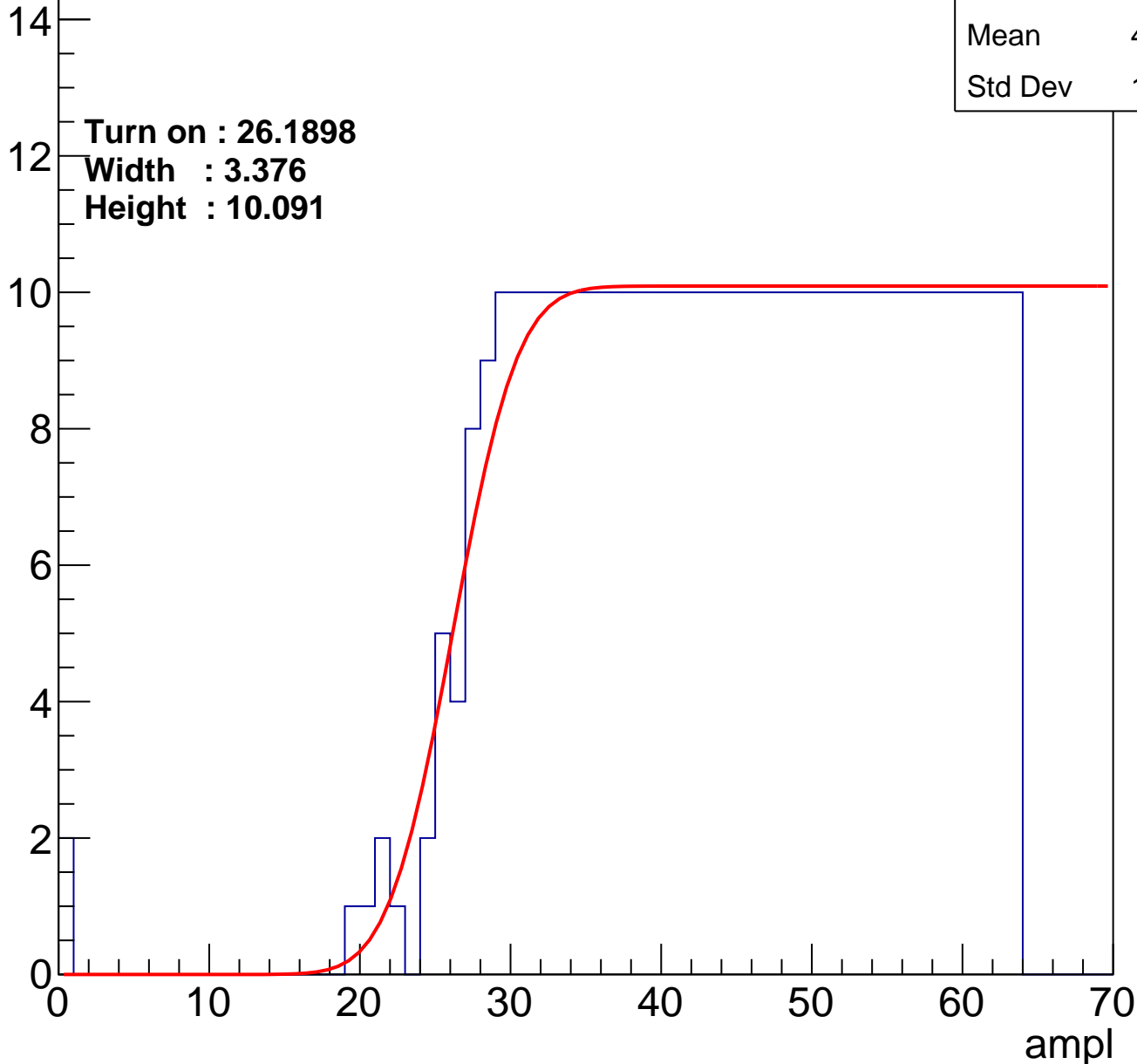
Entries	385
Mean	44.02
Std Dev	11.65

Turn on : 26.1898

Width : 3.376

Height : 10.091

Entry



B1L000S, U6-ch6

calib_packv5_042523_0143.root, FC#3, port B3

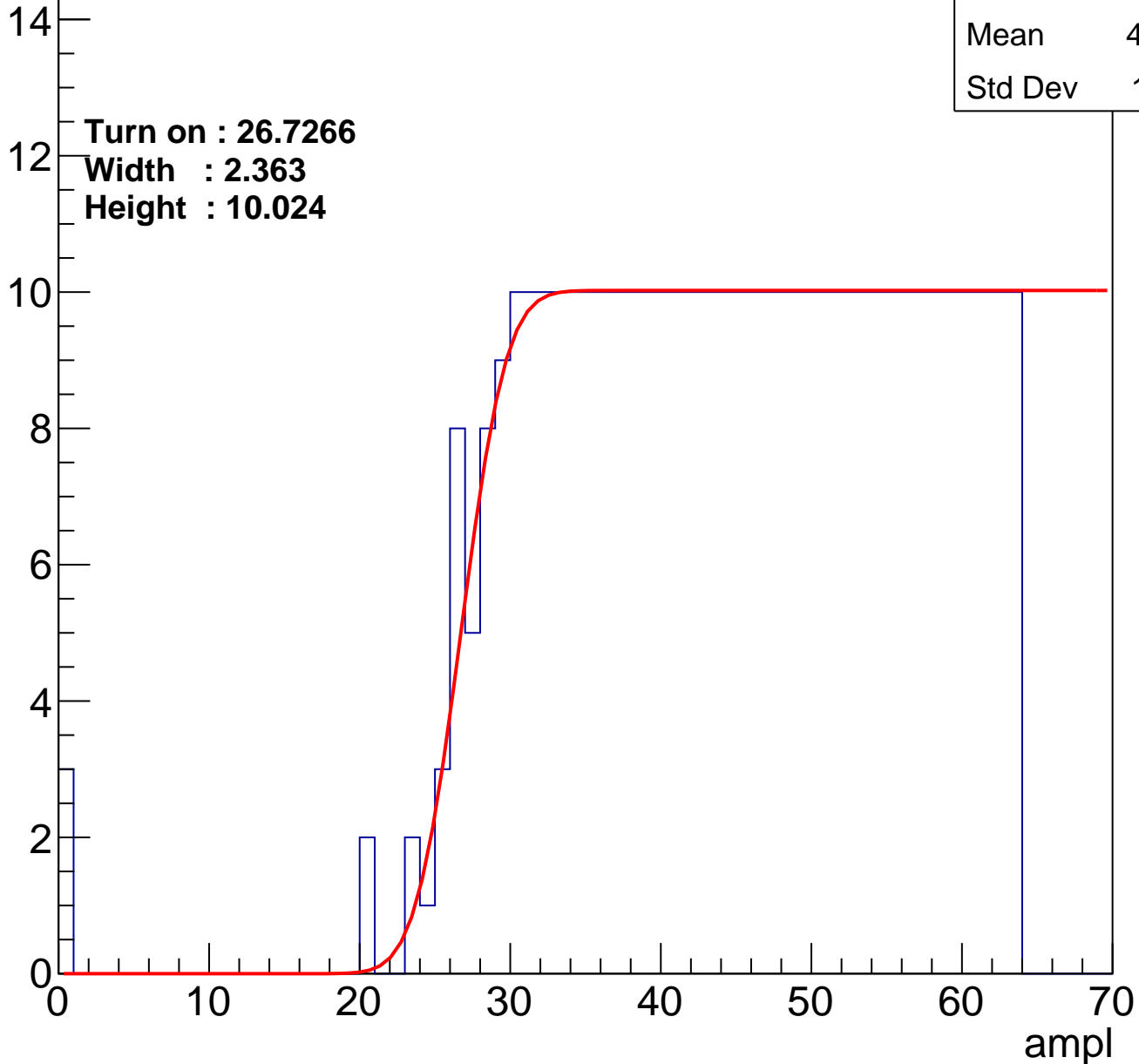
Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.7266

Width : 2.363

Height : 10.024

Entry



B1L000S, U6-ch7

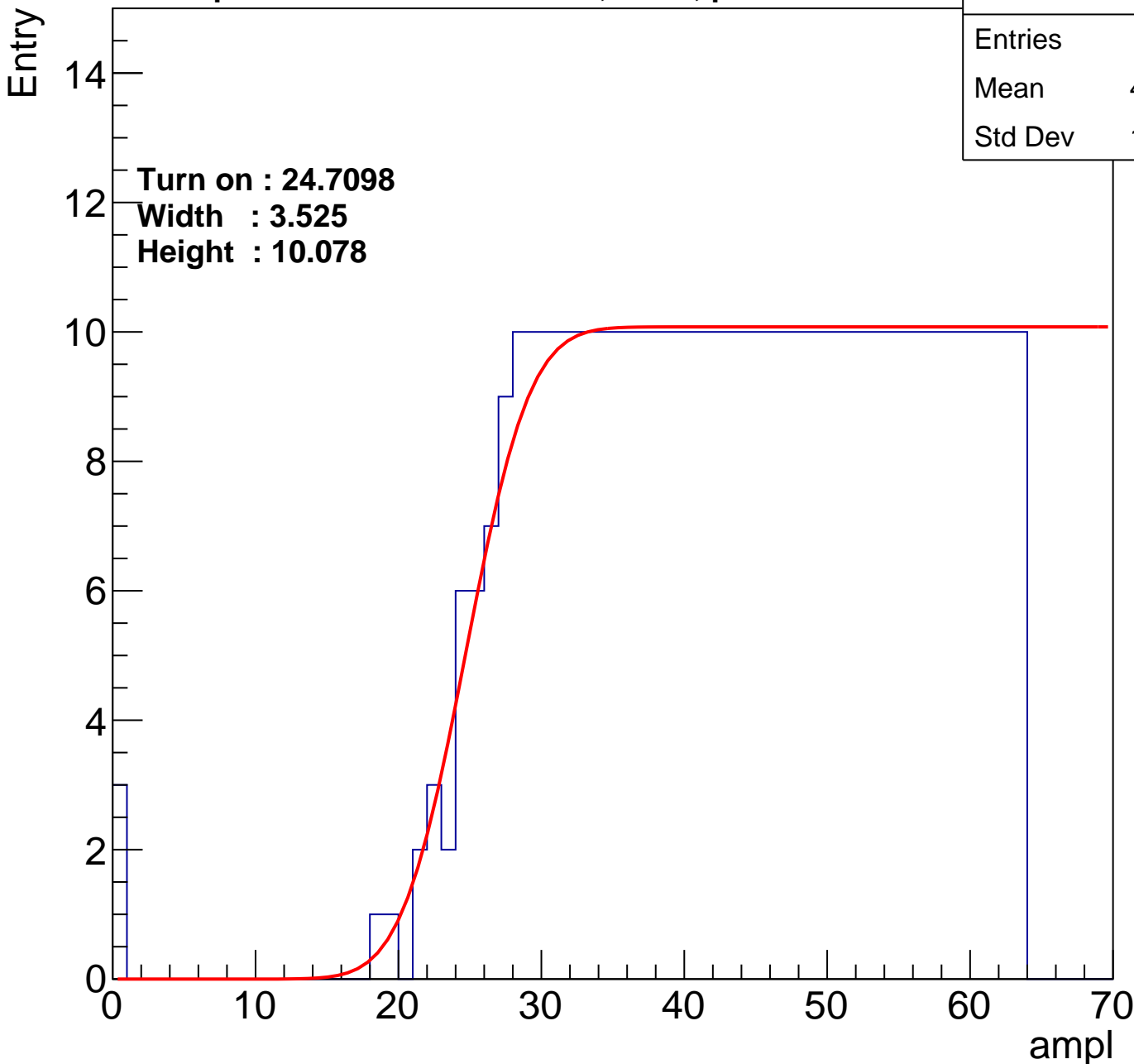
calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.23
Std Dev	12.18

Turn on : 24.7098

Width : 3.525

Height : 10.078



B1L000S, U6-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.22
Std Dev	11.48

Turn on : 26.1586

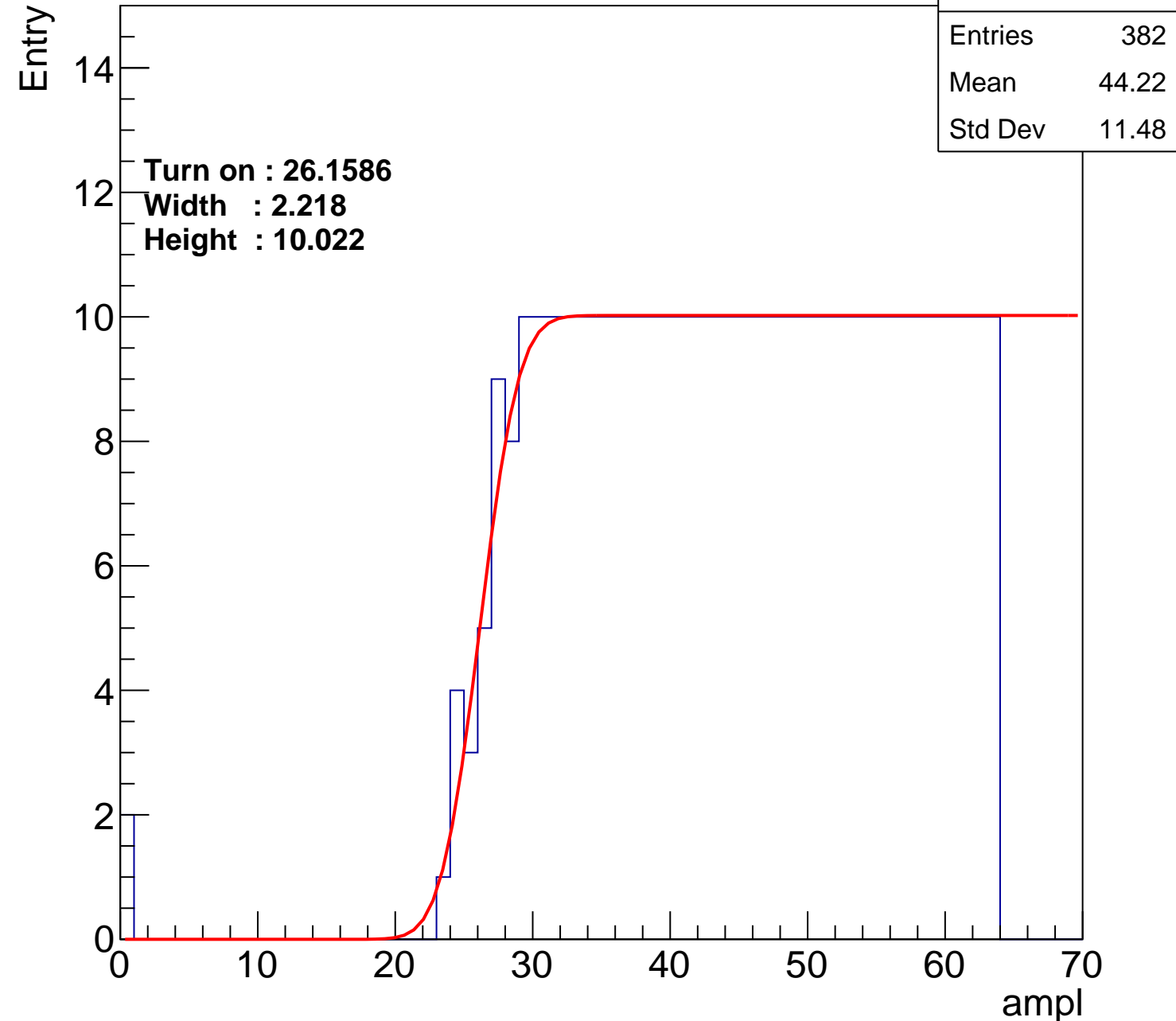
Width : 2.218

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch9

calib_packv5_042523_0143.root, FC#3, port B3

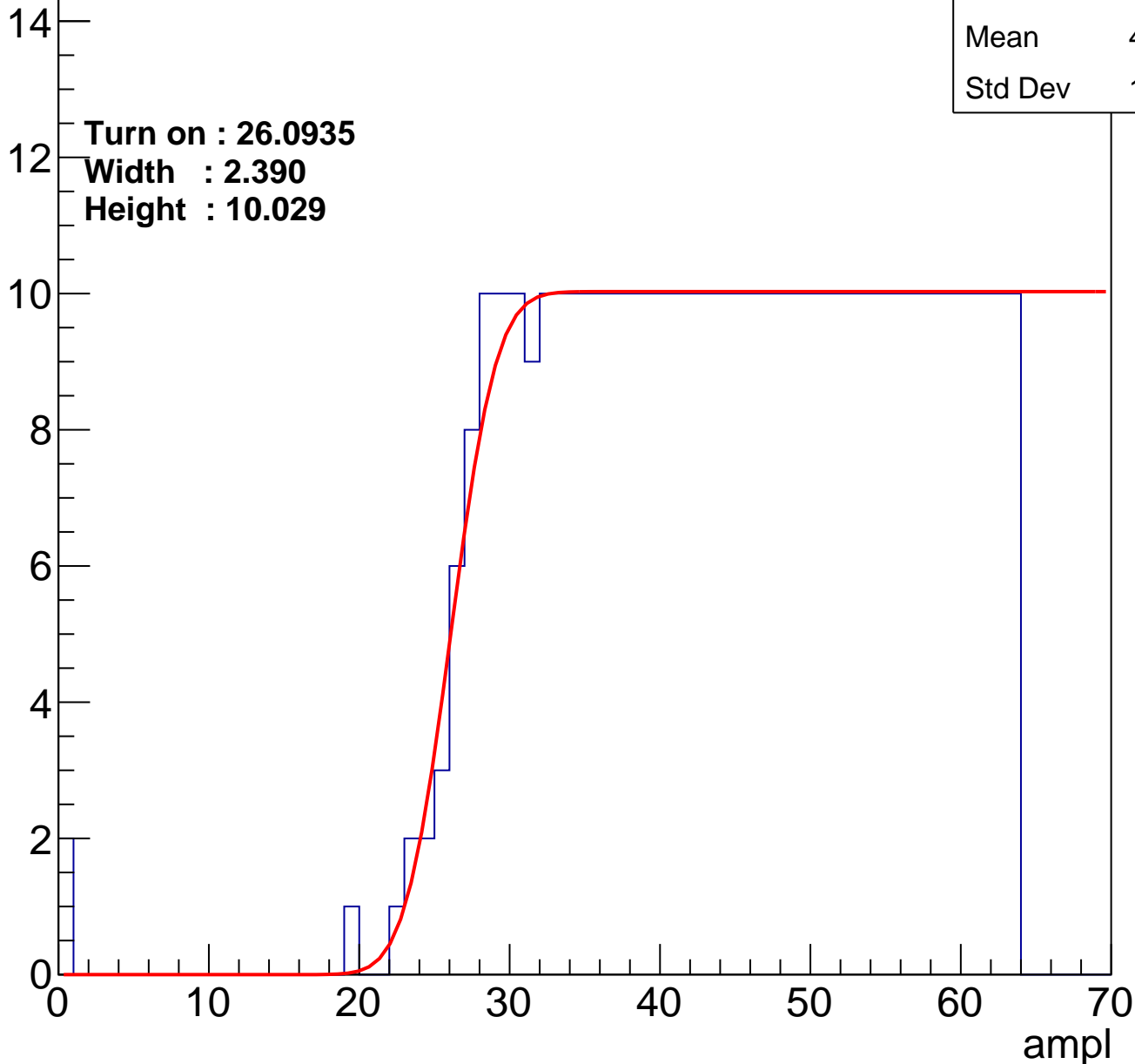
Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.0935

Width : 2.390

Height : 10.029

Entry



B1L000S, U6-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.36
Std Dev	12.45

Turn on : 25.2194

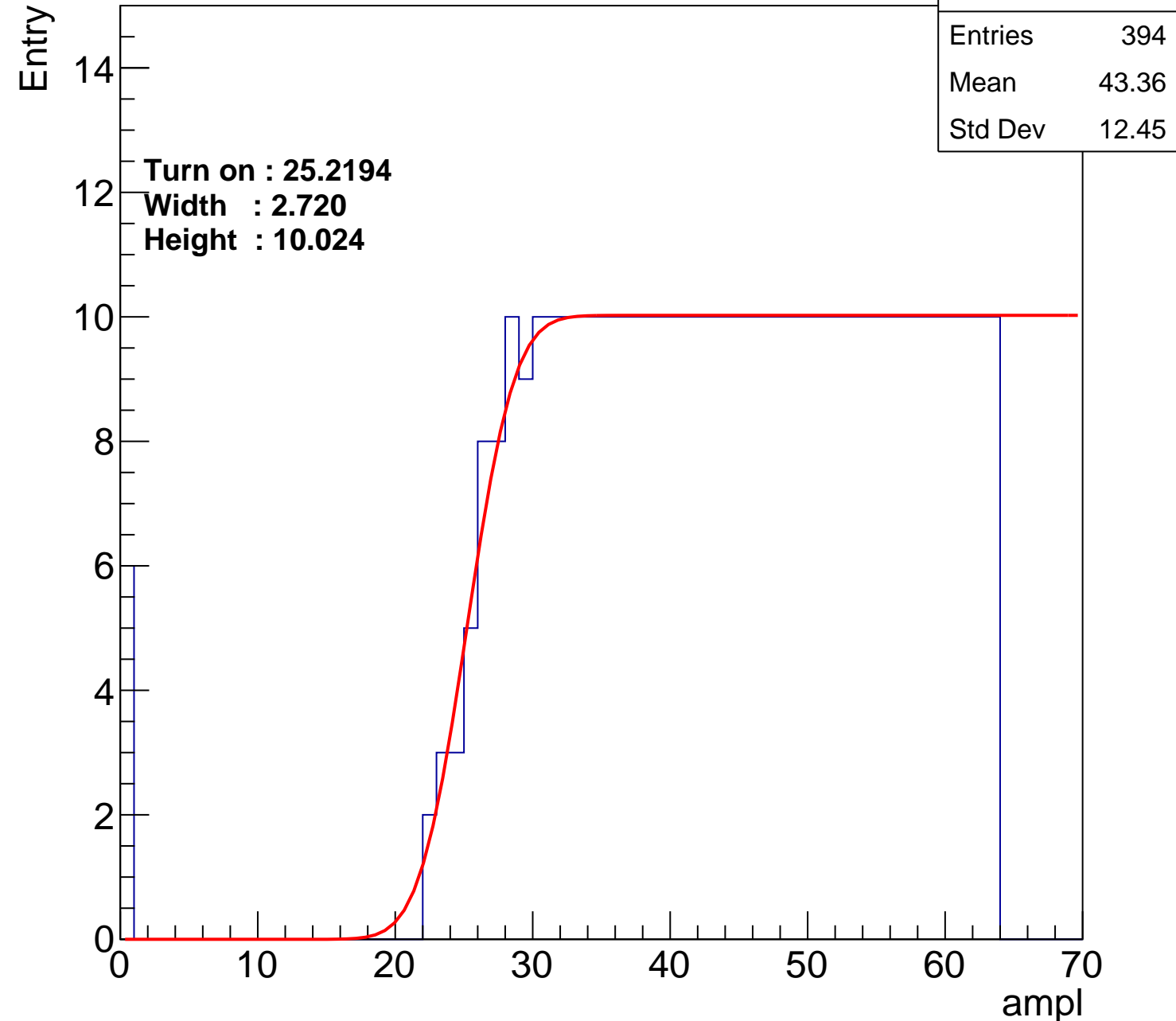
Width : 2.720

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.81
Std Dev	12.29

Turn on : 26.3329

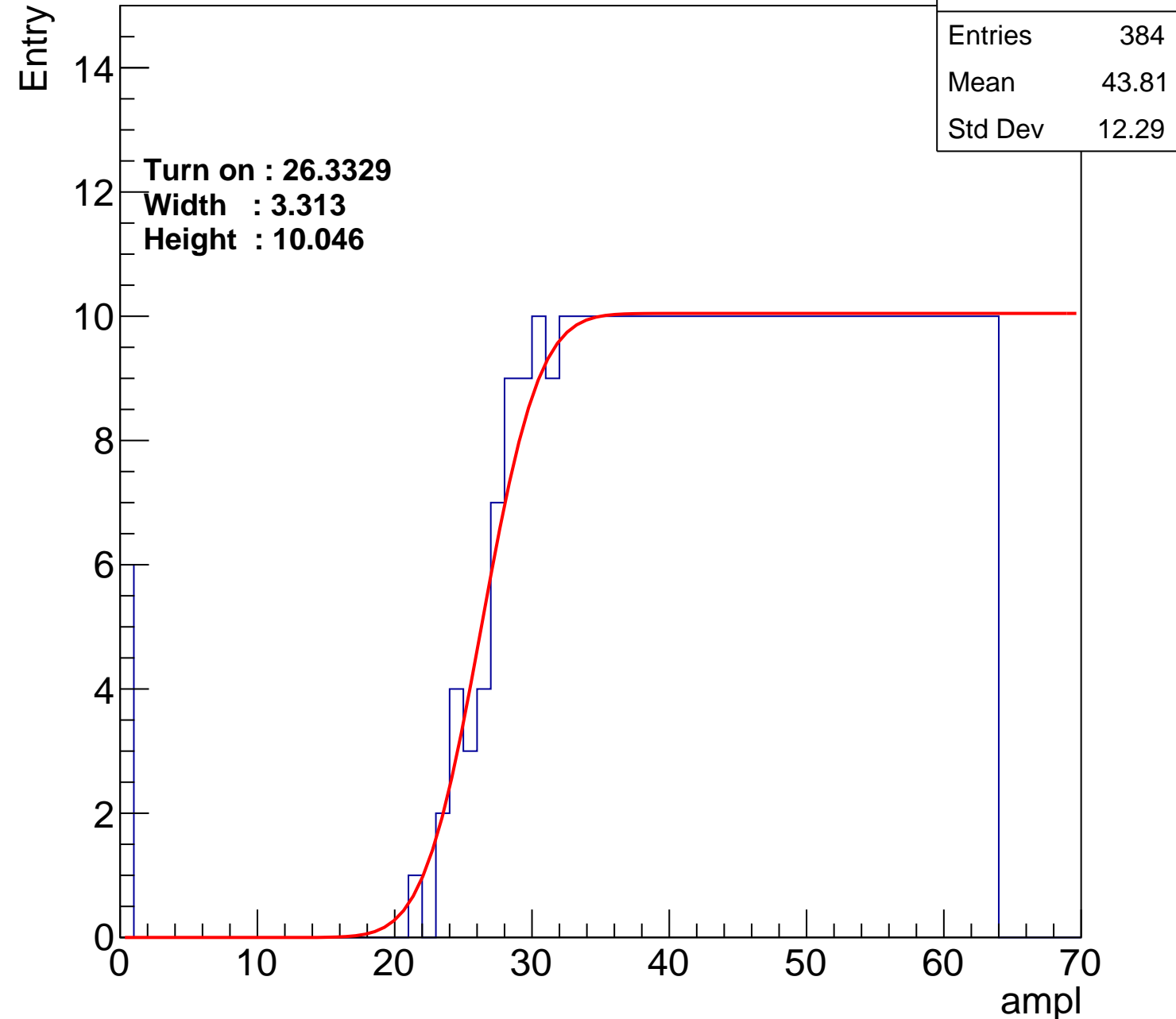
Width : 3.313

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.69
Std Dev	12.09

Turn on : 25.8289

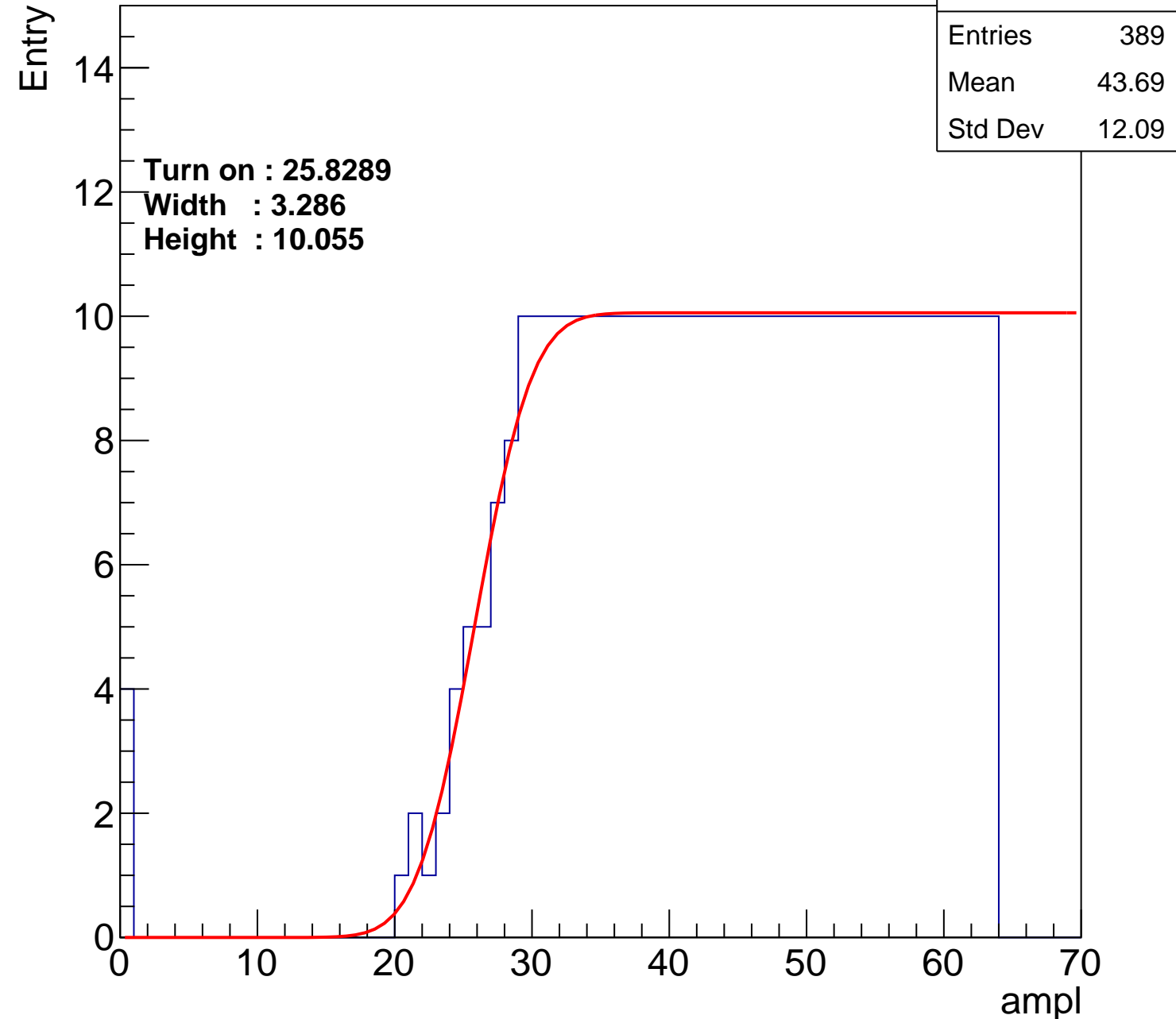
Width : 3.286

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch13

calib_packv5_042523_0143.root, FC#3, port B3

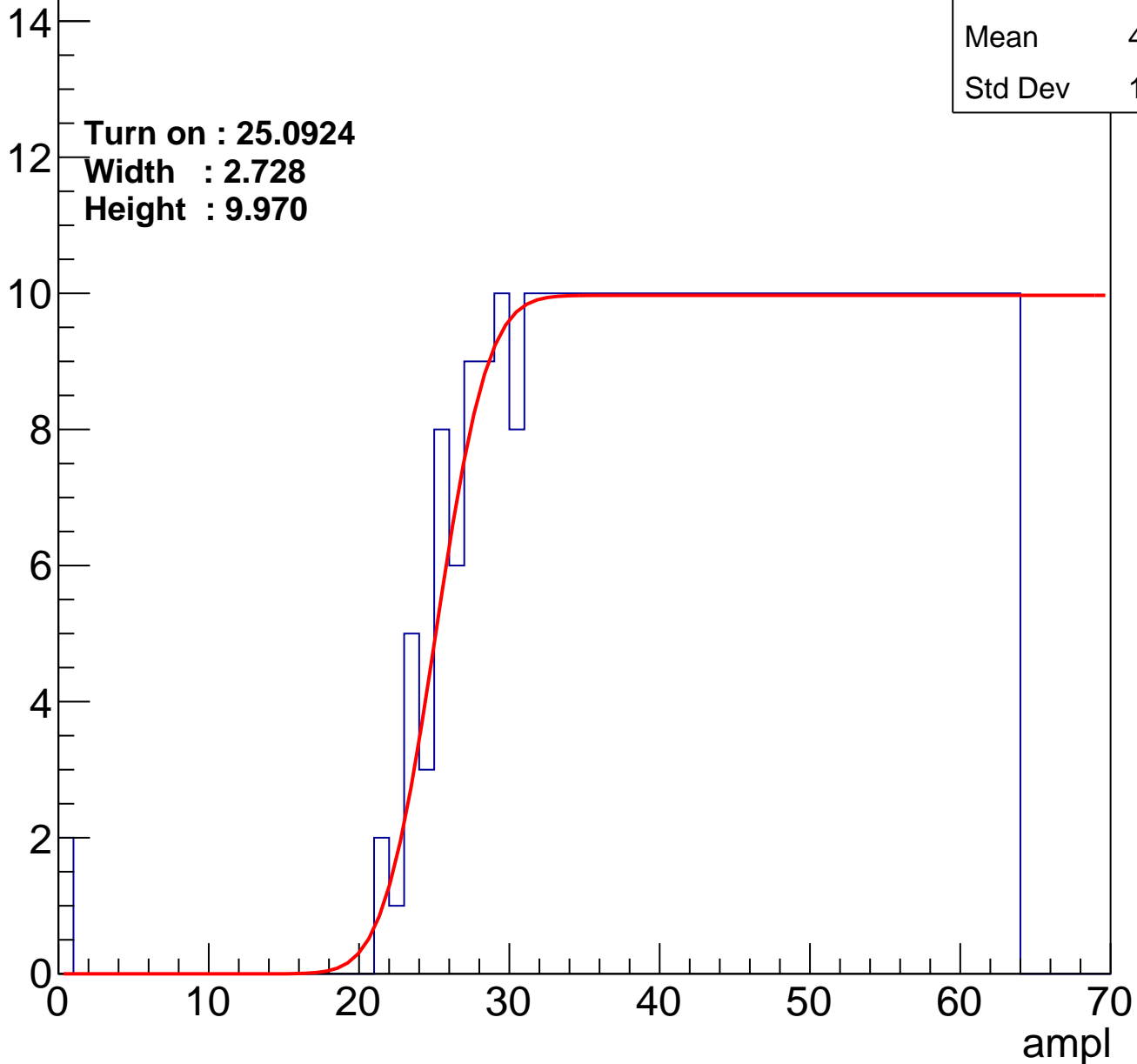
Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 25.0924

Width : 2.728

Height : 9.970

Entry



B1L000S, U6-ch14

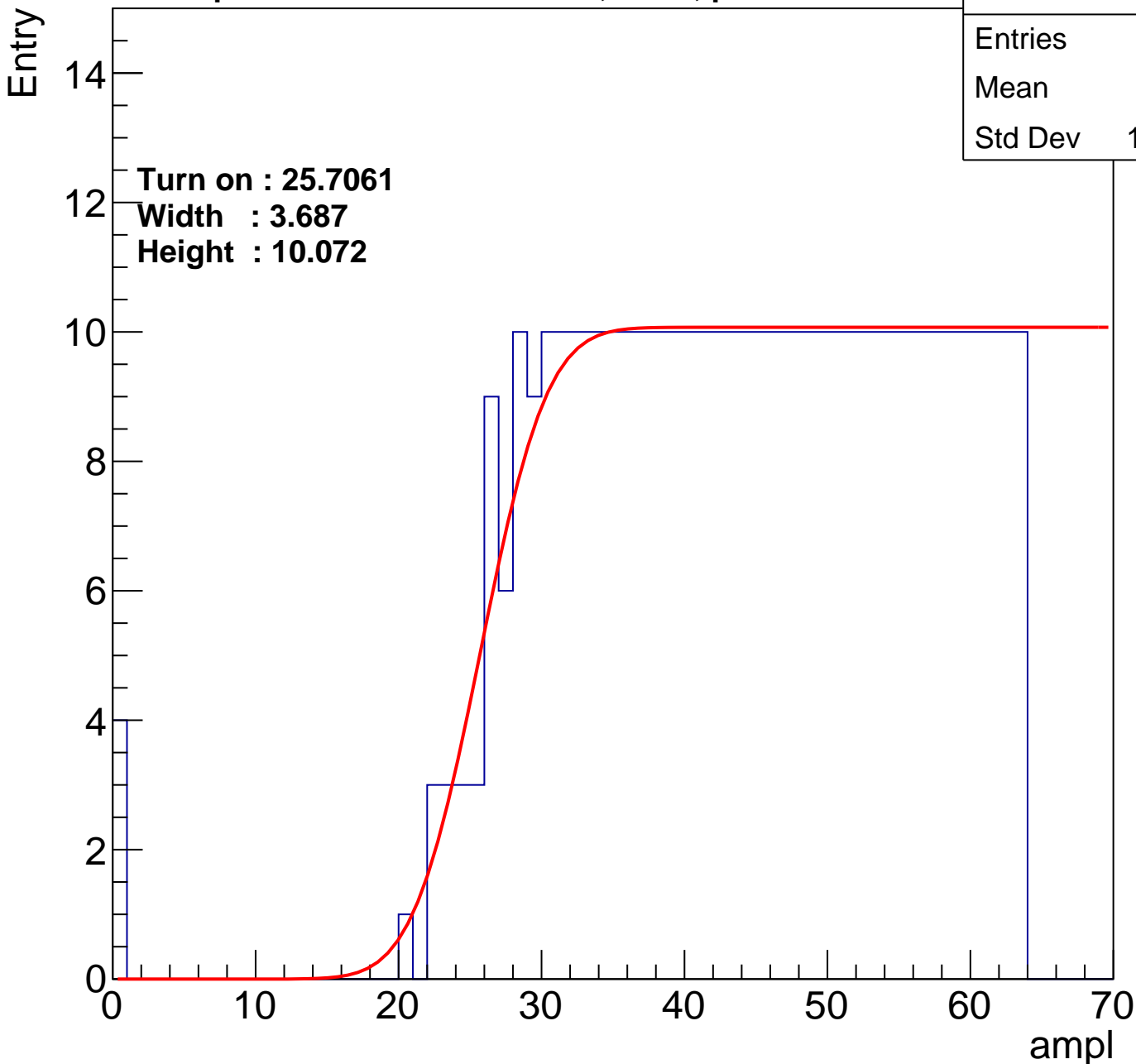
calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 25.7061

Width : 3.687

Height : 10.072



B1L000S, U6-ch15

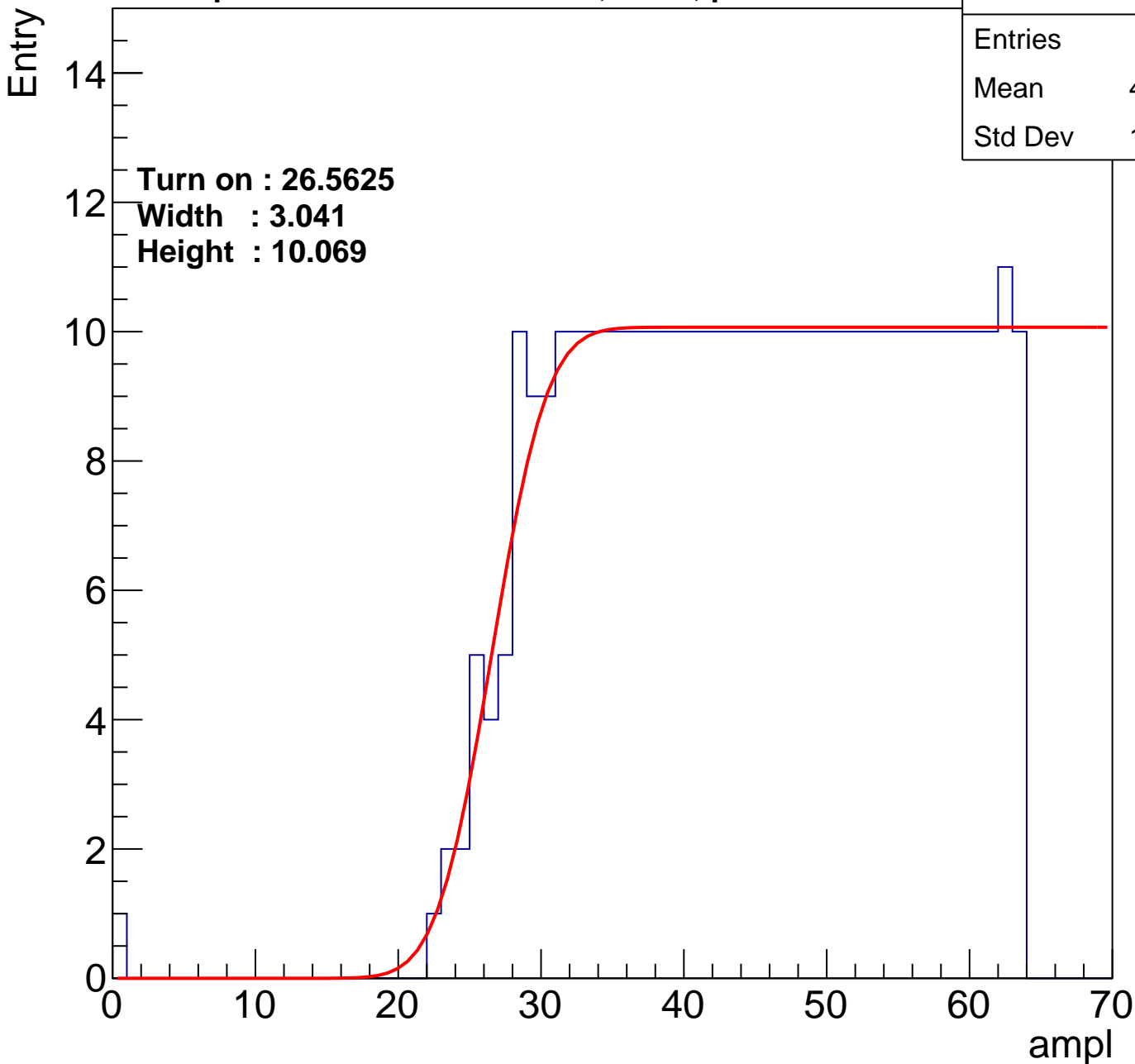
calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.49
Std Dev	11.27

Turn on : 26.5625

Width : 3.041

Height : 10.069



B1L000S, U6-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9817

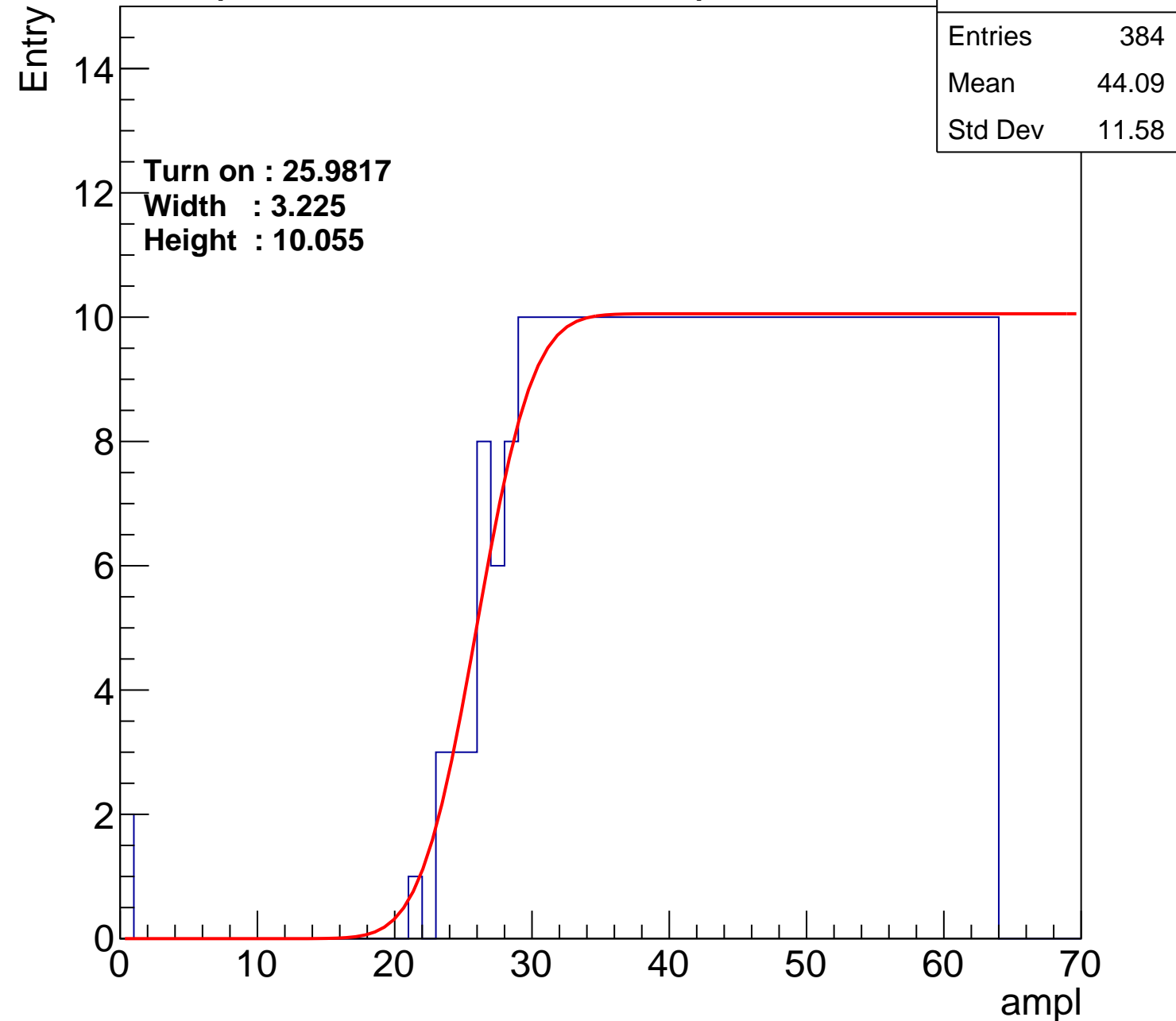
Width : 3.225

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.6952

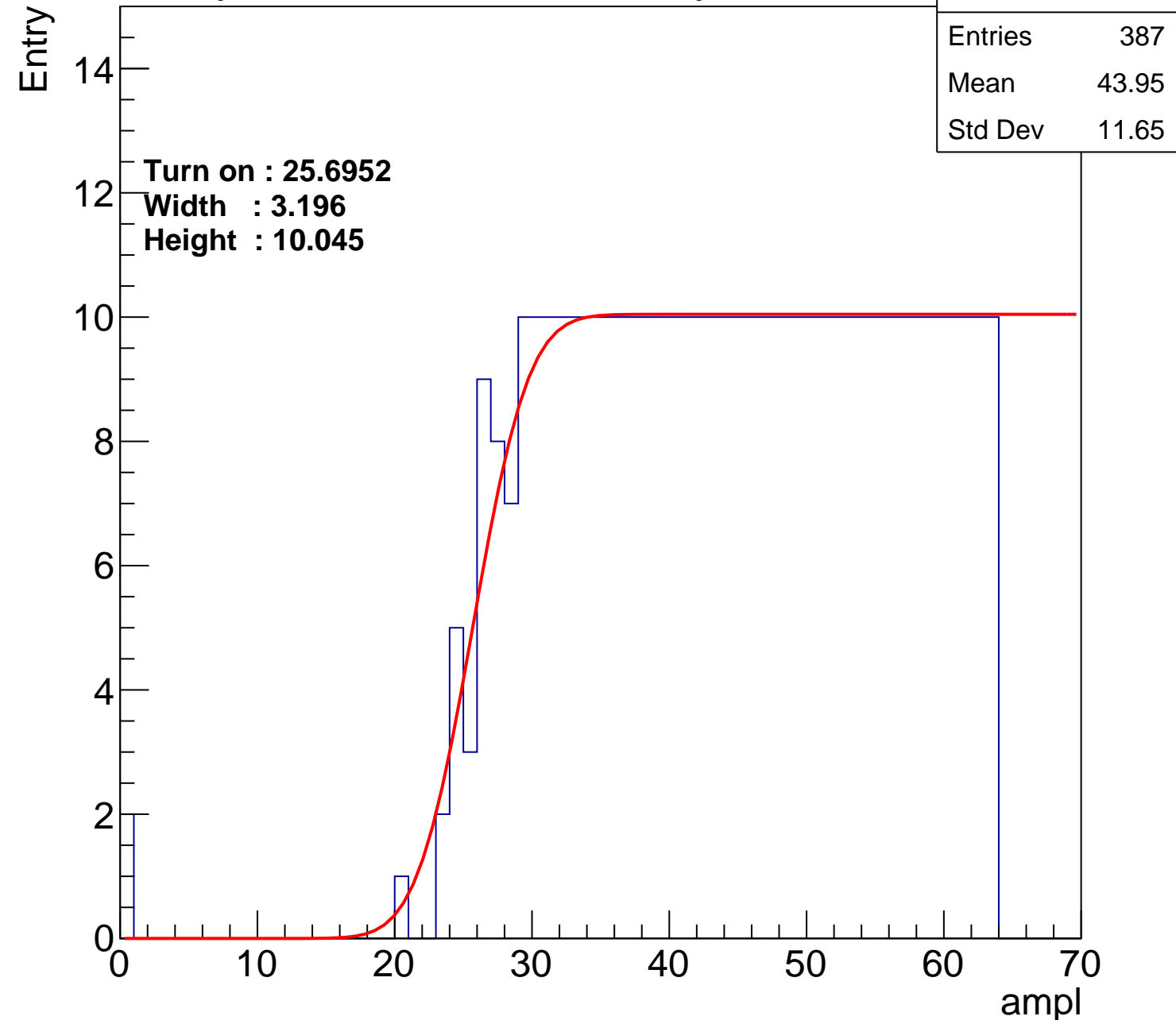
Width : 3.196

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.62
Std Dev	11.97

Turn on : 25.1252

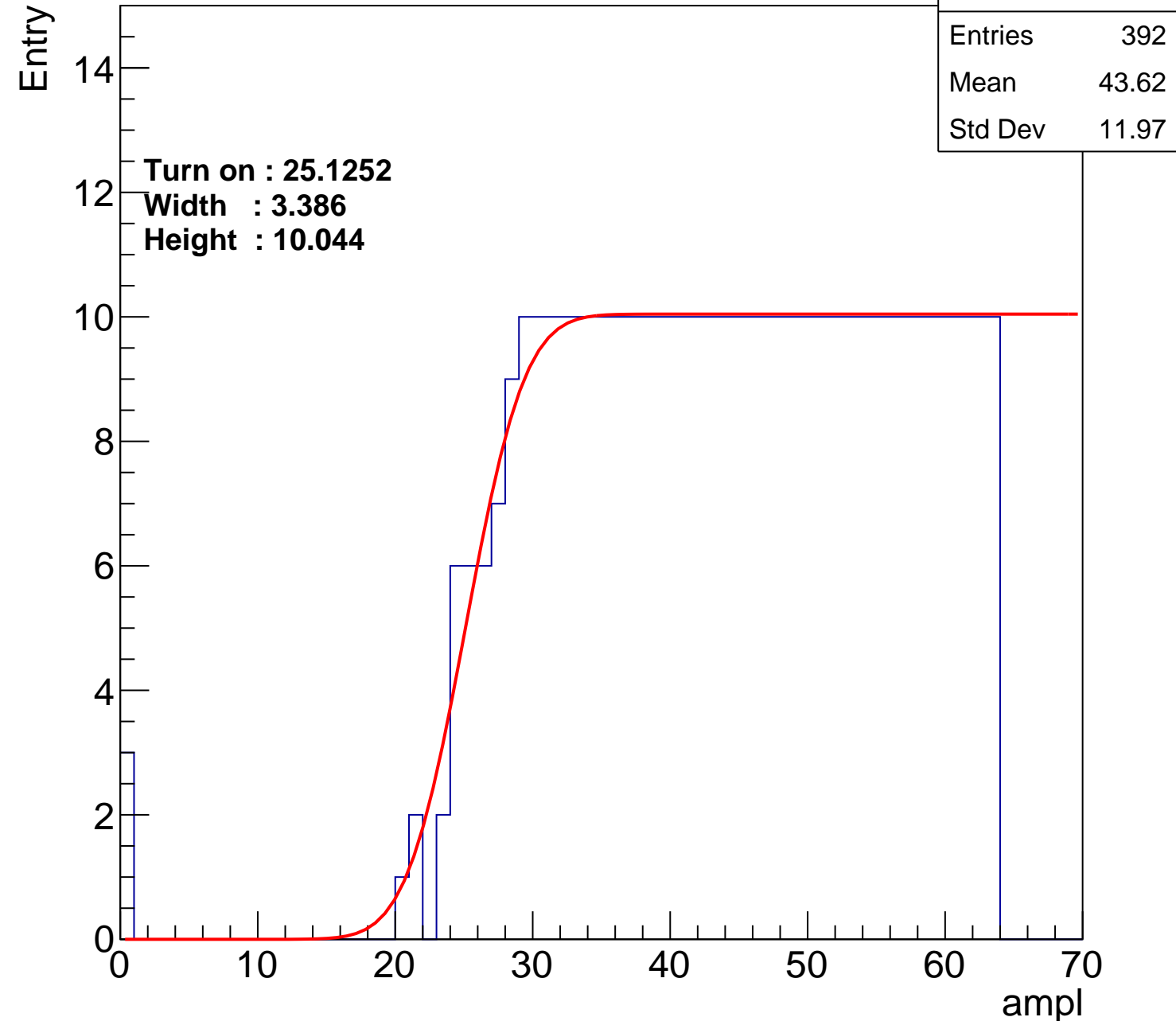
Width : 3.386

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.59
Std Dev	11.36

Turn on : 27.2989

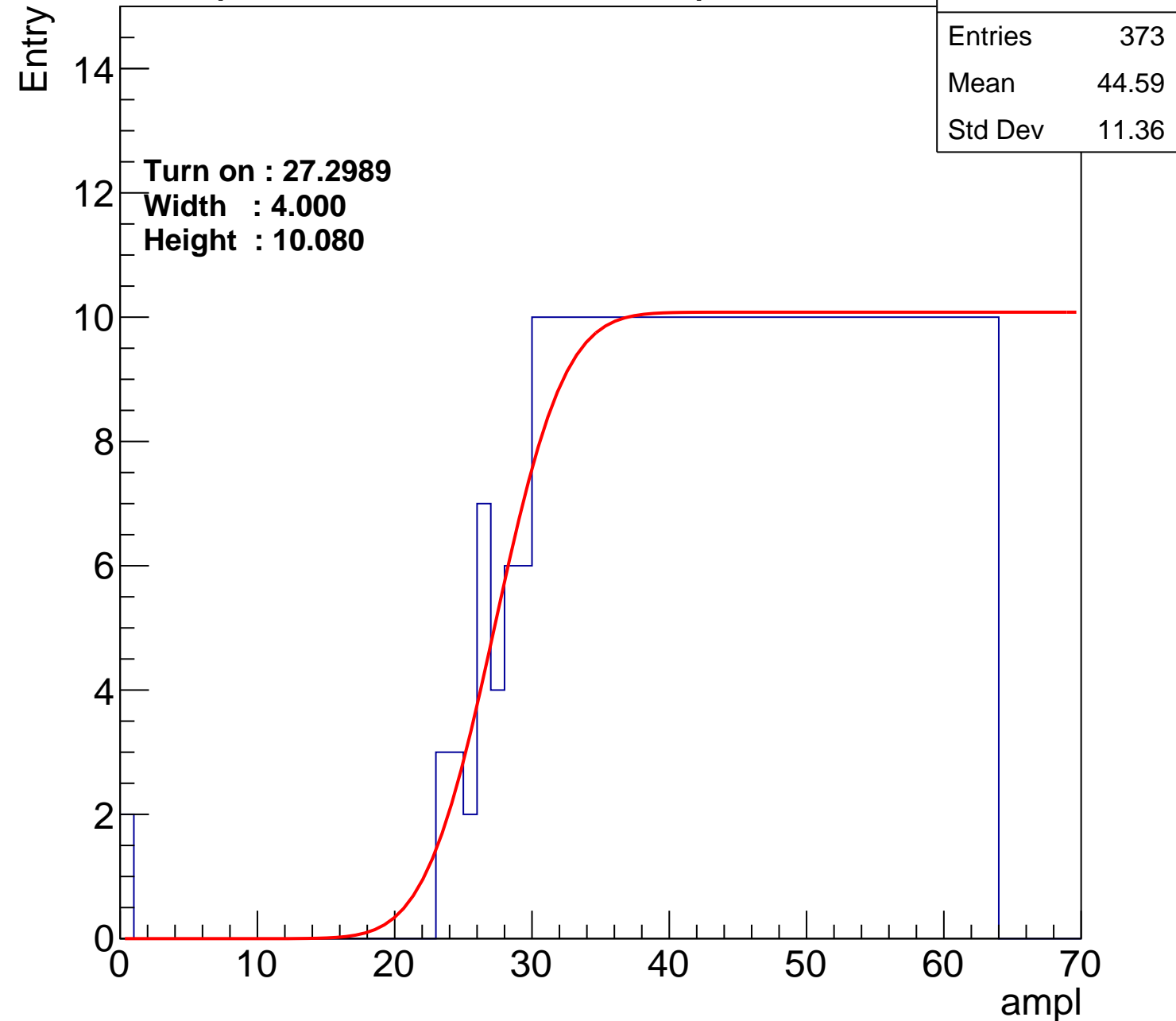
Width : 4.000

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.97
Std Dev	11.61

Turn on : 25.5963

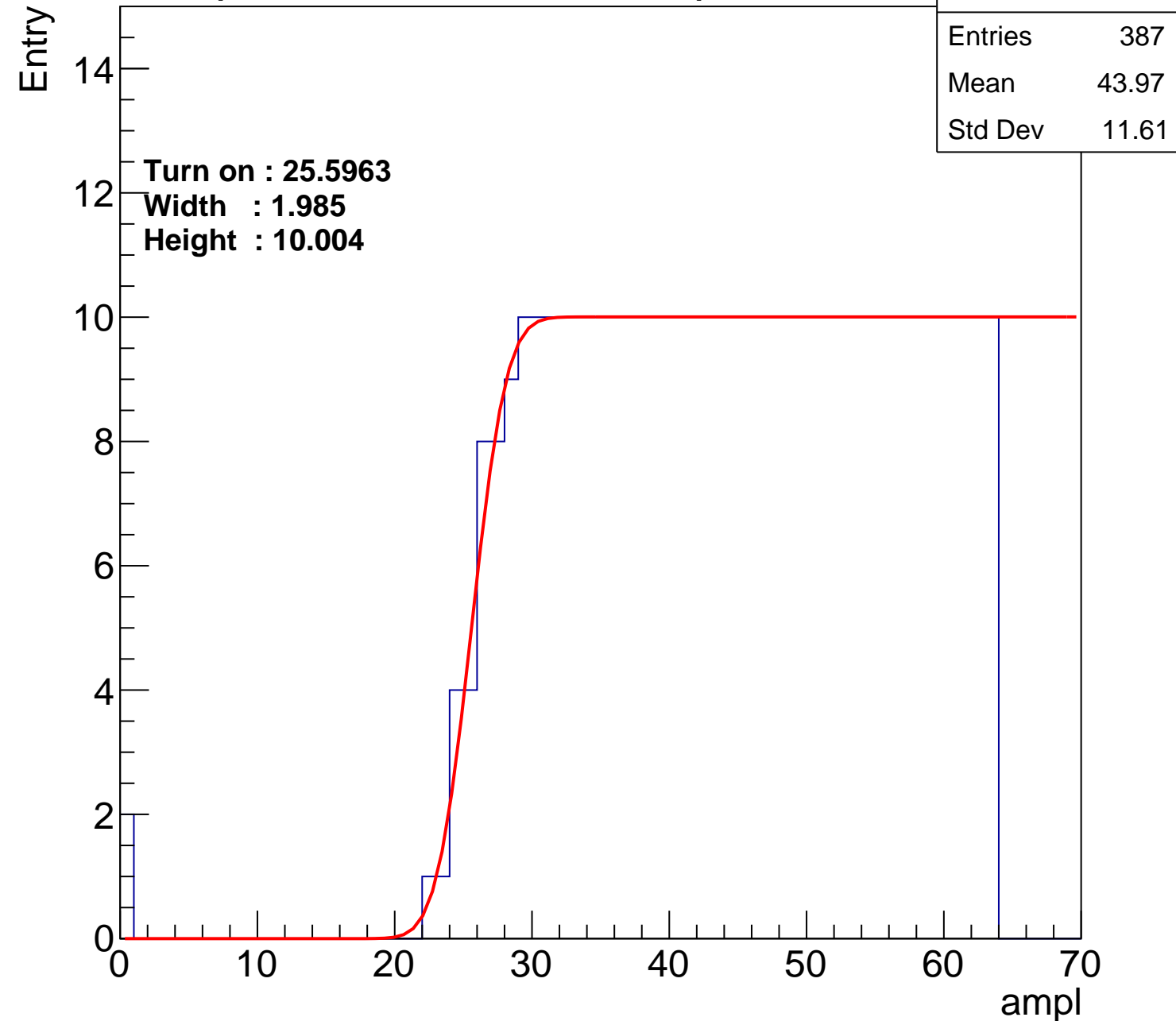
Width : 1.985

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch21

calib_packv5_042523_0143.root, FC#3, port B3

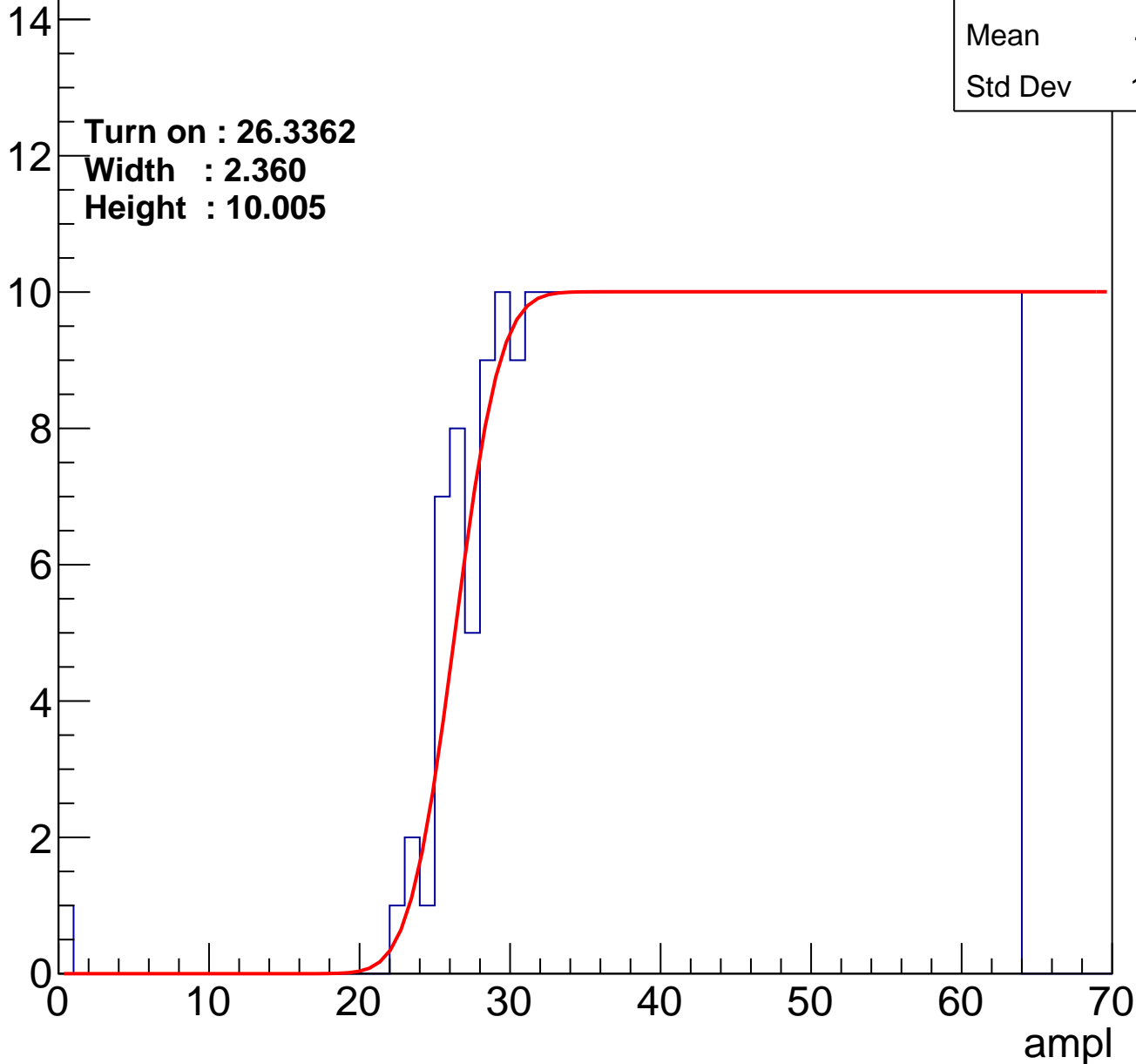
Entries	383
Mean	44.21
Std Dev	11.36

Turn on : 26.3362

Width : 2.360

Height : 10.005

Entry



B1L000S, U6-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 27.6794

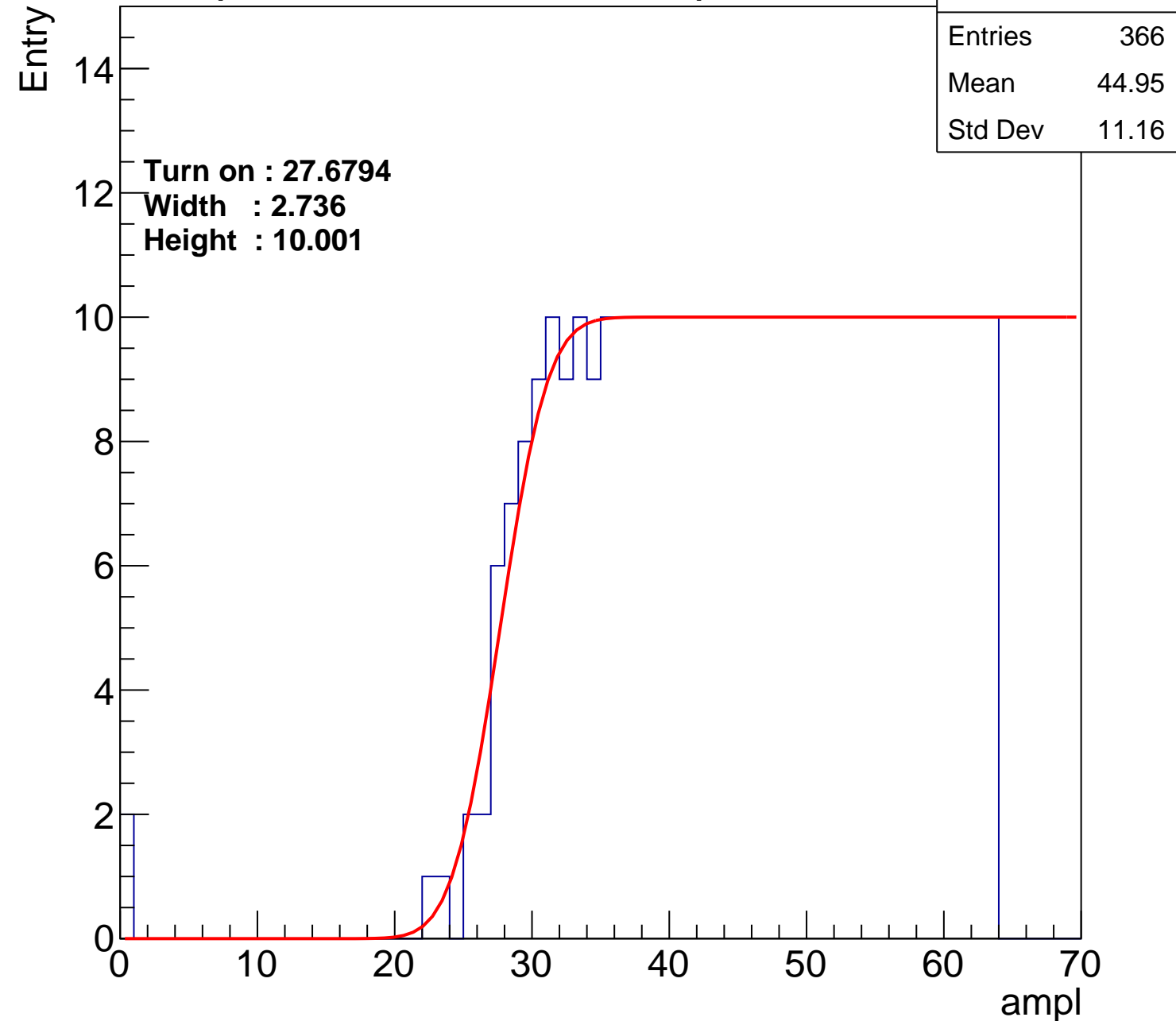
Width : 2.736

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.31
Std Dev	12.25

Turn on : 24.9240

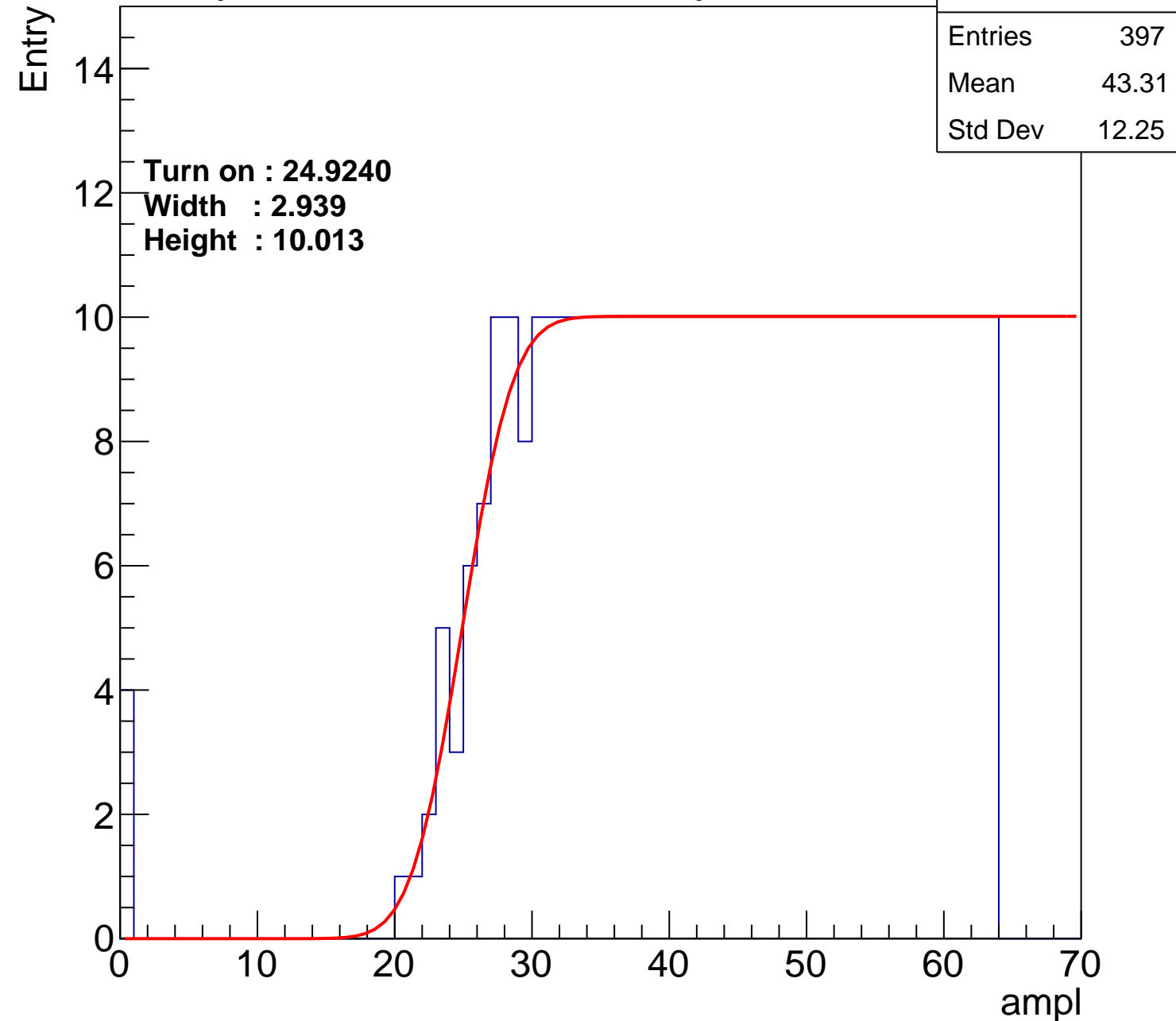
Width : 2.939

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.77
Std Dev	11.18

Turn on : 26.9003

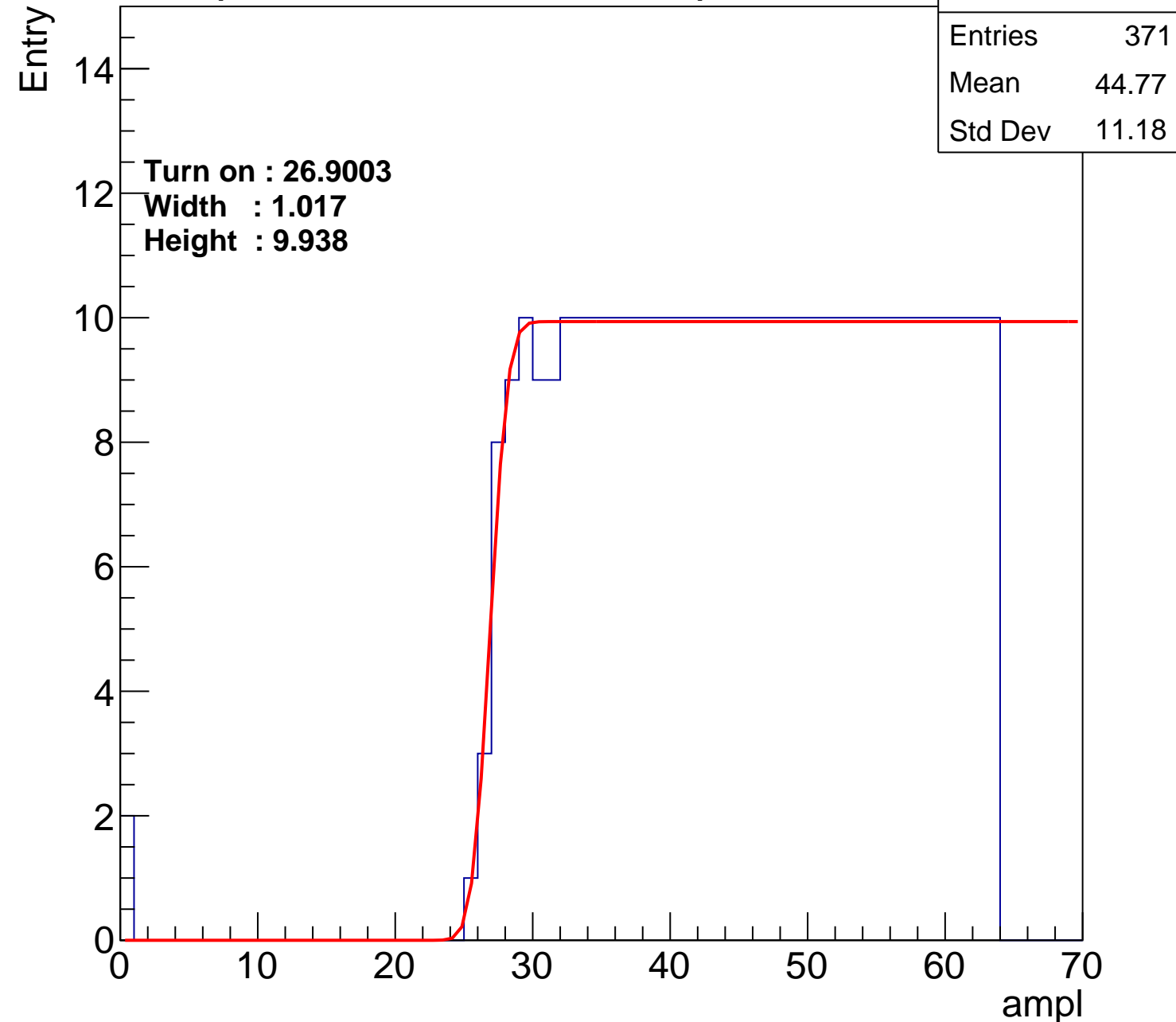
Width : 1.017

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.8
Std Dev	12.14

Turn on : 25.5512

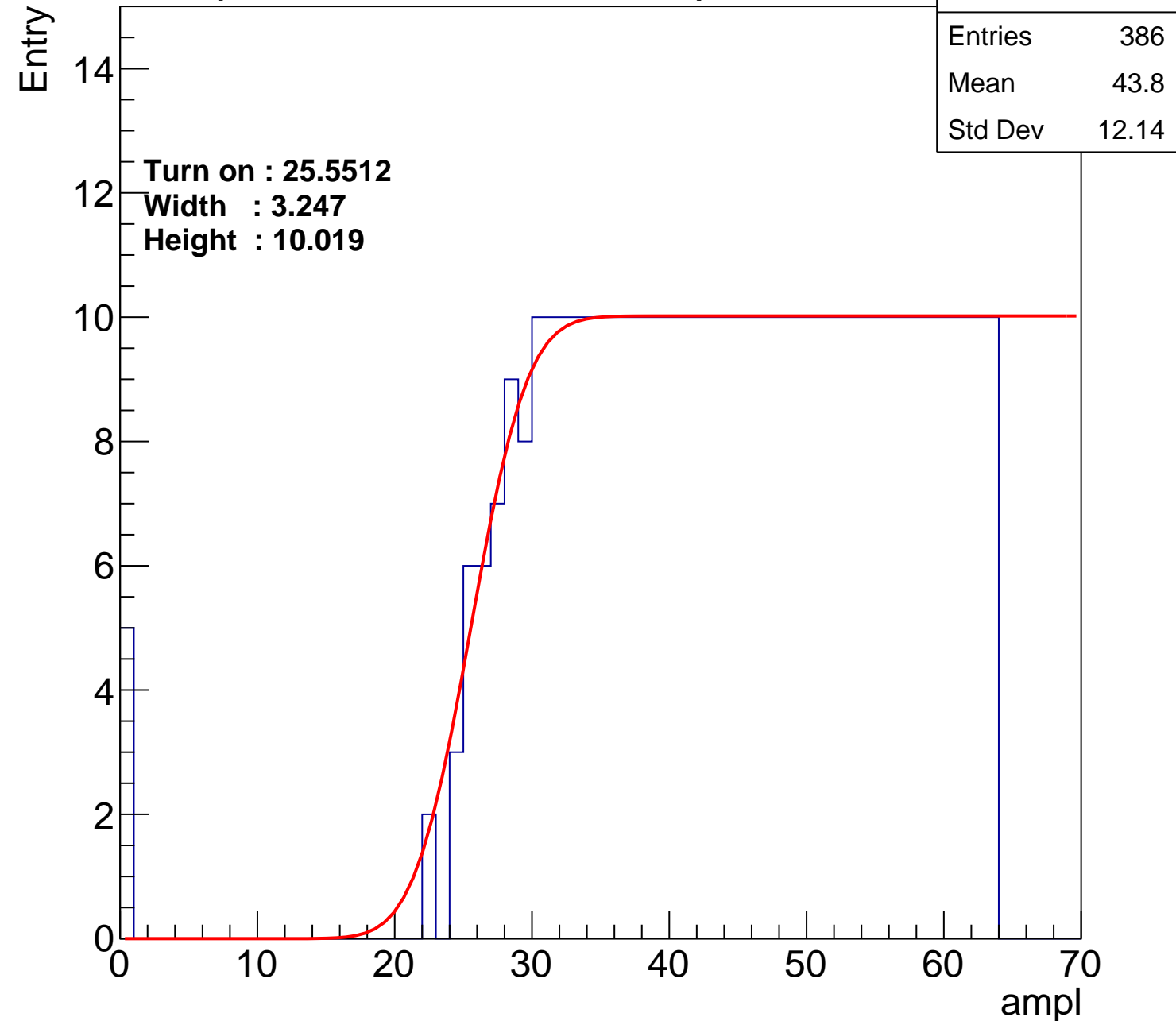
Width : 3.247

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.31
Std Dev	11.98

Turn on : 24.5688

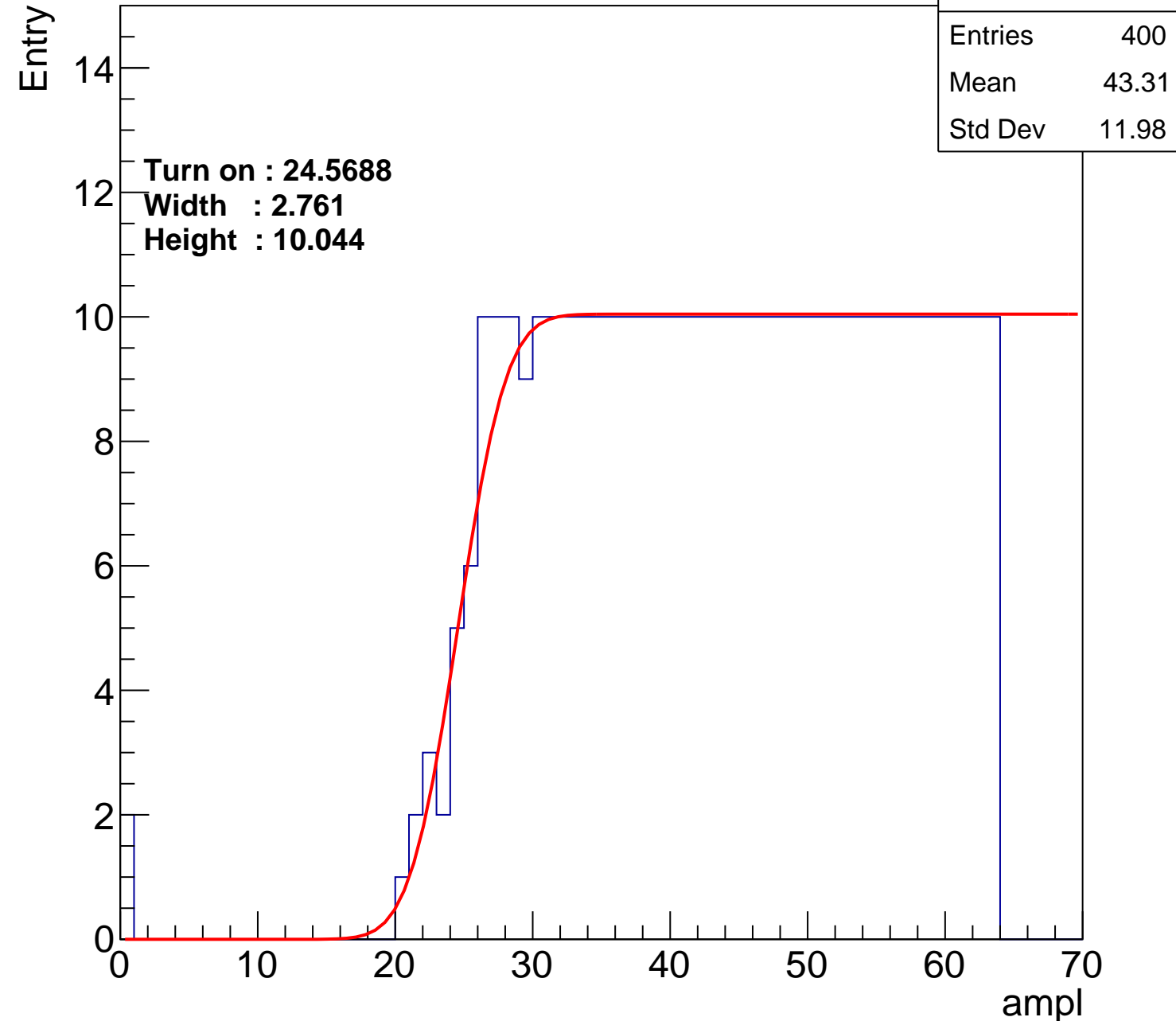
Width : 2.761

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.8
Std Dev	11.9

Turn on : 25.5387

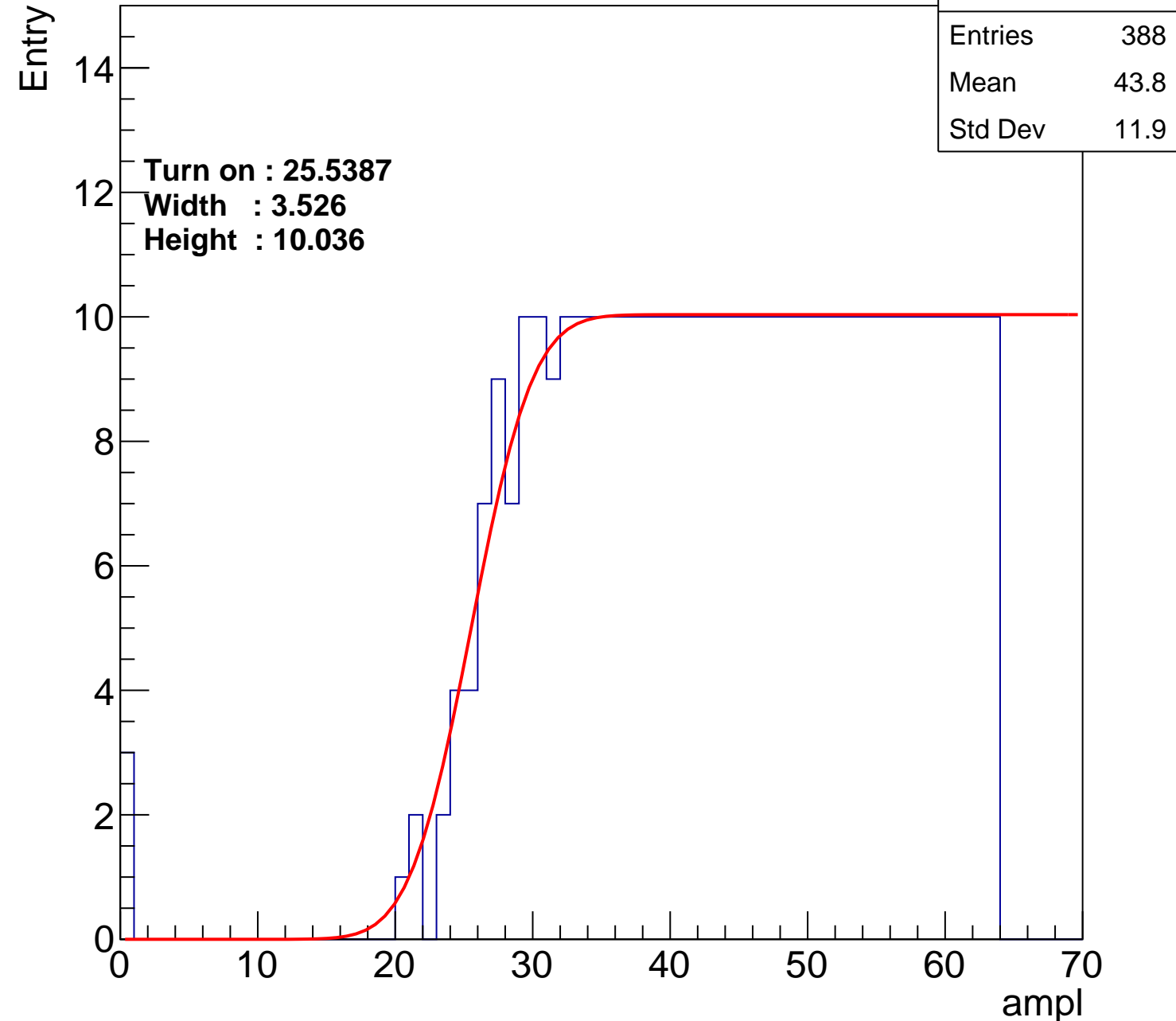
Width : 3.526

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch28

calib_packv5_042523_0143.root, FC#3, port B3

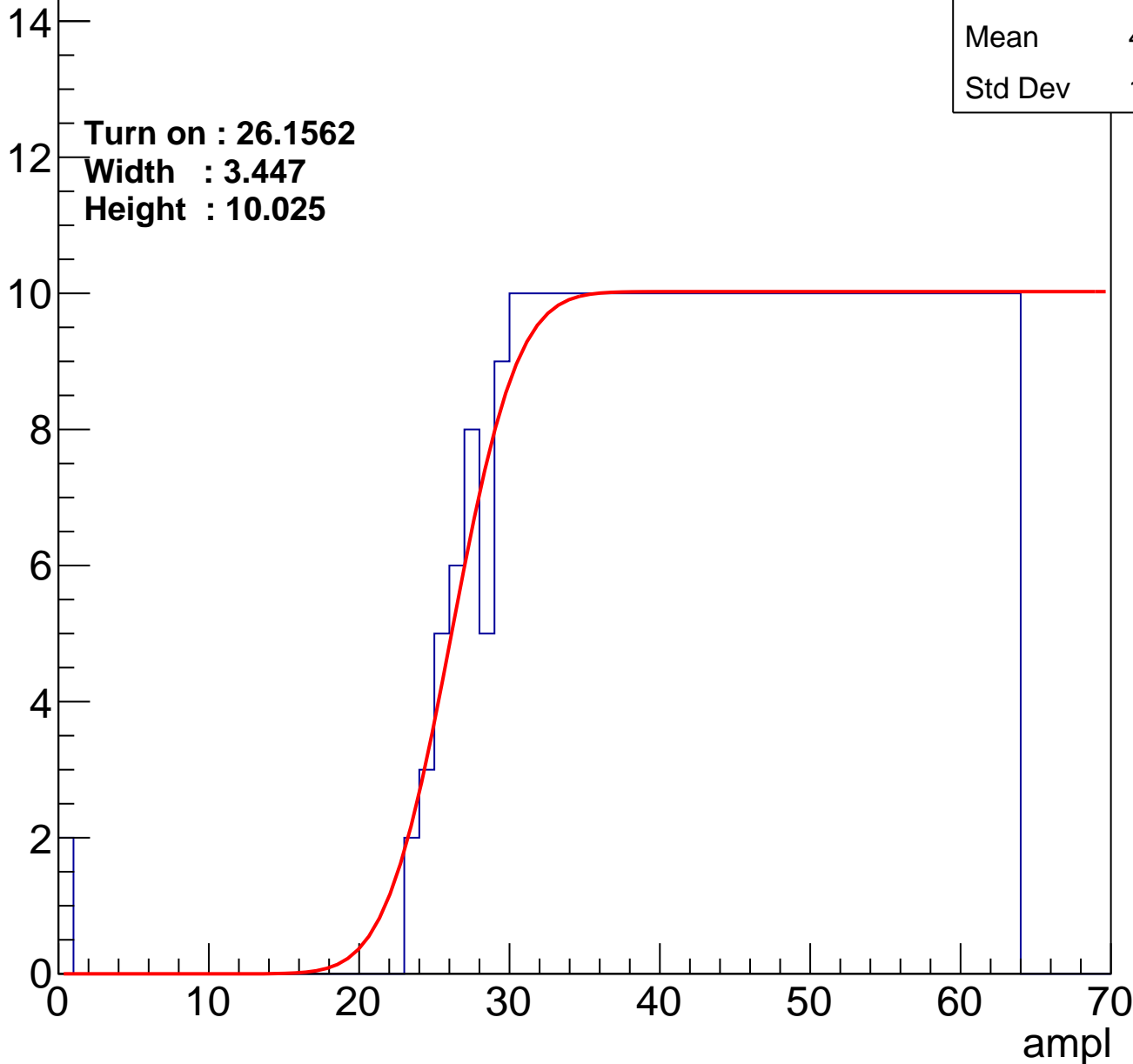
Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.1562

Width : 3.447

Height : 10.025

Entry



B1L000S, U6-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	358
Mean	45.44
Std Dev	10.7

Turn on : 28.5323

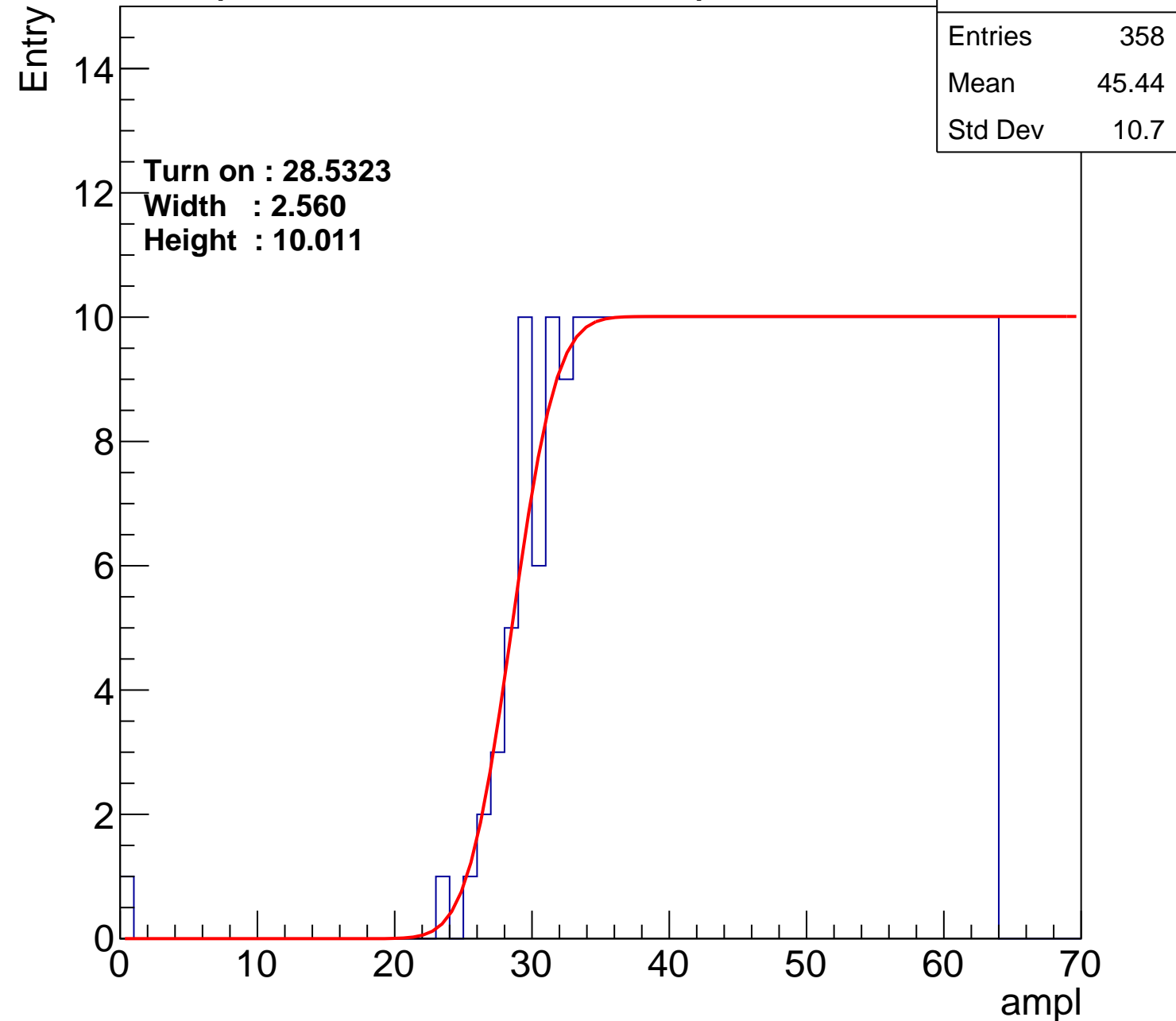
Width : 2.560

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.75
Std Dev	11.74

Turn on : 24.9724

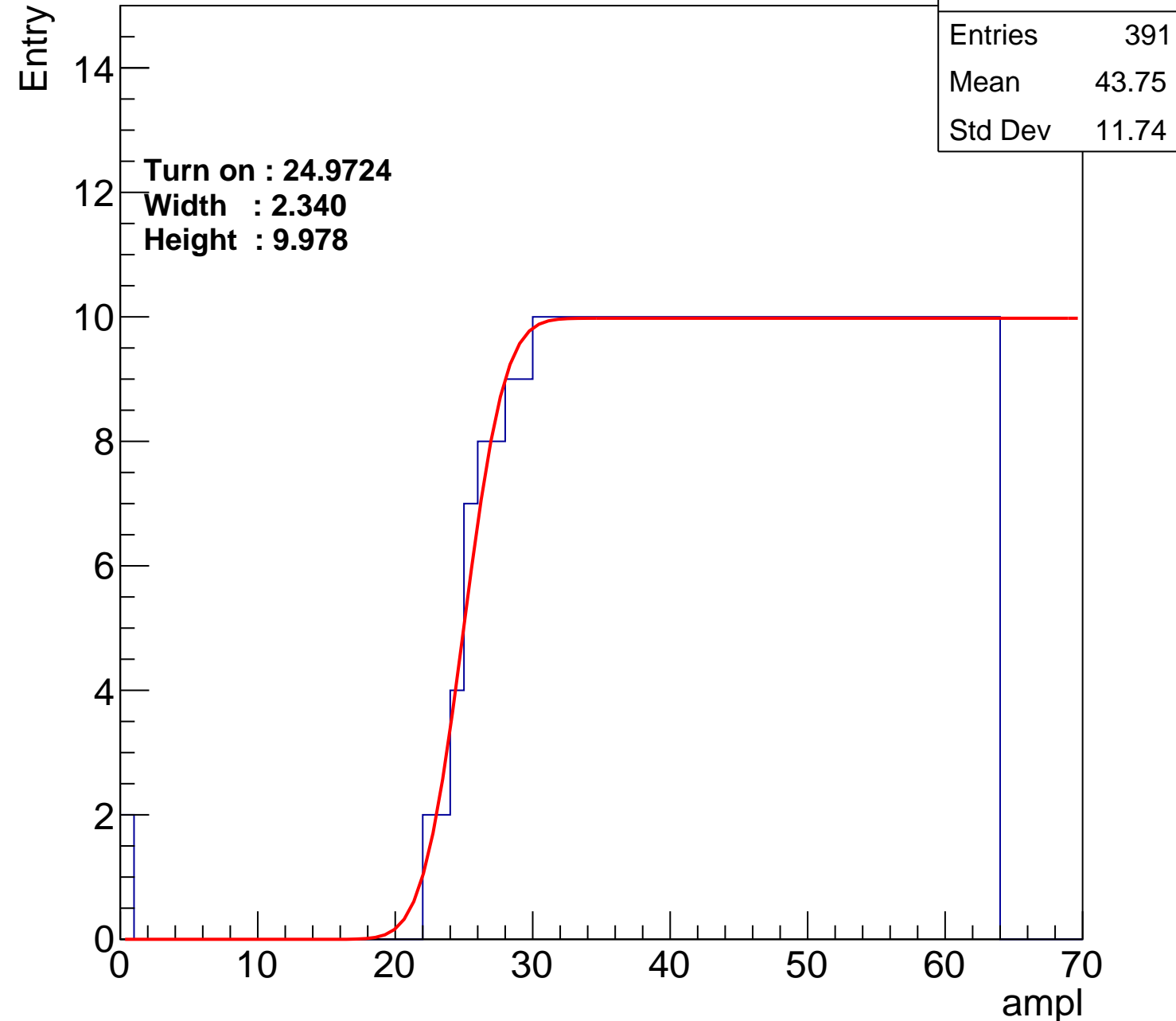
Width : 2.340

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.48
Std Dev	12.21

Turn on : 25.6736

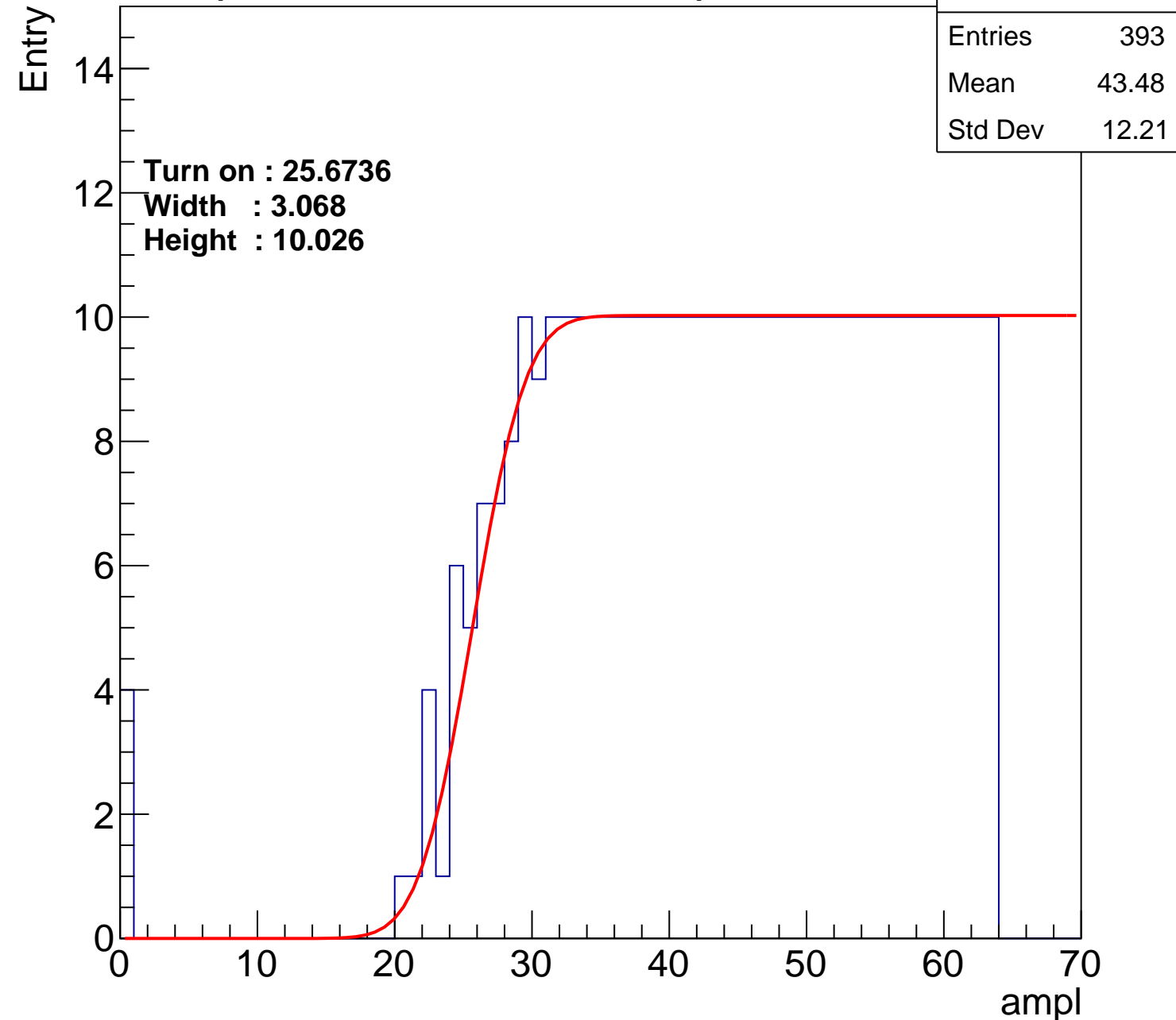
Width : 3.068

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch32

calib_packv5_042523_0143.root, FC#3, port B3

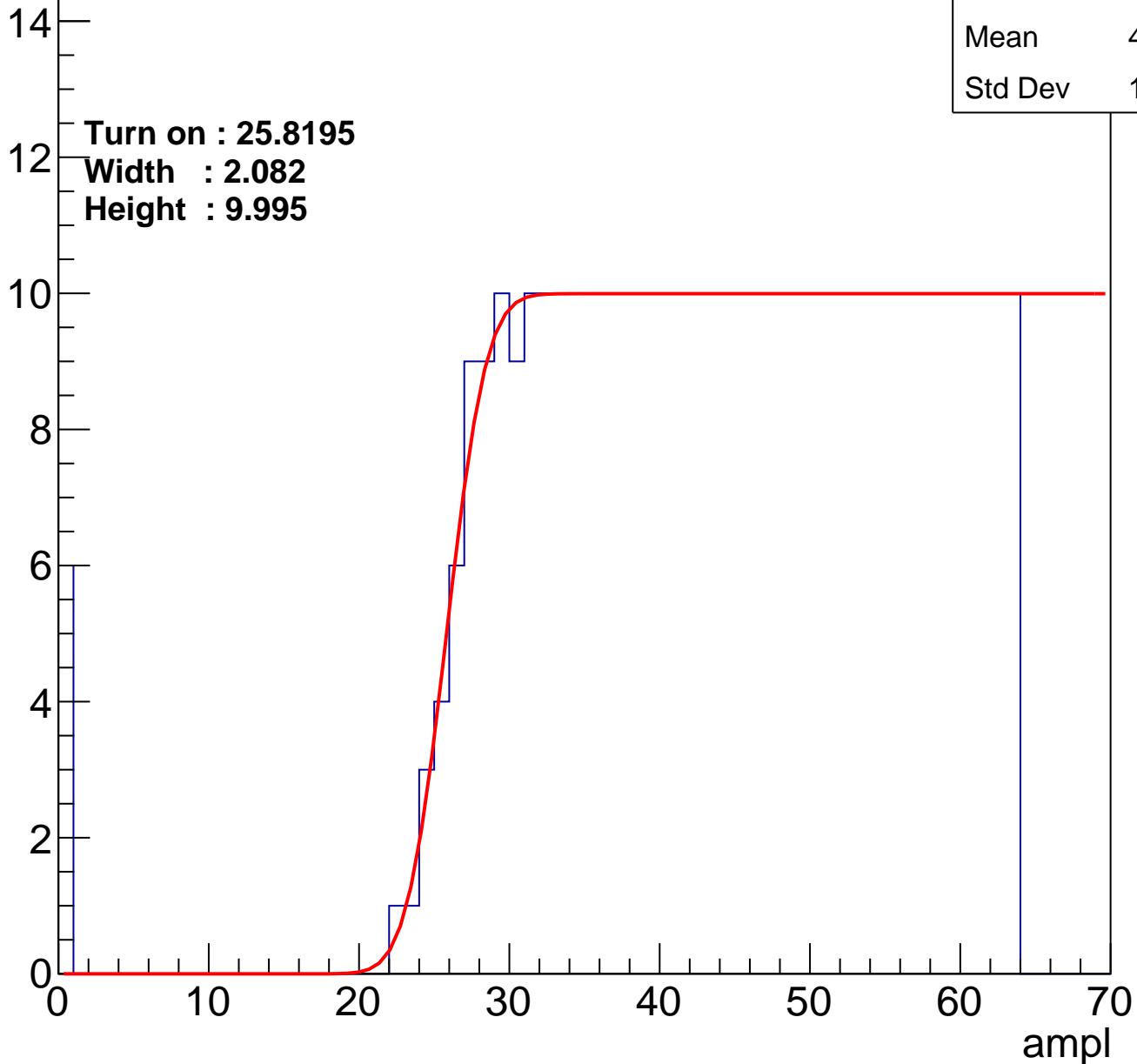
Entries	388
Mean	43.65
Std Dev	12.32

Turn on : 25.8195

Width : 2.082

Height : 9.995

Entry



B1L000S, U6-ch33

calib_packv5_042523_0143.root, FC#3, port B3

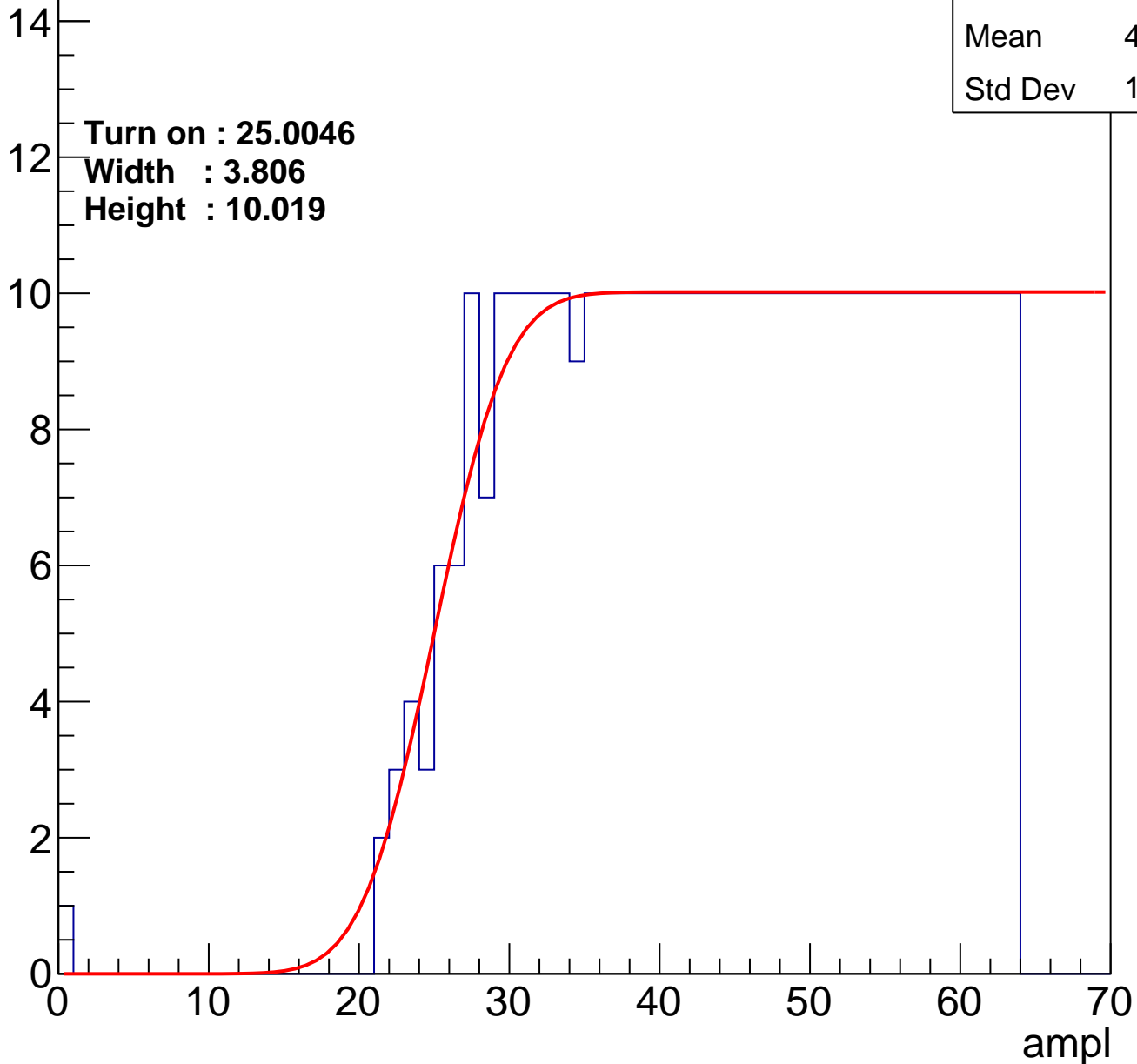
Entries	391
Mean	43.76
Std Dev	11.66

Turn on : 25.0046

Width : 3.806

Height : 10.019

Entry



B1L000S, U6-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.9488

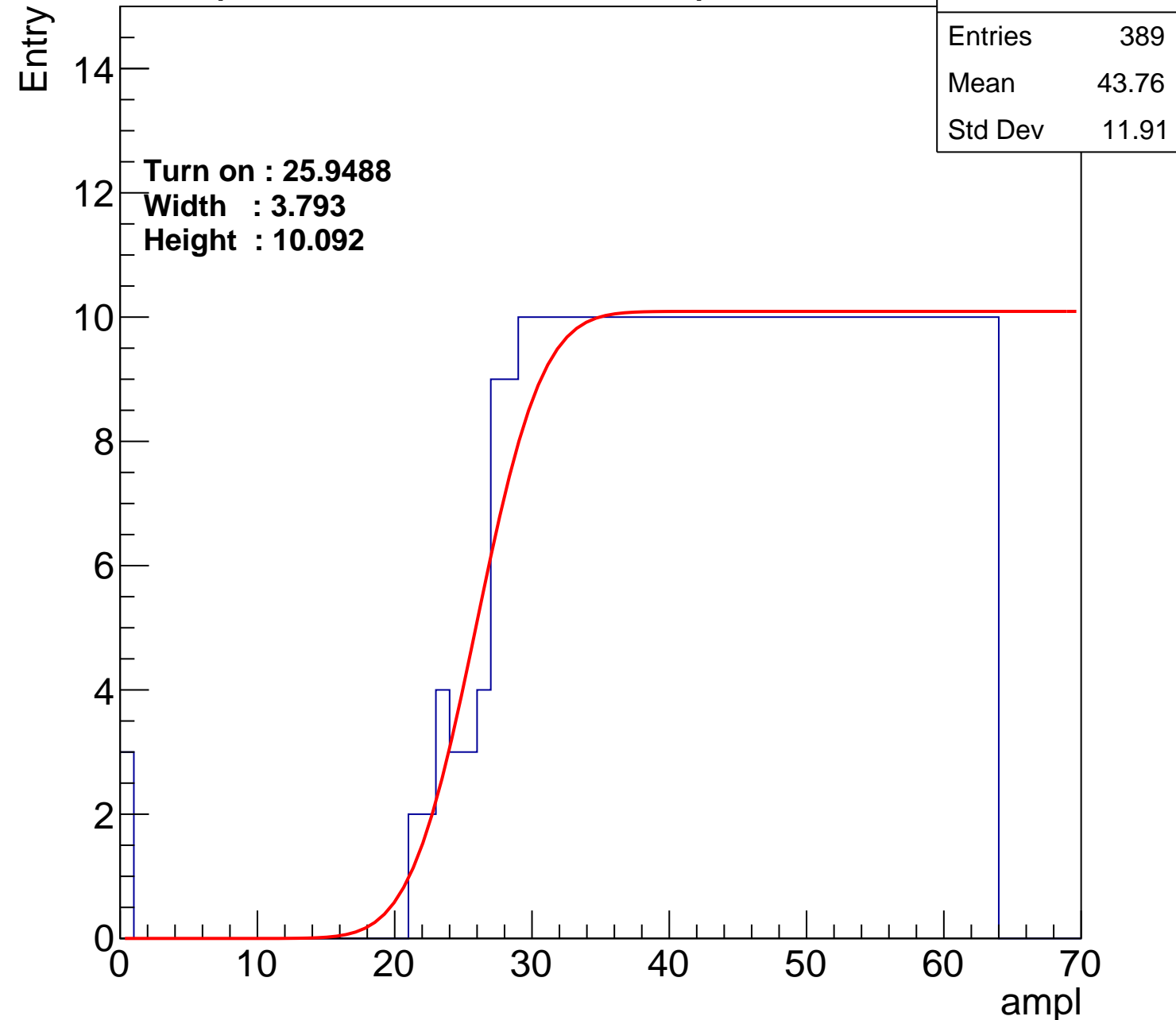
Width : 3.793

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.8872

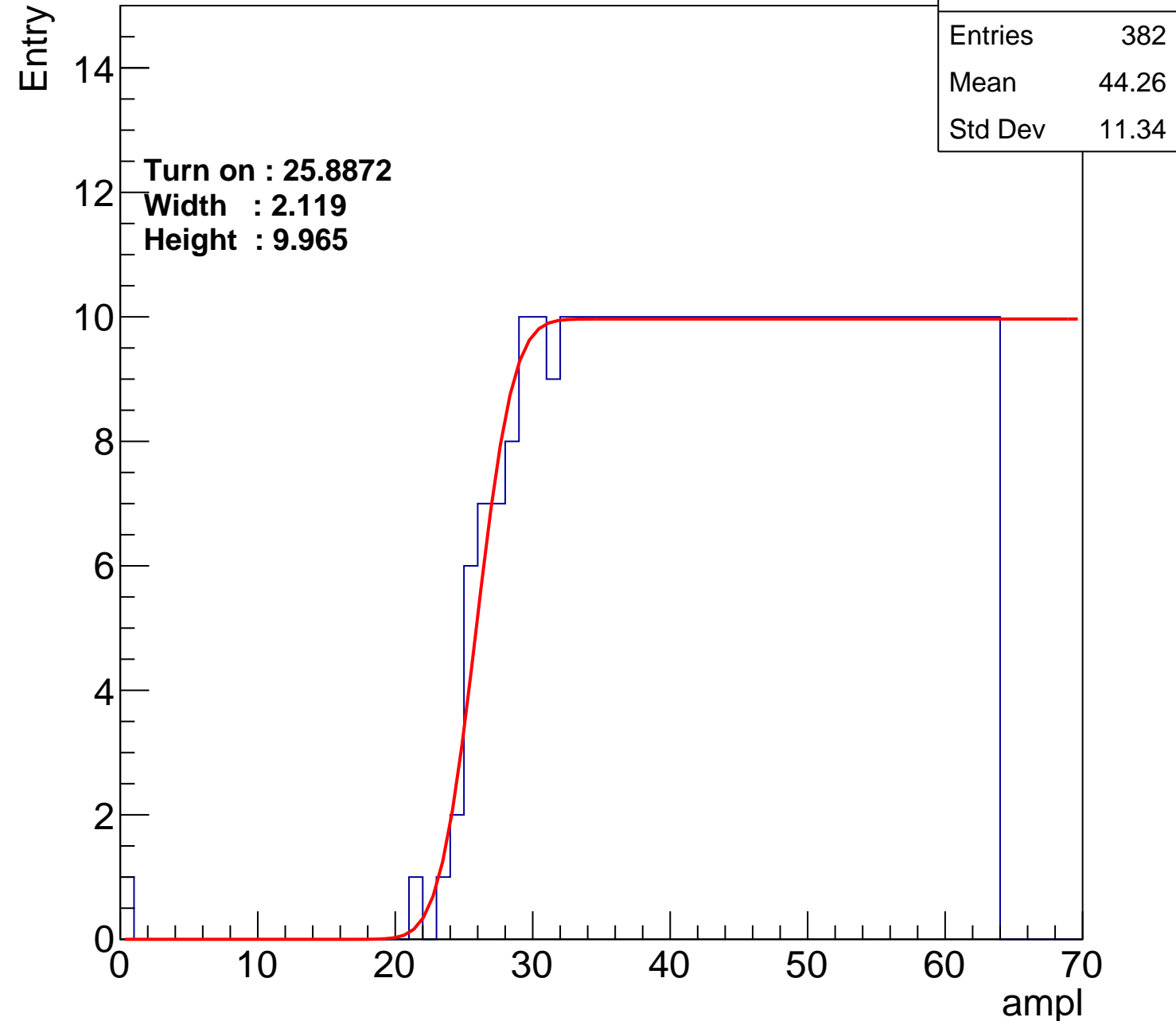
Width : 2.119

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch36

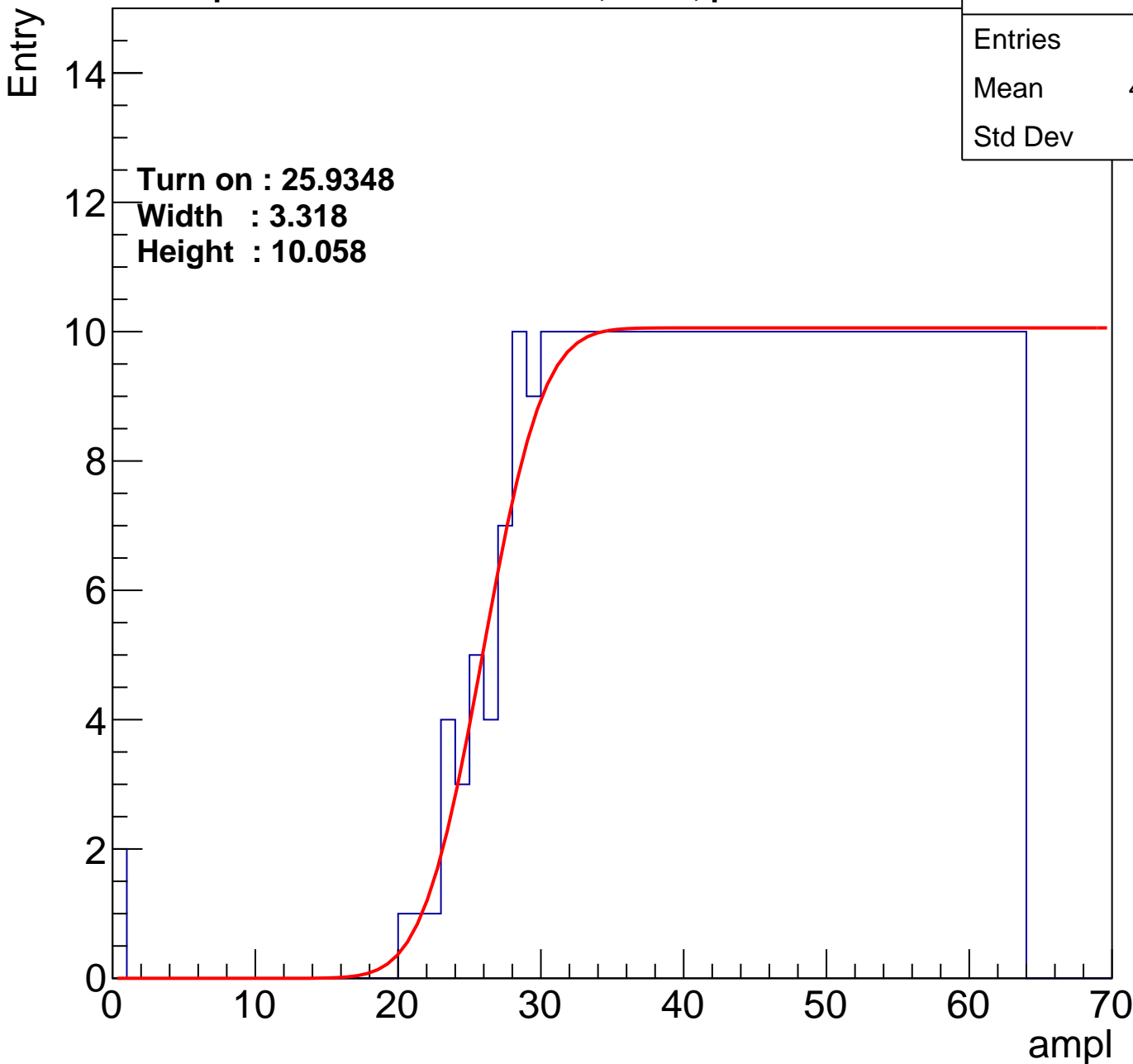
calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.9348

Width : 3.318

Height : 10.058



B1L000S, U6-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.65
Std Dev	11.64

Turn on : 24.7274

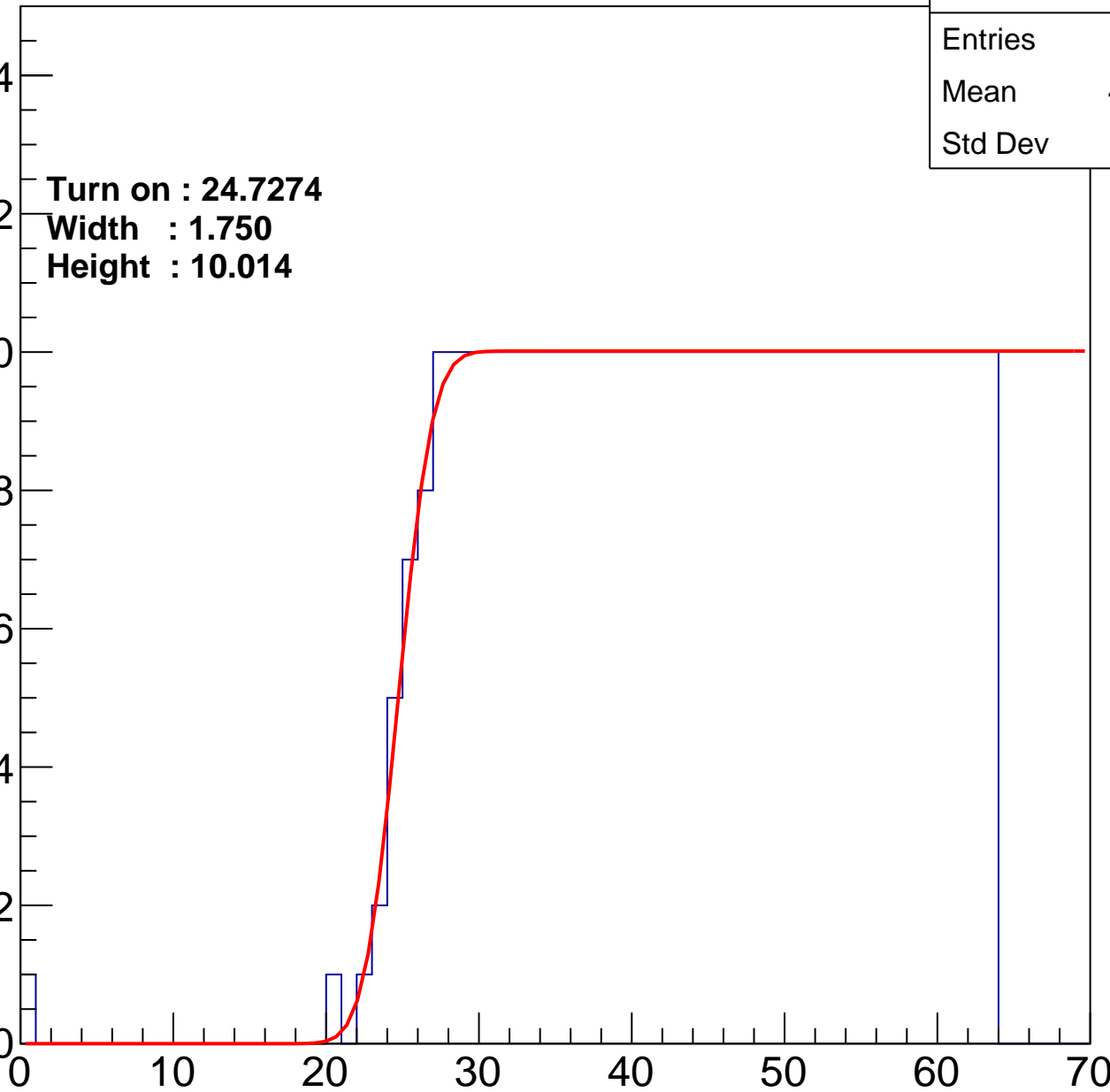
Width : 1.750

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.4643

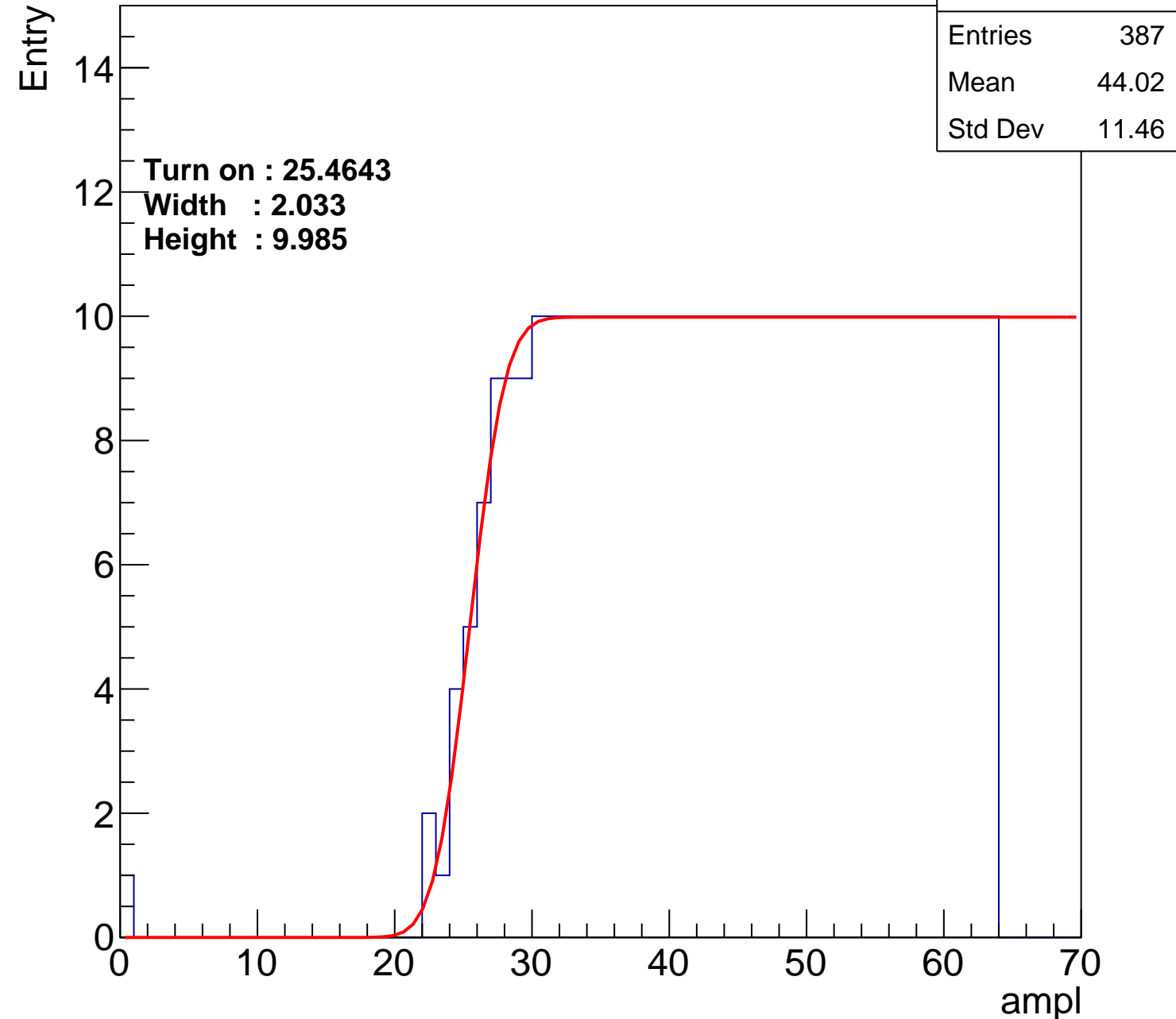
Width : 2.033

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch39

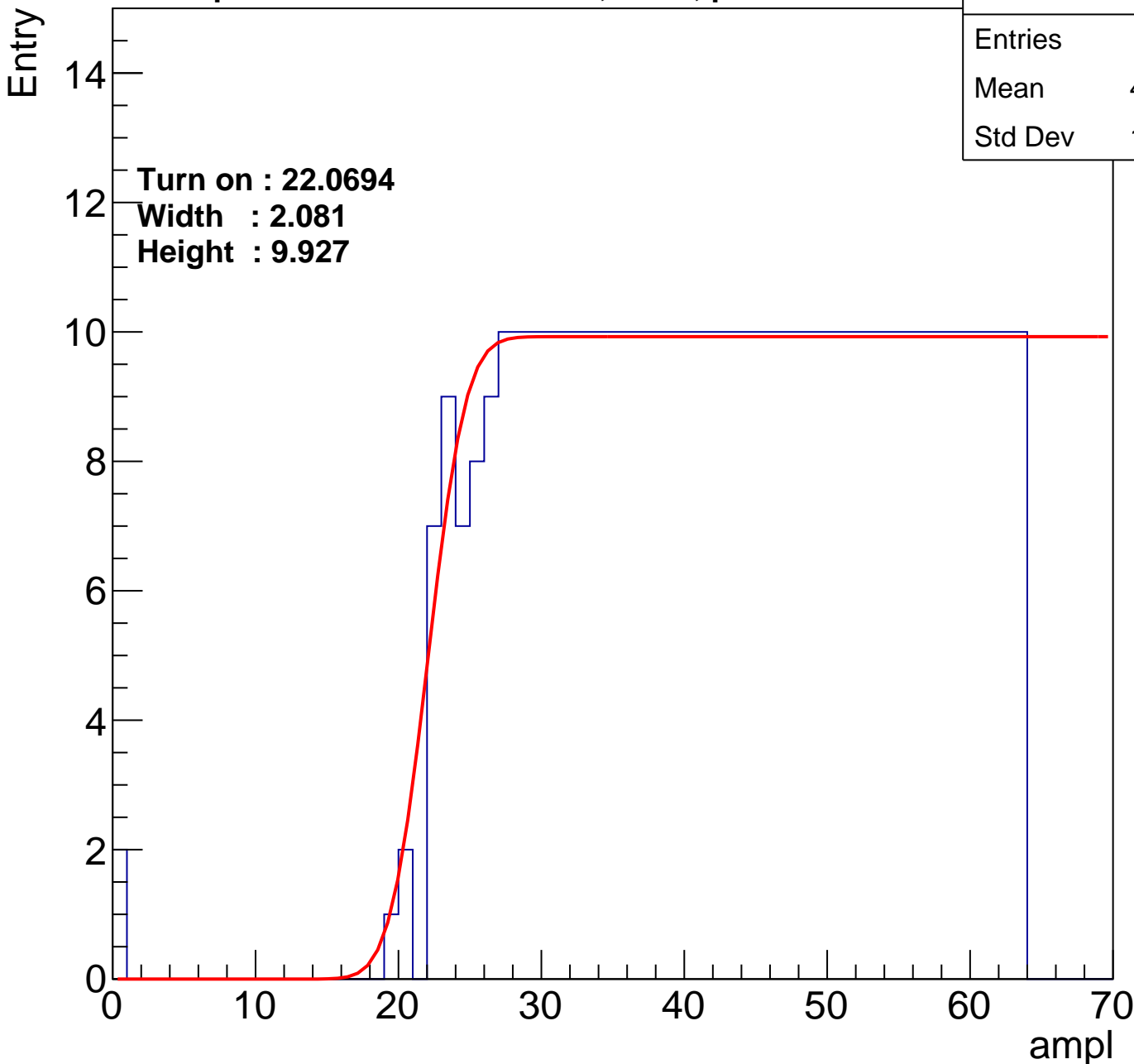
calib_packv5_042523_0143.root, FC#3, port B3

Entries	415
Mean	42.58
Std Dev	12.35

Turn on : 22.0694

Width : 2.081

Height : 9.927



B1L000S, U6-ch40

calib_packv5_042523_0143.root, FC#3, port B3

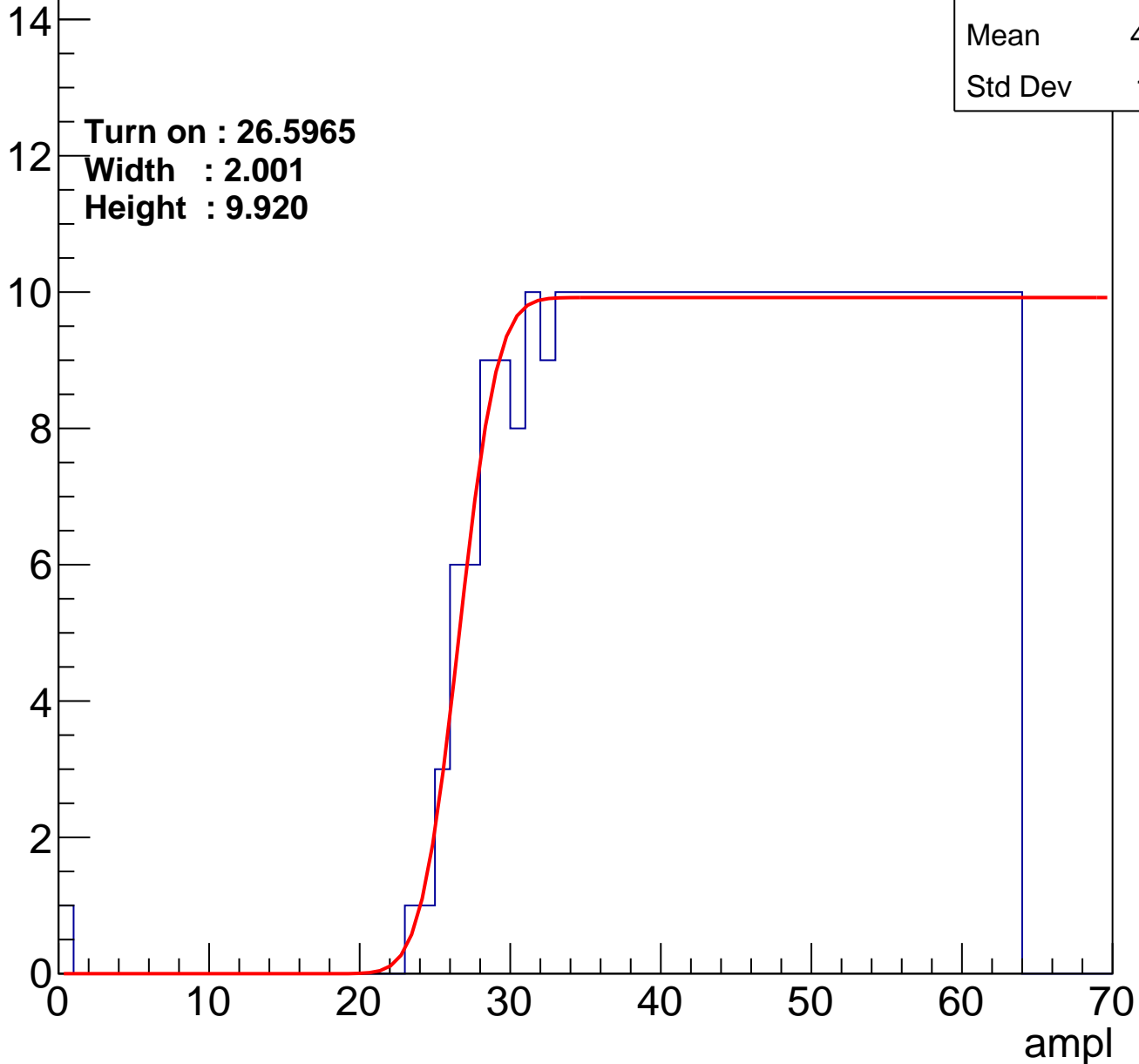
Entry

Entries	373
Mean	44.69
Std Dev	11.11

Turn on : 26.5965

Width : 2.001

Height : 9.920



B1L000S, U6-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.12
Std Dev	10.84

Turn on : 27.4863

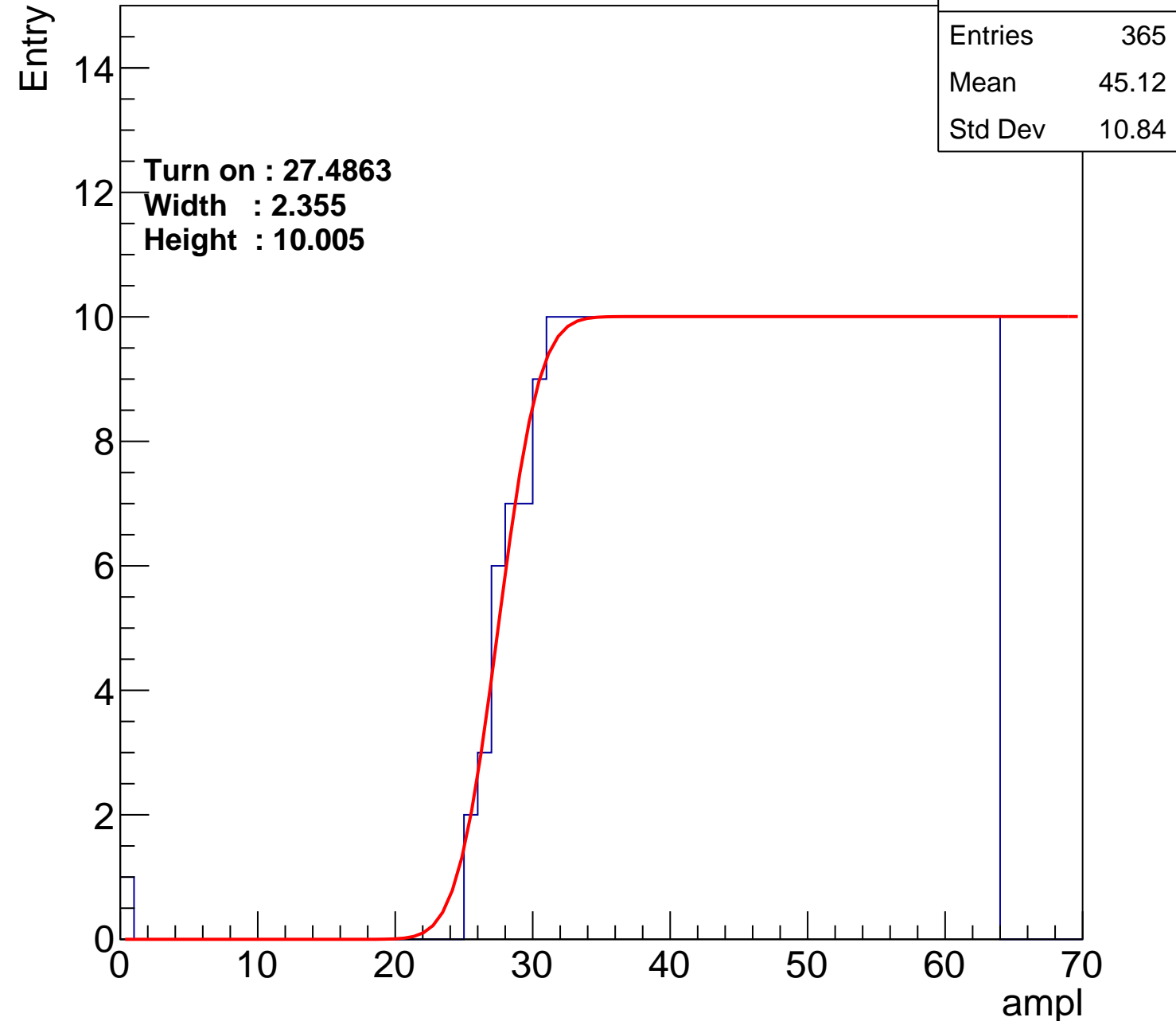
Width : 2.355

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.46
Std Dev	12.07

Turn on : 24.9050

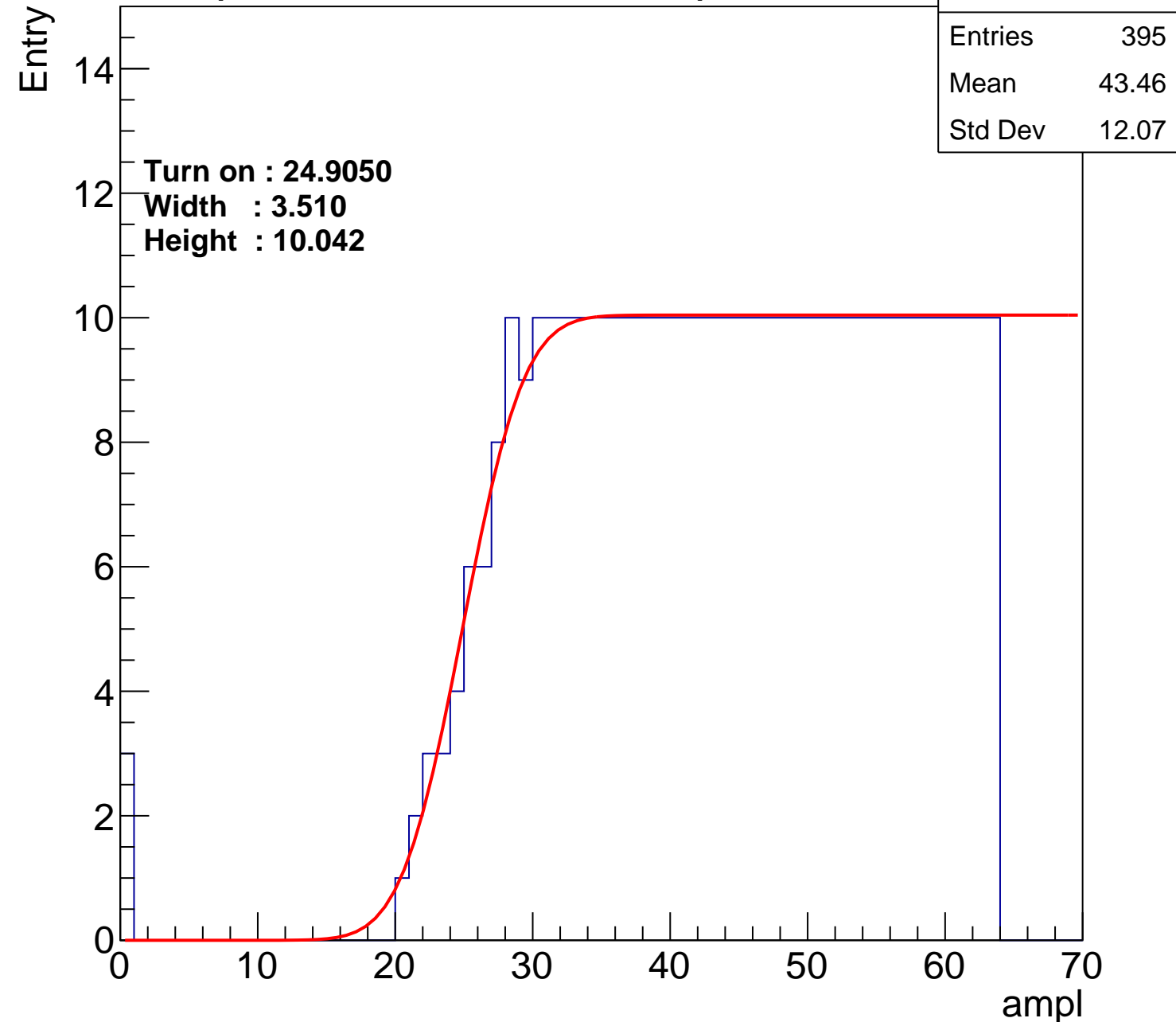
Width : 3.510

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.25
Std Dev	11.96

Turn on : 24.5963

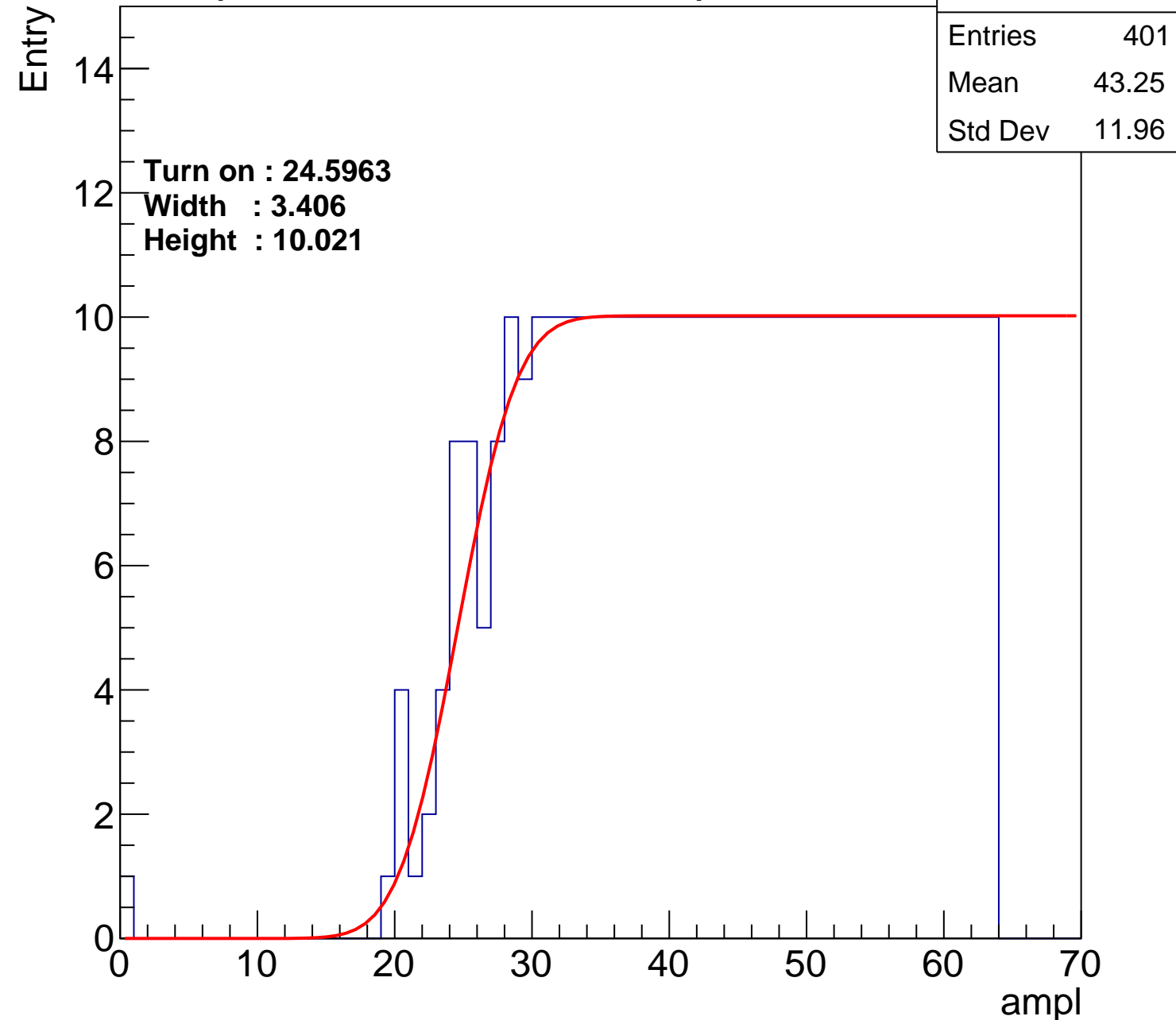
Width : 3.406

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.6983

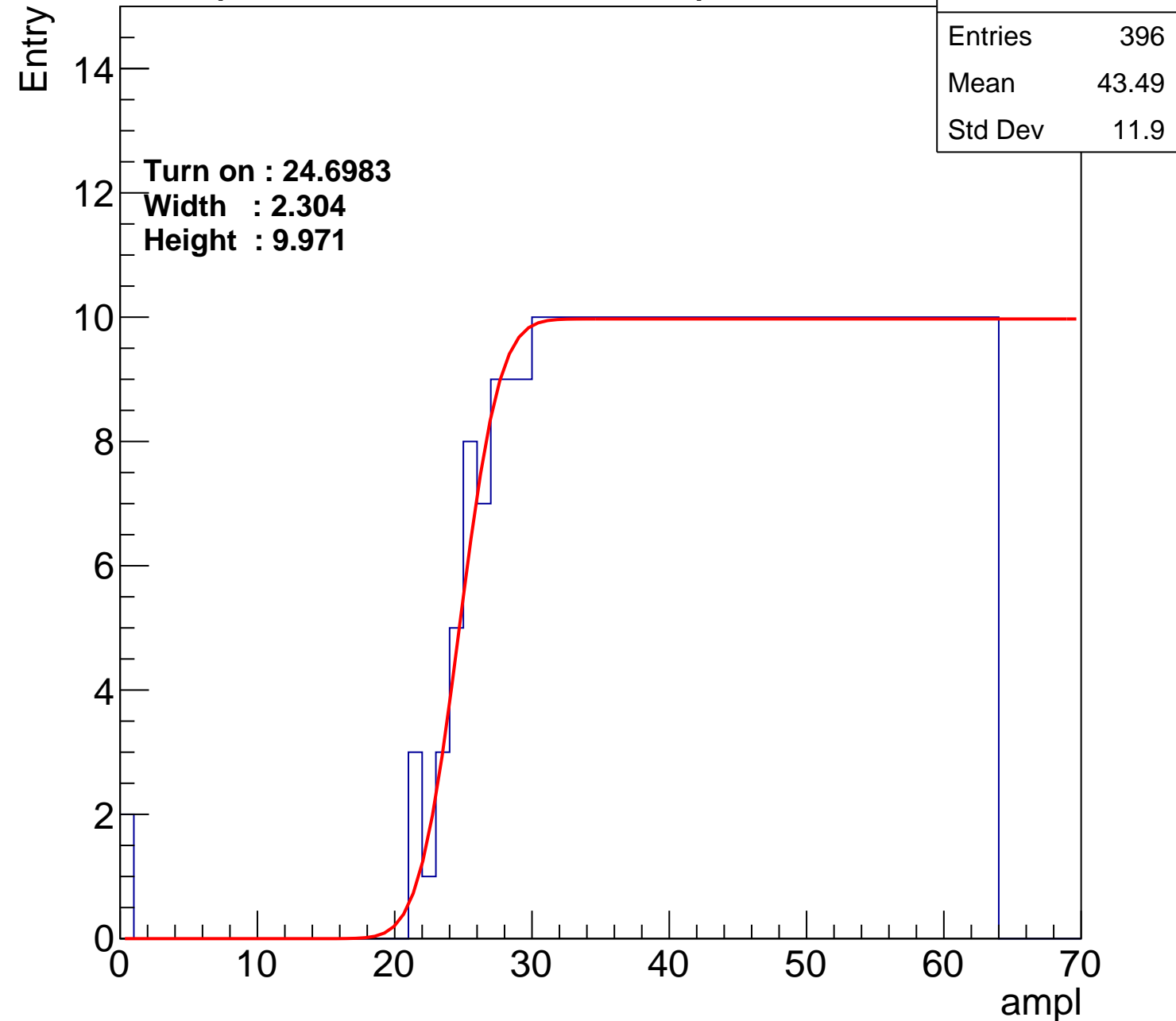
Width : 2.304

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.4715

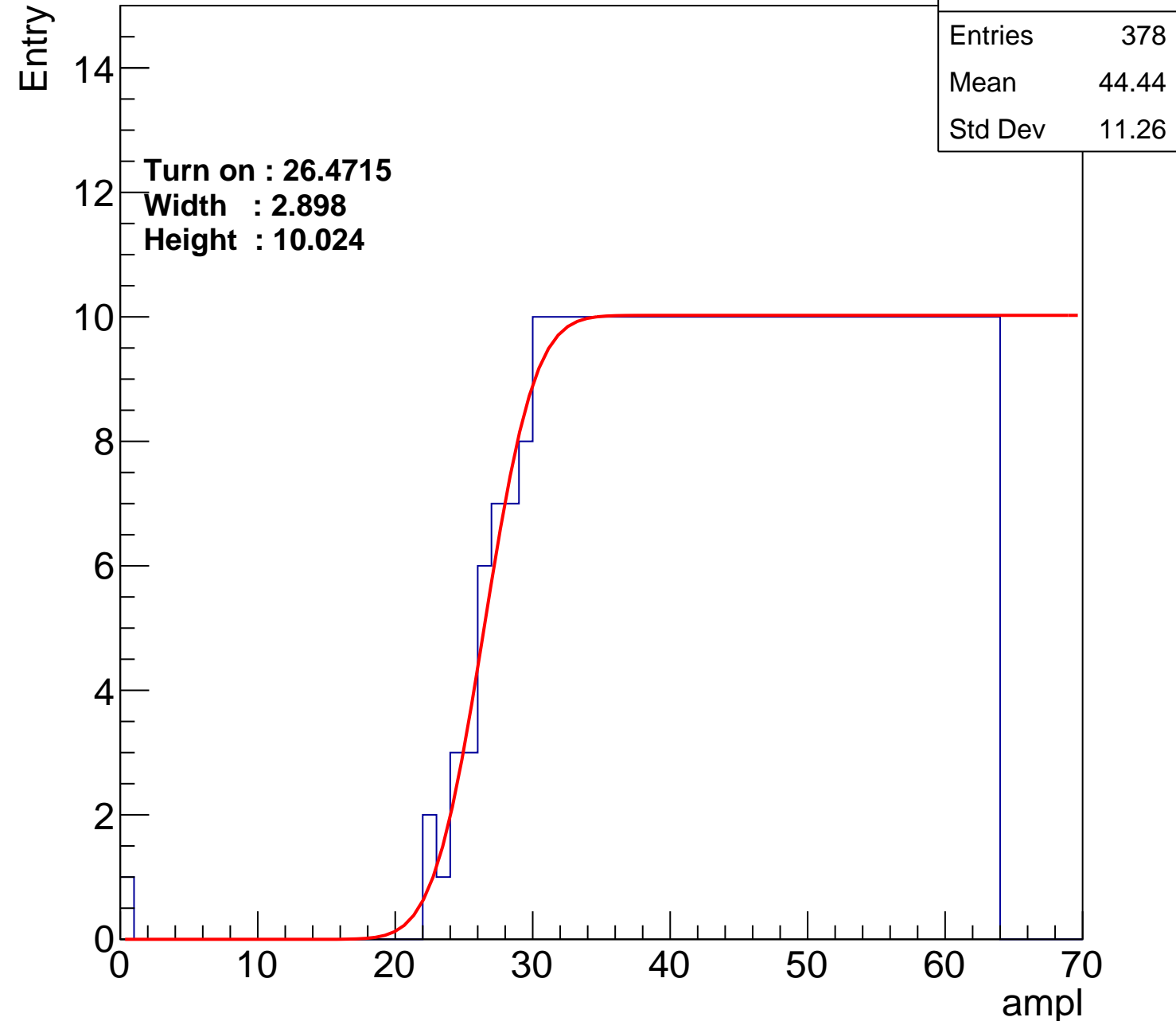
Width : 2.898

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.7782

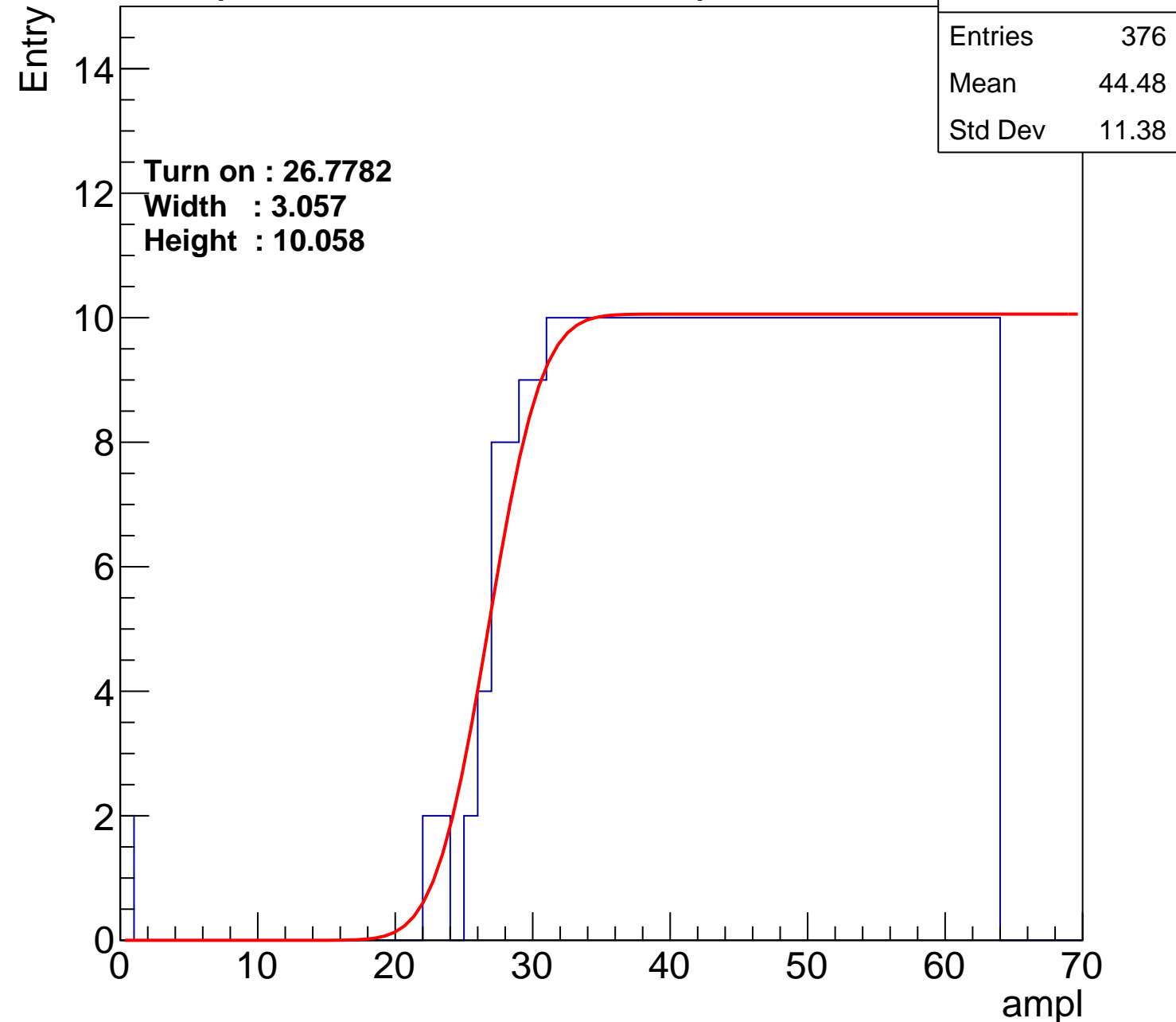
Width : 3.057

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	42.85
Std Dev	12.56

Turn on : 24.0803

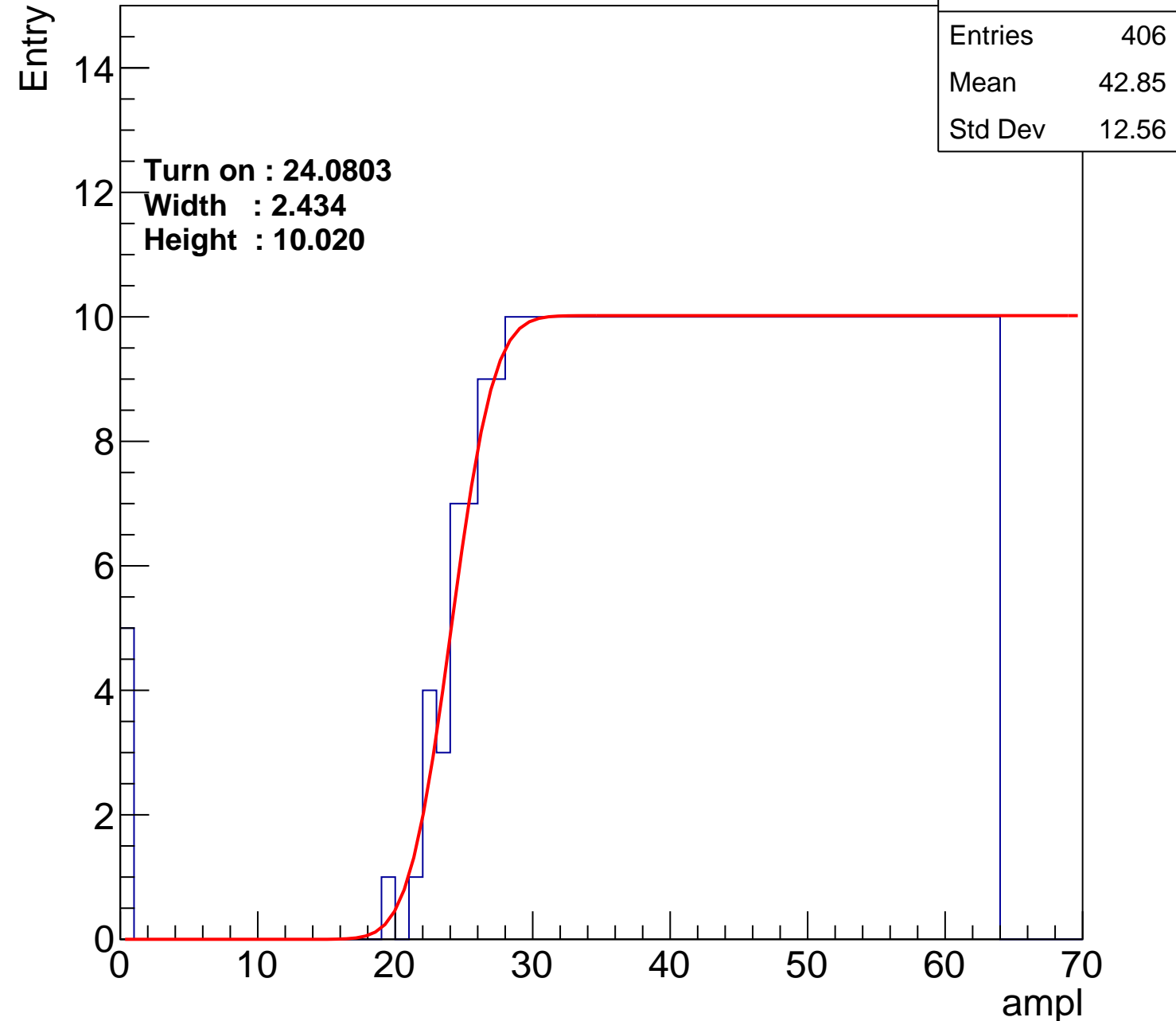
Width : 2.434

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.98
Std Dev	11.26

Turn on : 27.6872

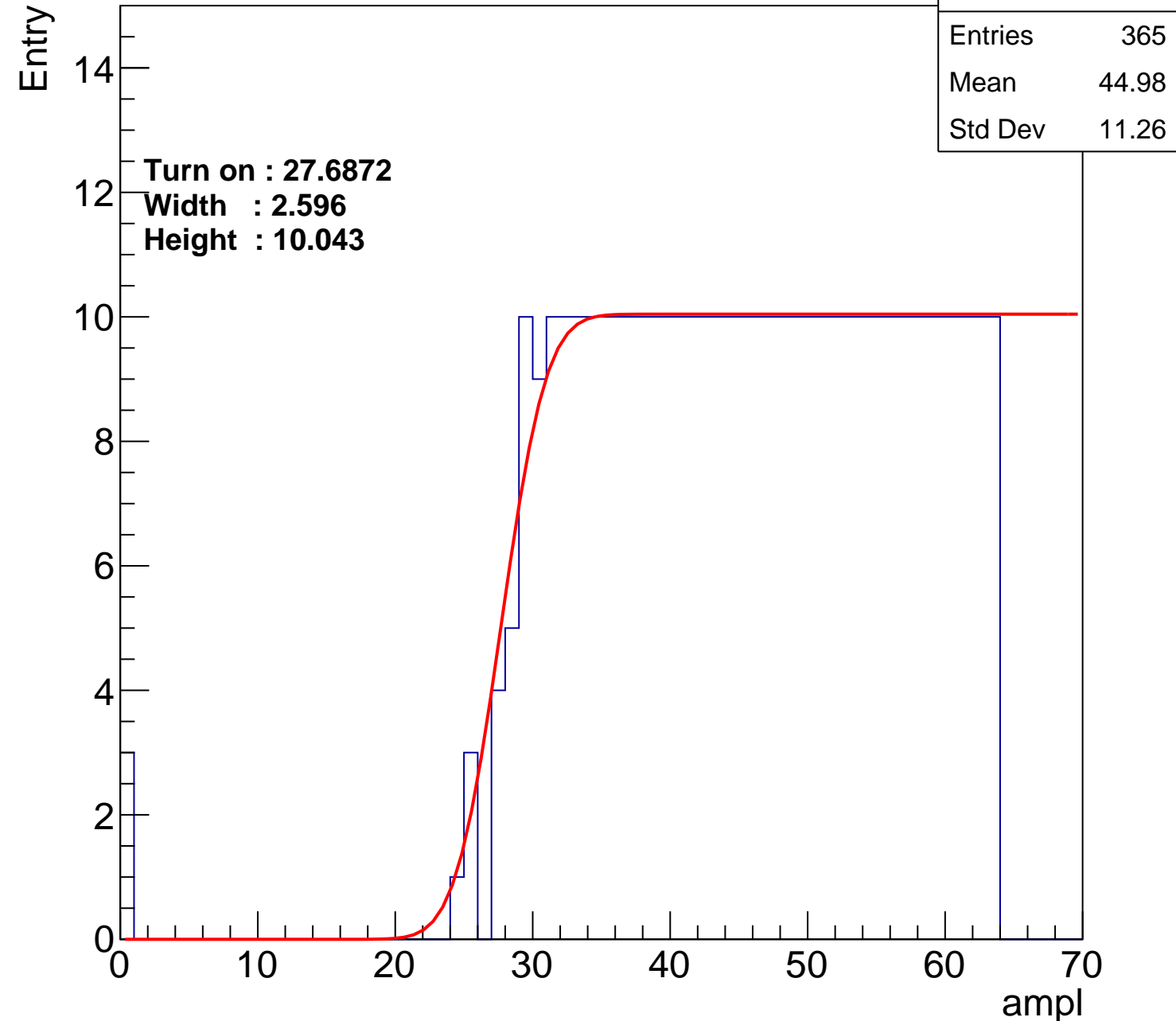
Width : 2.596

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch49

calib_packv5_042523_0143.root, FC#3, port B3

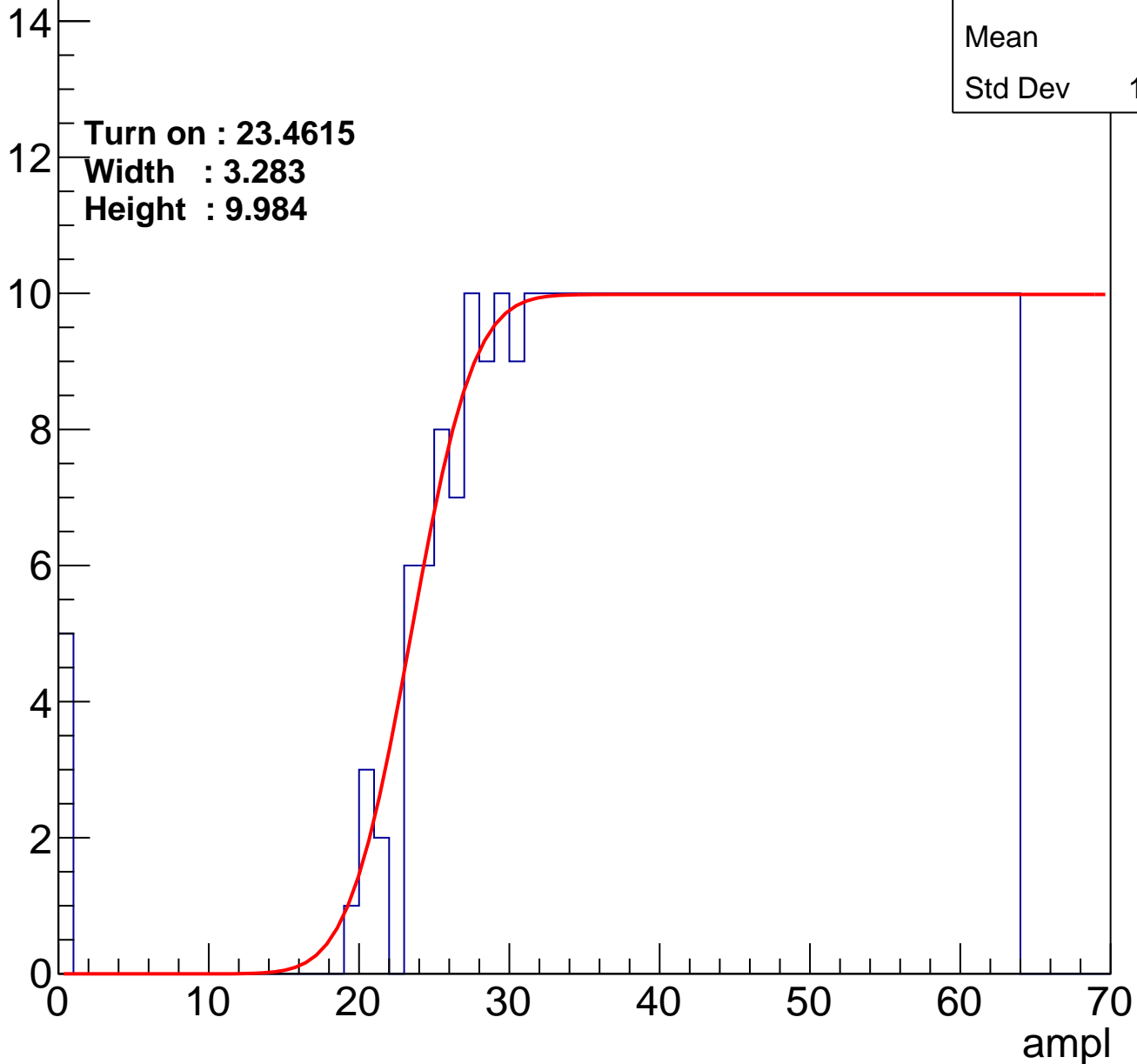
Entries	406
Mean	42.8
Std Dev	12.63

Turn on : 23.4615

Width : 3.283

Height : 9.984

Entry



B1L000S, U6-ch50

calib_packv5_042523_0143.root, FC#3, port B3

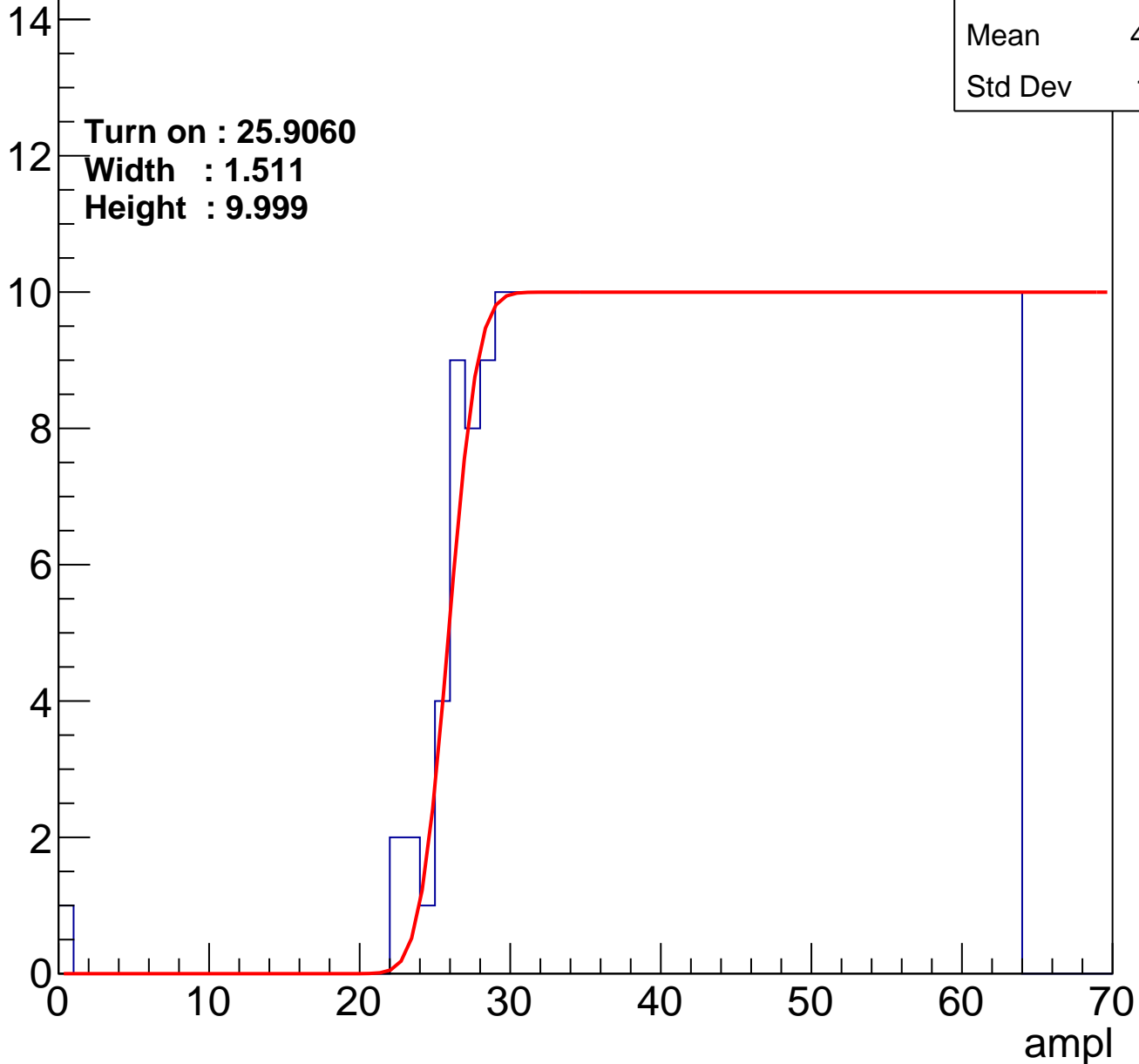
Entries	386
Mean	44.08
Std Dev	11.41

Turn on : 25.9060

Width : 1.511

Height : 9.999

Entry



B1L000S, U6-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.72
Std Dev	11.74

Turn on : 25.3143

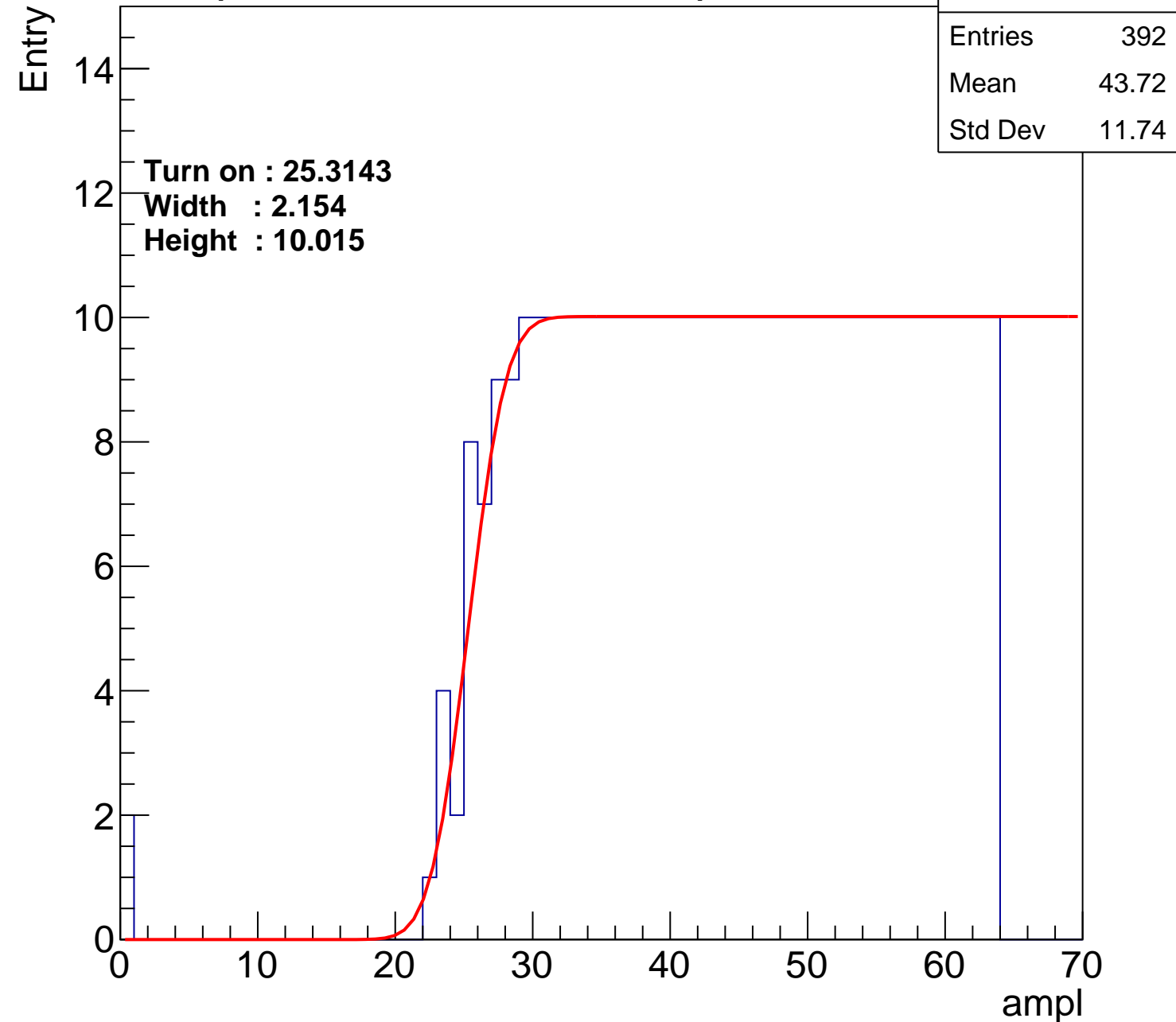
Width : 2.154

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.24
Std Dev	11.67

Turn on : 26.4853

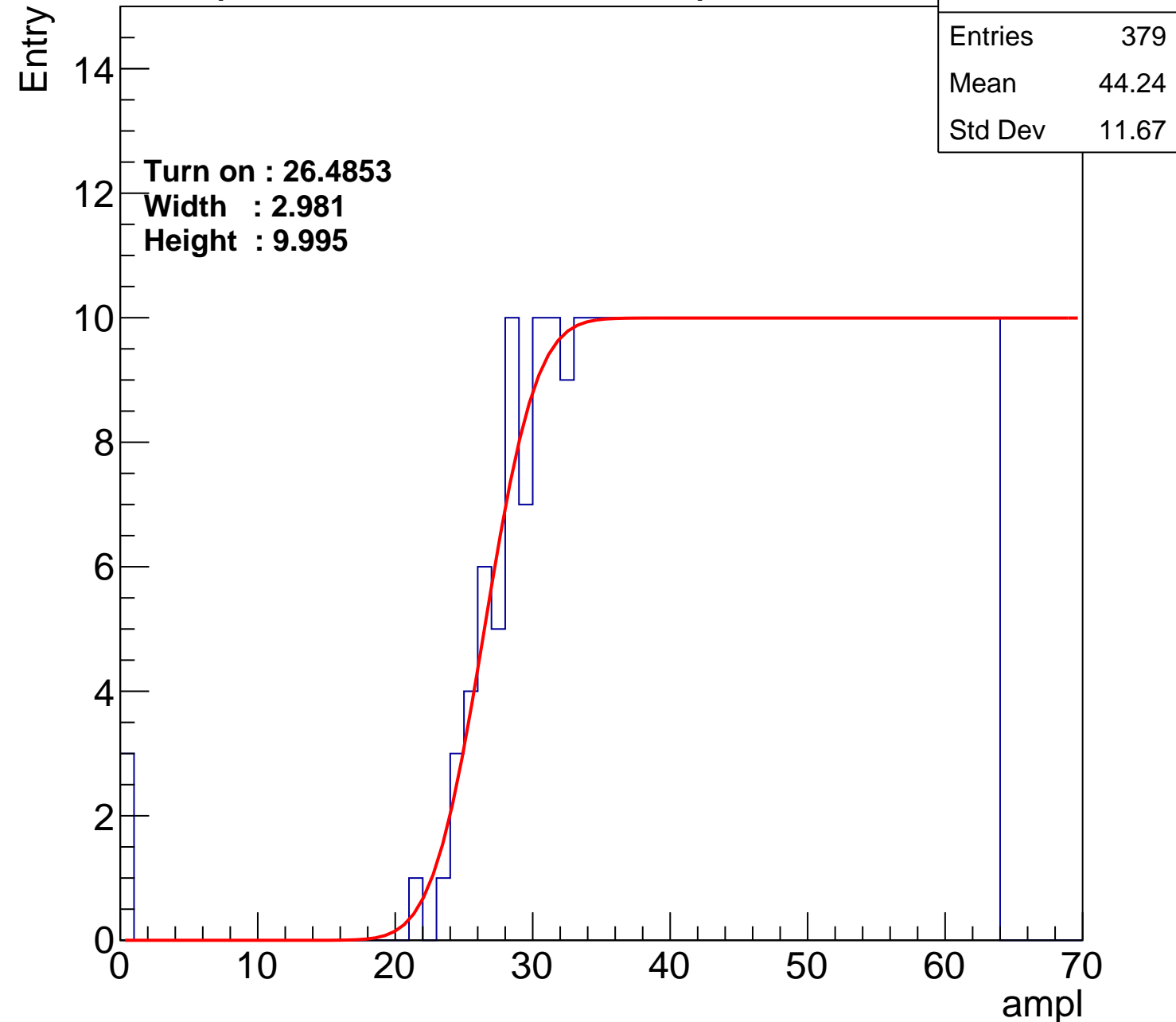
Width : 2.981

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.4631

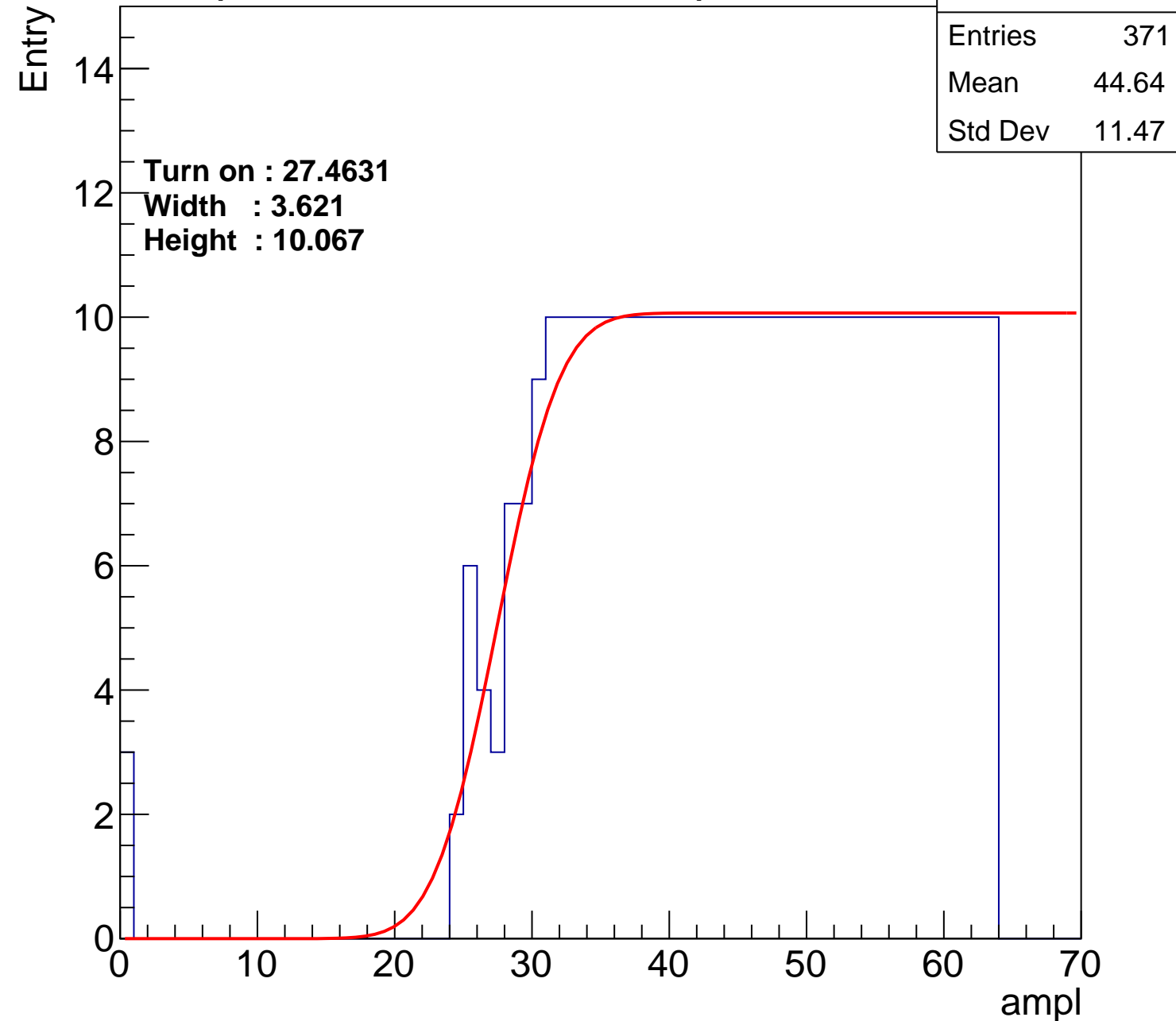
Width : 3.621

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.99
Std Dev	11.45

Turn on : 24.5443

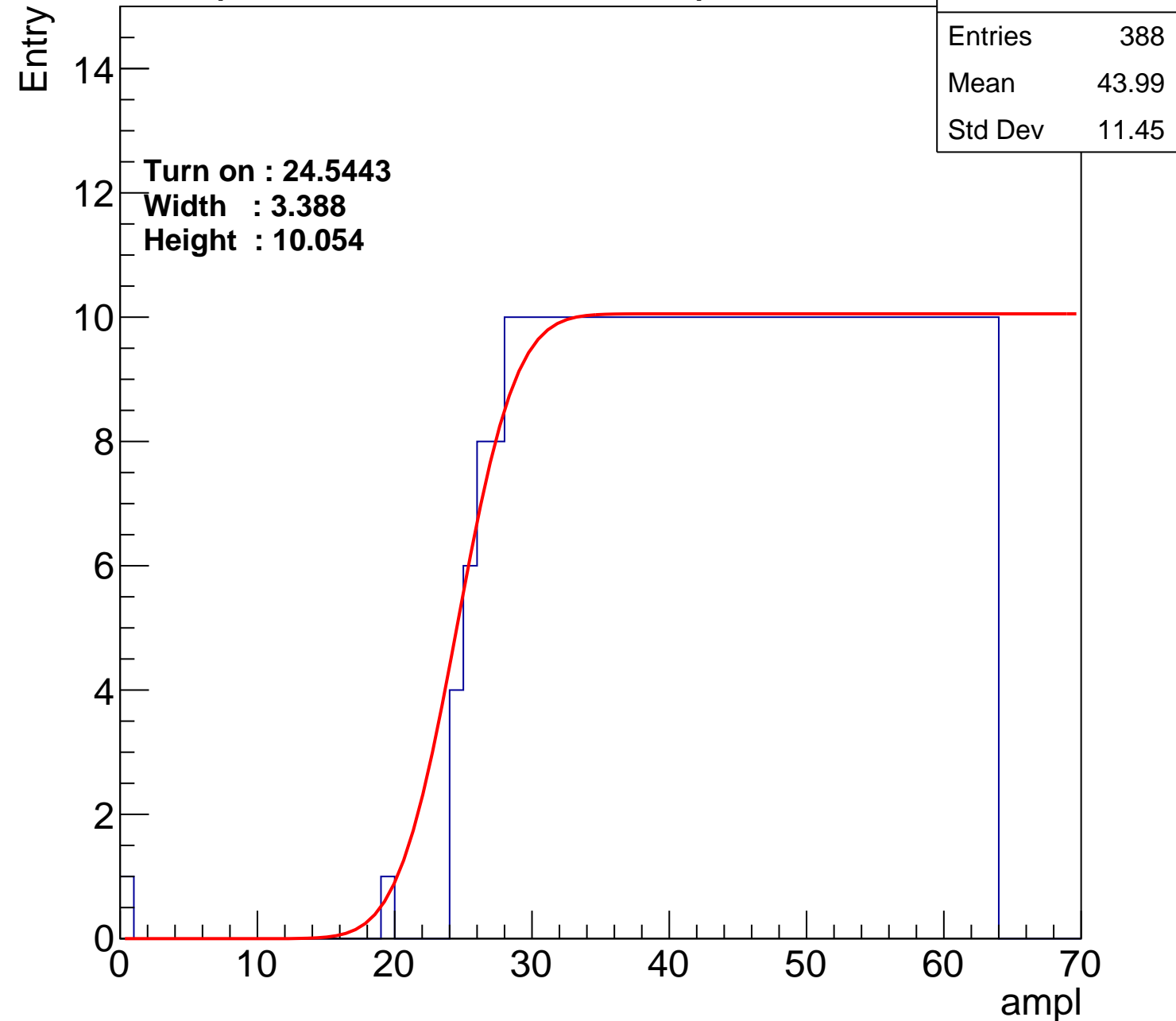
Width : 3.388

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch55

calib_packv5_042523_0143.root, FC#3, port B3

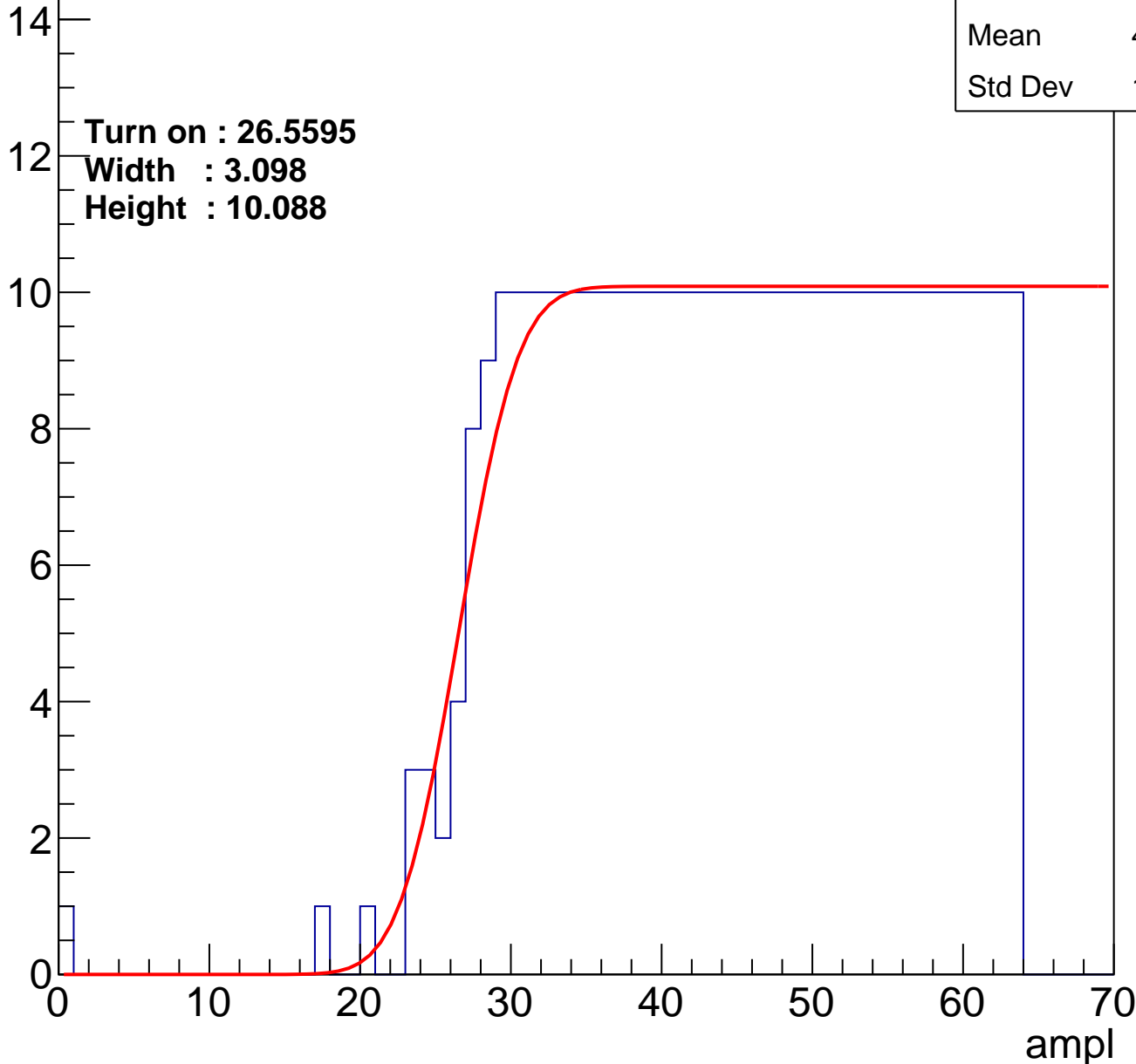
Entries	382
Mean	44.24
Std Dev	11.38

Turn on : 26.5595

Width : 3.098

Height : 10.088

Entry



B1L000S, U6-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.17
Std Dev	11.49

Turn on : 25.7350

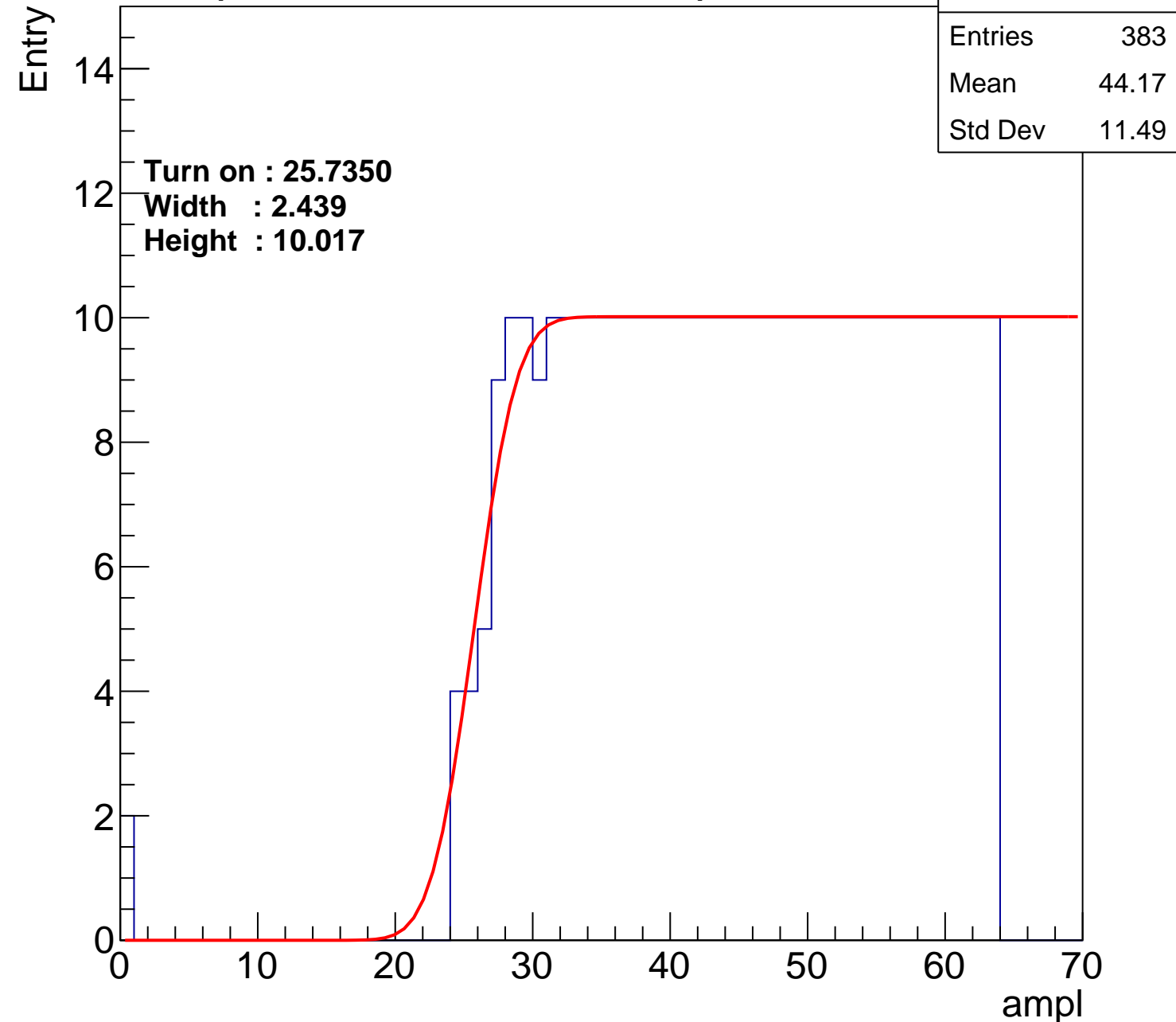
Width : 2.439

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.93
Std Dev	11.85

Turn on : 26.2191

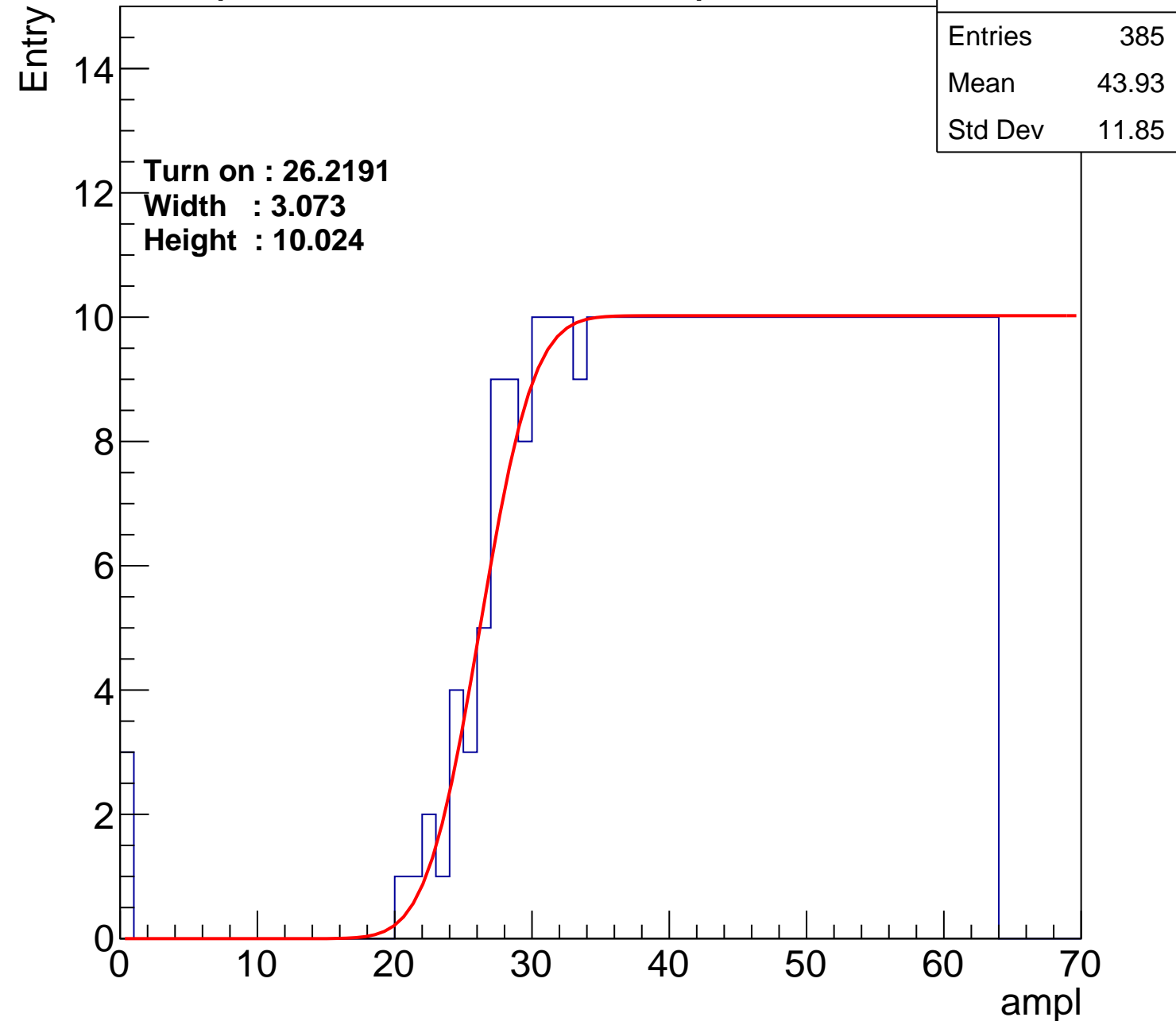
Width : 3.073

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch58

calib_packv5_042523_0143.root, FC#3, port B3

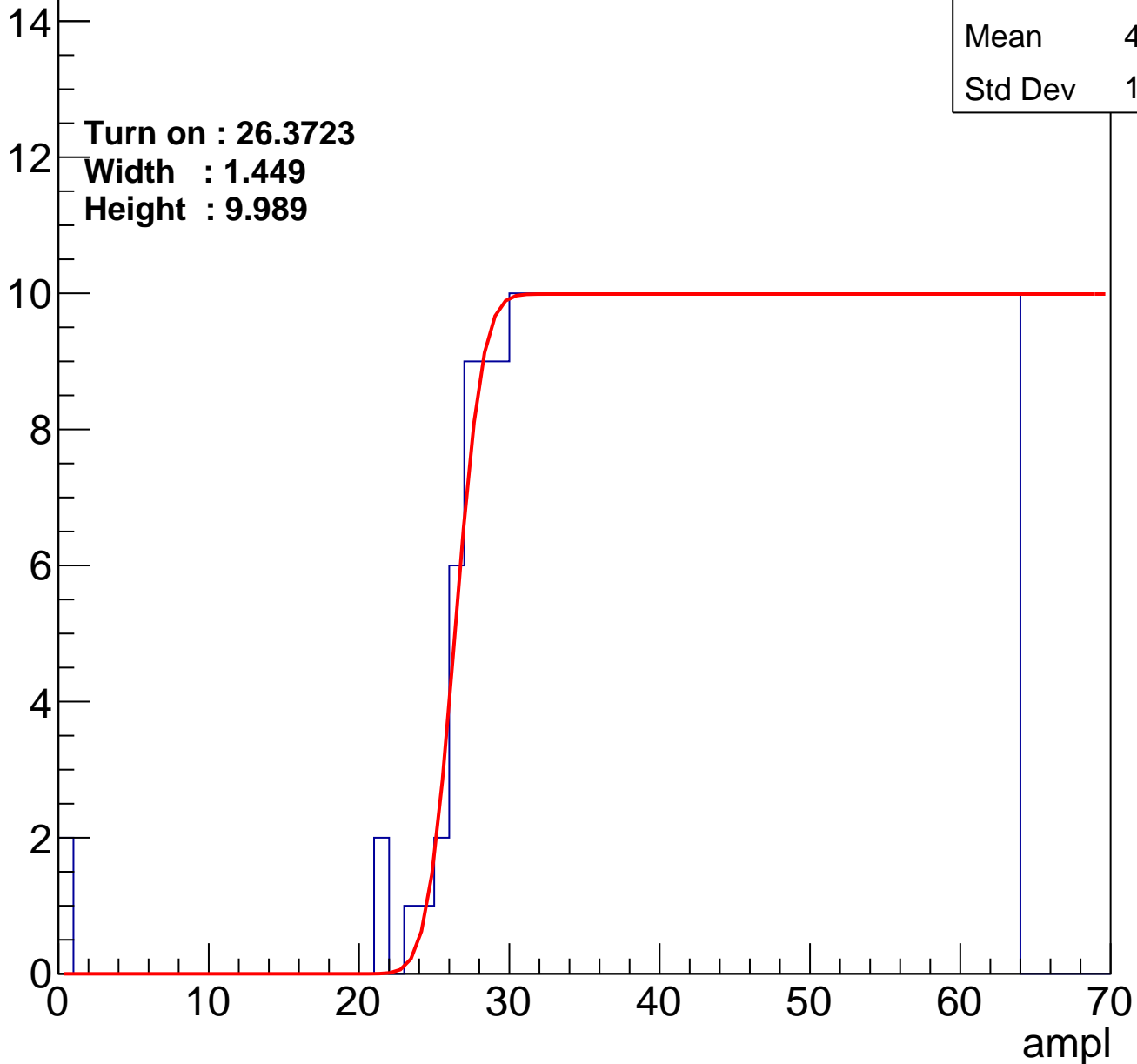
Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.3723

Width : 1.449

Height : 9.989

Entry



B1L000S, U6-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.64
Std Dev	11.99

Turn on : 25.7858

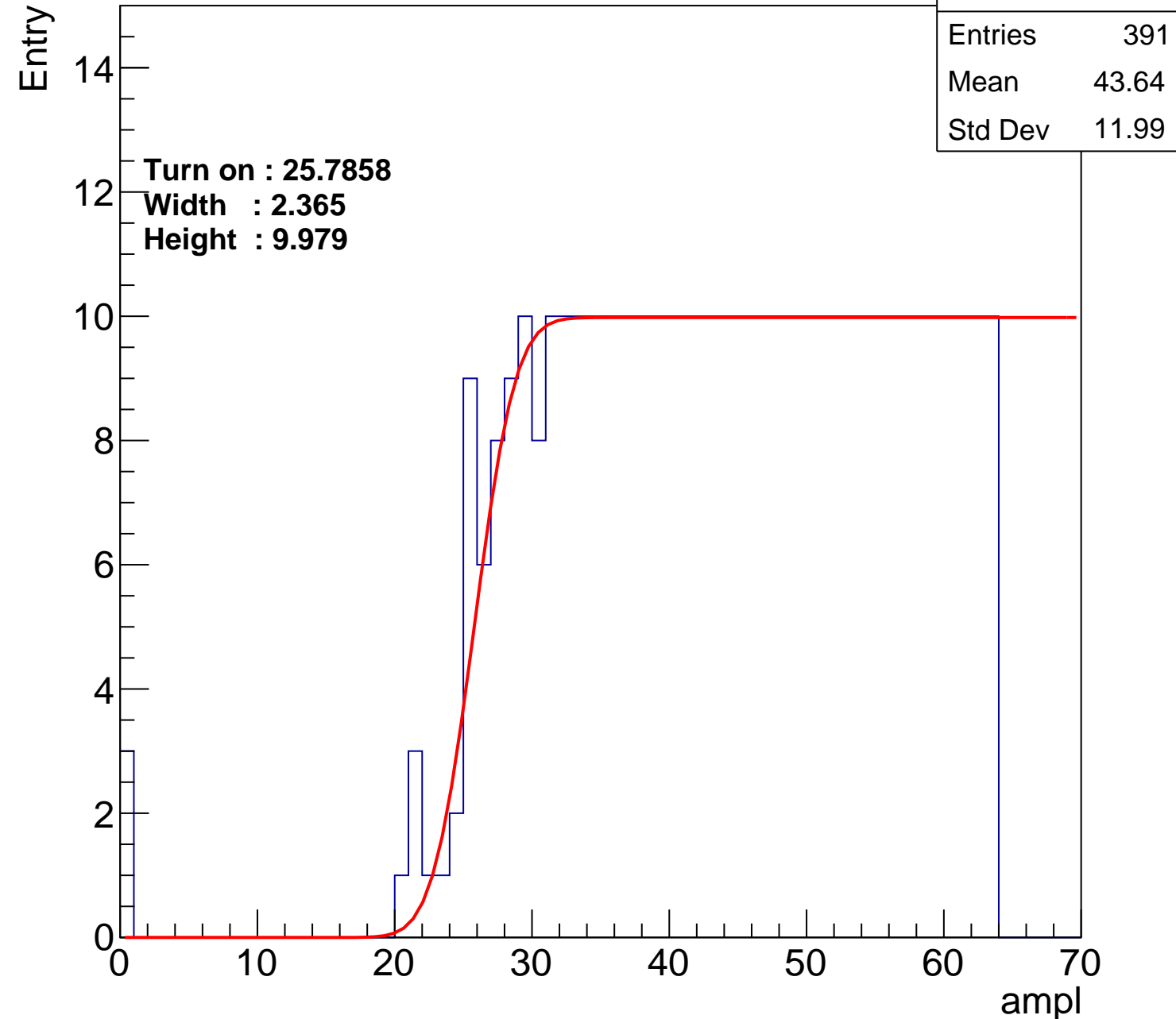
Width : 2.365

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch60

calib_packv5_042523_0143.root, FC#3, port B3

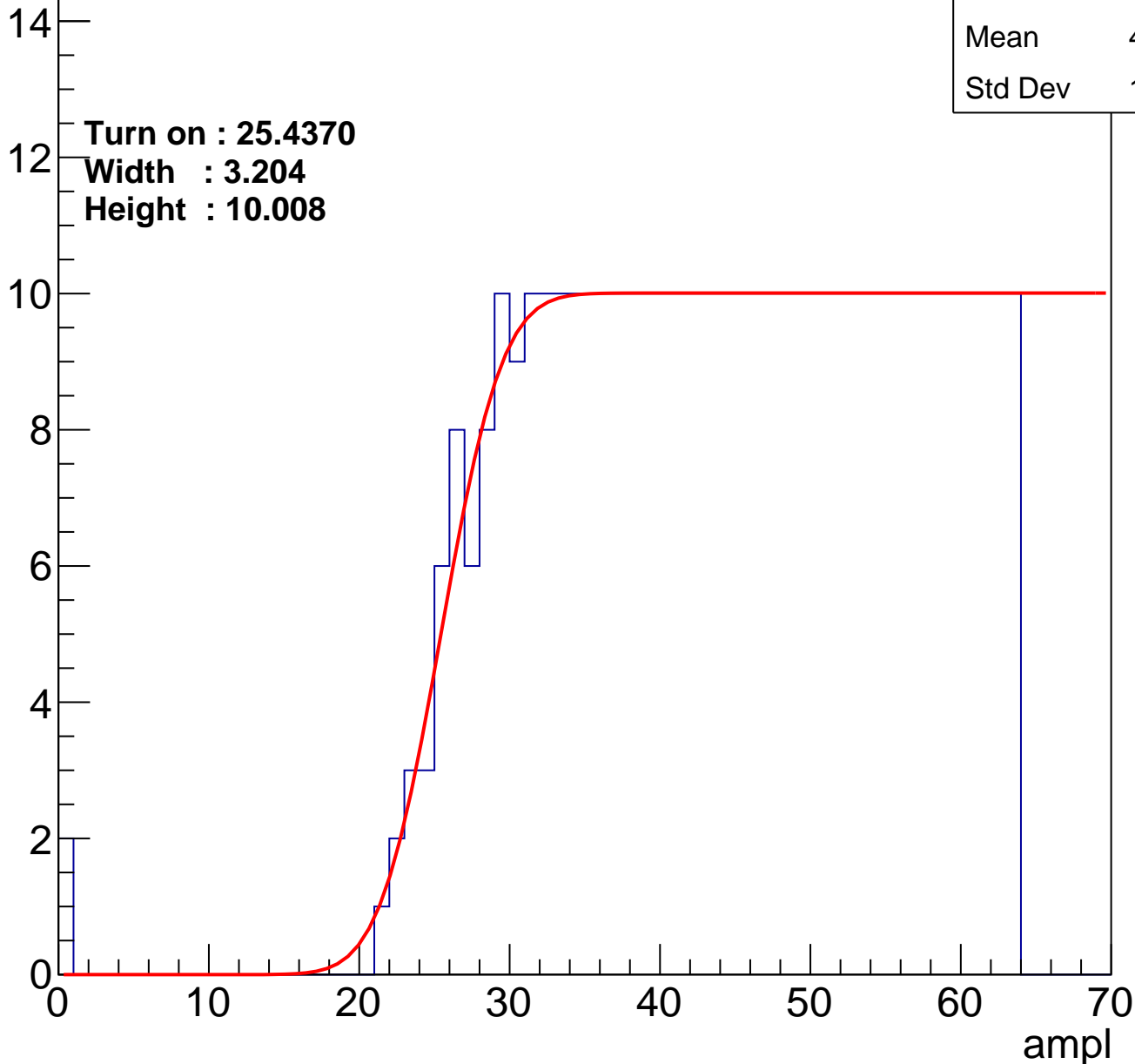
Entry

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.4370

Width : 3.204

Height : 10.008



B1L000S, U6-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.59
Std Dev	12.09

Turn on : 25.9302

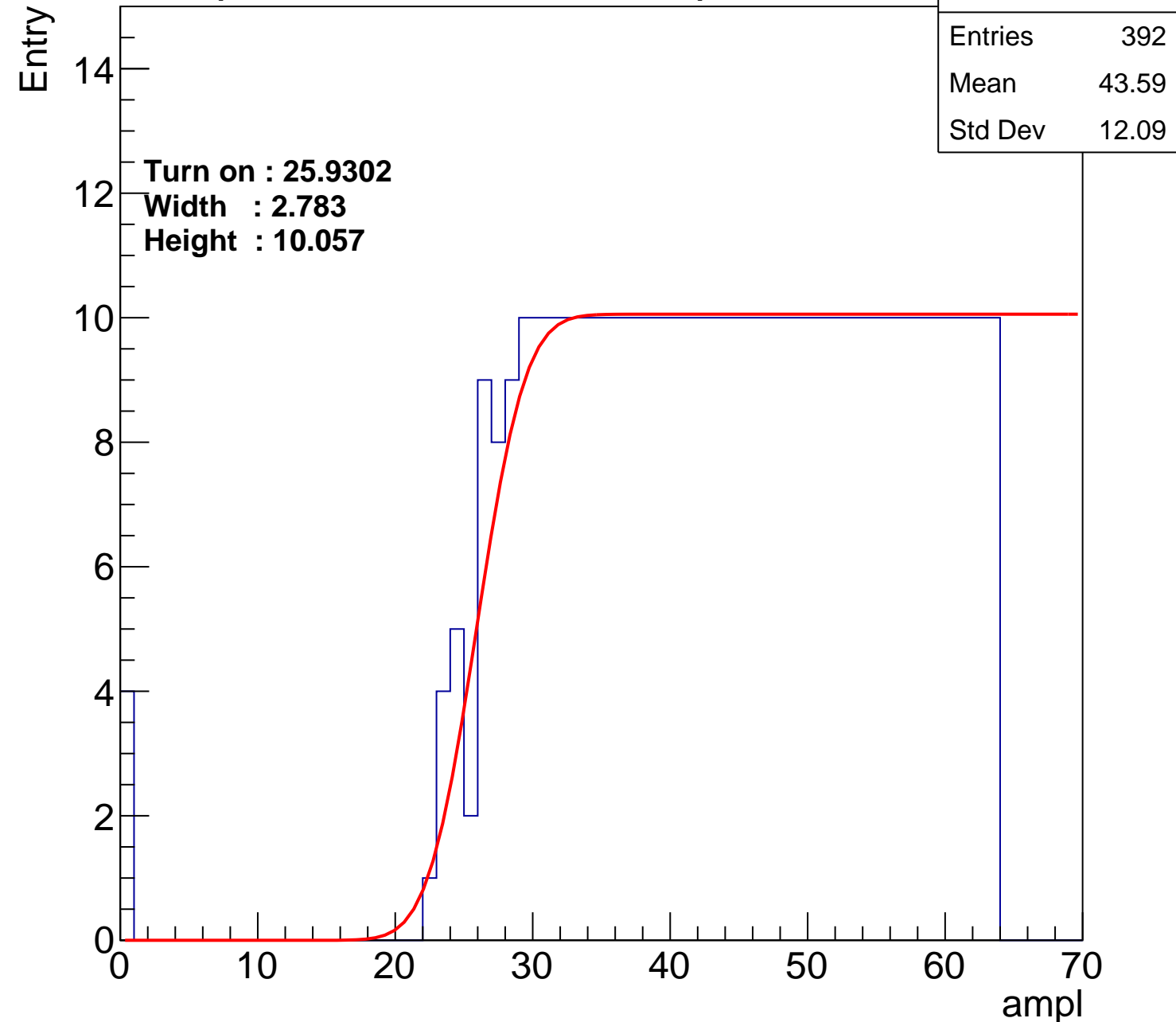
Width : 2.783

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	42.95
Std Dev	12.4

Turn on : 23.7662

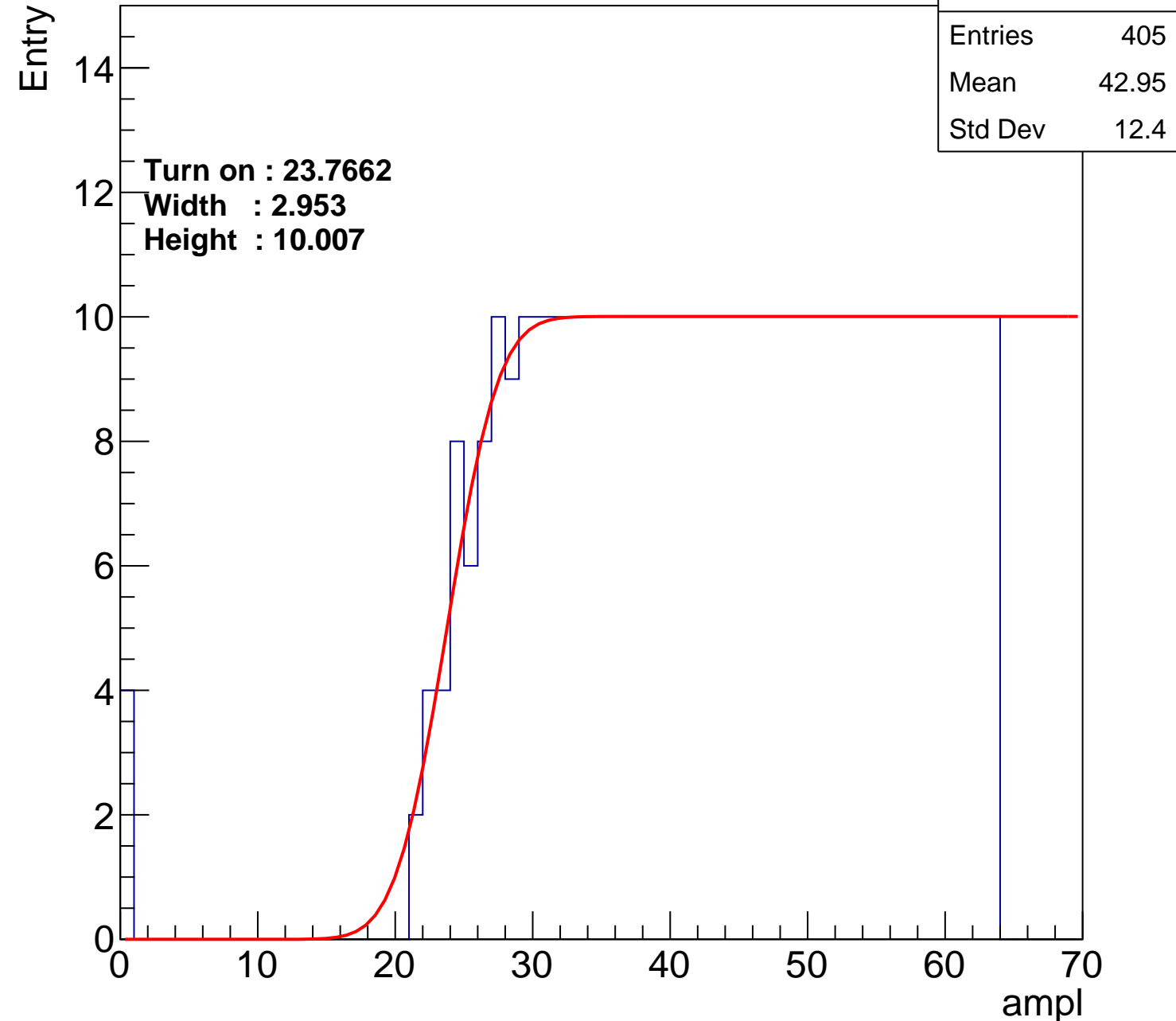
Width : 2.953

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 26.4941

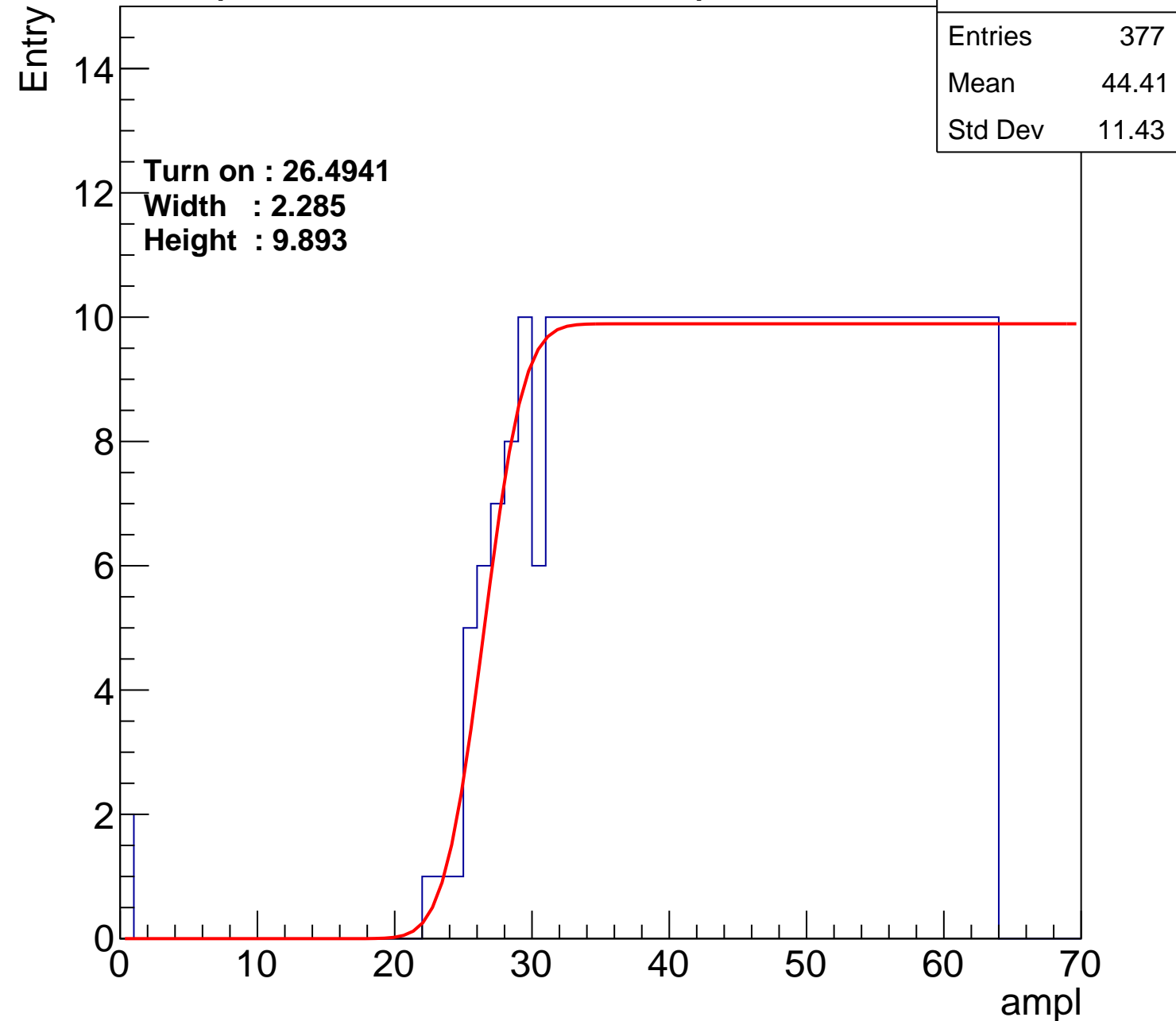
Width : 2.285

Height : 9.893

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.06
Std Dev	12.49

Turn on : 24.7863

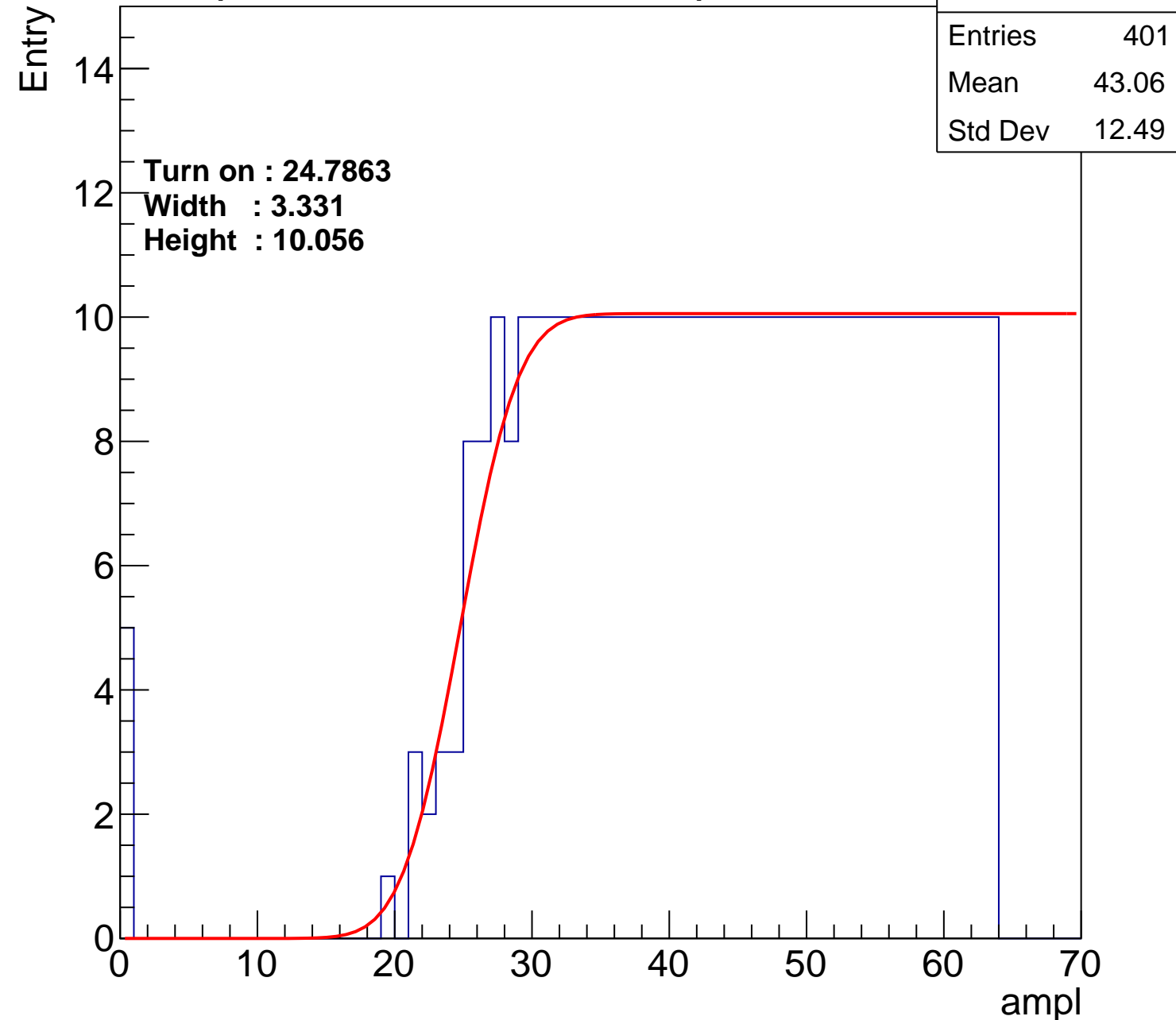
Width : 3.331

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.31
Std Dev	11.32

Turn on : 26.2749

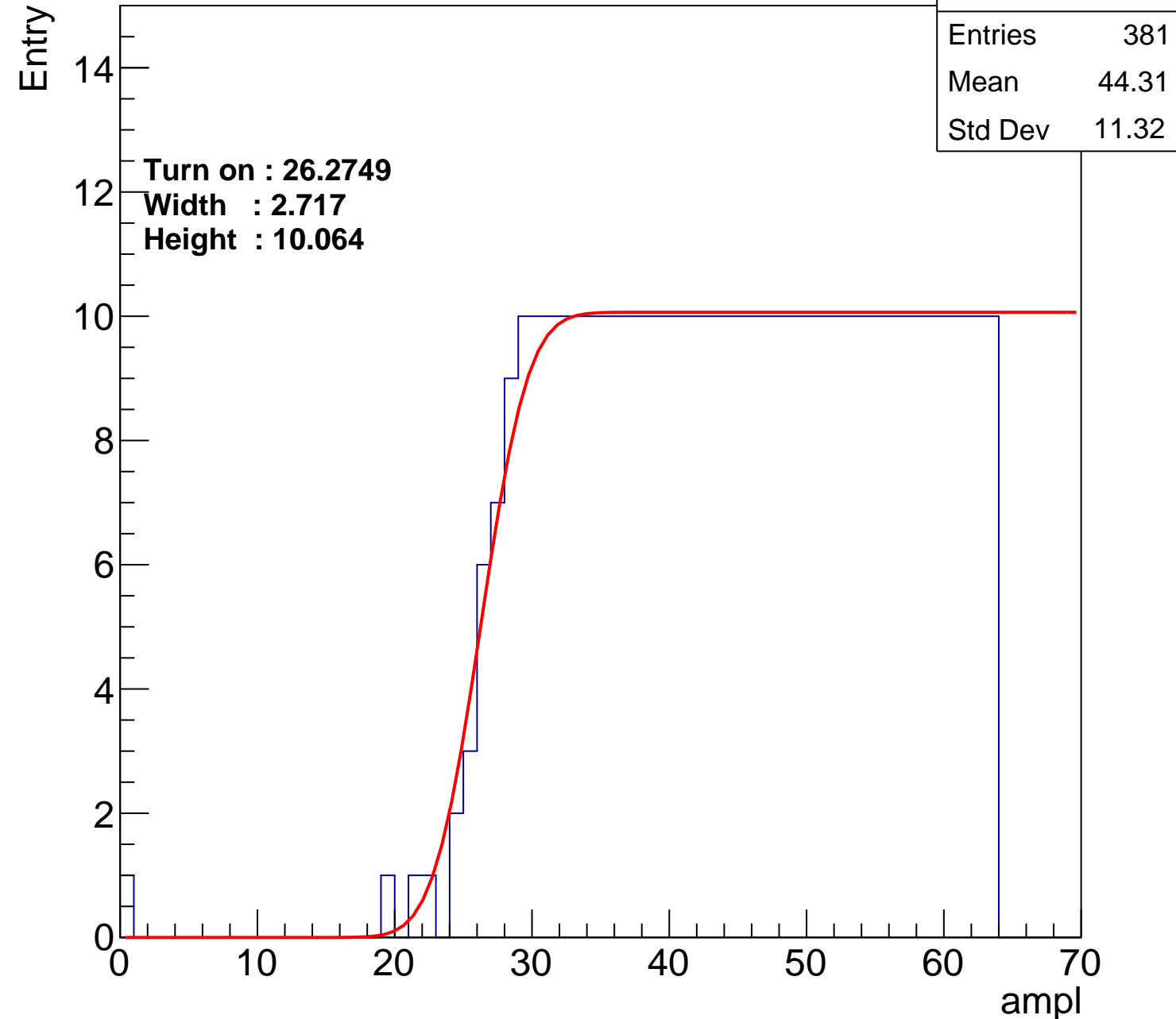
Width : 2.717

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch66

calib_packv5_042523_0143.root, FC#3, port B3

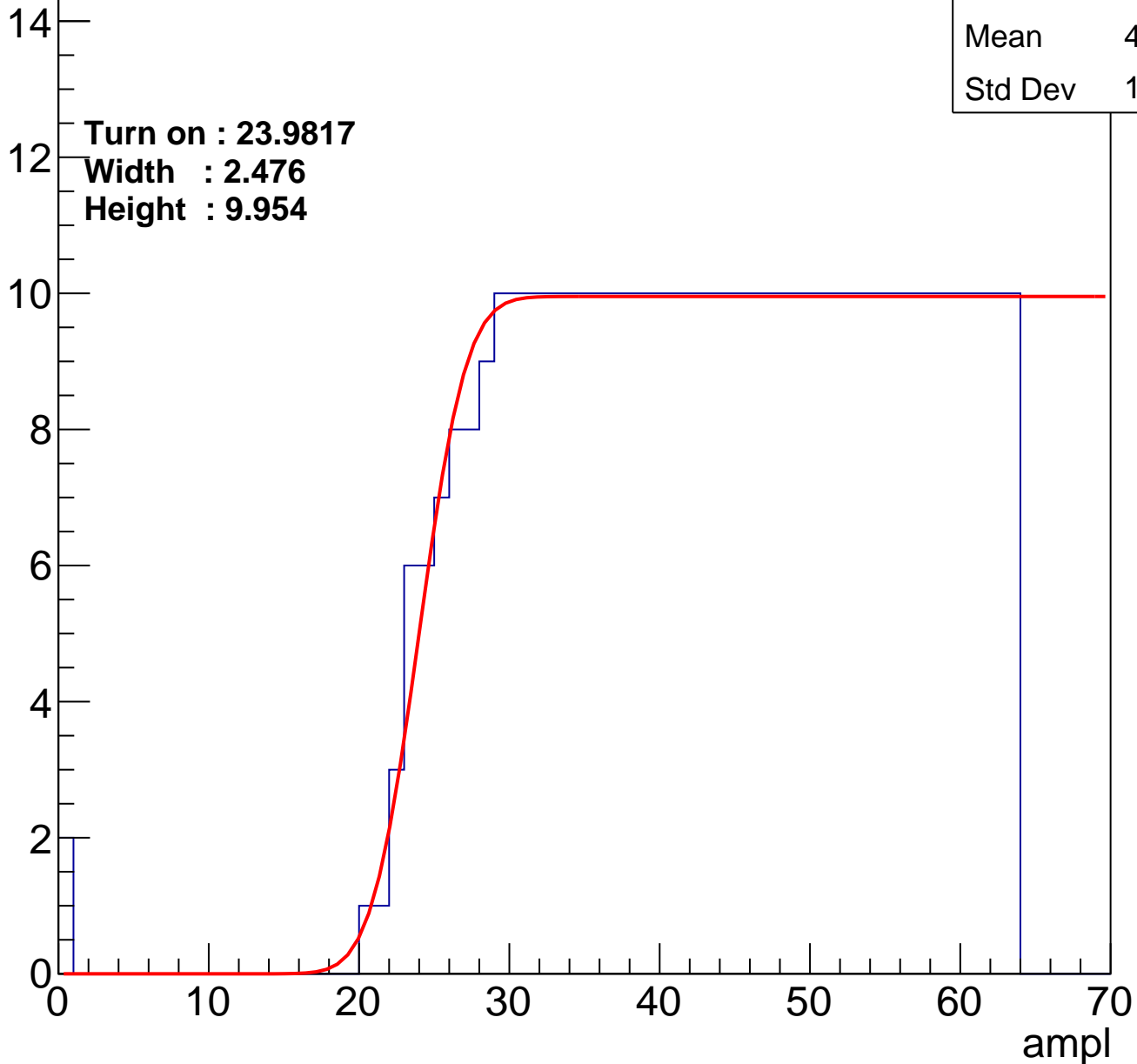
Entries	401
Mean	43.24
Std Dev	12.03

Turn on : 23.9817

Width : 2.476

Height : 9.954

Entry



B1L000S, U6-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.98
Std Dev	11.83

Turn on : 25.7806

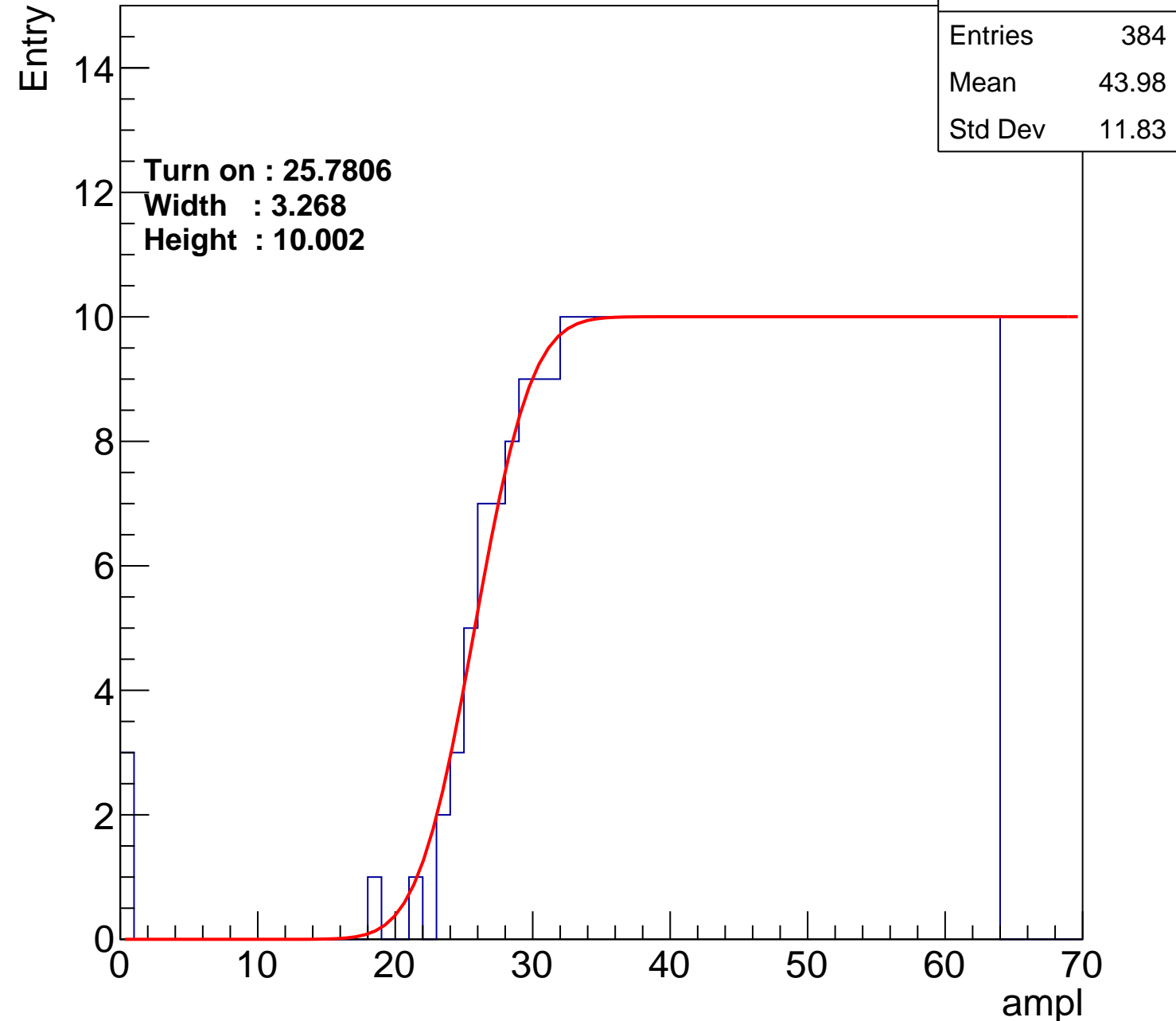
Width : 3.268

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.6311

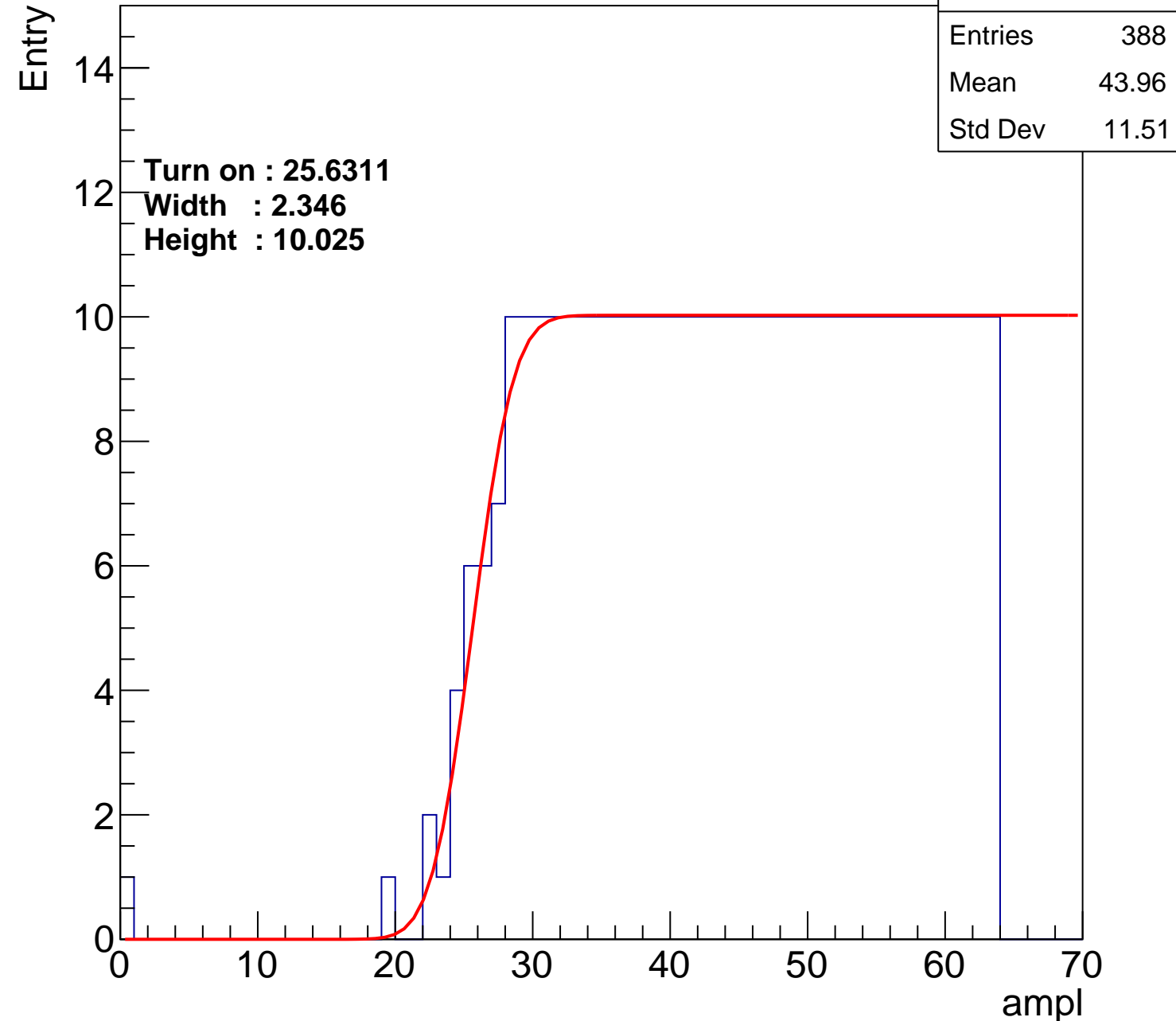
Width : 2.346

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.8163

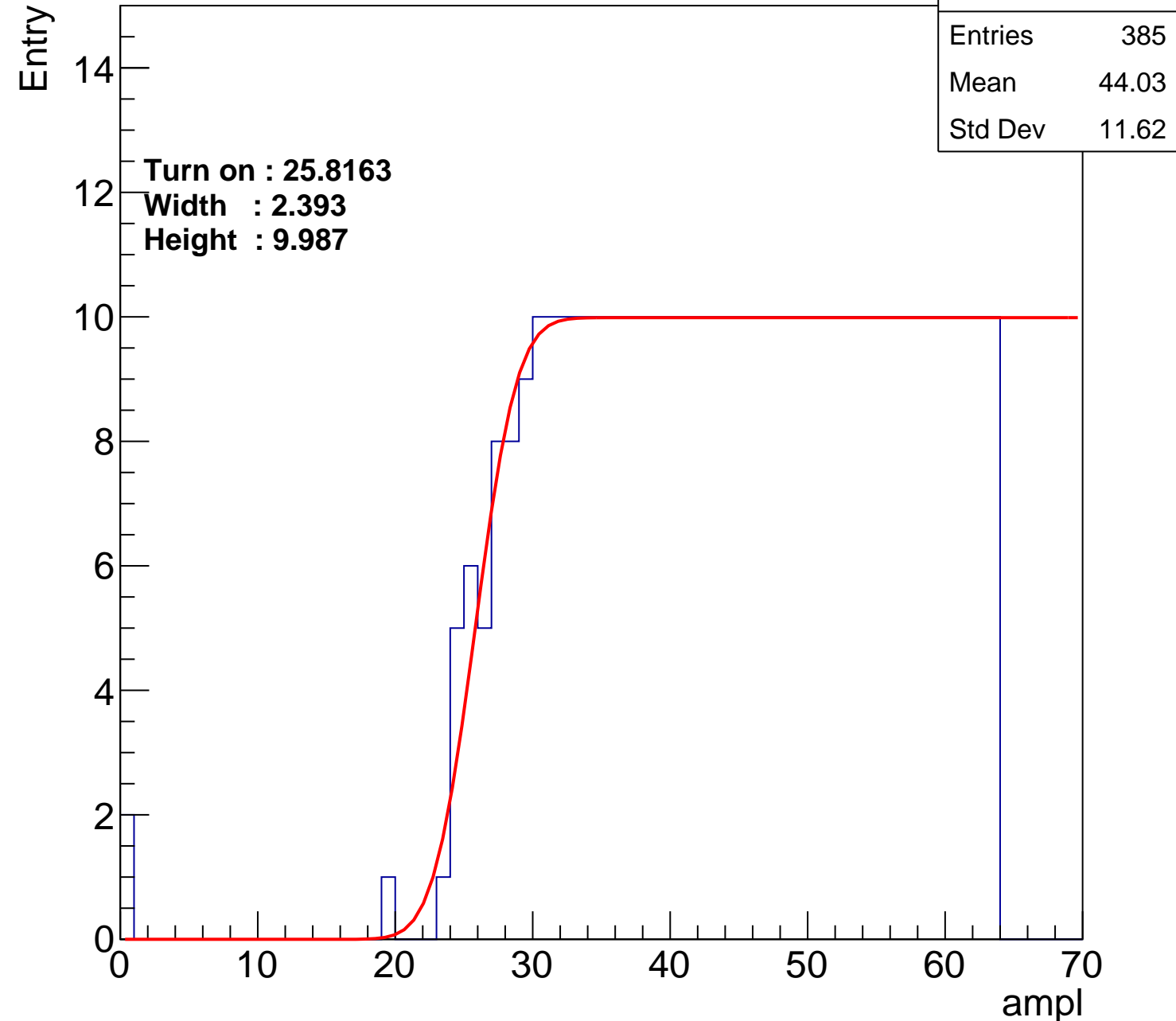
Width : 2.393

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.54
Std Dev	12.04

Turn on : 25.2617

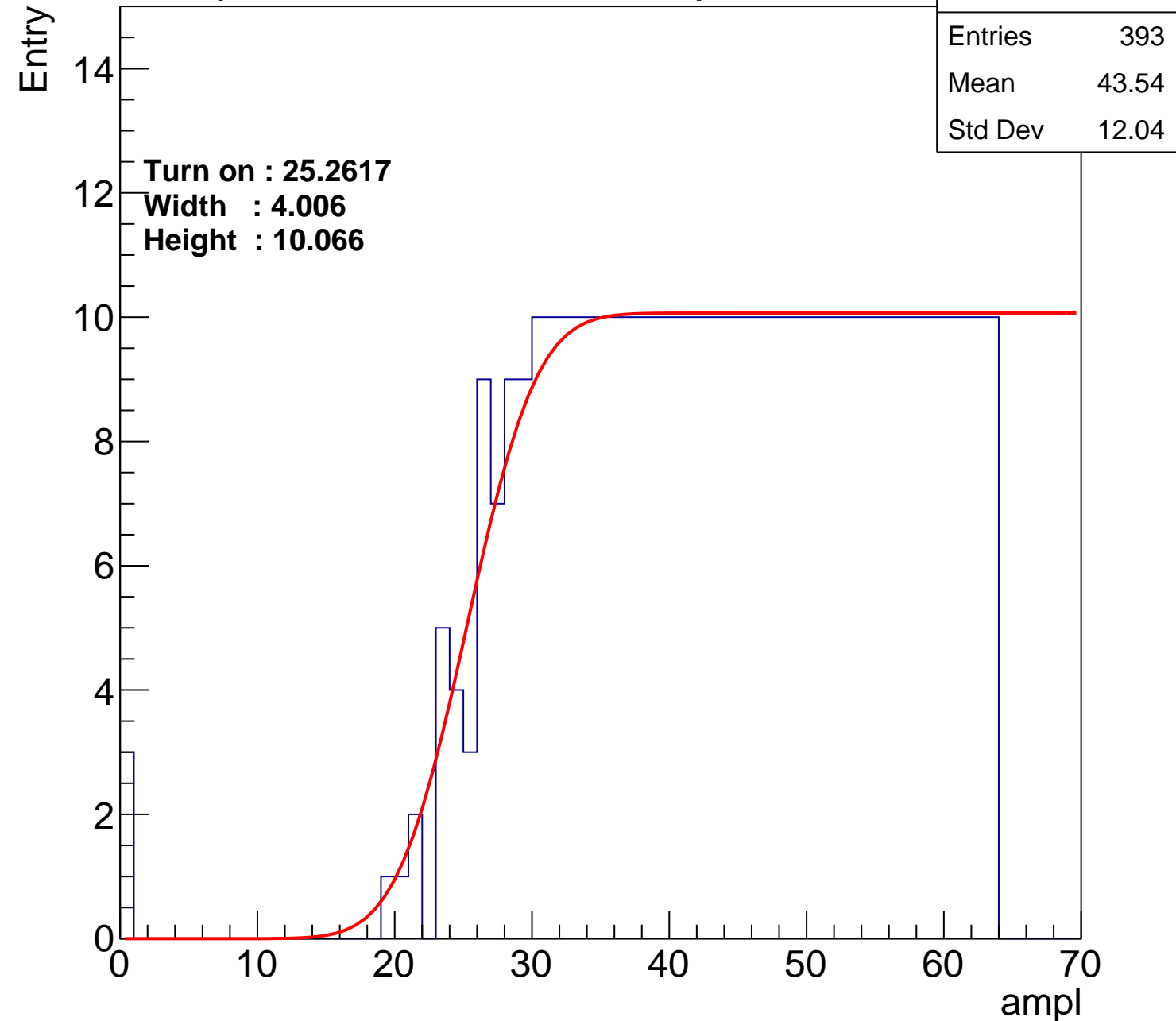
Width : 4.006

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch71

calib_packv5_042523_0143.root, FC#3, port B3

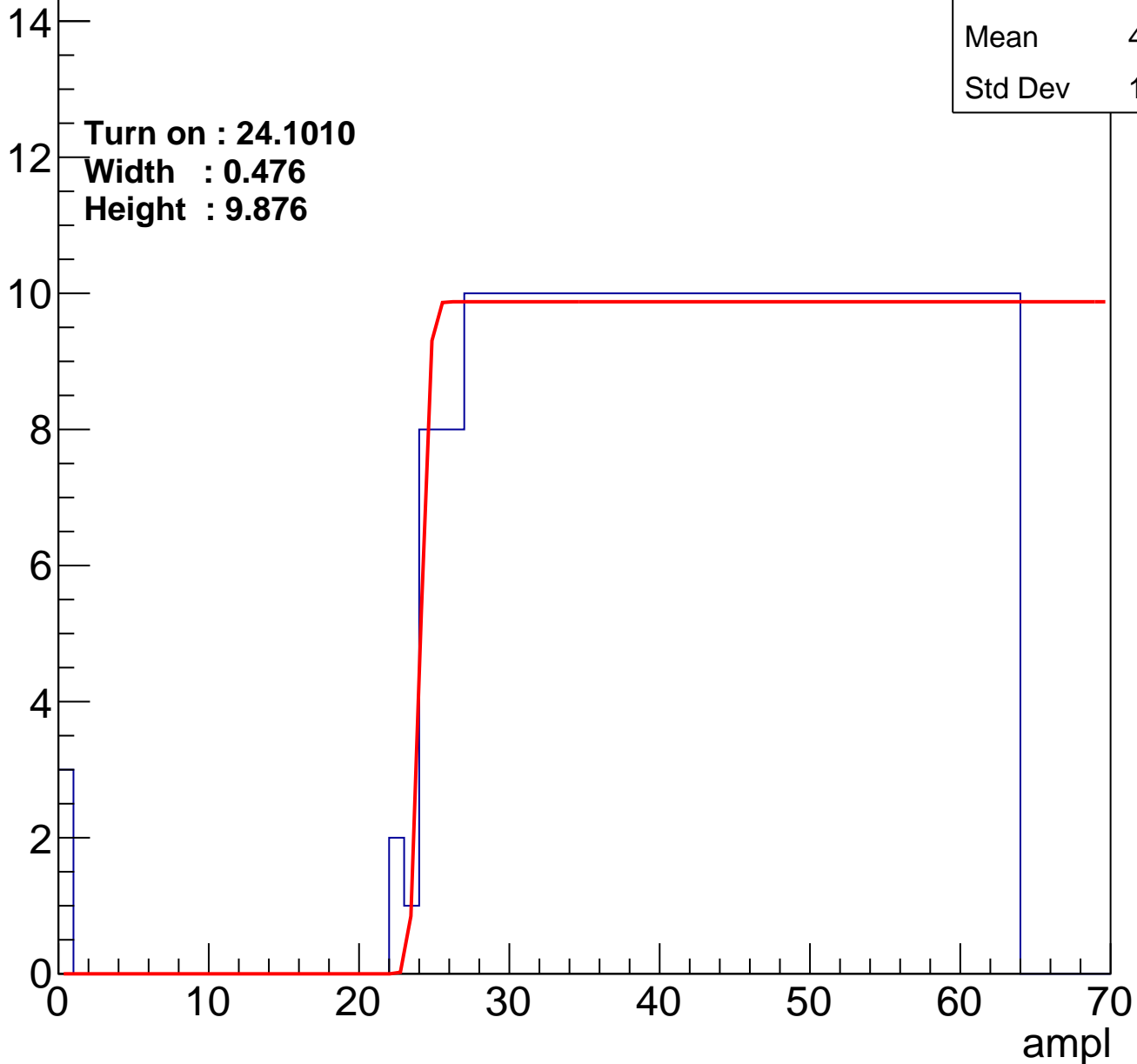
Entries	400
Mean	43.29
Std Dev	12.07

Turn on : 24.1010

Width : 0.476

Height : 9.876

Entry



B1L000S, U6-ch72

calib_packv5_042523_0143.root, FC#3, port B3

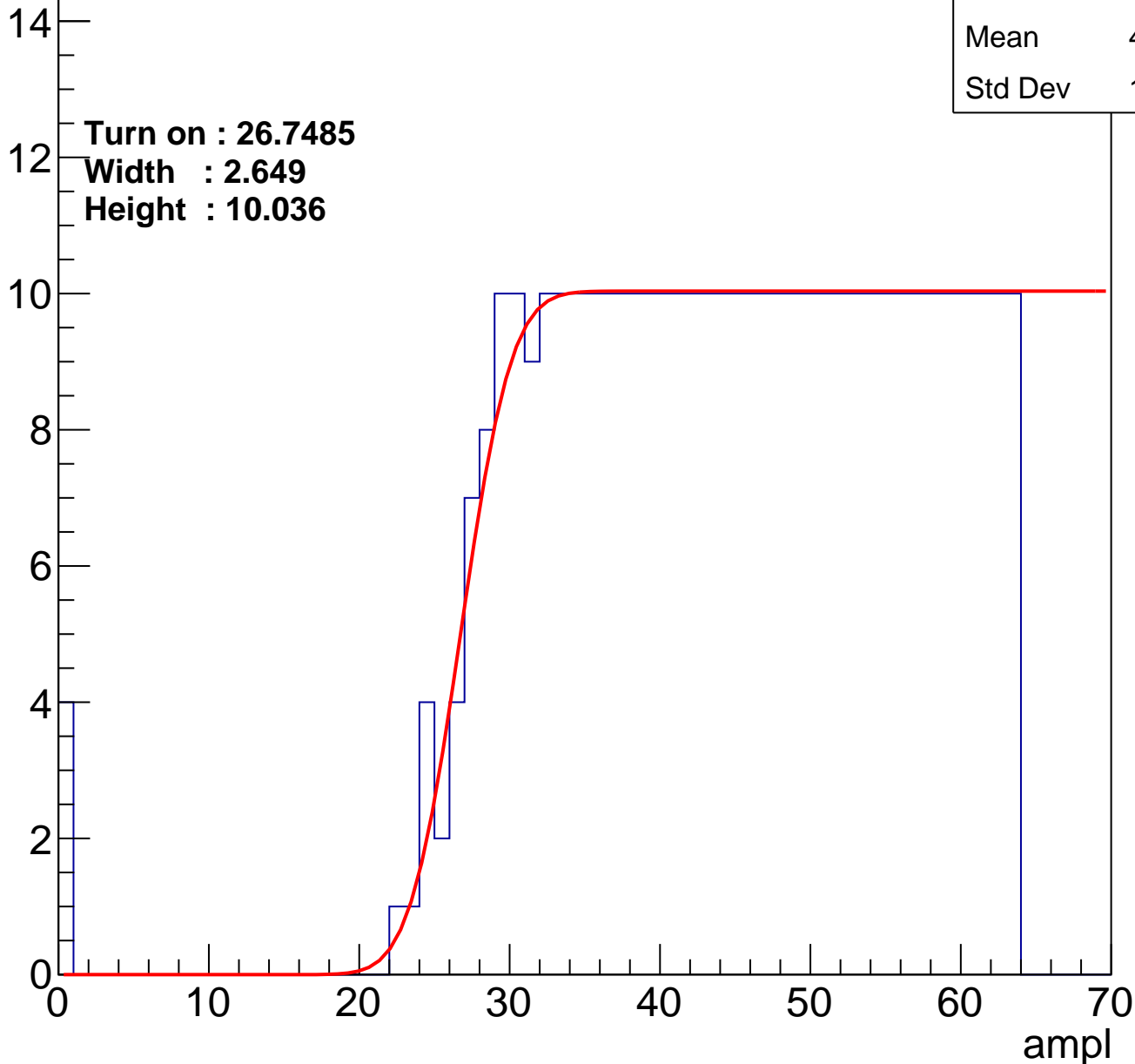
Entry

Entries	380
Mean	44.15
Std Dev	11.84

Turn on : 26.7485

Width : 2.649

Height : 10.036



B1L000S, U6-ch73

calib_packv5_042523_0143.root, FC#3, port B3

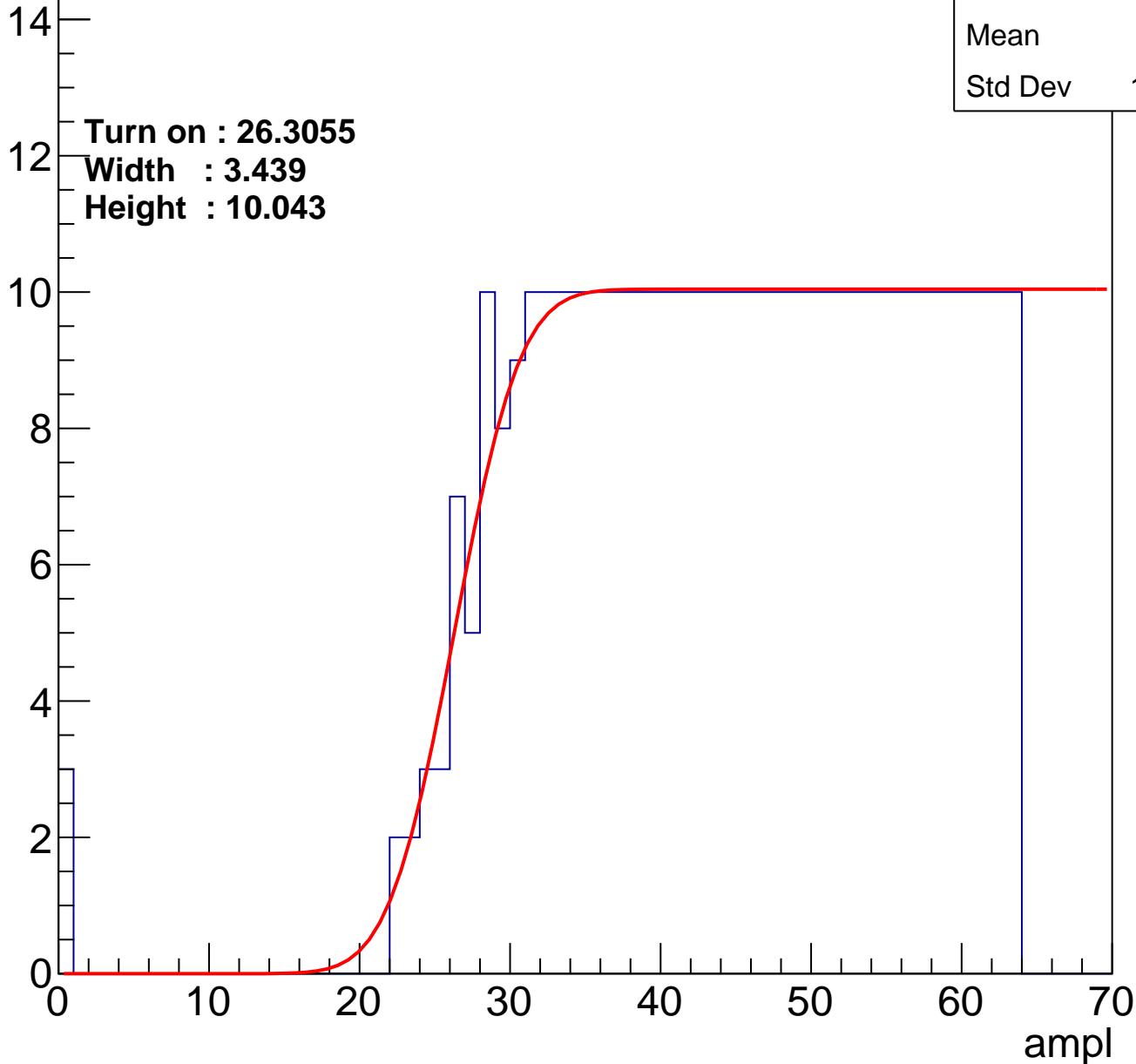
Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.3055

Width : 3.439

Height : 10.043

Entry



B1L000S, U6-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 24.7777

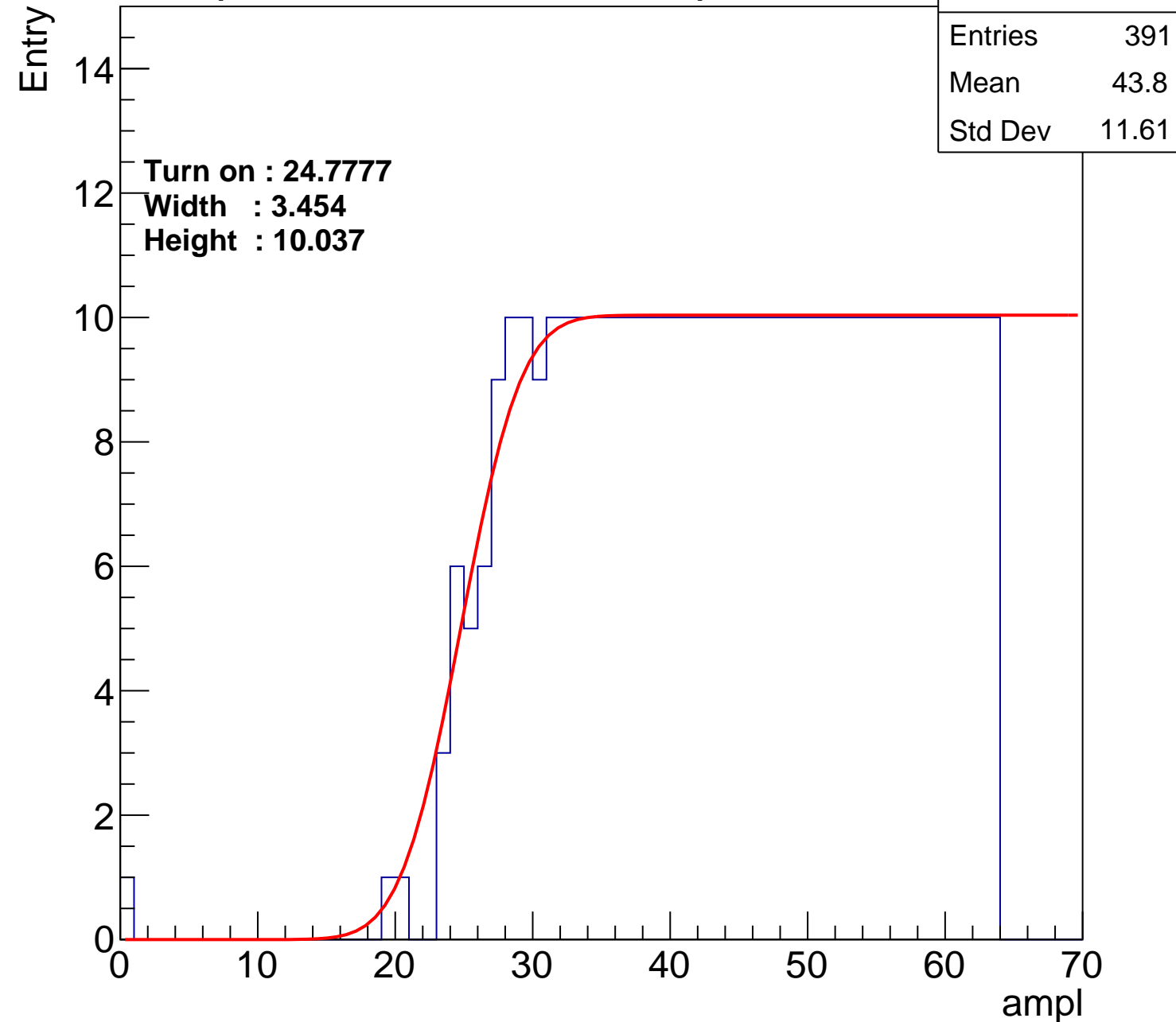
Width : 3.454

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.16
Std Dev	11.56

Turn on : 26.8935

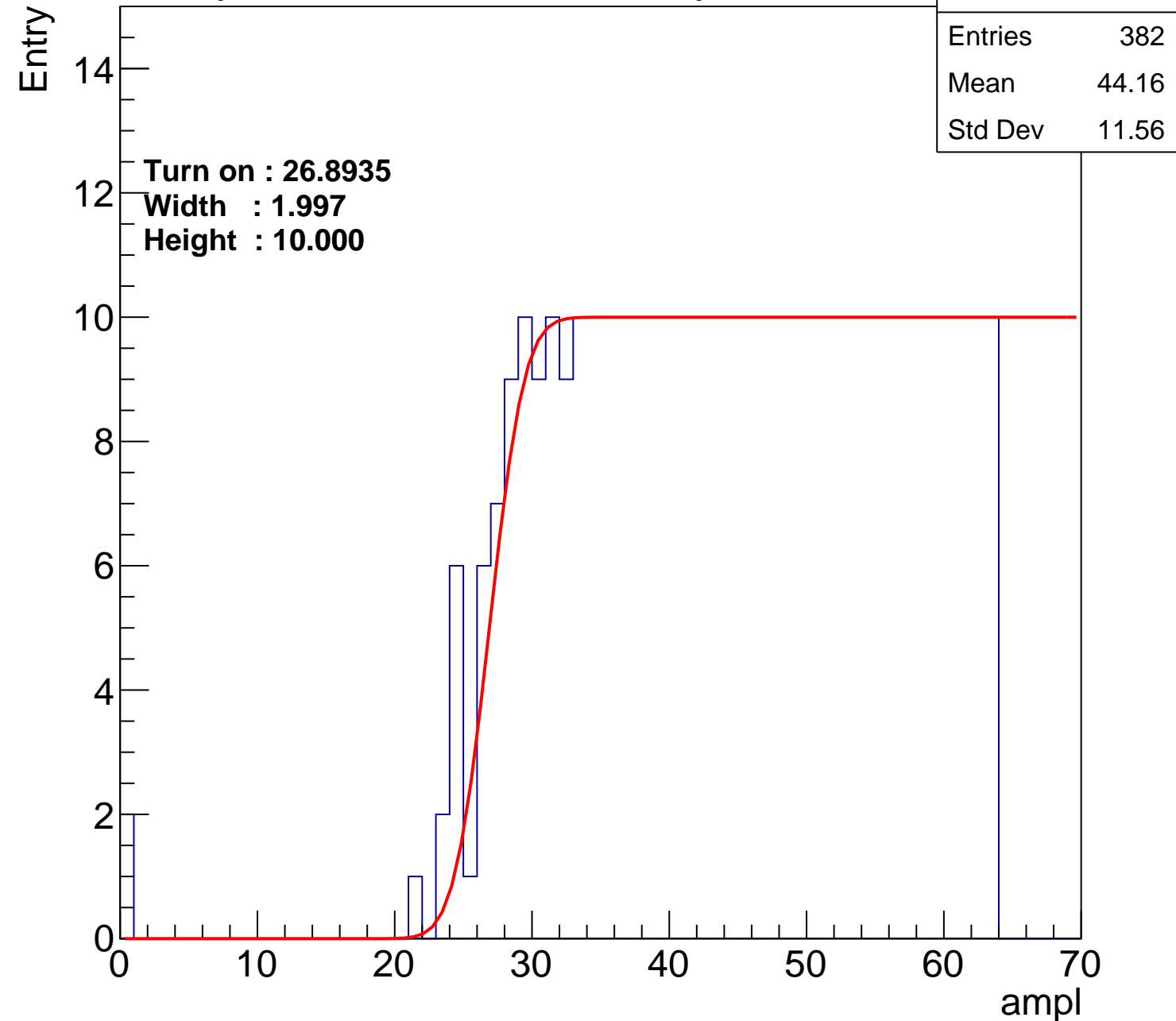
Width : 1.997

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.28
Std Dev	11.79

Turn on : 26.6972

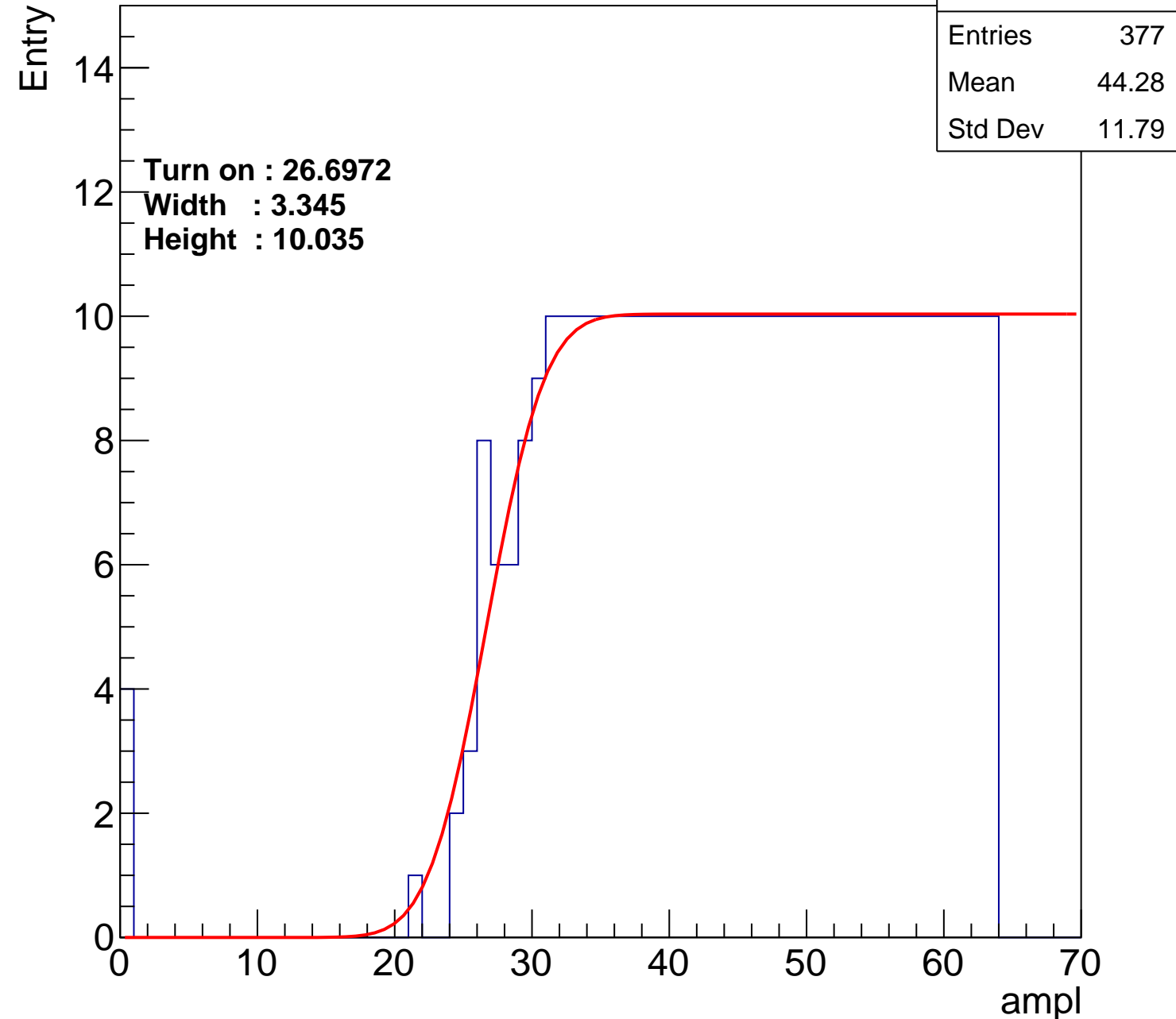
Width : 3.345

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	45
Std Dev	10.94

Turn on : 27.6091

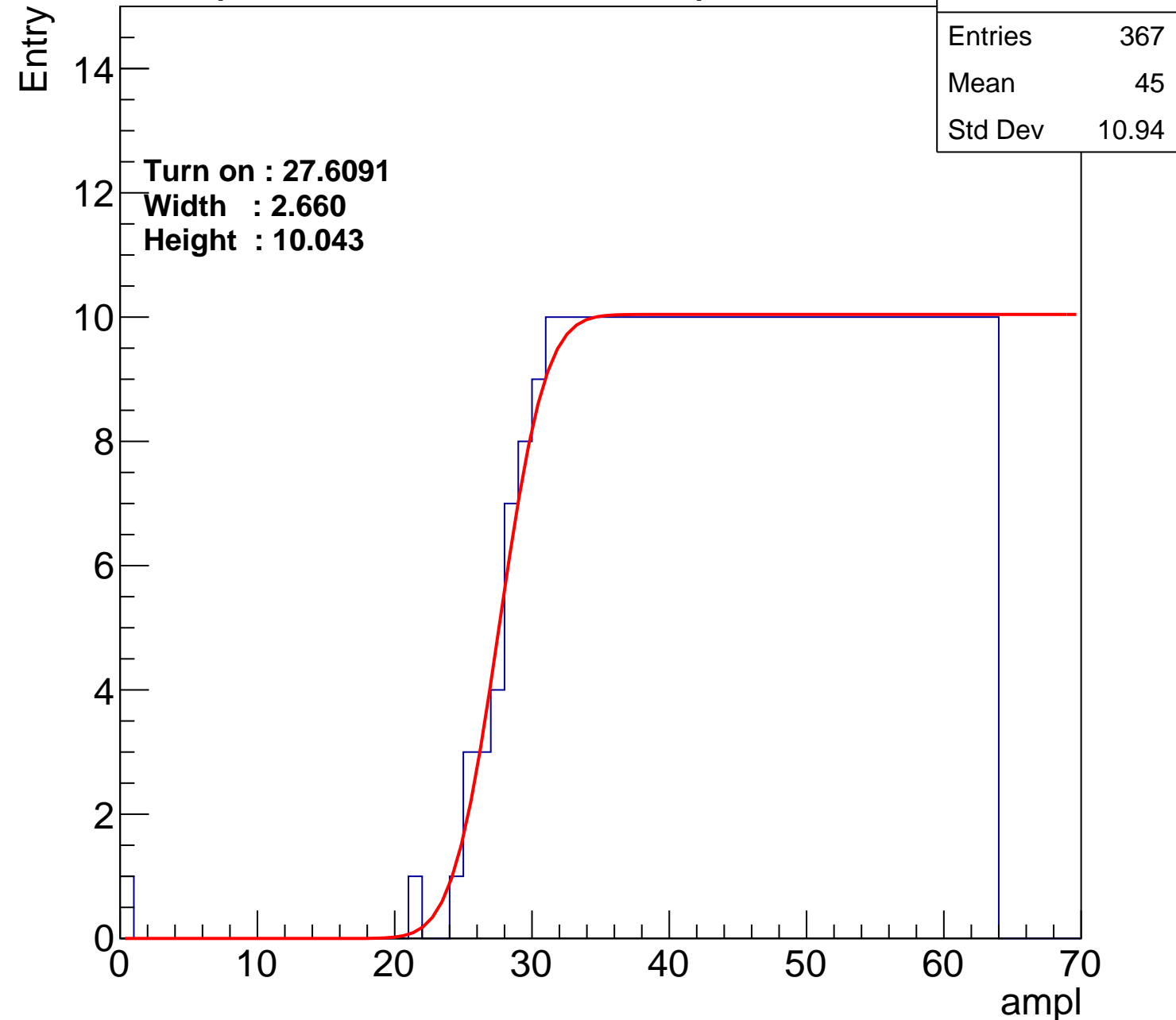
Width : 2.660

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 24.8549

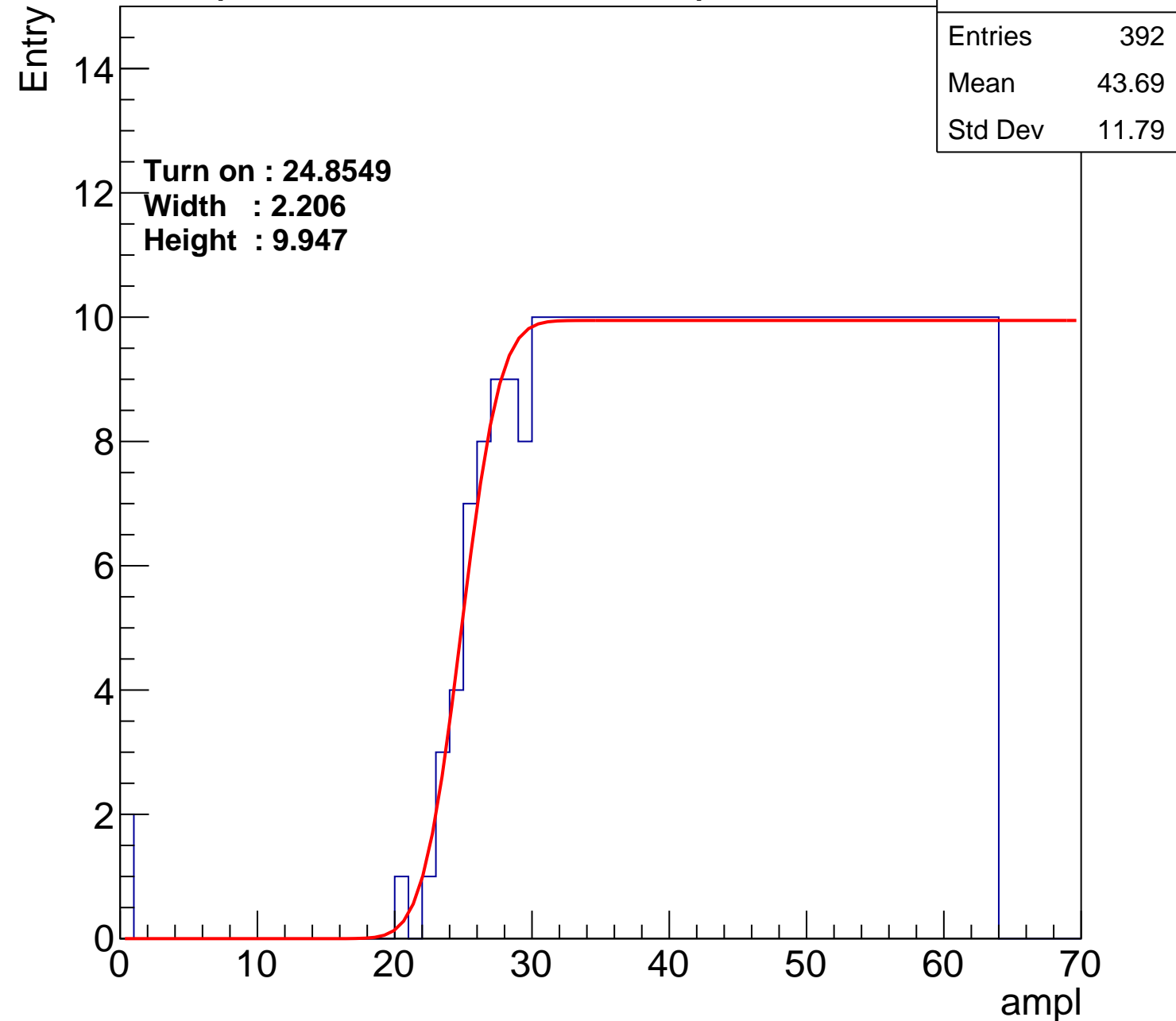
Width : 2.206

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.57
Std Dev	11.47

Turn on : 27.3697

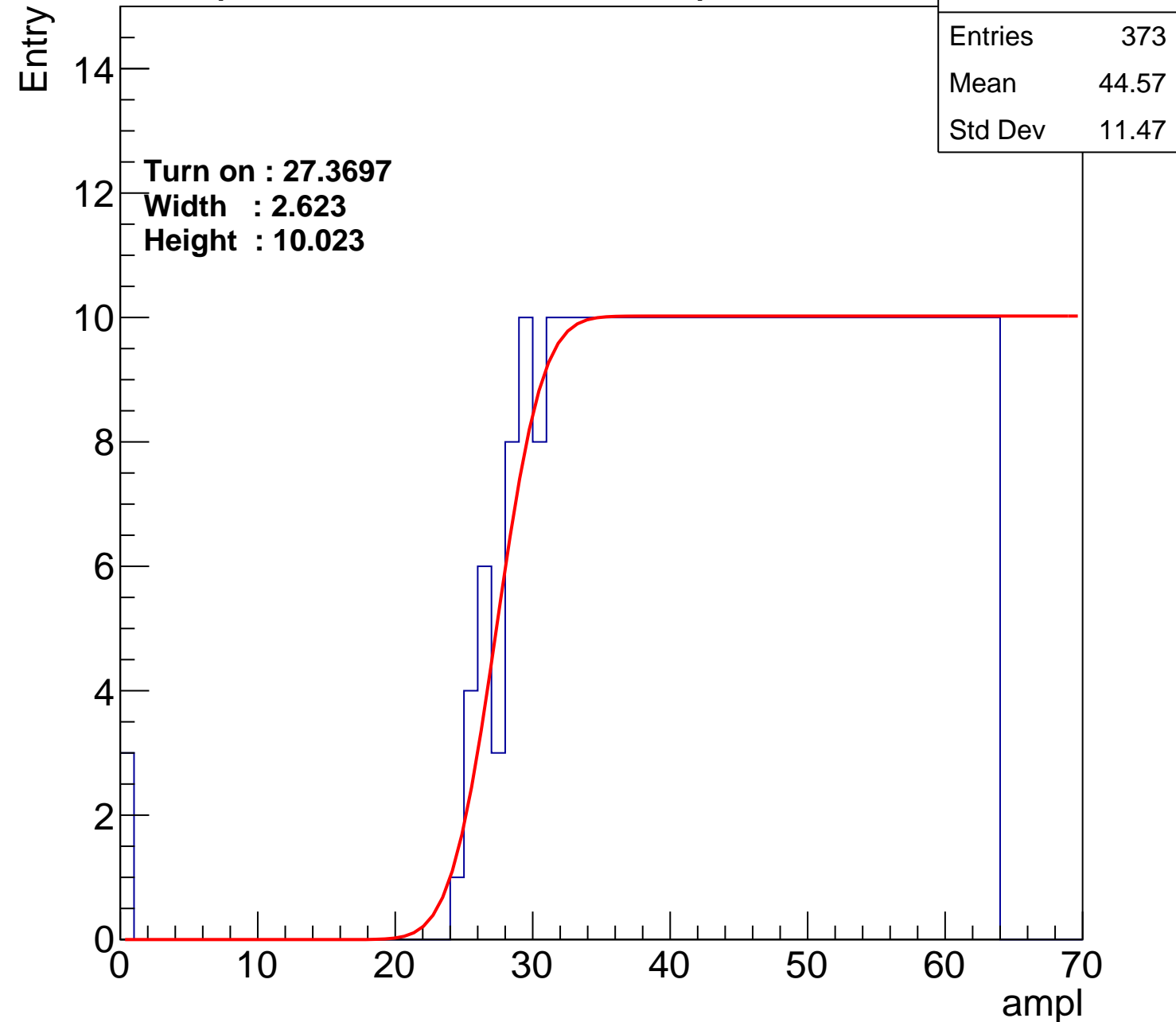
Width : 2.623

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.7656

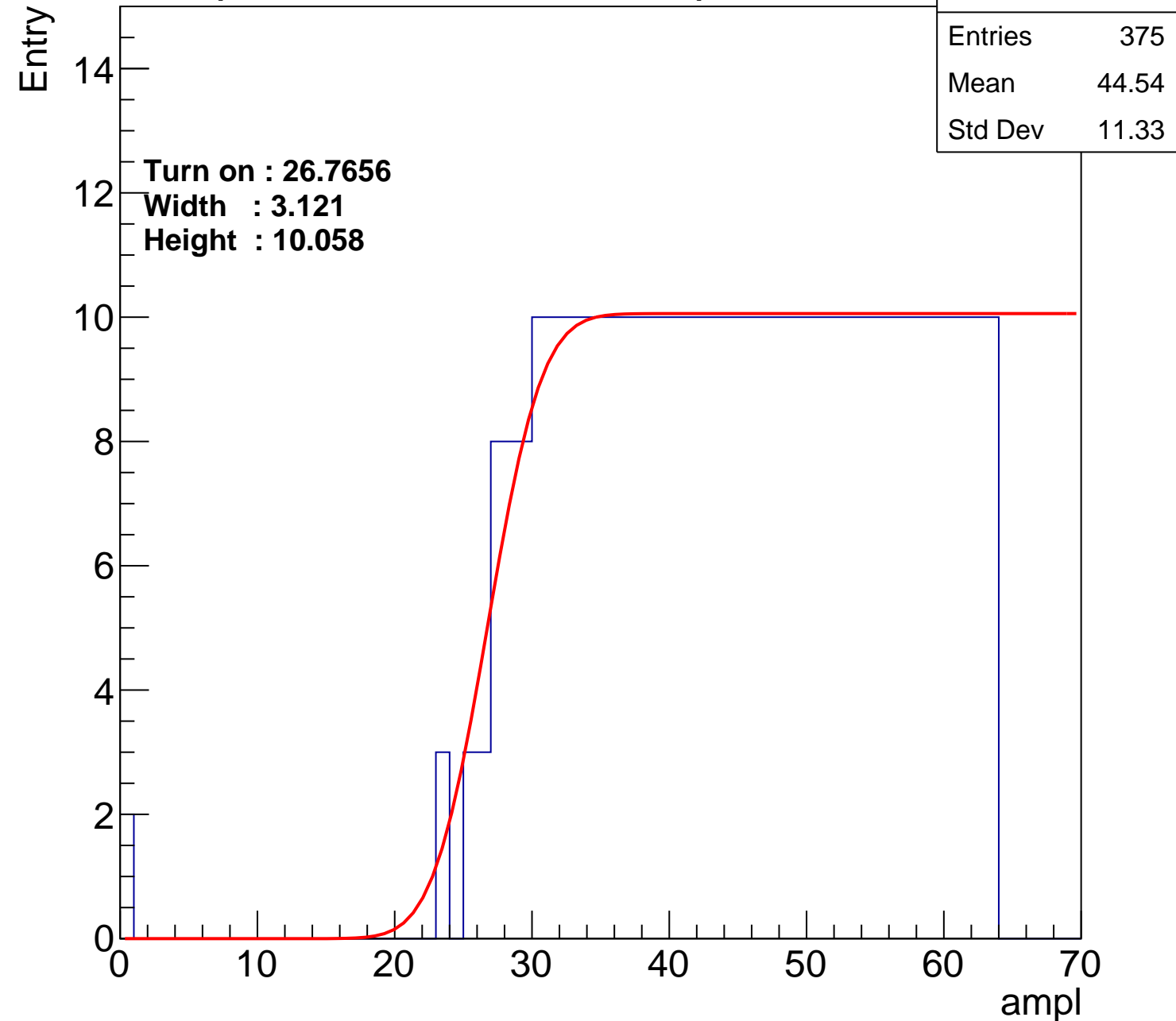
Width : 3.121

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 26.3133

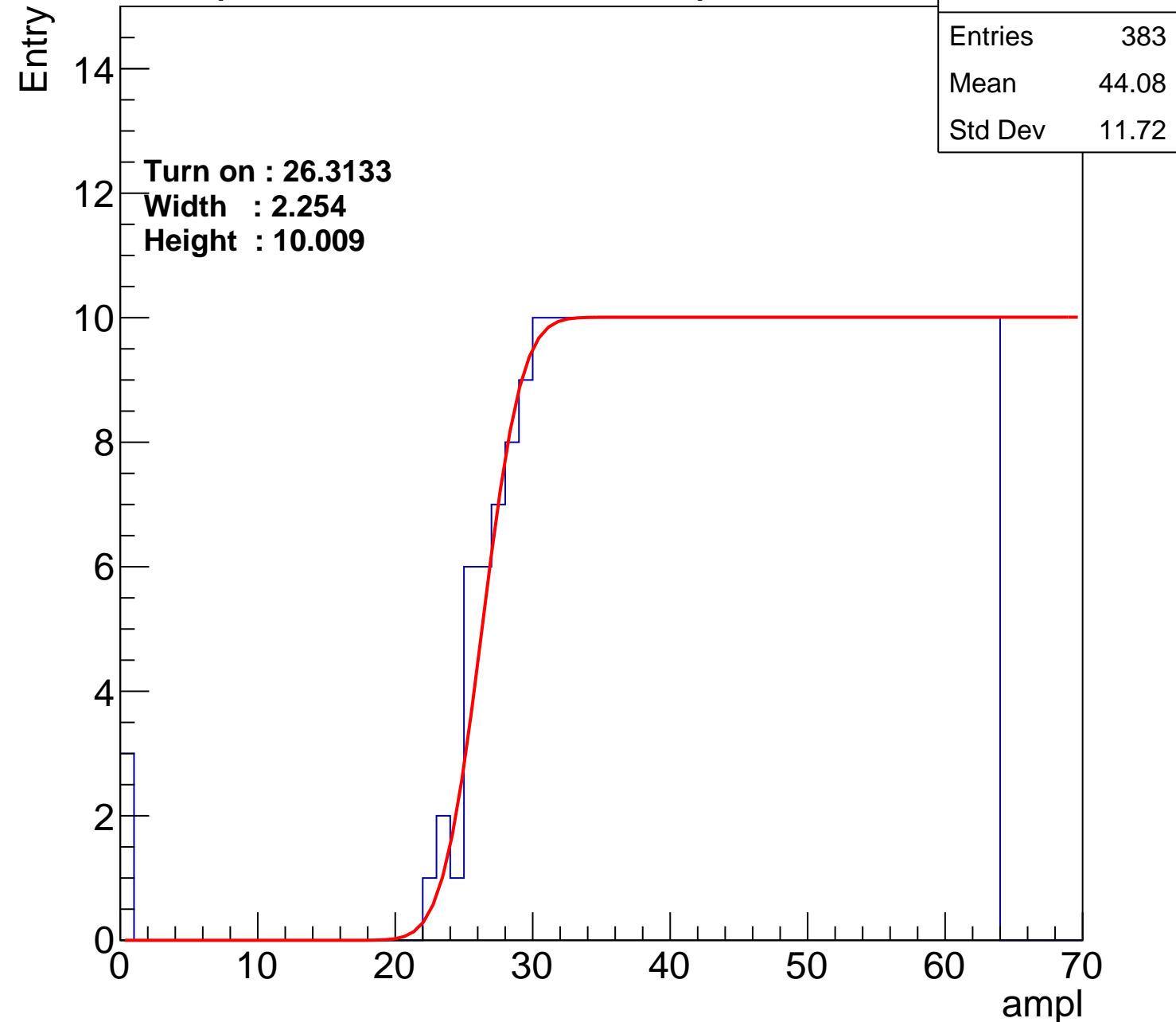
Width : 2.254

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
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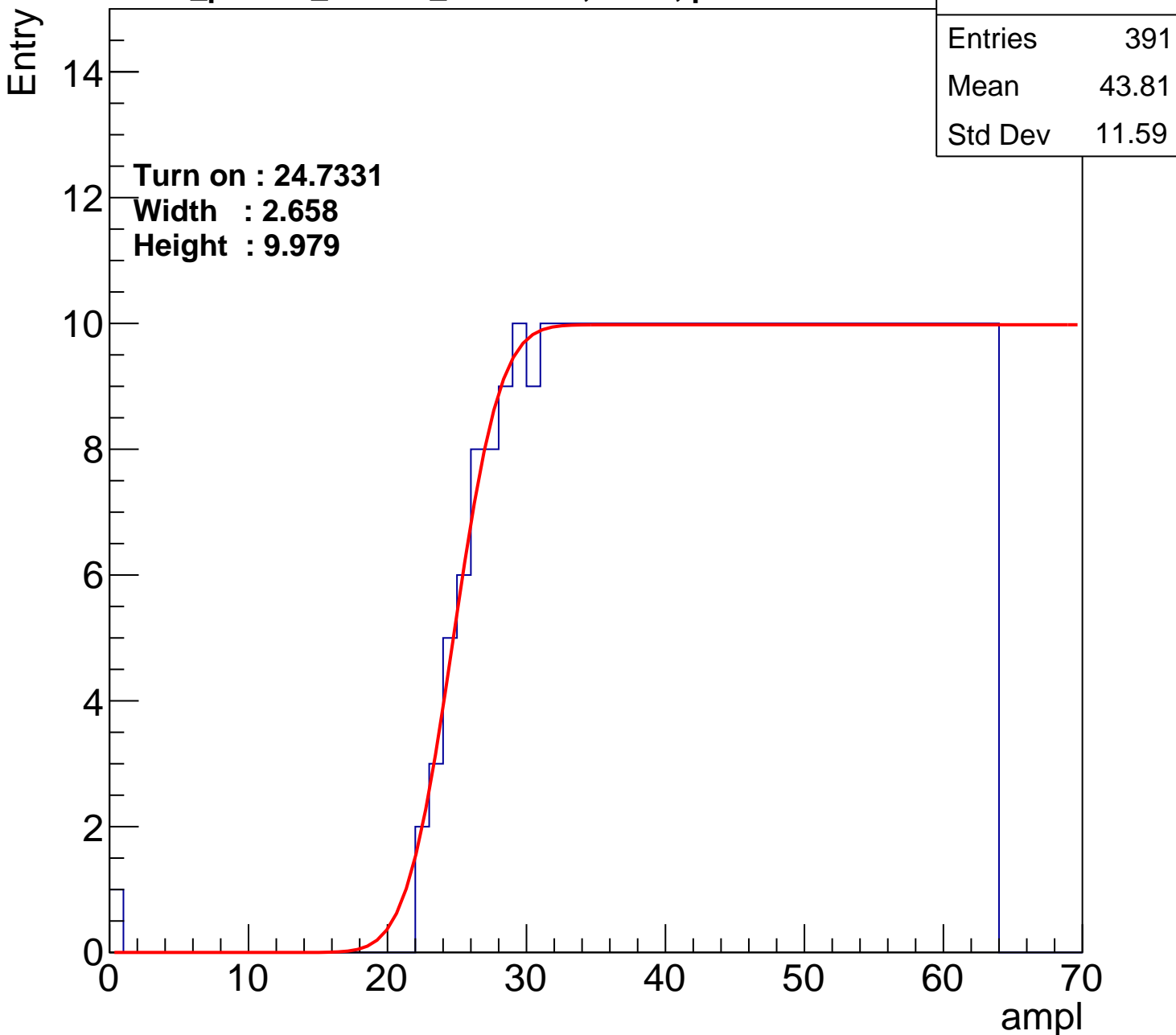
Mean	43.81
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Std Dev	11.59
---------	-------

Turn on : 24.7331

Width : 2.658

Height : 9.979



B1L000S, U6-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 27.6442

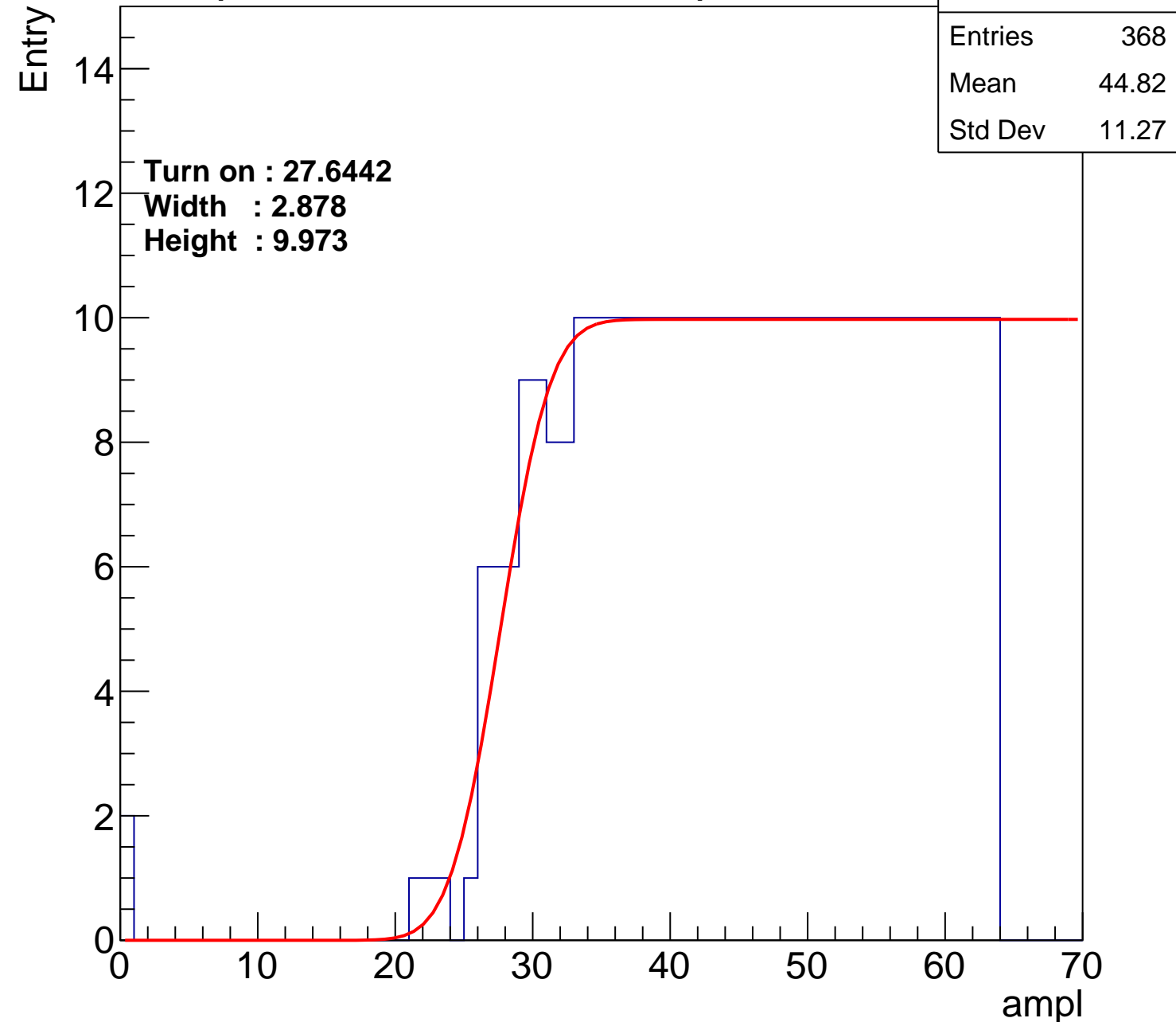
Width : 2.878

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch84

calib_packv5_042523_0143.root, FC#3, port B3

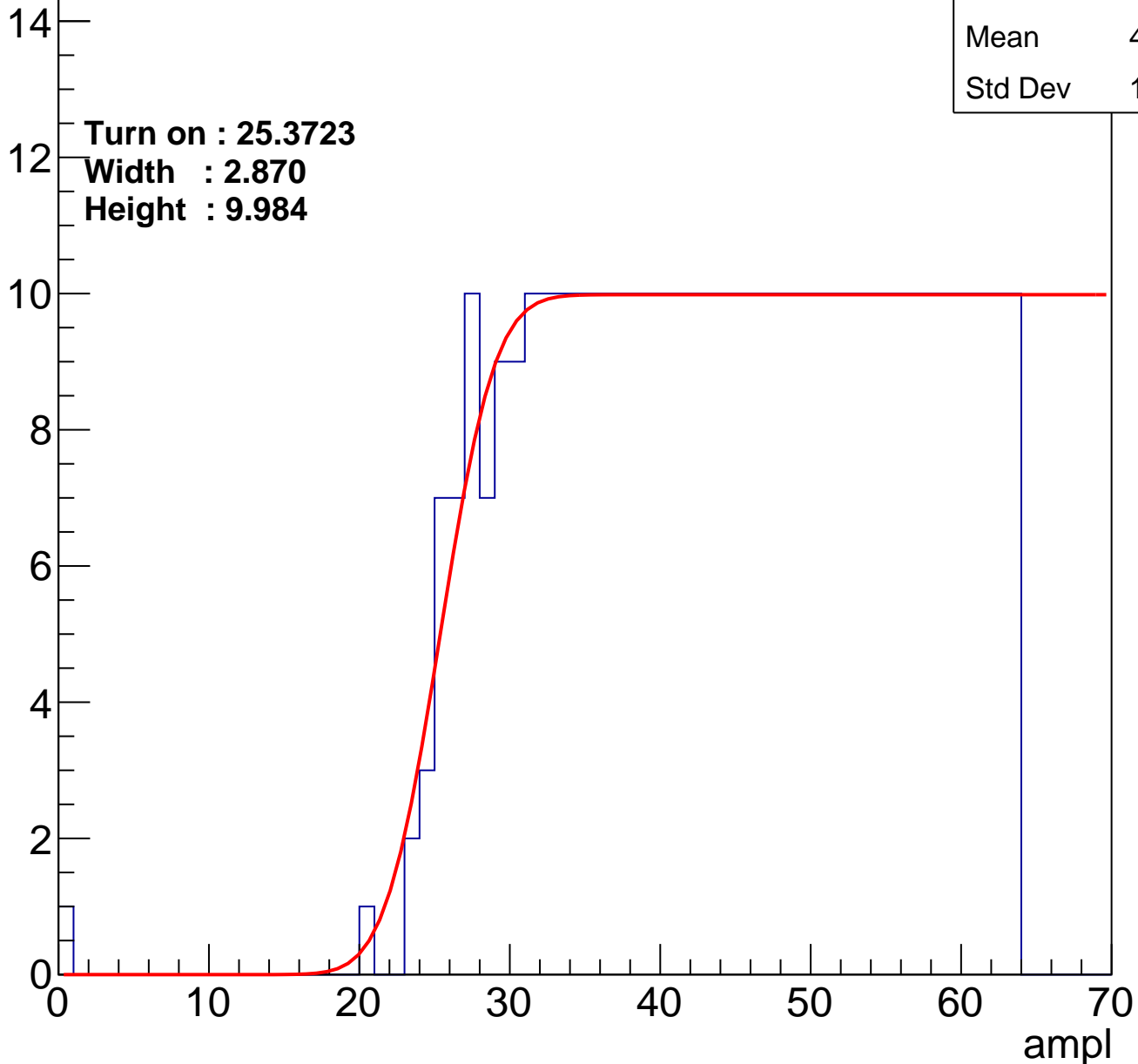
Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.3723

Width : 2.870

Height : 9.984

Entry



B1L000S, U6-ch85

calib_packv5_042523_0143.root, FC#3, port B3

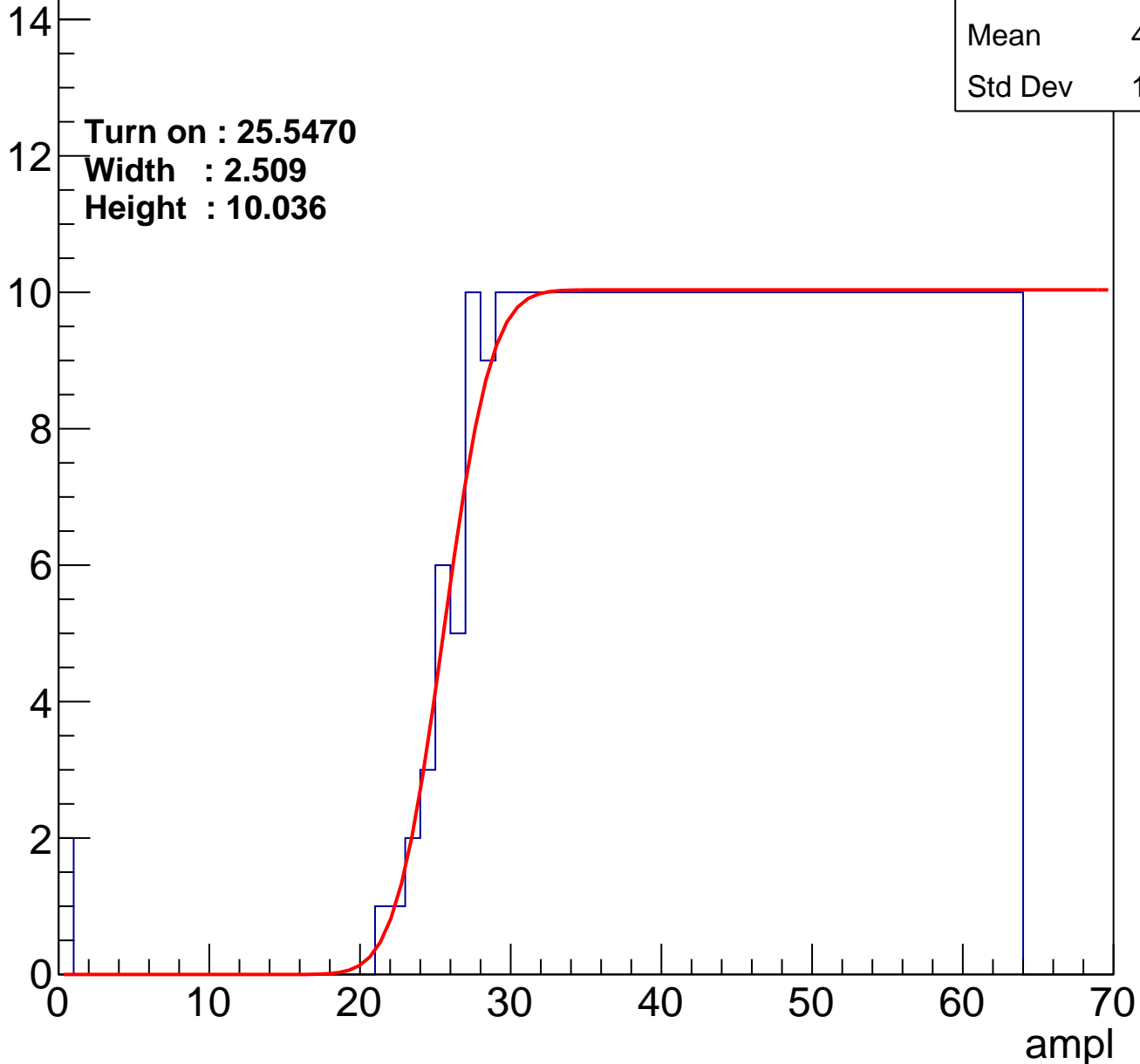
Entries	389
Mean	43.86
Std Dev	11.68

Turn on : 25.5470

Width : 2.509

Height : 10.036

Entry



B1L000S, U6-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.2293

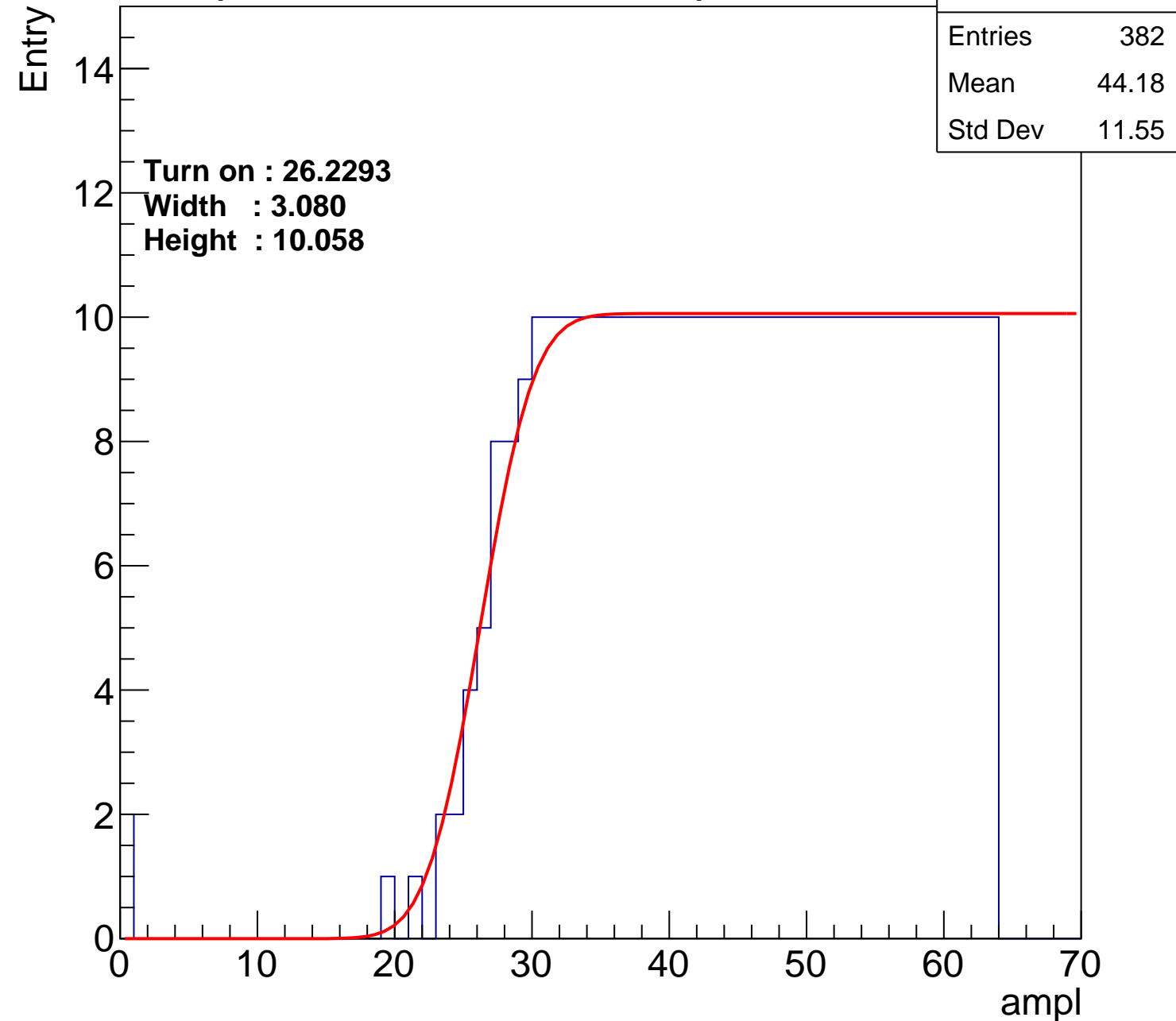
Width : 3.080

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.65
Std Dev	11.26

Turn on : 27.0758

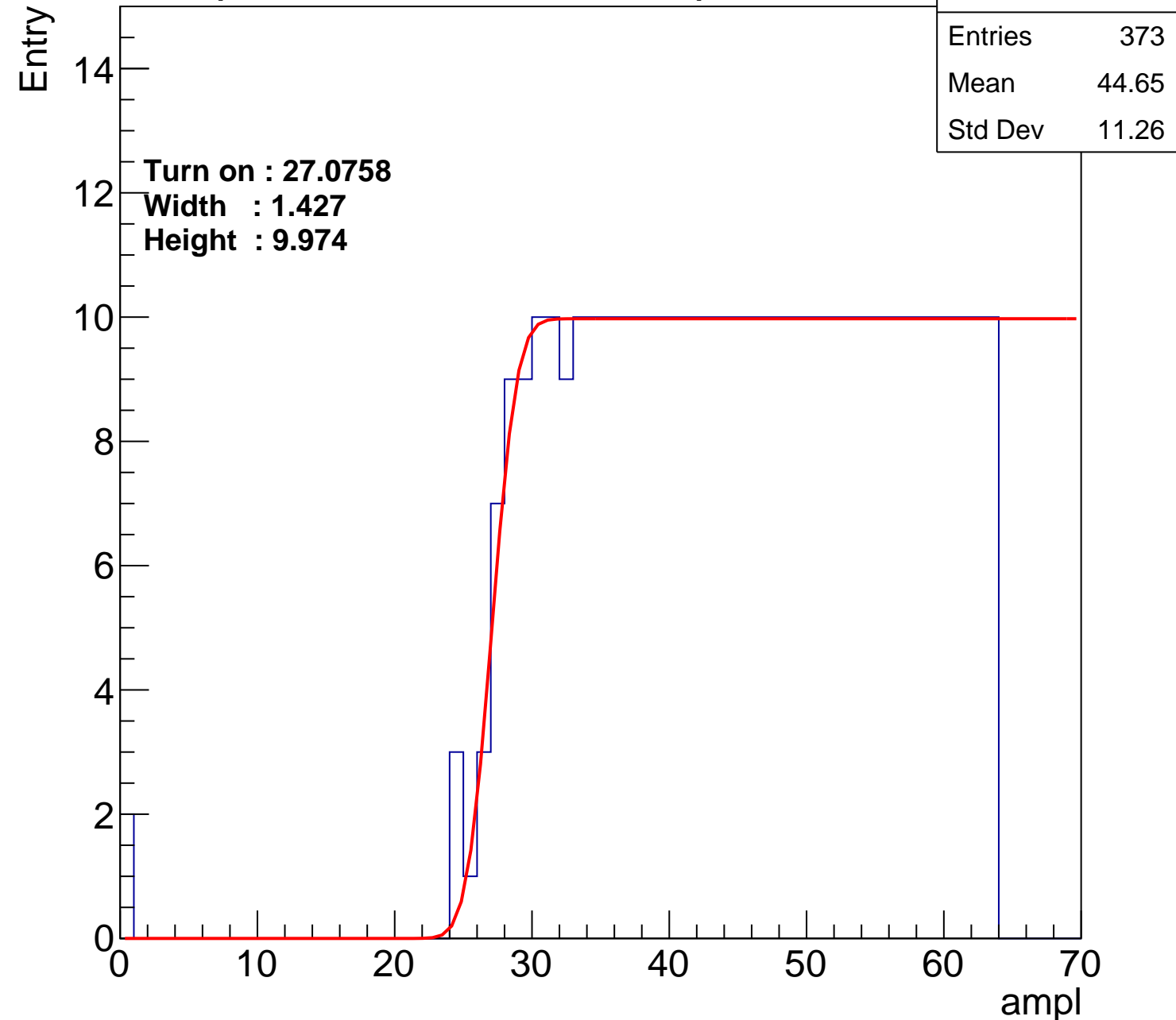
Width : 1.427

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.11
Std Dev	12.1

Turn on : 24.1045

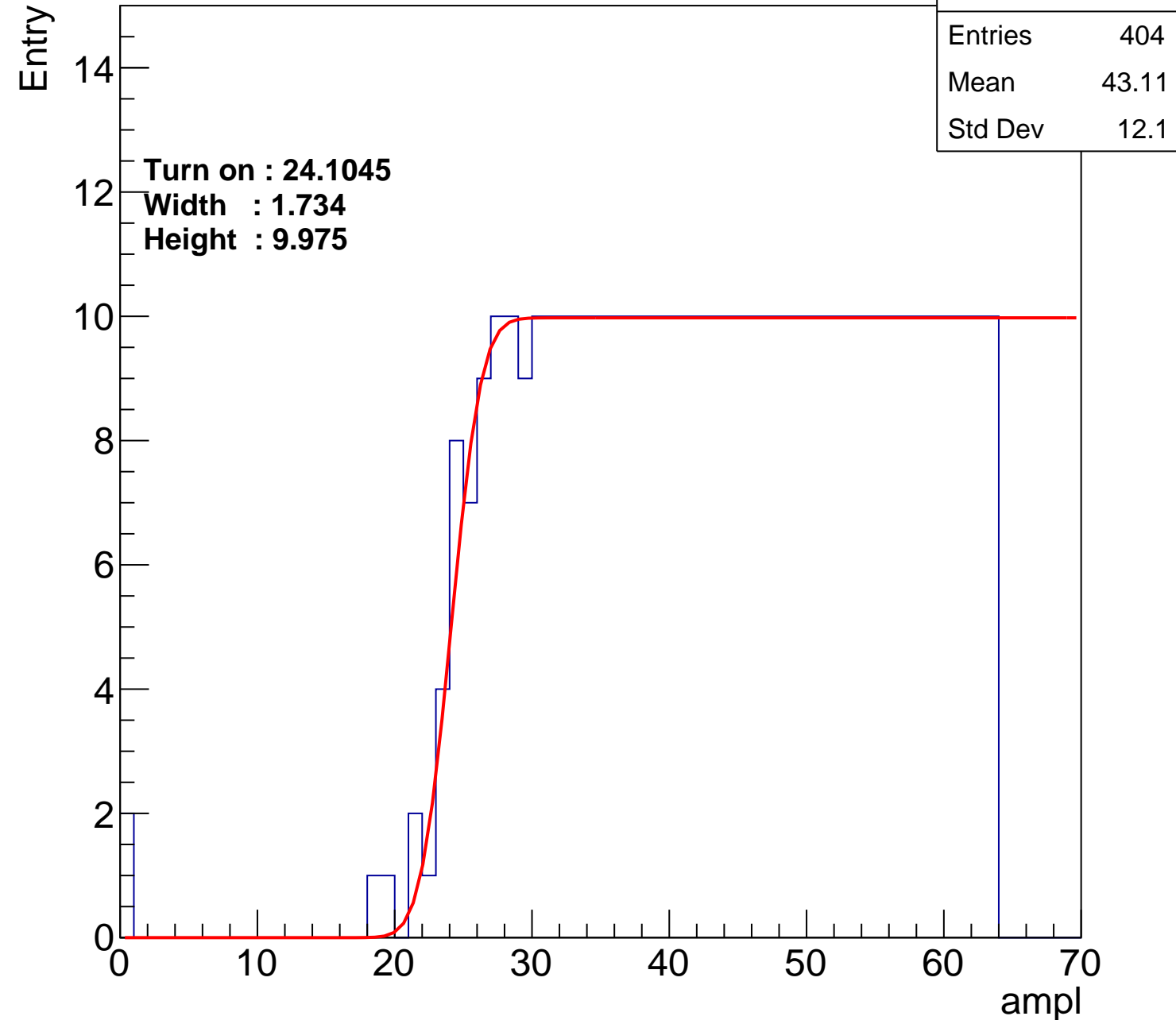
Width : 1.734

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 25.8929

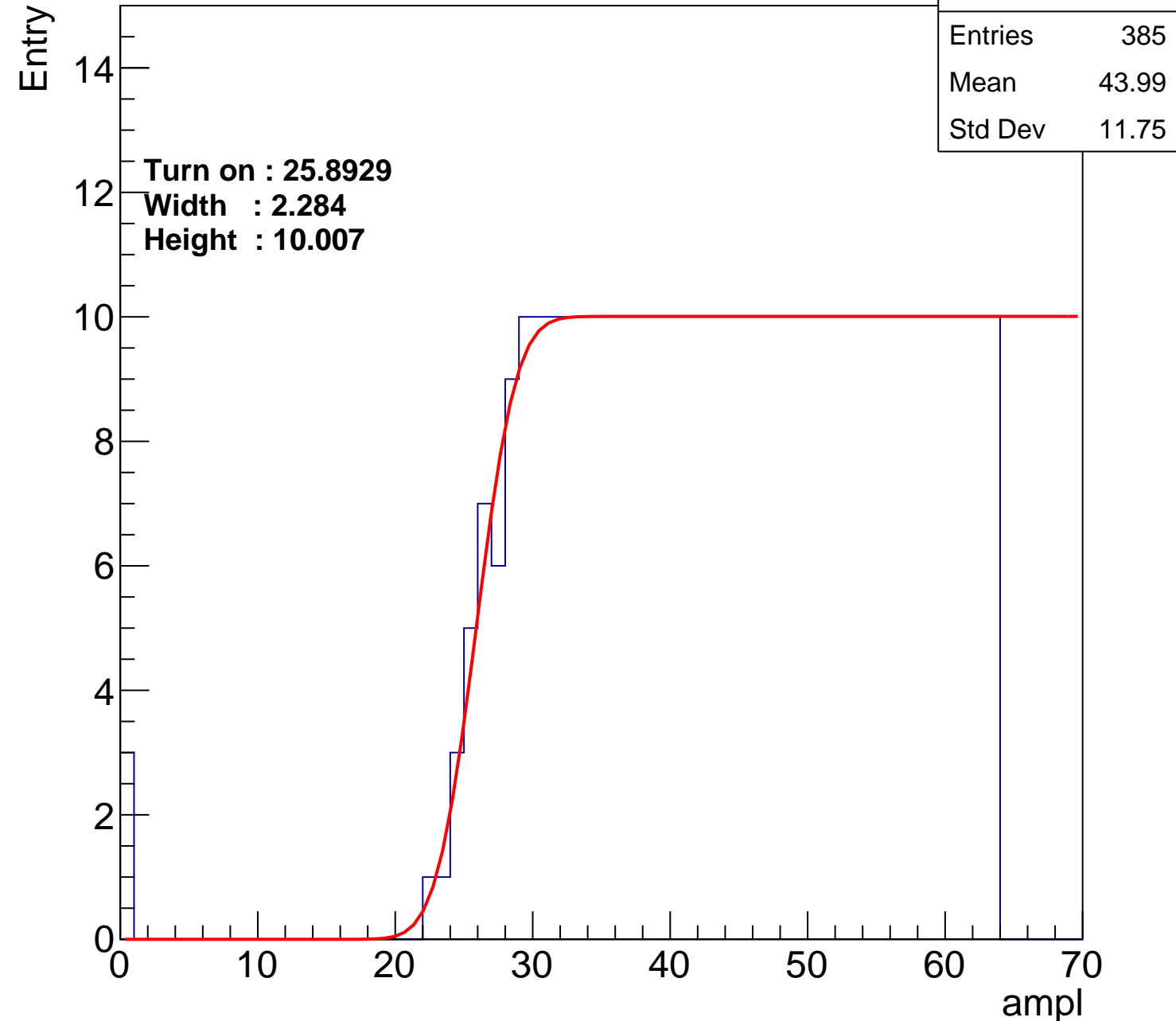
Width : 2.284

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.3803

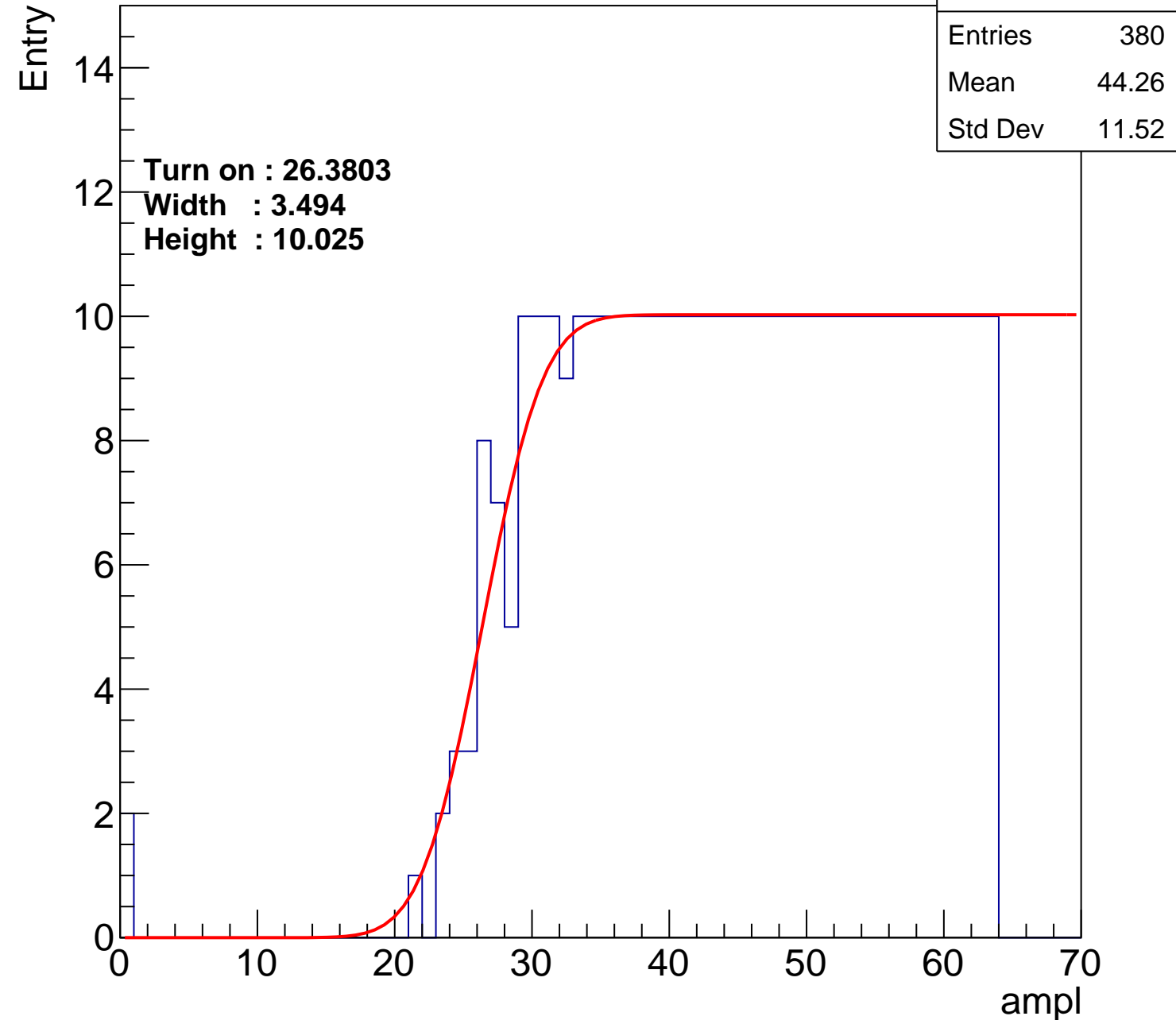
Width : 3.494

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 24.4571

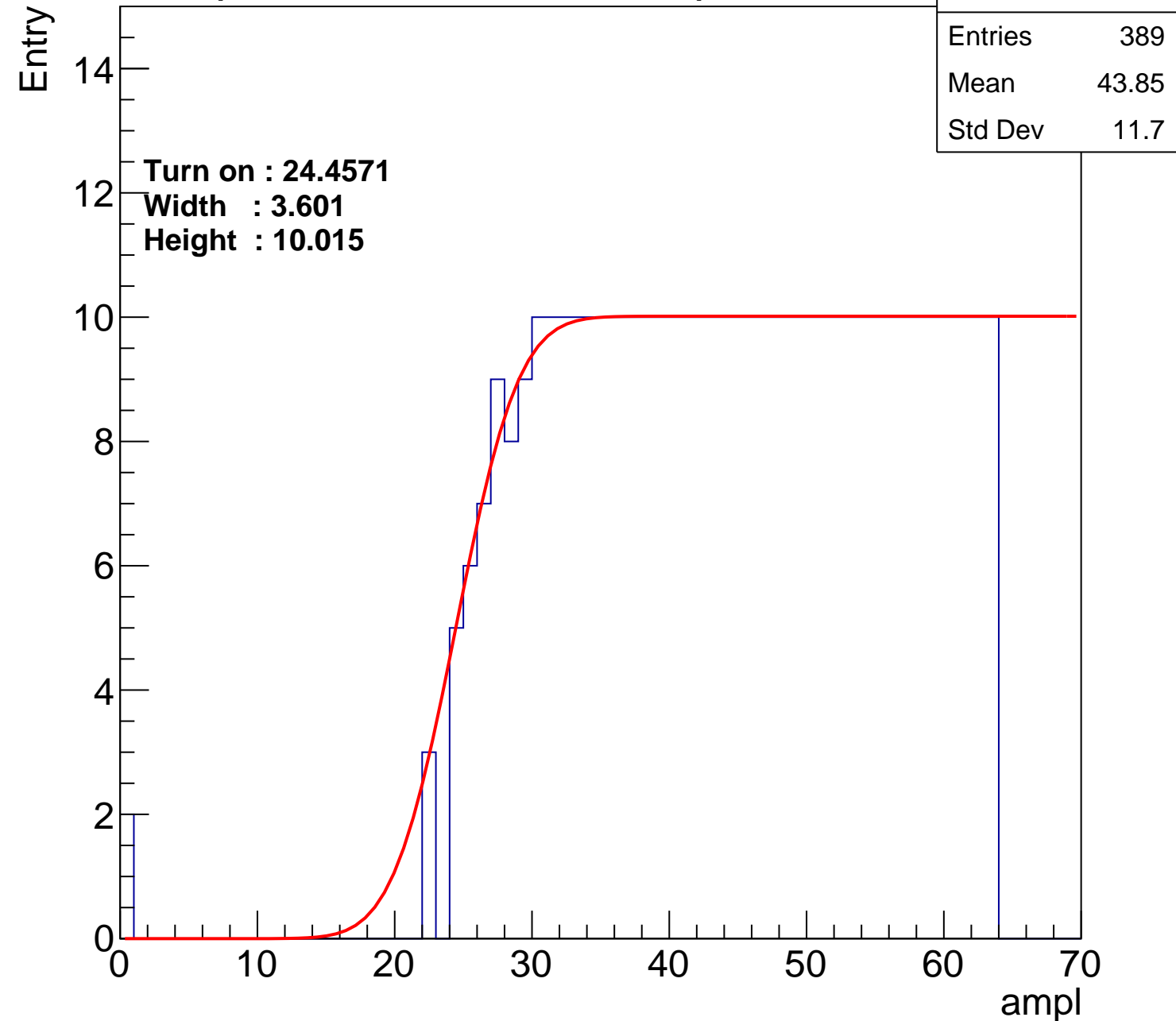
Width : 3.601

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.1824

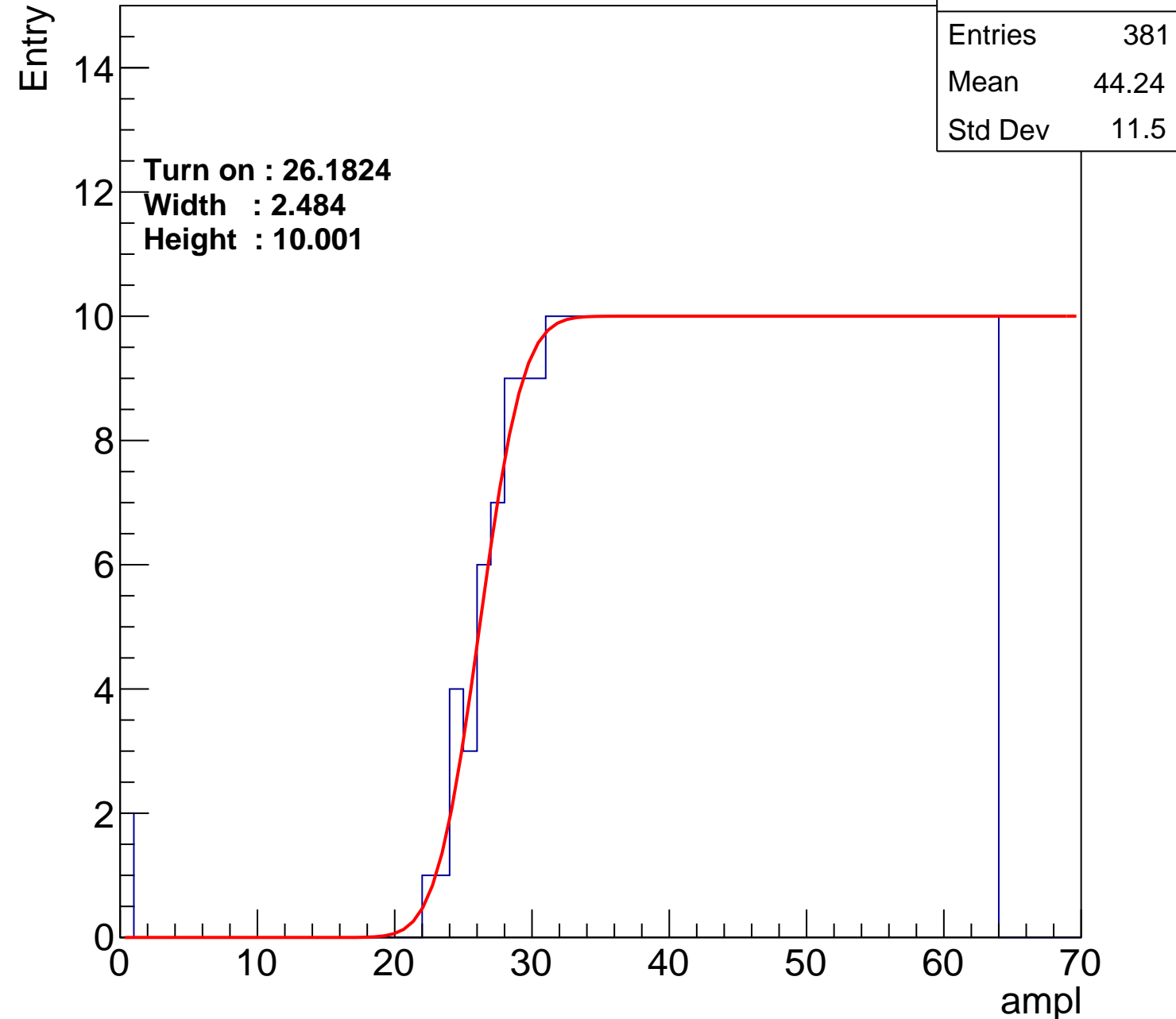
Width : 2.484

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.9587

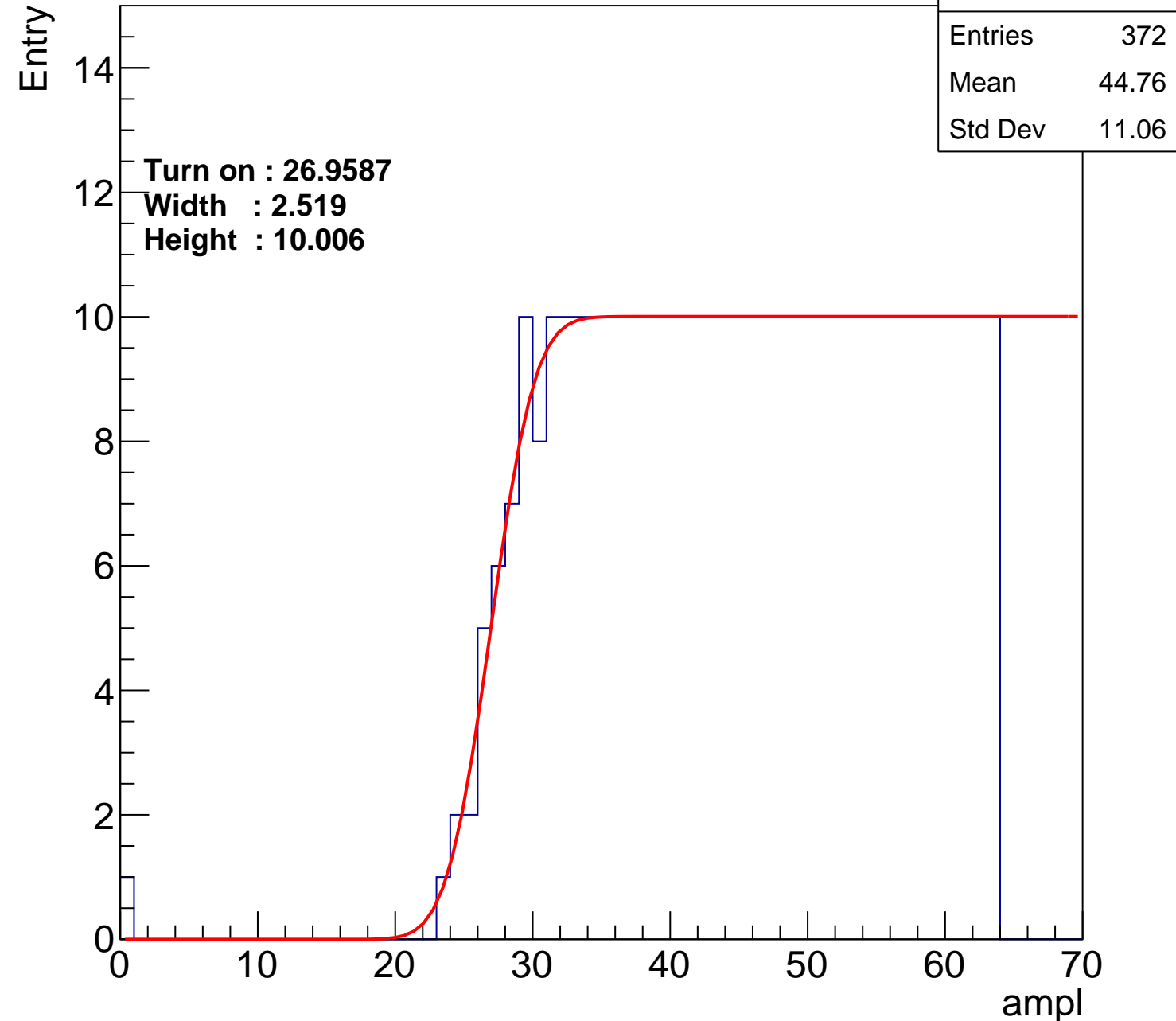
Width : 2.519

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.74
Std Dev	11.66

Turn on : 25.1426

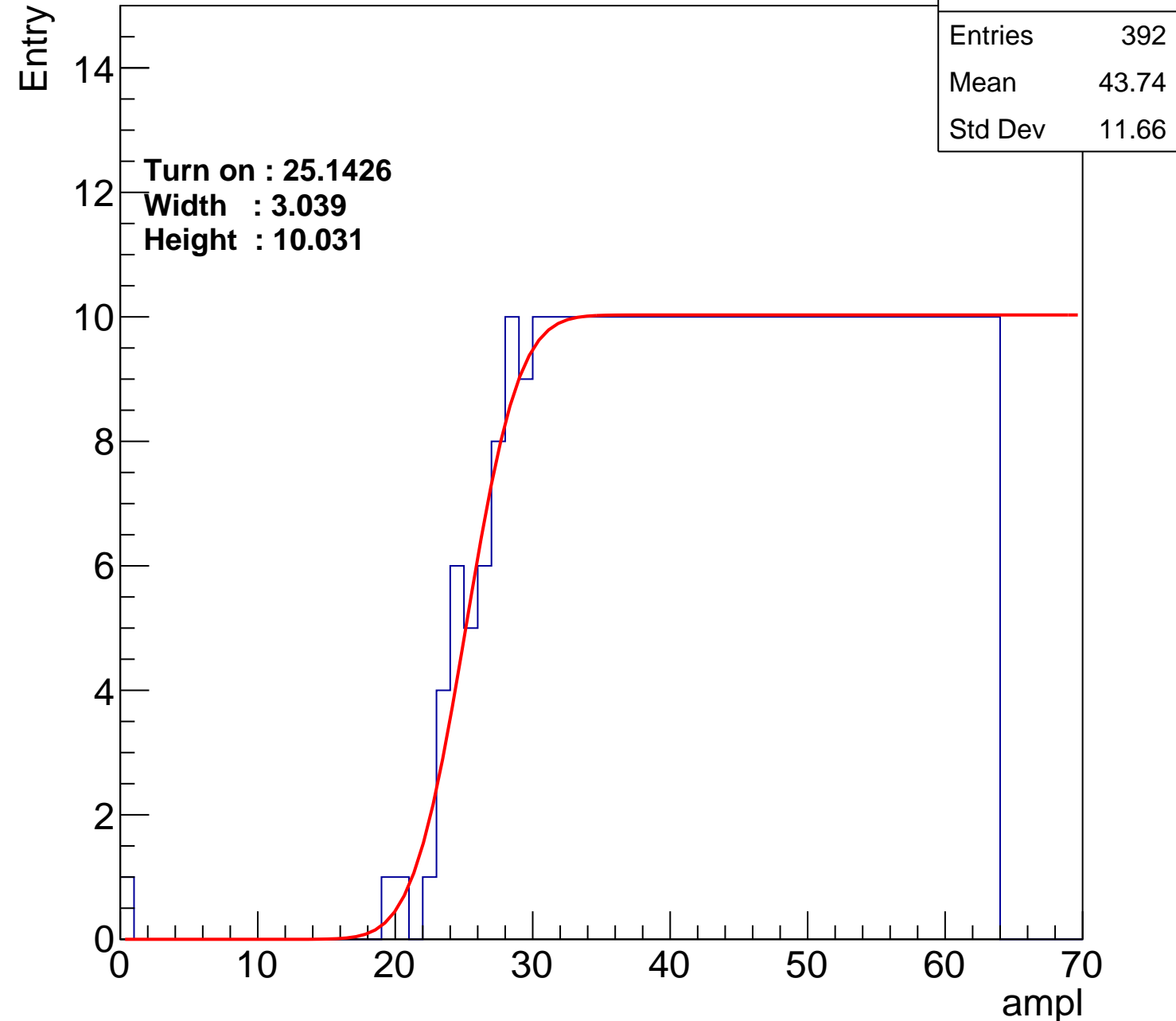
Width : 3.039

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.93
Std Dev	11.63

Turn on : 25.3712

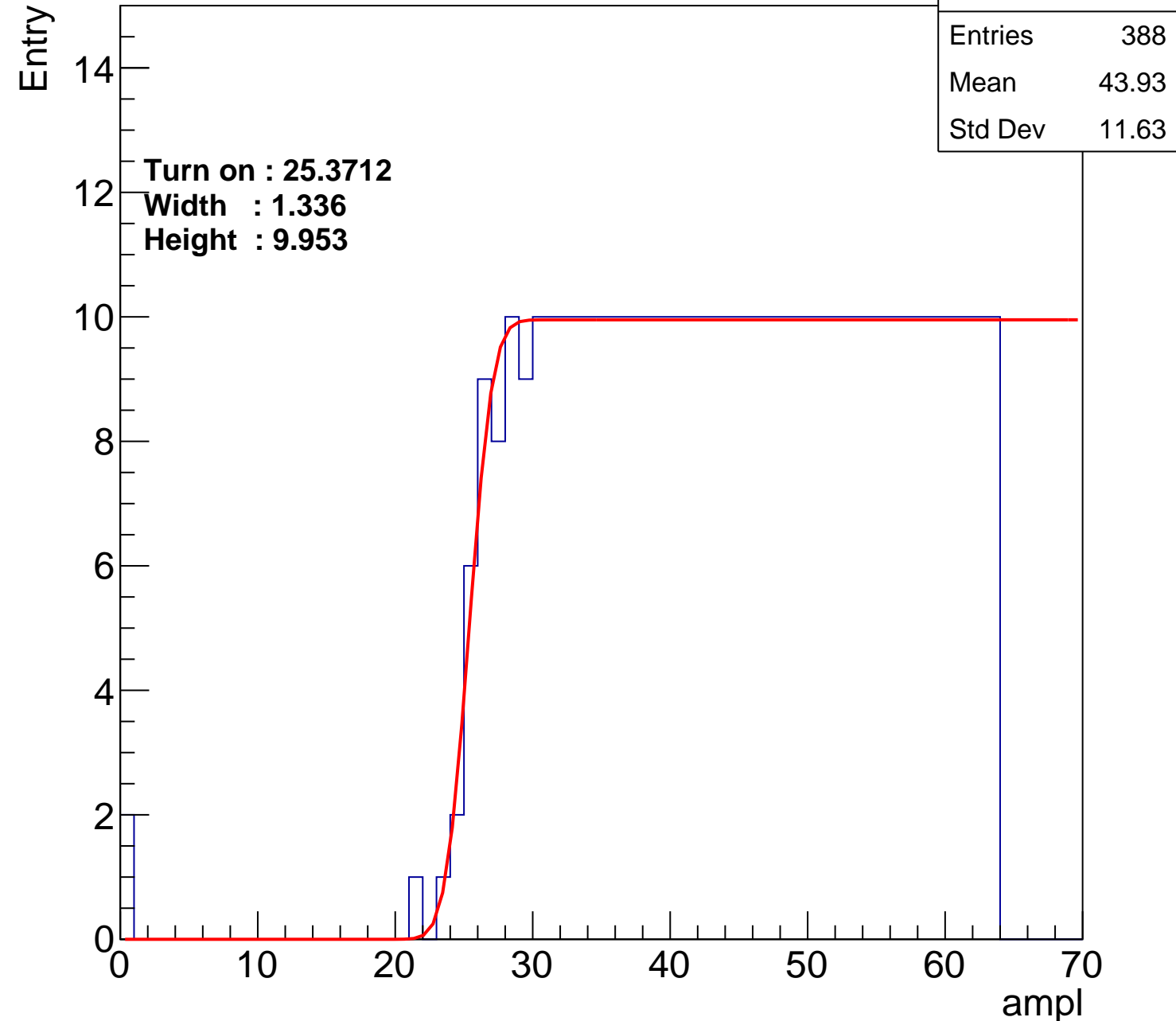
Width : 1.336

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch96

calib_packv5_042523_0143.root, FC#3, port B3

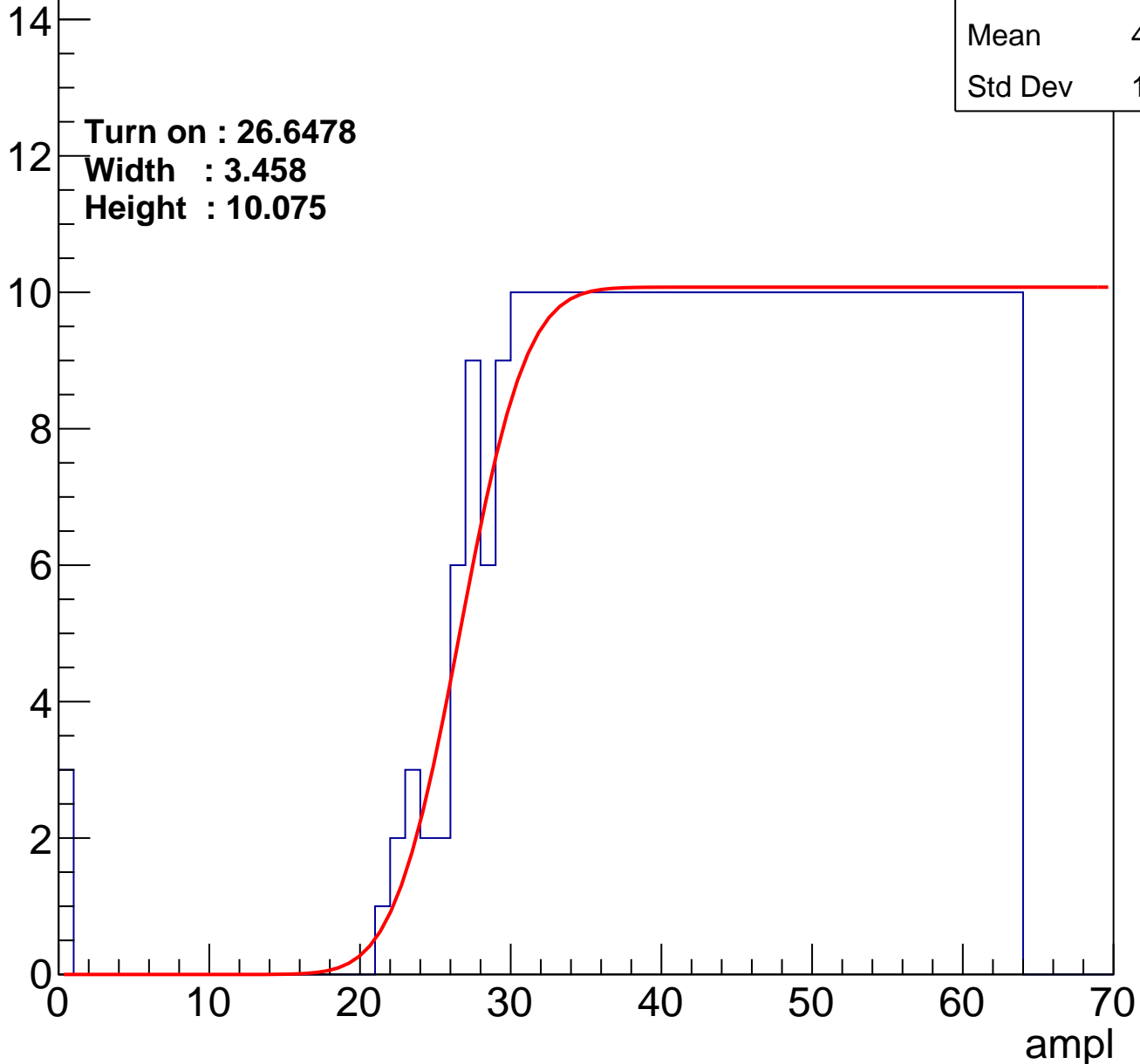
Entries	383
Mean	44.05
Std Dev	11.78

Turn on : 26.6478

Width : 3.458

Height : 10.075

Entry



B1L000S, U6-ch97

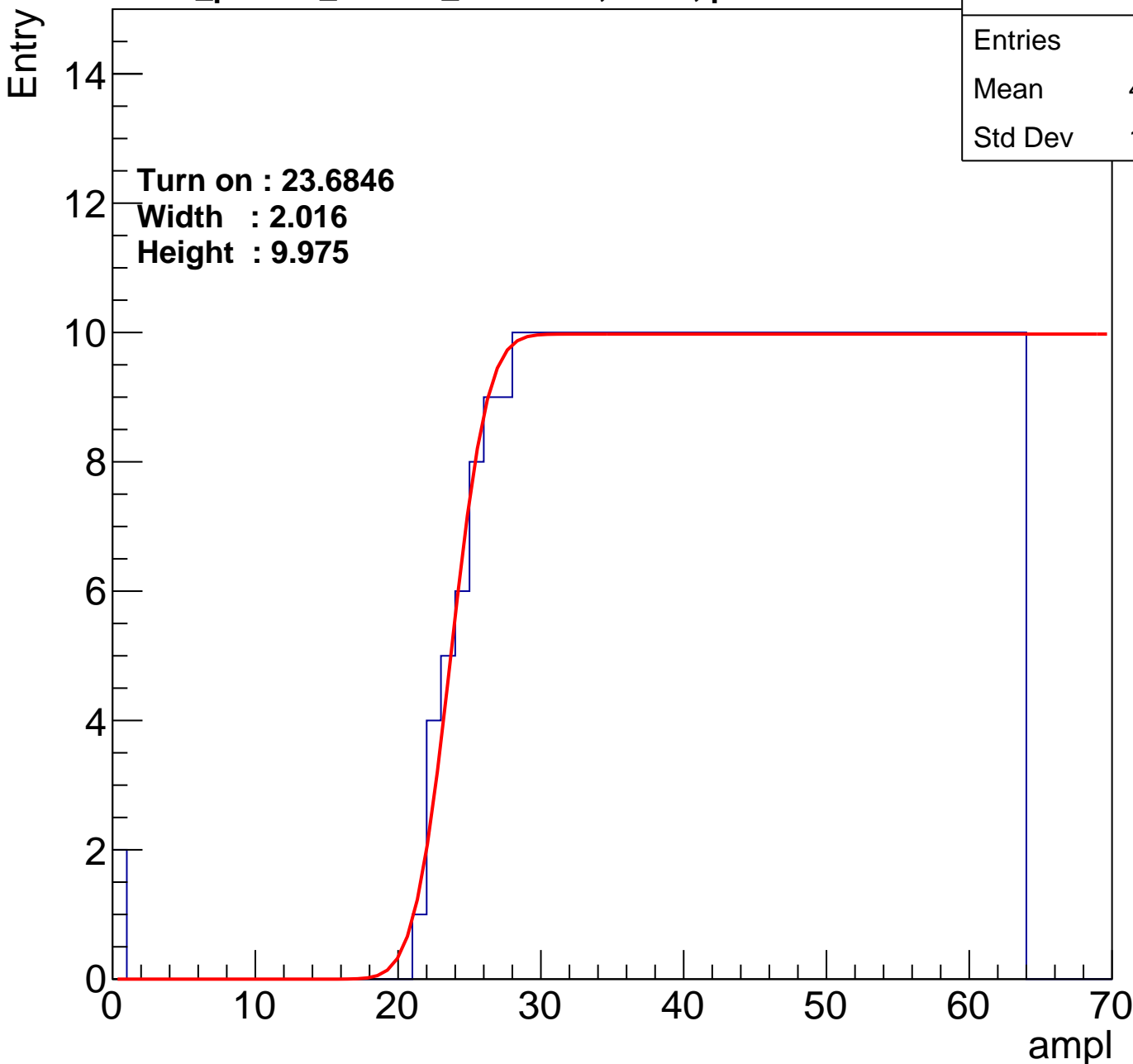
calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.13
Std Dev	12.05

Turn on : 23.6846

Width : 2.016

Height : 9.975



B1L000S, U6-ch98

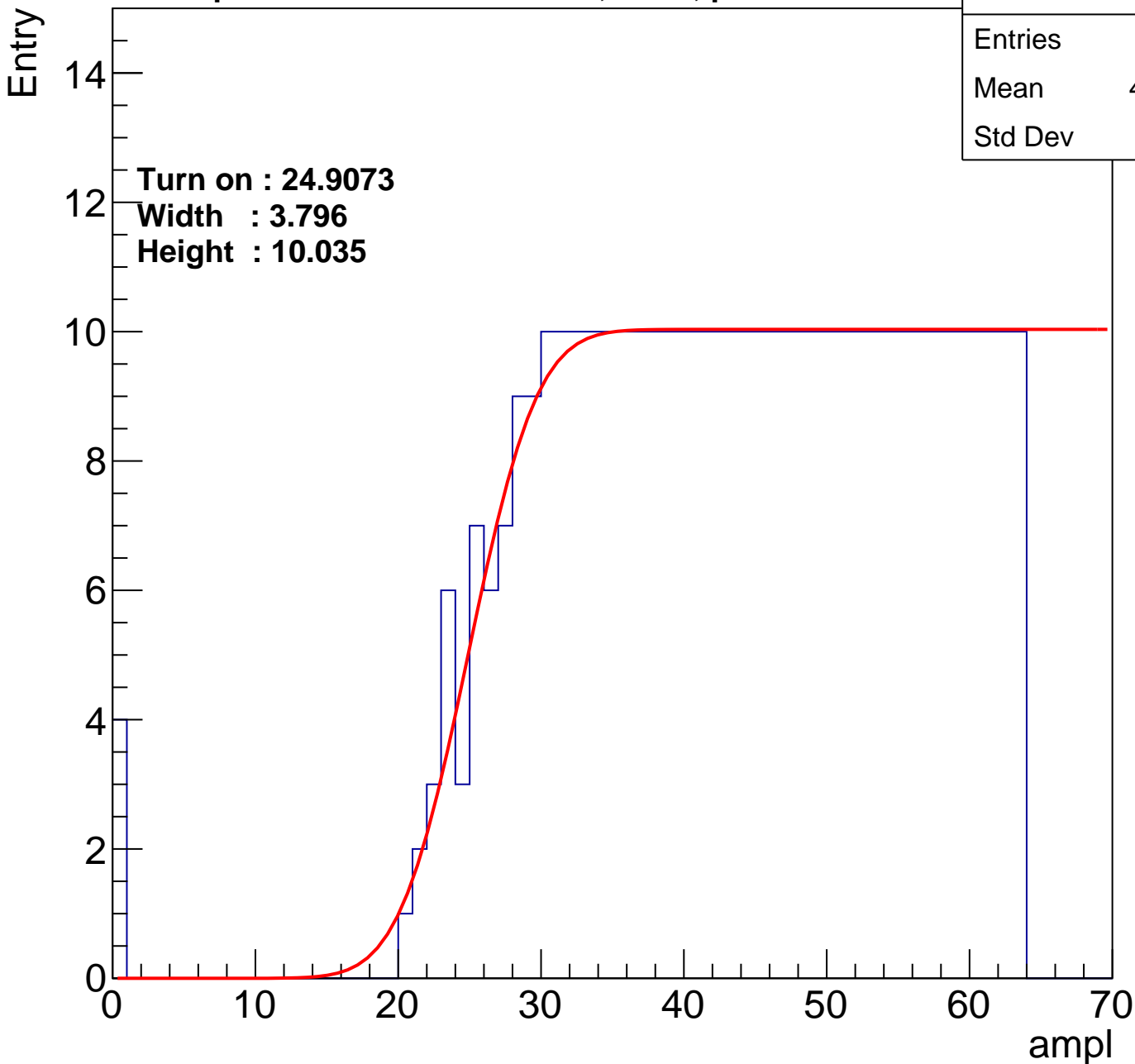
calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.28
Std Dev	12.31

Turn on : 24.9073

Width : 3.796

Height : 10.035



B1L000S, U6-ch99

calib_packv5_042523_0143.root, FC#3, port B3

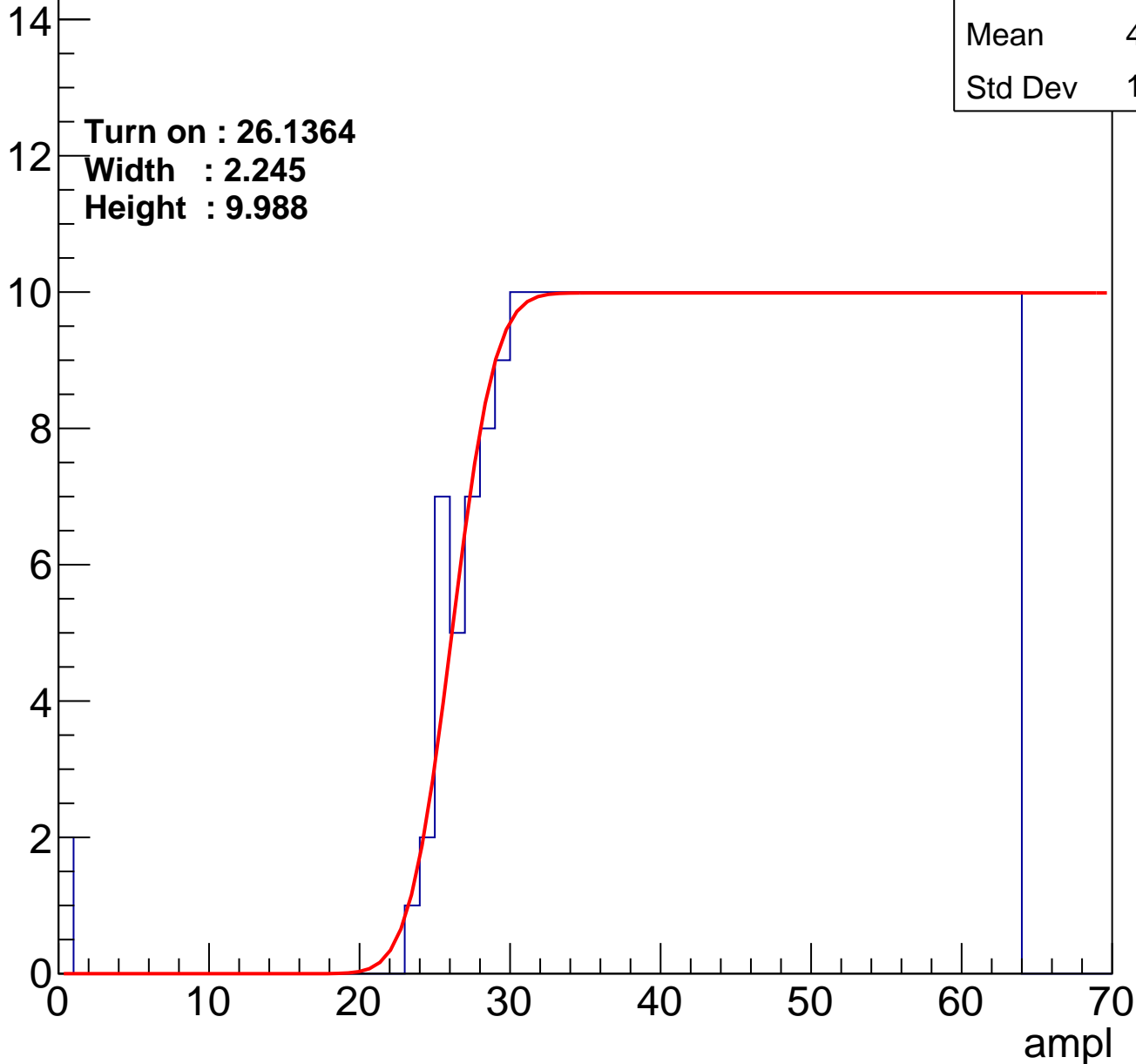
Entry

Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.1364

Width : 2.245

Height : 9.988



B1L000S, U6-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 25.1871

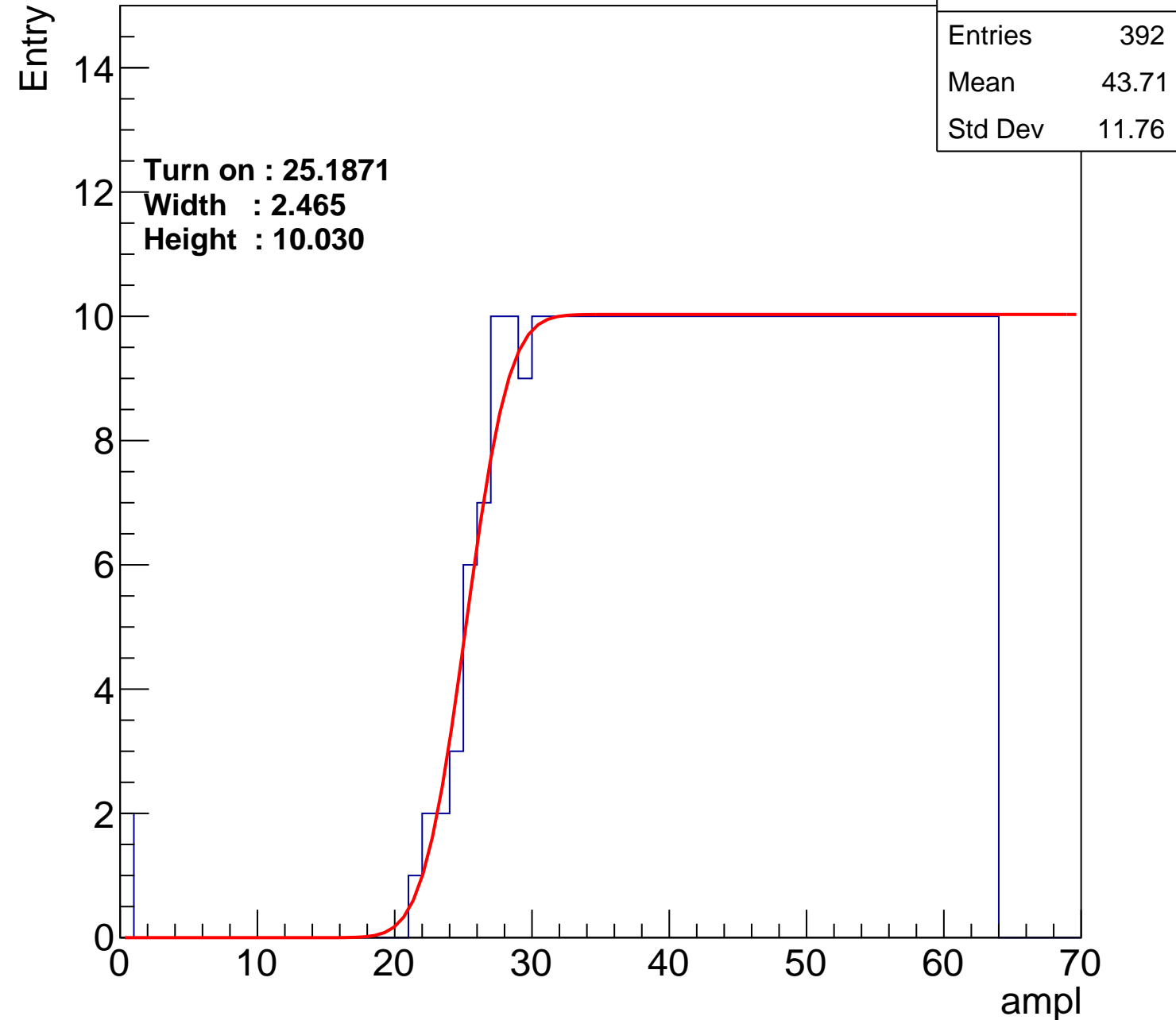
Width : 2.465

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch101

calib_packv5_042523_0143.root, FC#3, port B3

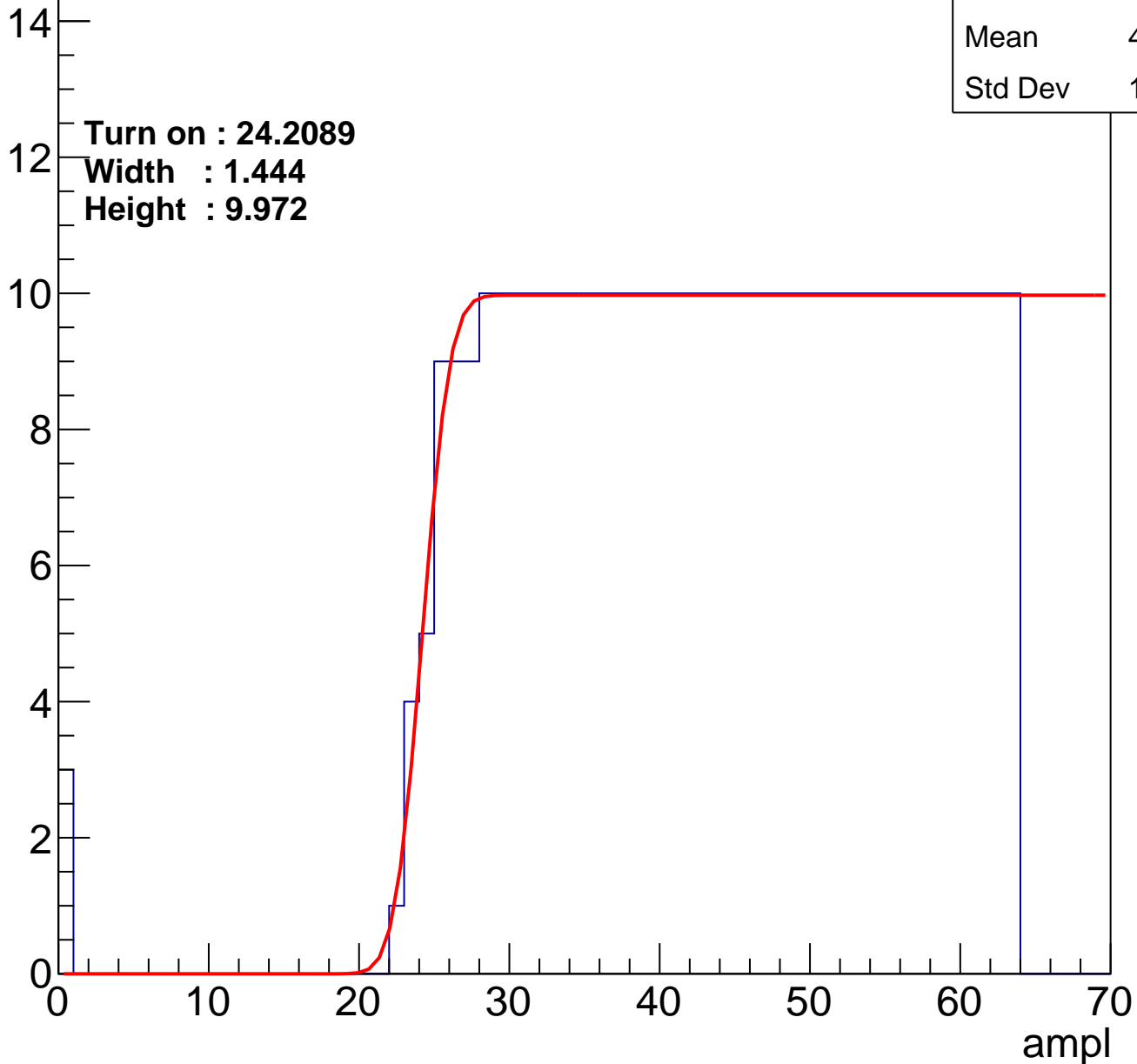
Entries	400
Mean	43.29
Std Dev	12.07

Turn on : 24.2089

Width : 1.444

Height : 9.972

Entry



B1L000S, U6-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 25.8823

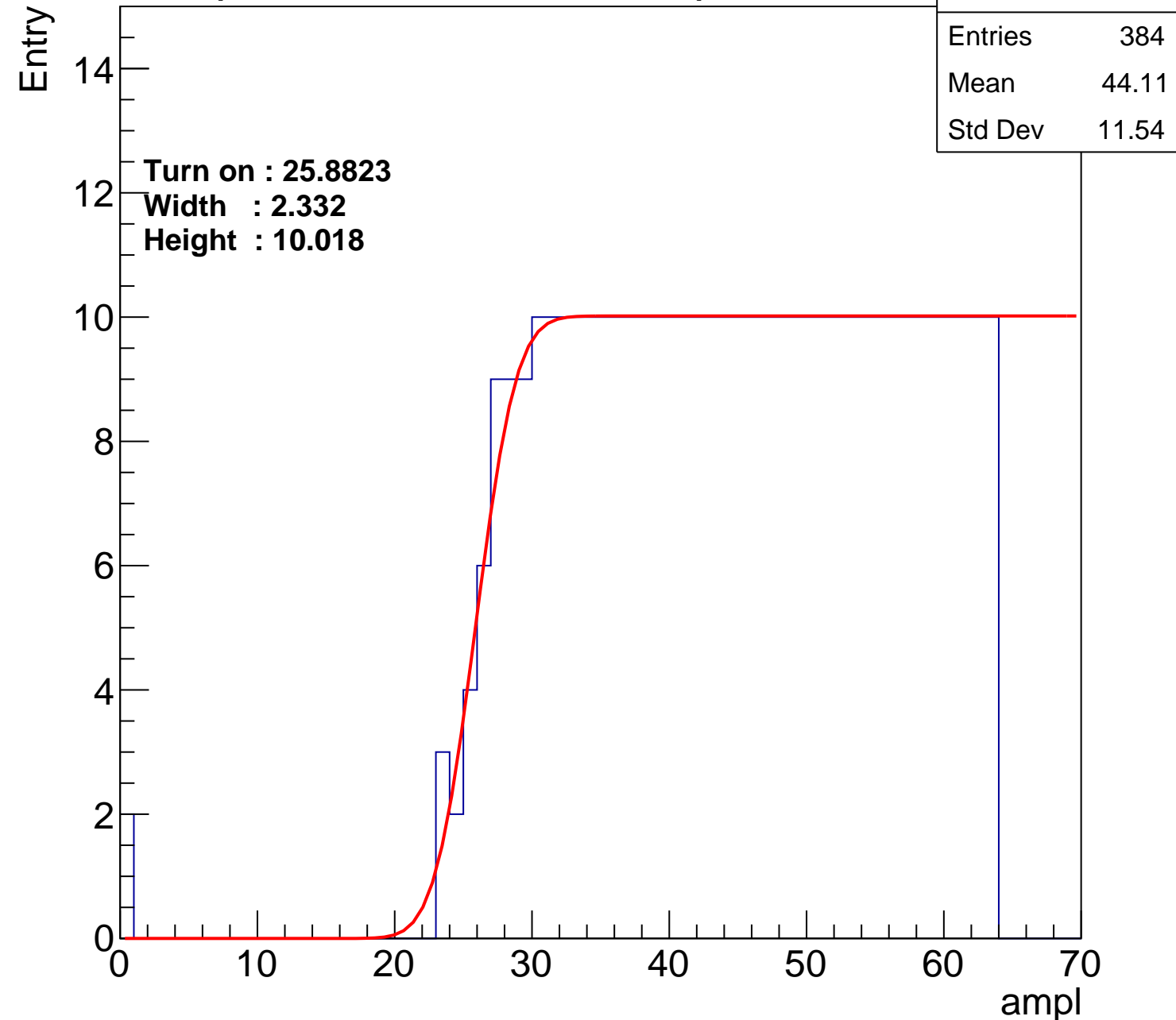
Width : 2.332

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.57
Std Dev	11.88

Turn on : 24.7214

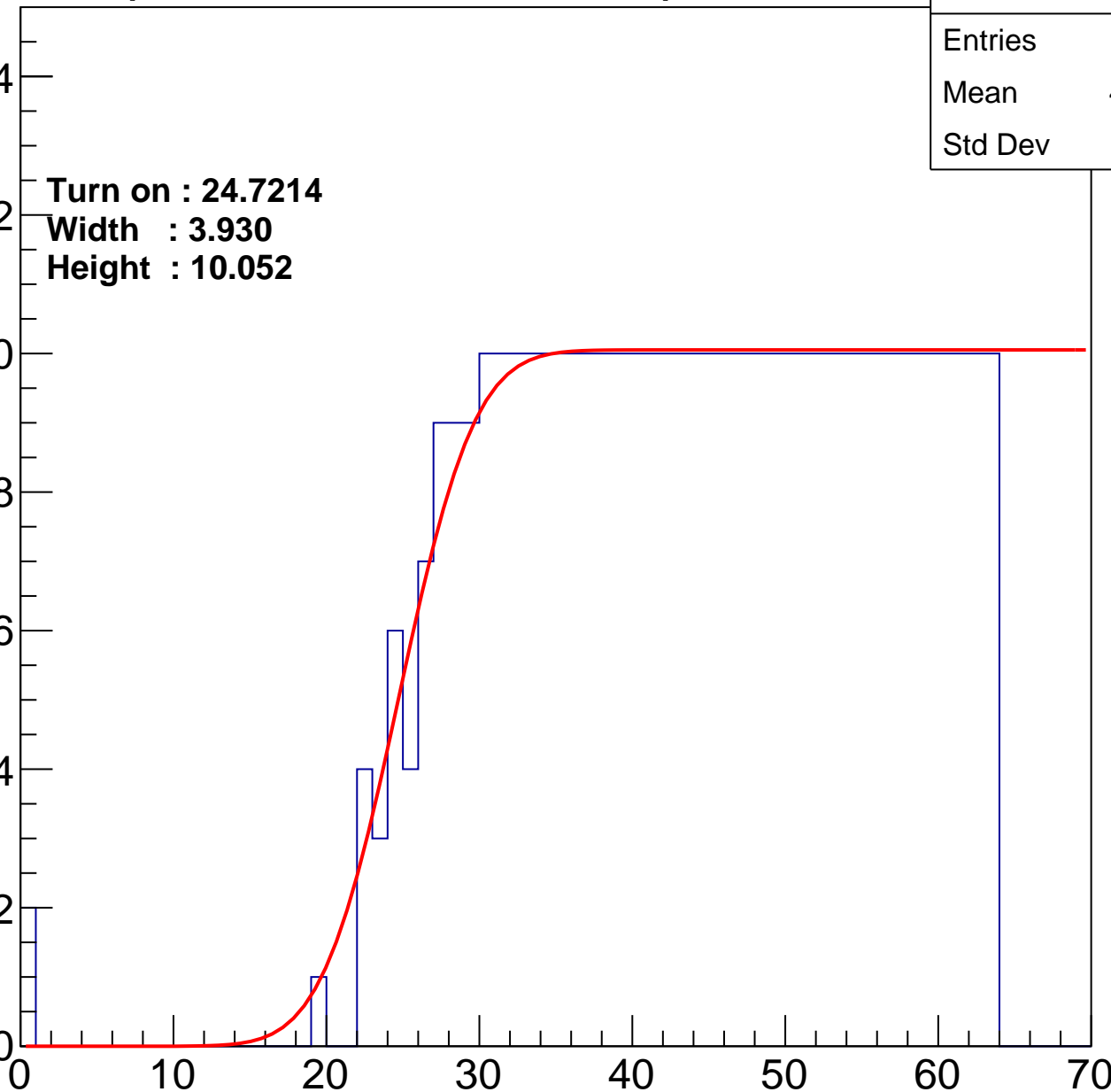
Width : 3.930

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.6818

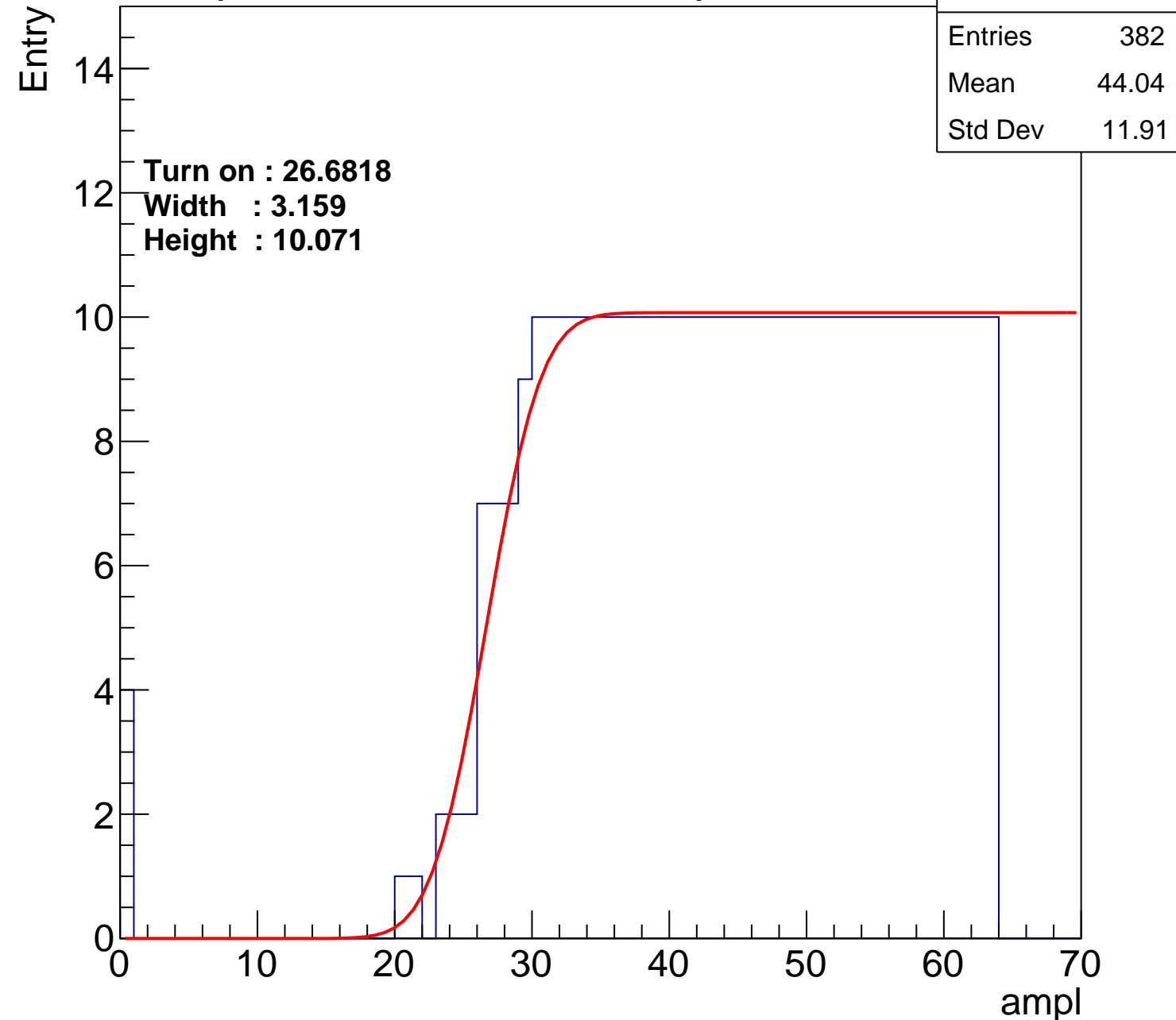
Width : 3.159

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 26.2298

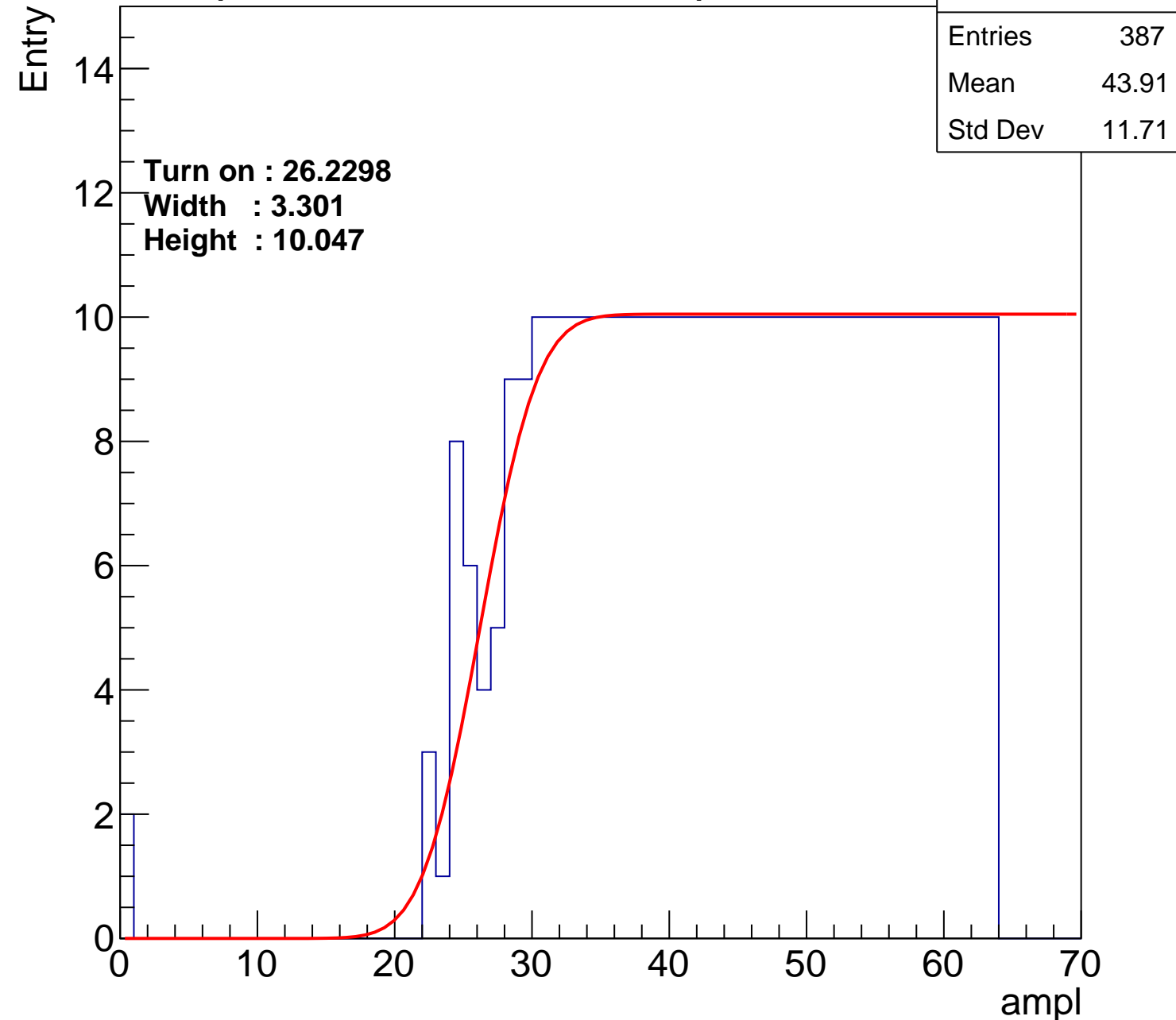
Width : 3.301

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch106

calib_packv5_042523_0143.root, FC#3, port B3

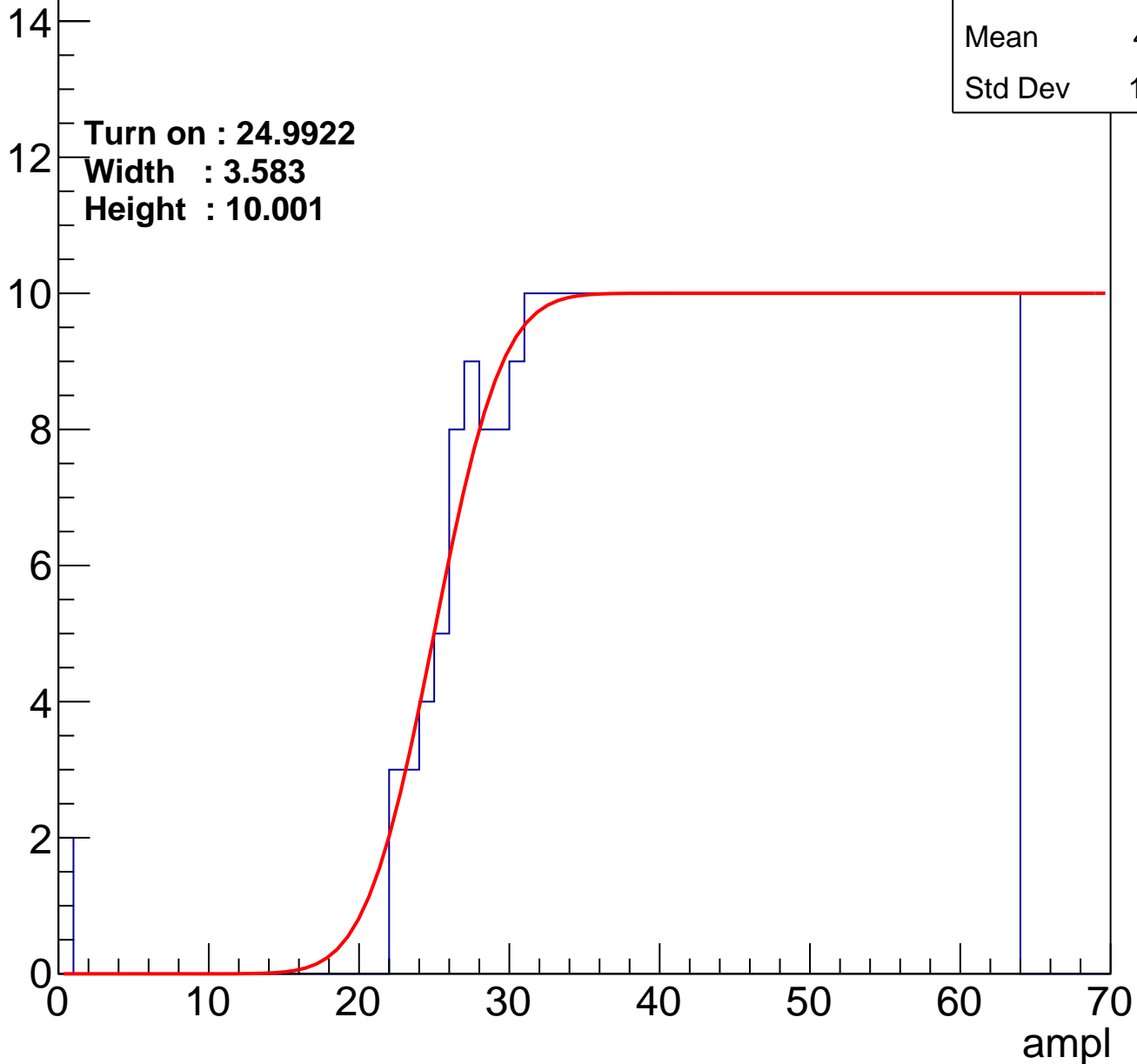
Entries	389
Mean	43.81
Std Dev	11.75

Turn on : 24.9922

Width : 3.583

Height : 10.001

Entry



B1L000S, U6-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.37
Std Dev	11.32

Turn on : 26.3927

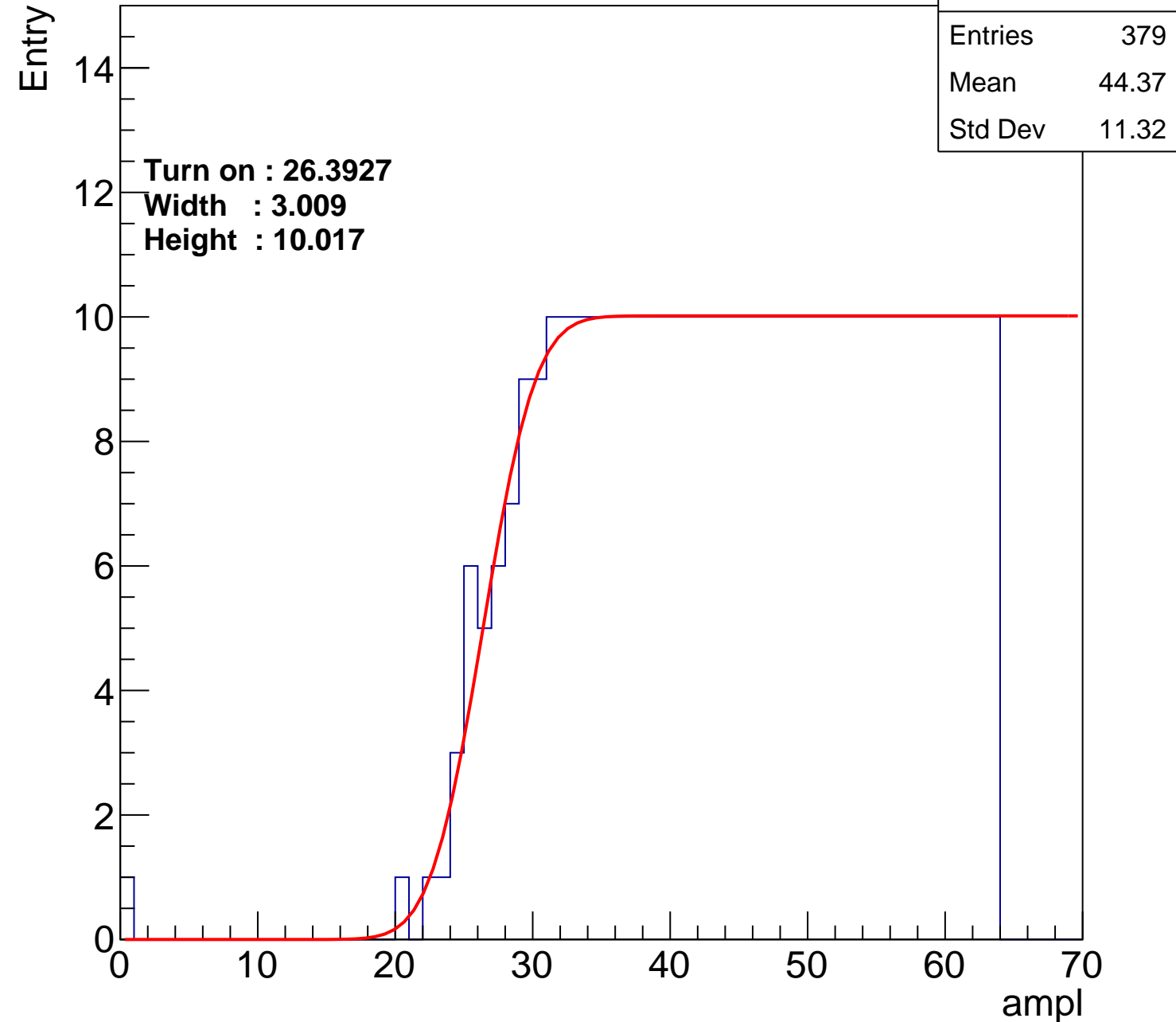
Width : 3.009

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch108

calib_packv5_042523_0143.root, FC#3, port B3

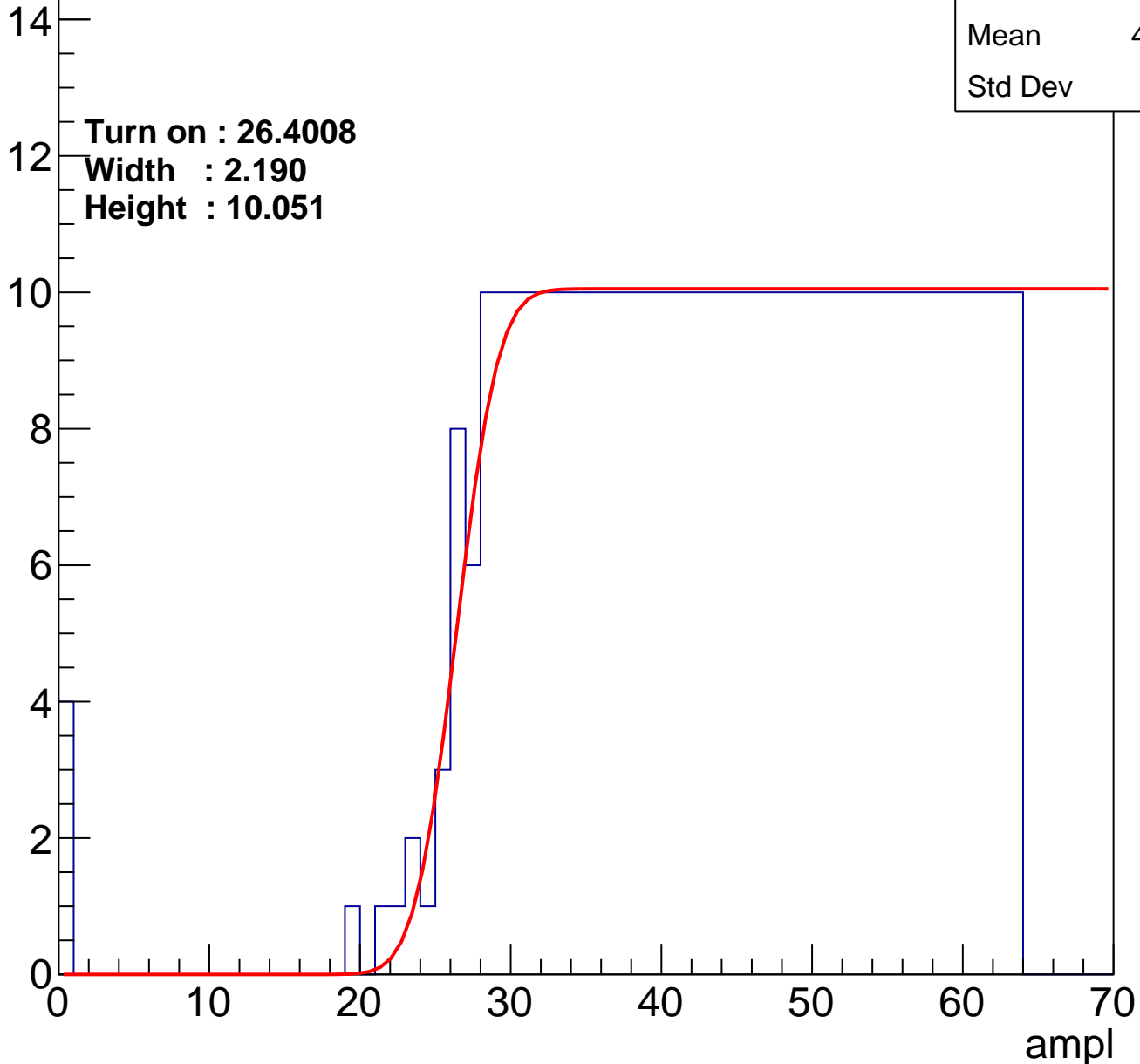
Entries	387
Mean	43.82
Std Dev	12

Turn on : 26.4008

Width : 2.190

Height : 10.051

Entry



B1L000S, U6-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.54
Std Dev	11.88

Turn on : 24.8748

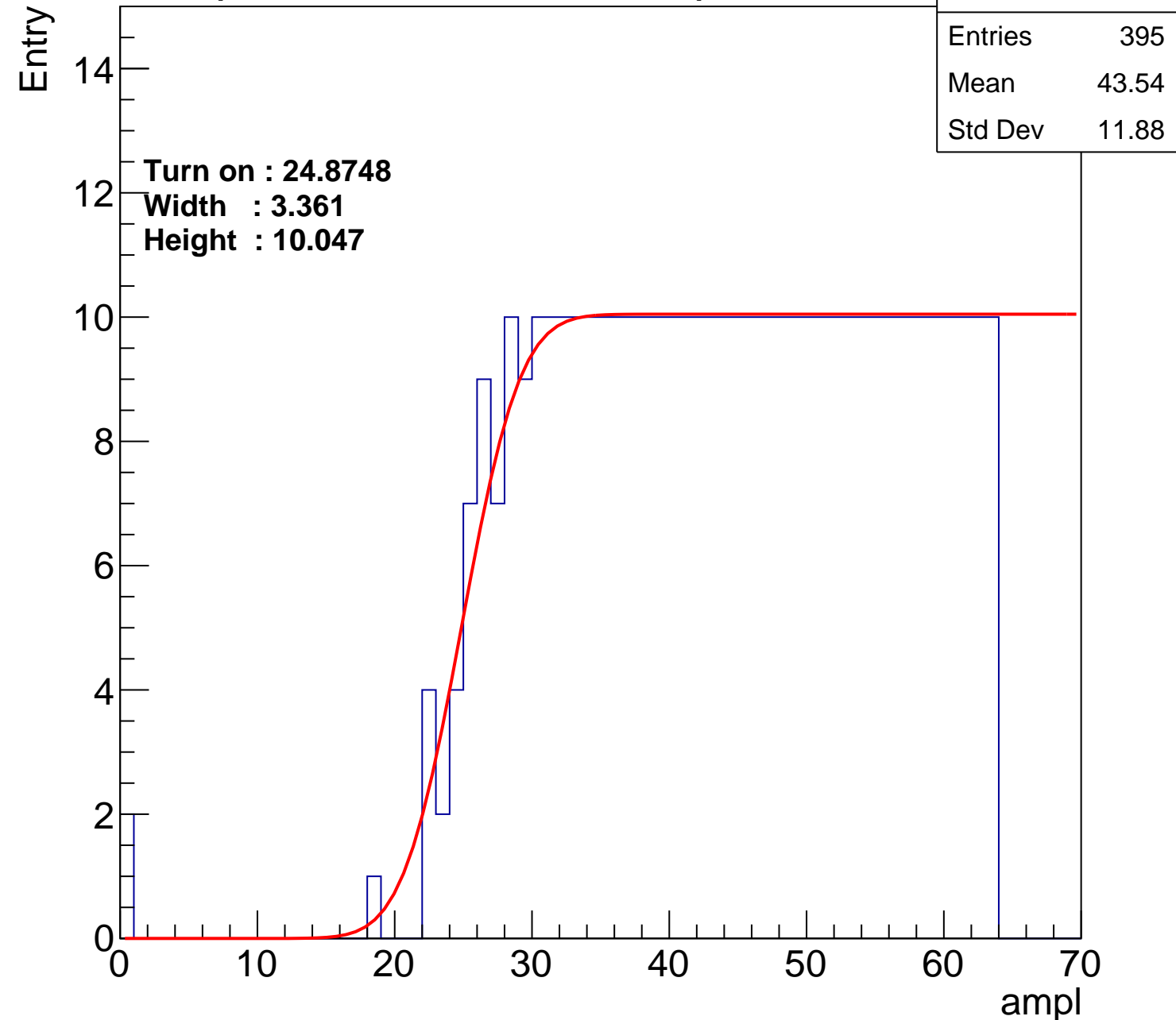
Width : 3.361

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch110

calib_packv5_042523_0143.root, FC#3, port B3

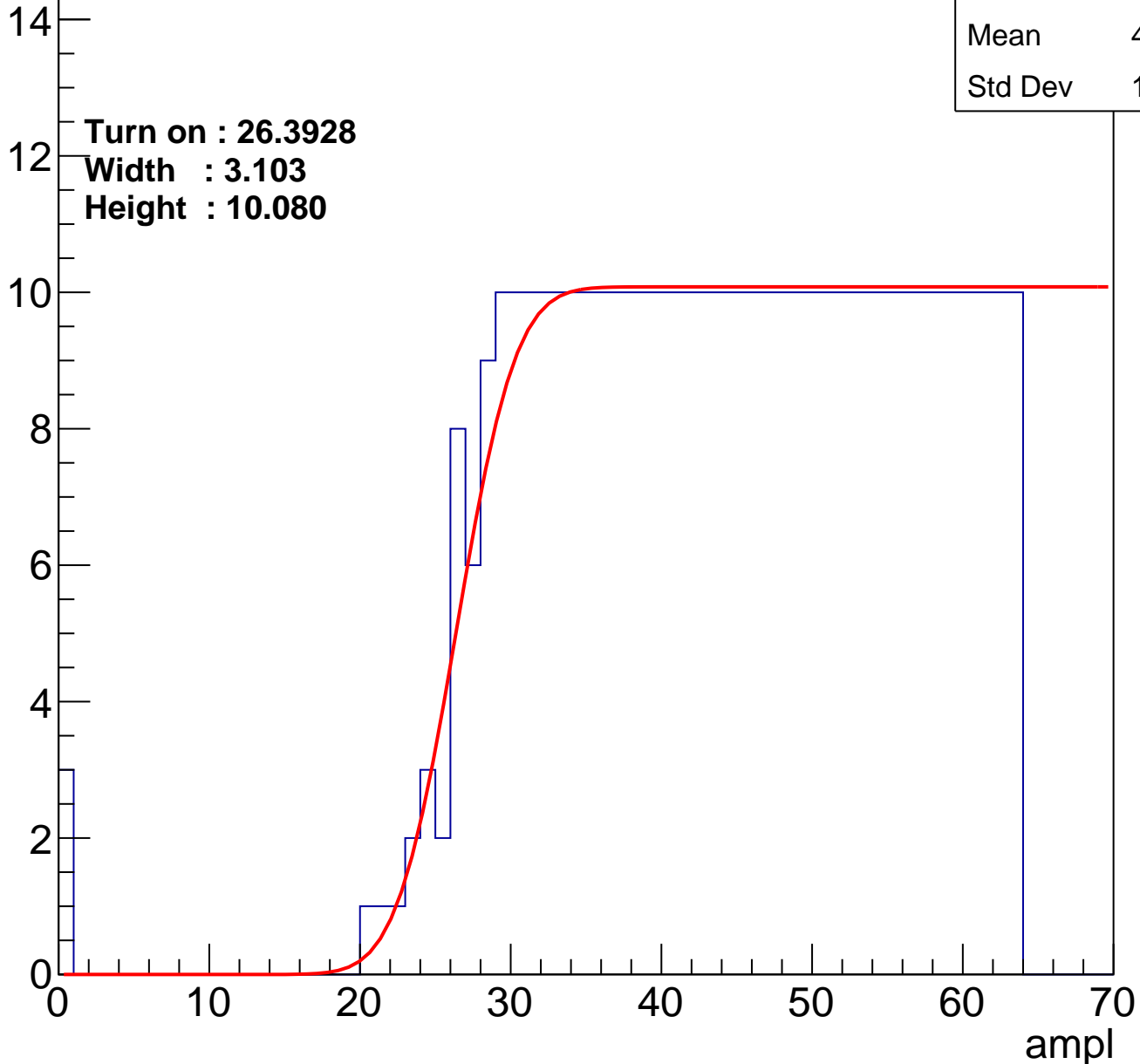
Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.3928

Width : 3.103

Height : 10.080

Entry



B1L000S, U6-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.84
Std Dev	11.9

Turn on : 26.2372

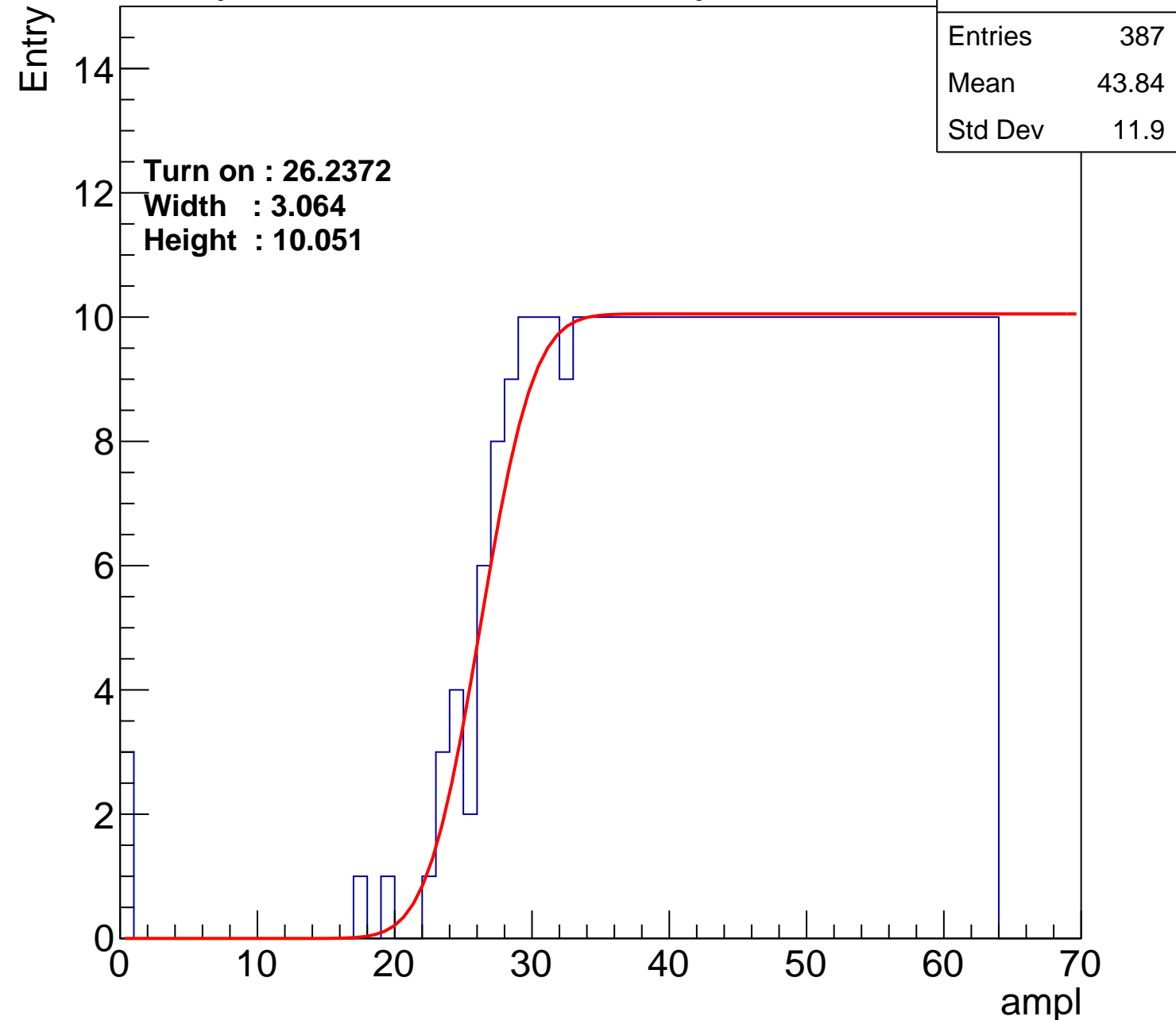
Width : 3.064

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.6280

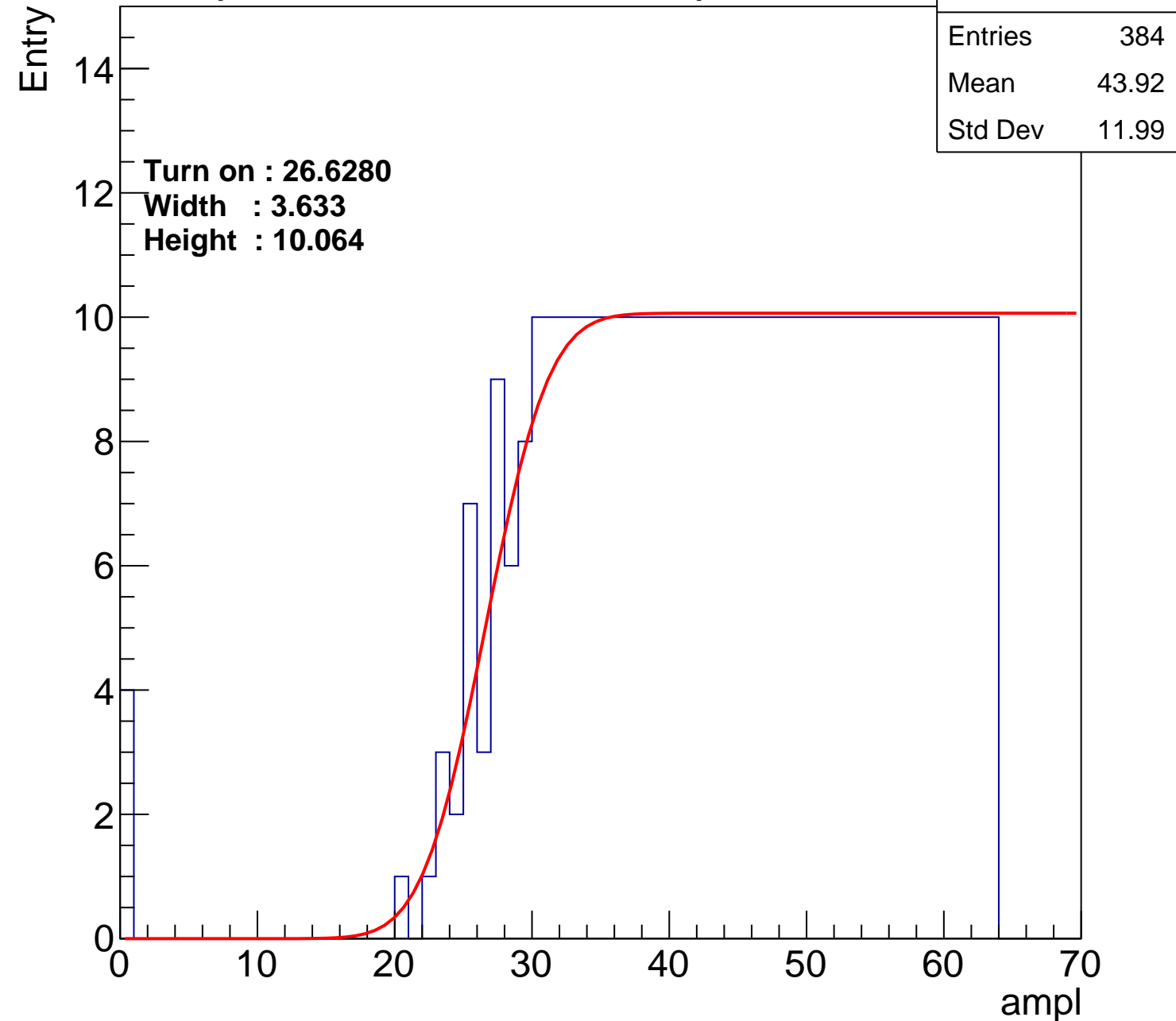
Width : 3.633

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch113

calib_packv5_042523_0143.root, FC#3, port B3

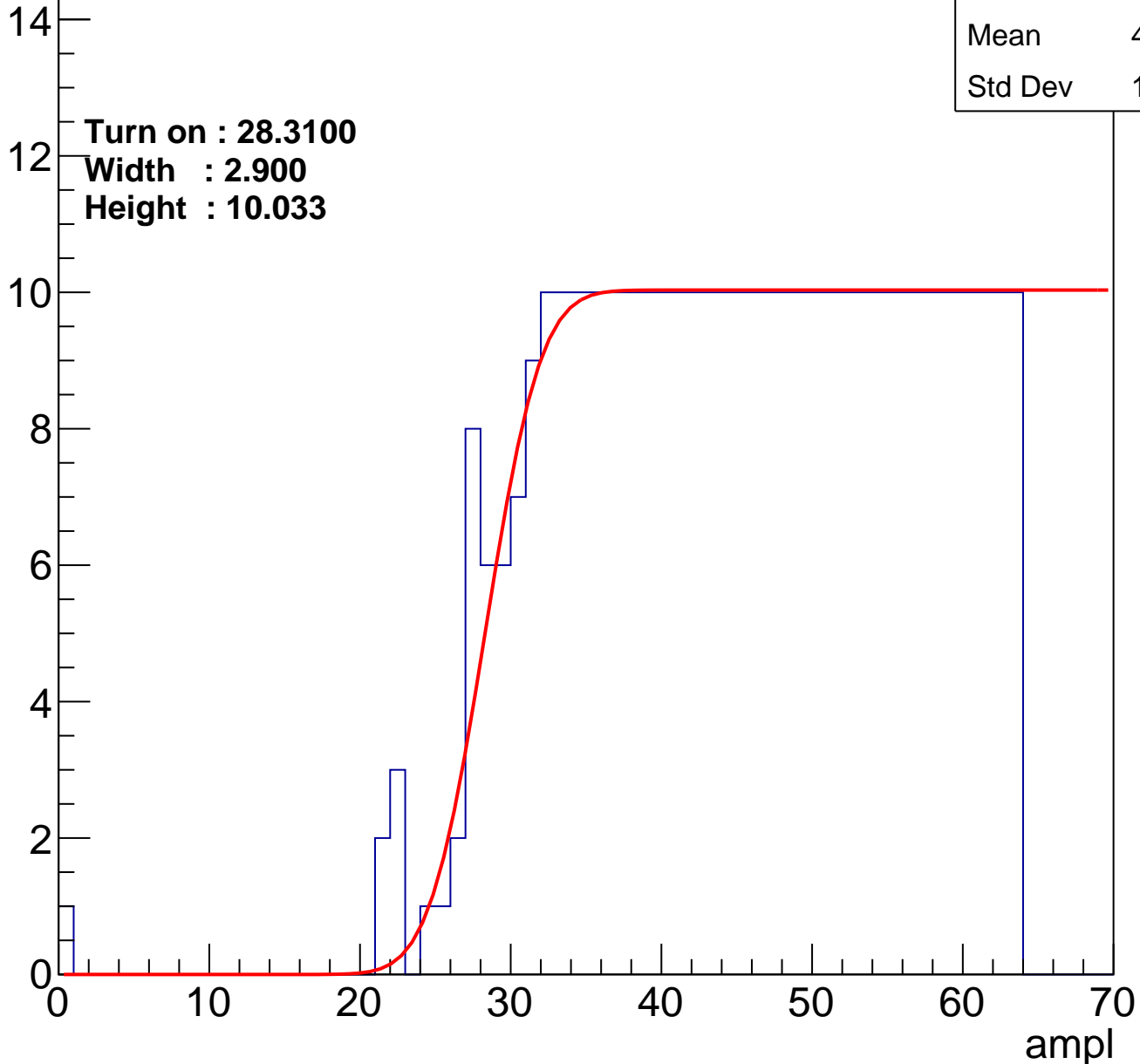
Entries	366
Mean	44.96
Std Dev	11.06

Turn on : 28.3100

Width : 2.900

Height : 10.033

Entry



B1L000S, U6-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.73
Std Dev	11.73

Turn on : 25.0351

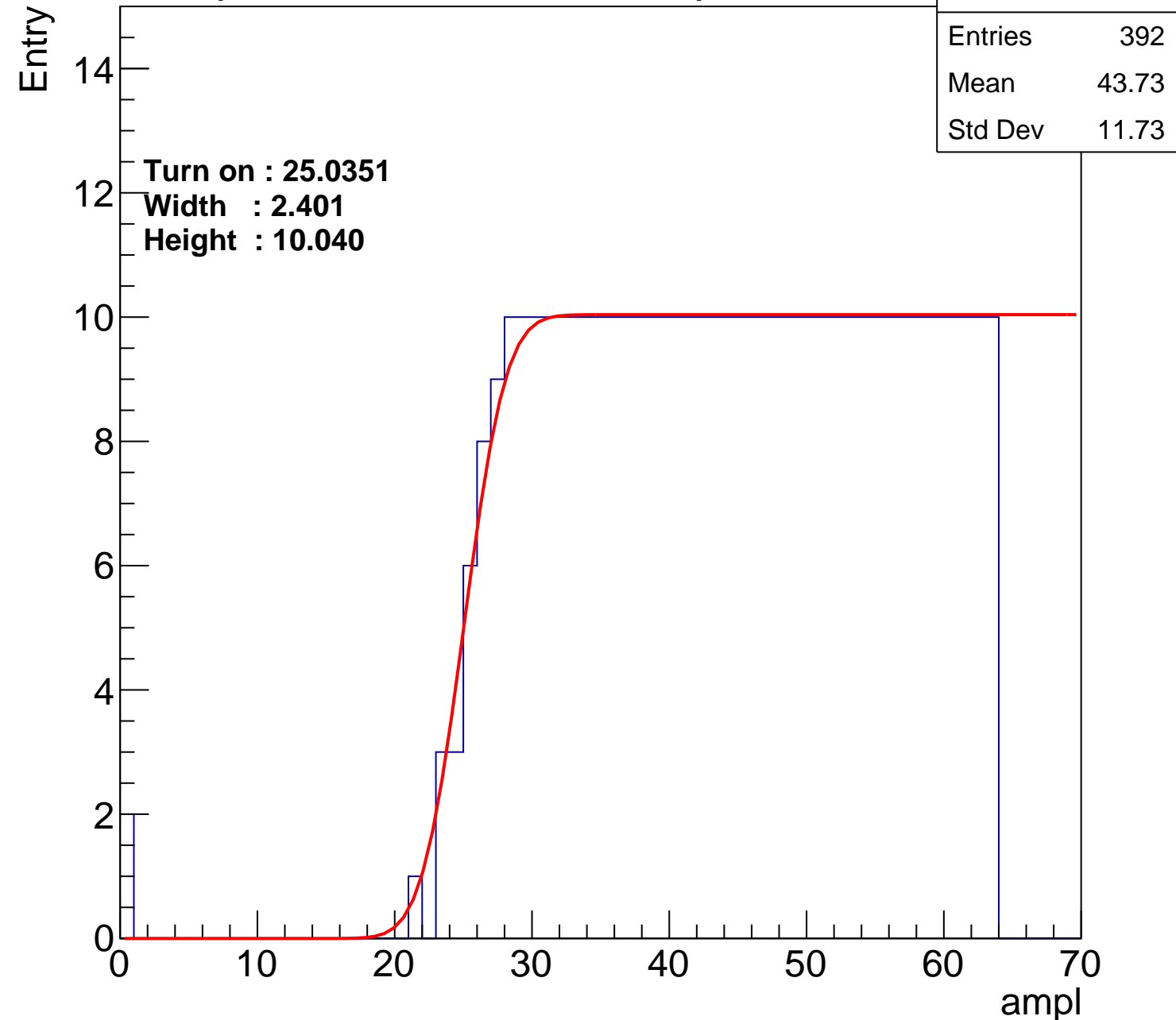
Width : 2.401

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch115

calib_packv5_042523_0143.root, FC#3, port B3

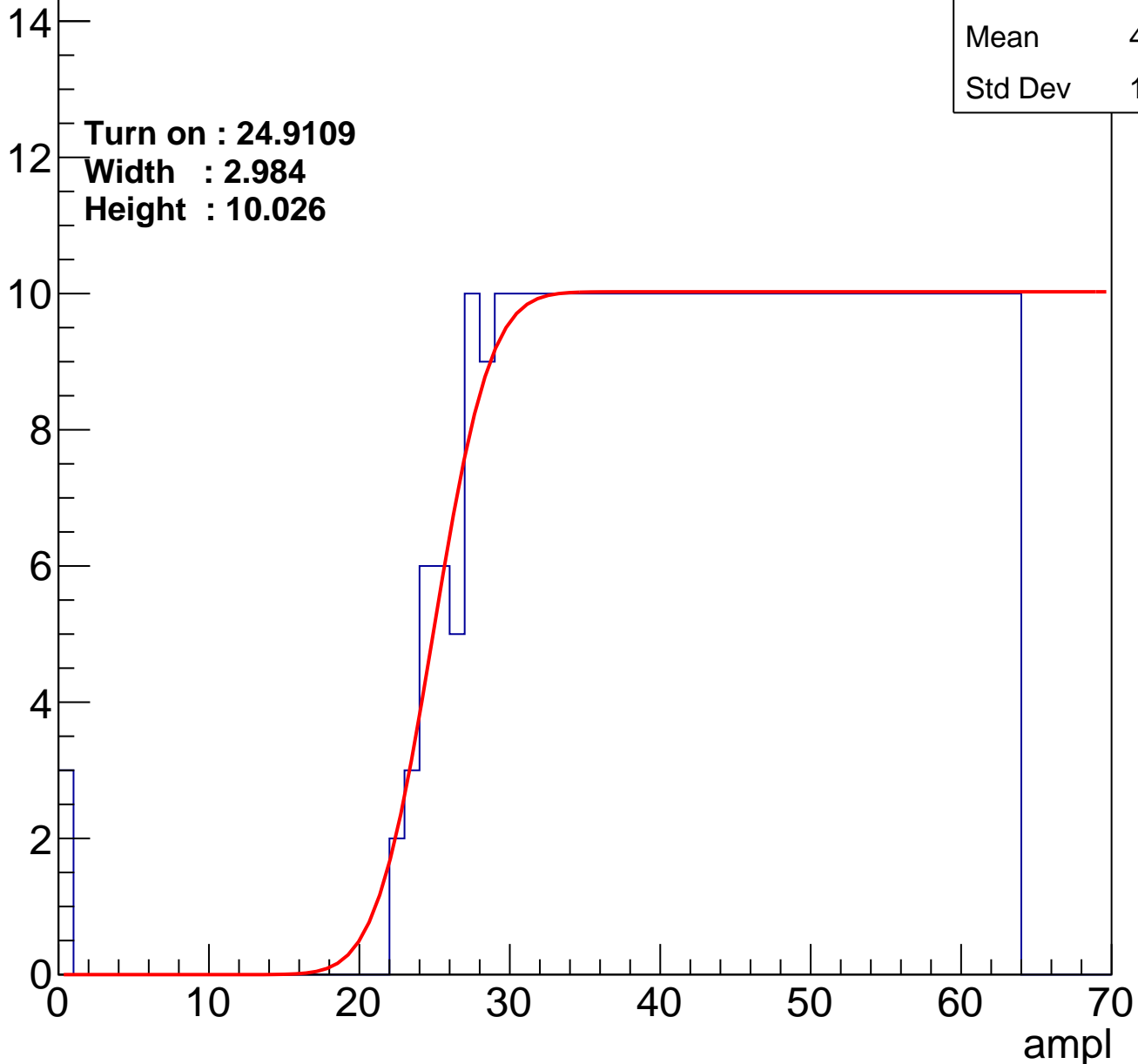
Entries	394
Mean	43.55
Std Dev	11.97

Turn on : 24.9109

Width : 2.984

Height : 10.026

Entry



B1L000S, U6-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 26.8547

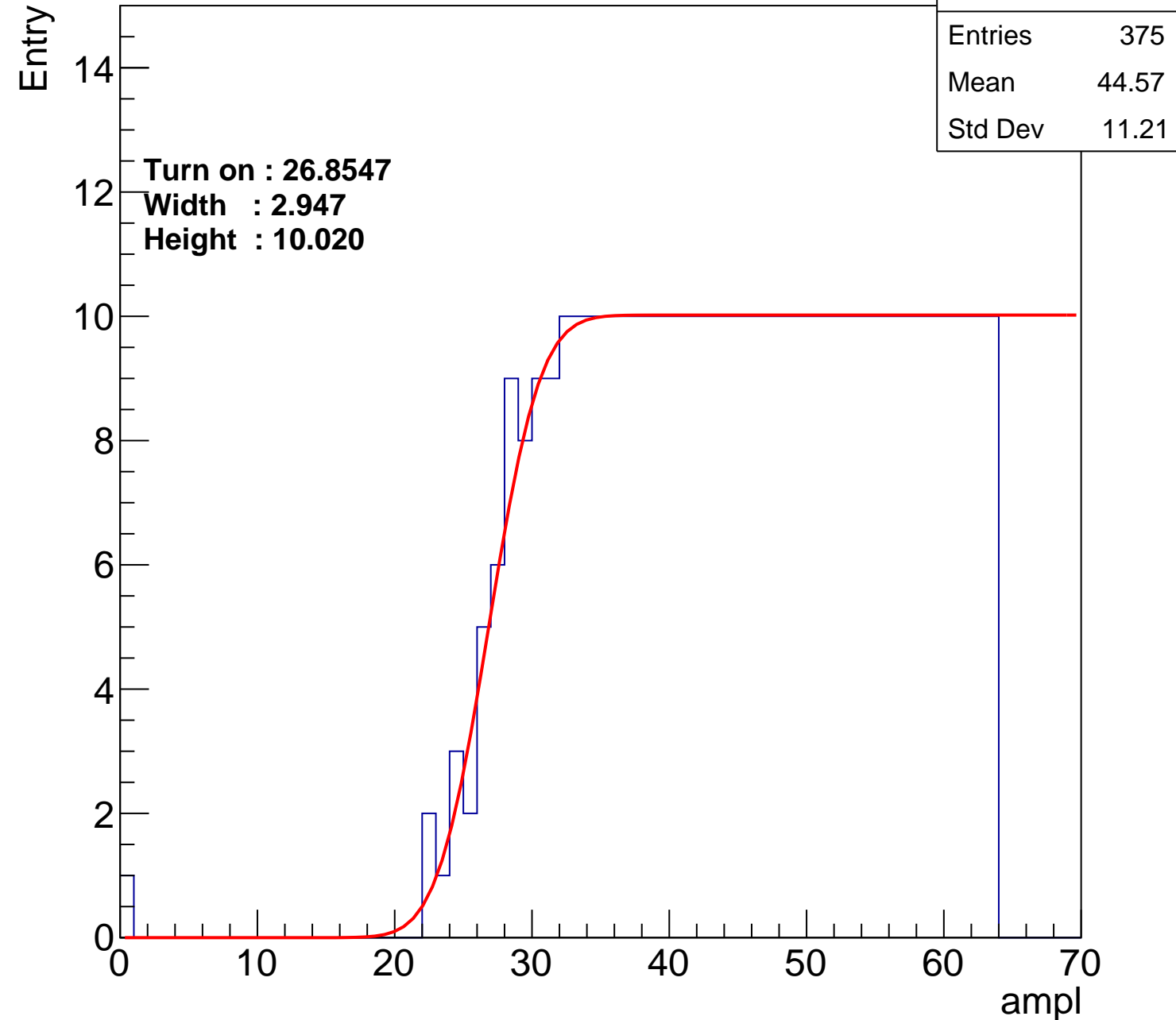
Width : 2.947

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.93
Std Dev	11.97

Turn on : 26.2374

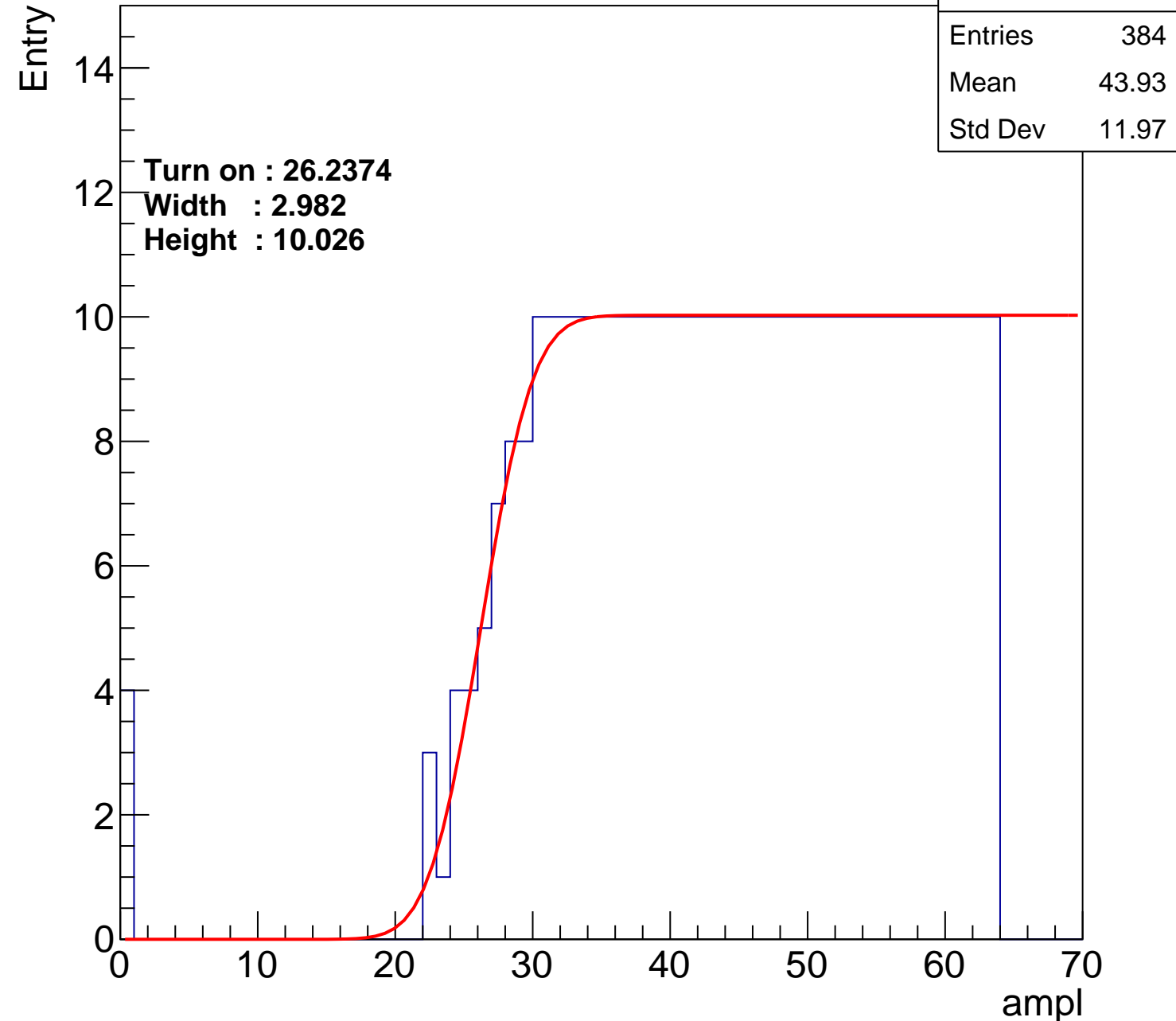
Width : 2.982

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.06
Std Dev	11.56

Turn on : 25.7224

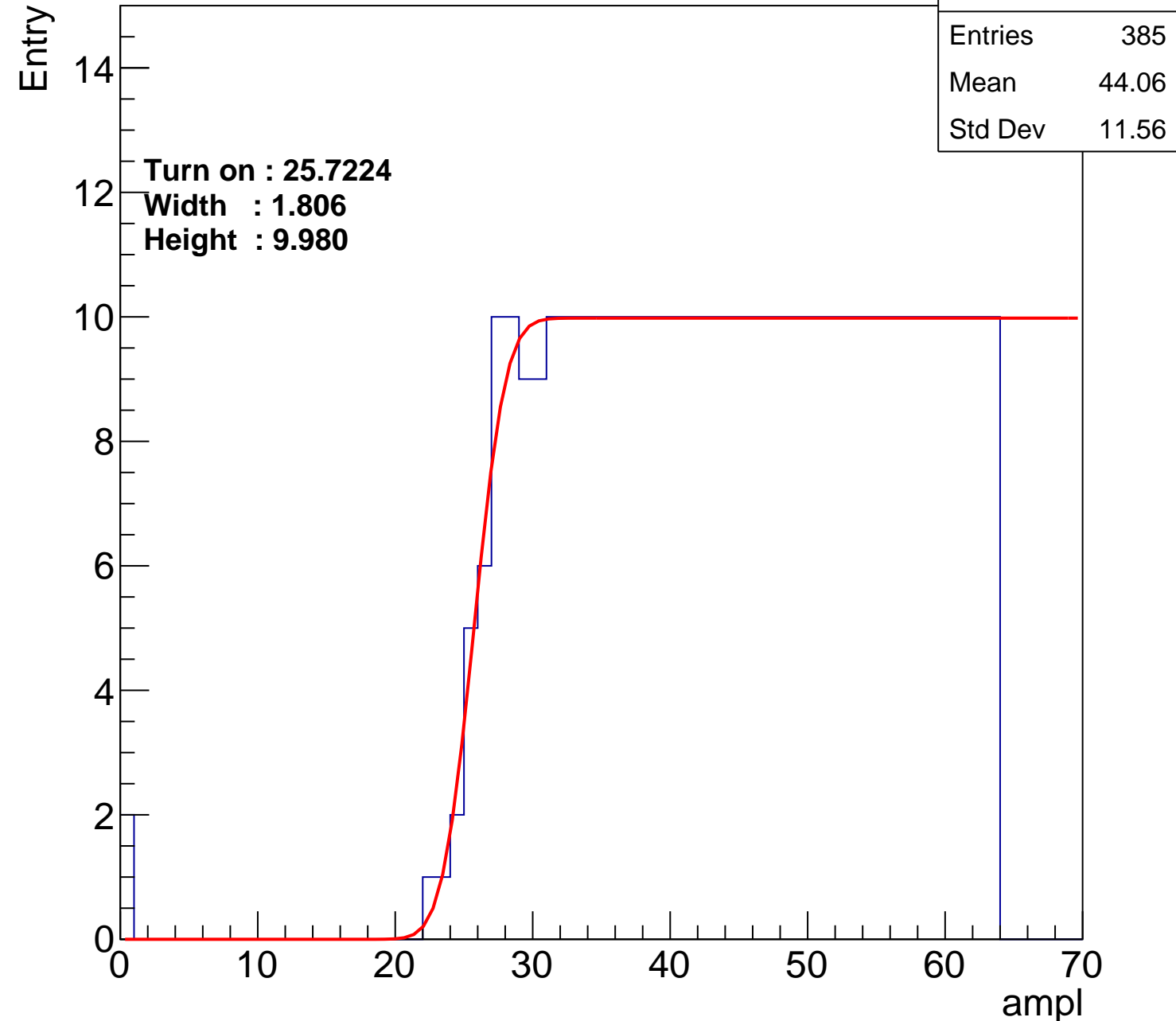
Width : 1.806

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.52
Std Dev	11.5

Turn on : 26.9003

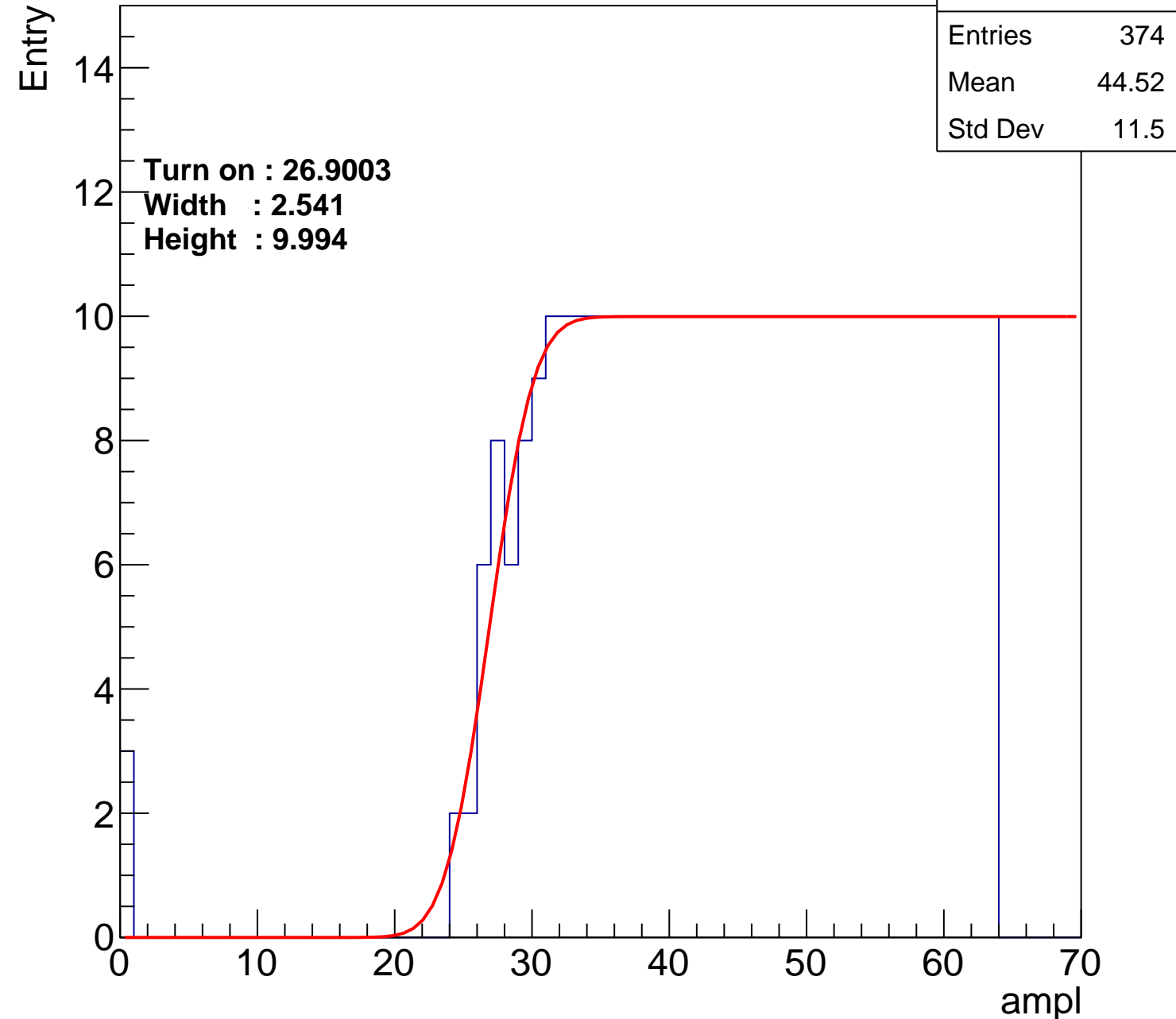
Width : 2.541

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch120

calib_packv5_042523_0143.root, FC#3, port B3

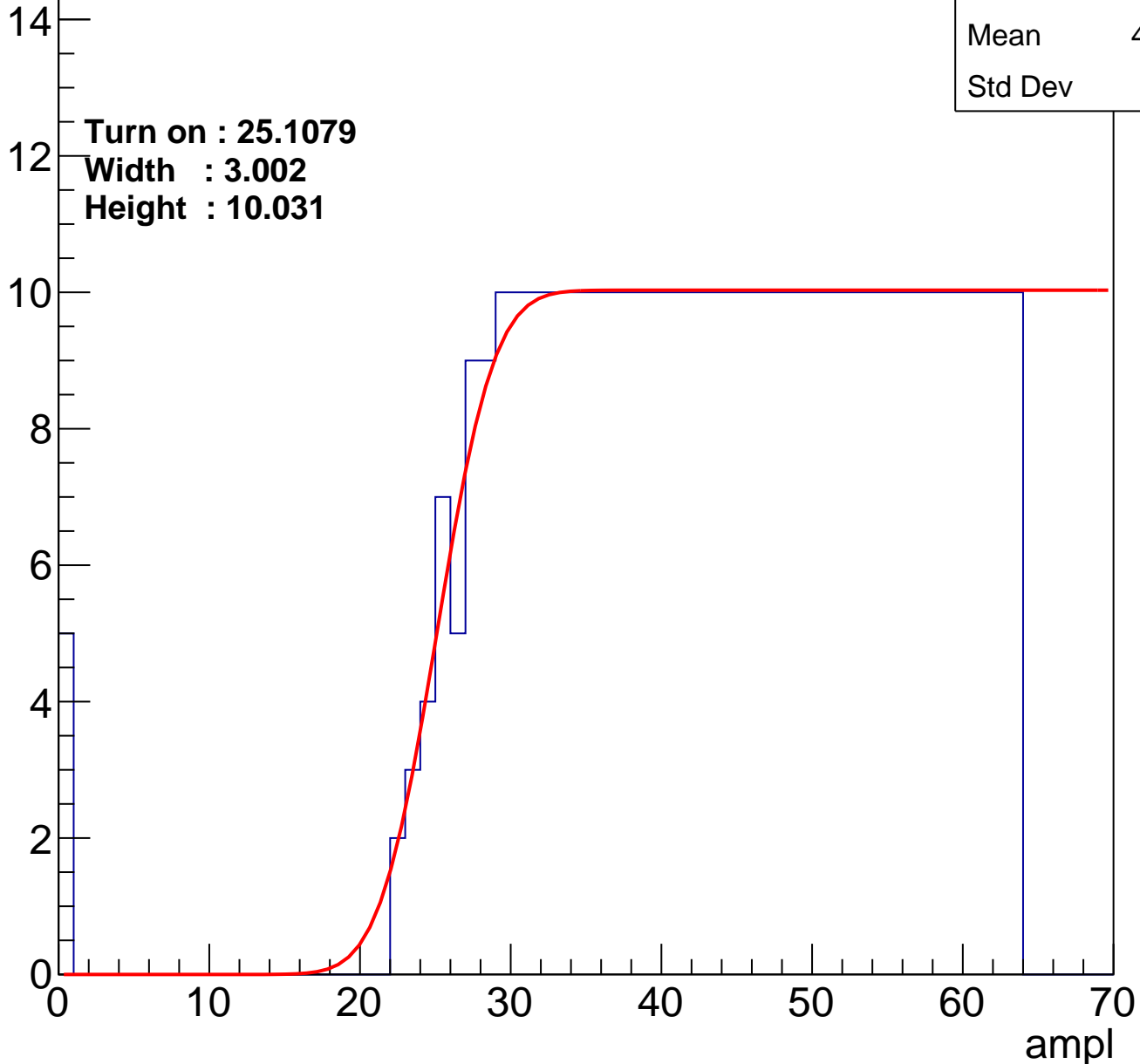
Entries	394
Mean	43.42
Std Dev	12.3

Turn on : 25.1079

Width : 3.002

Height : 10.031

Entry



B1L000S, U6-ch121

calib_packv5_042523_0143.root, FC#3, port B3

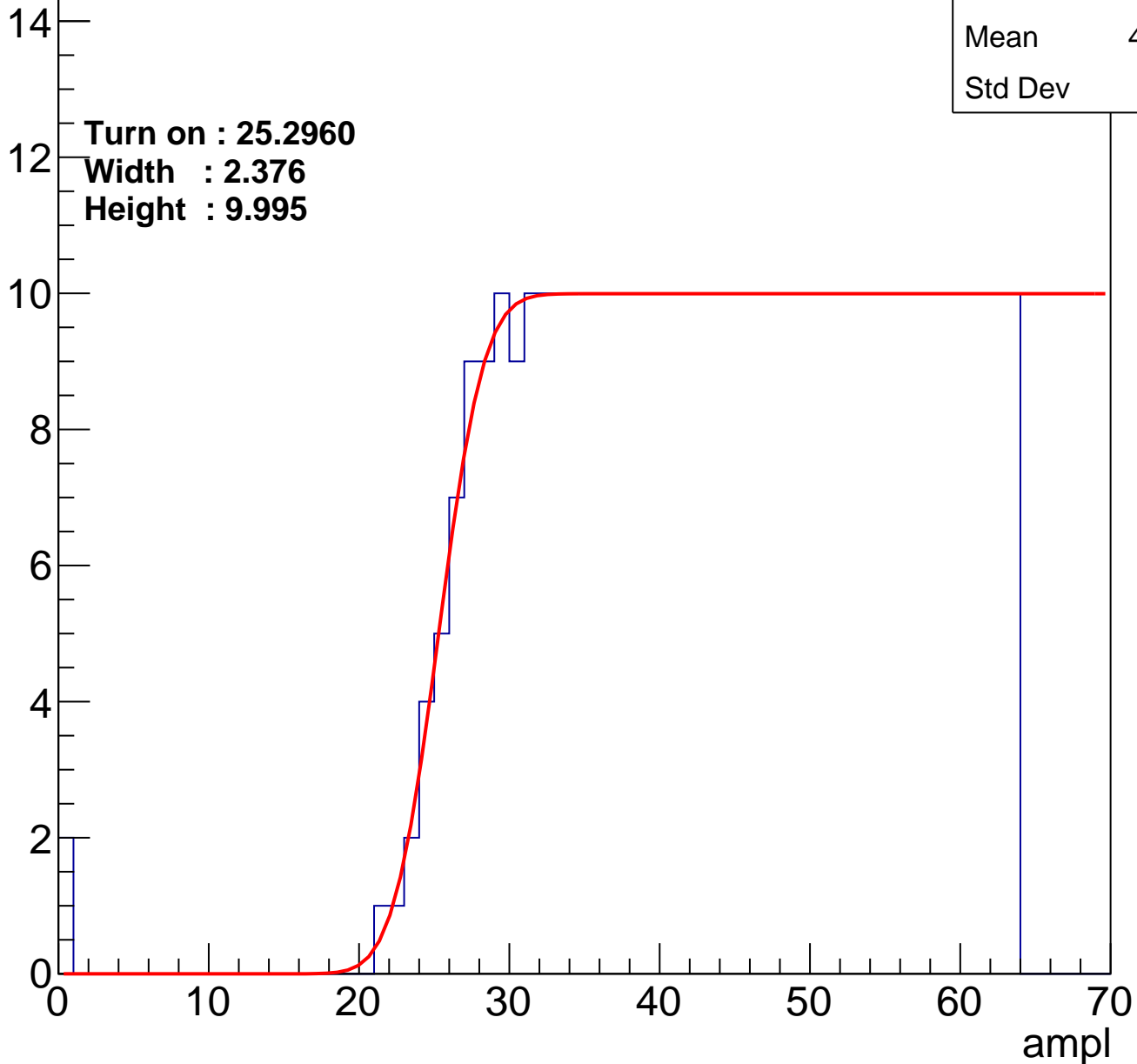
Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.2960

Width : 2.376

Height : 9.995

Entry



B1L000S, U6-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.51
Std Dev	11.65

Turn on : 26.5152

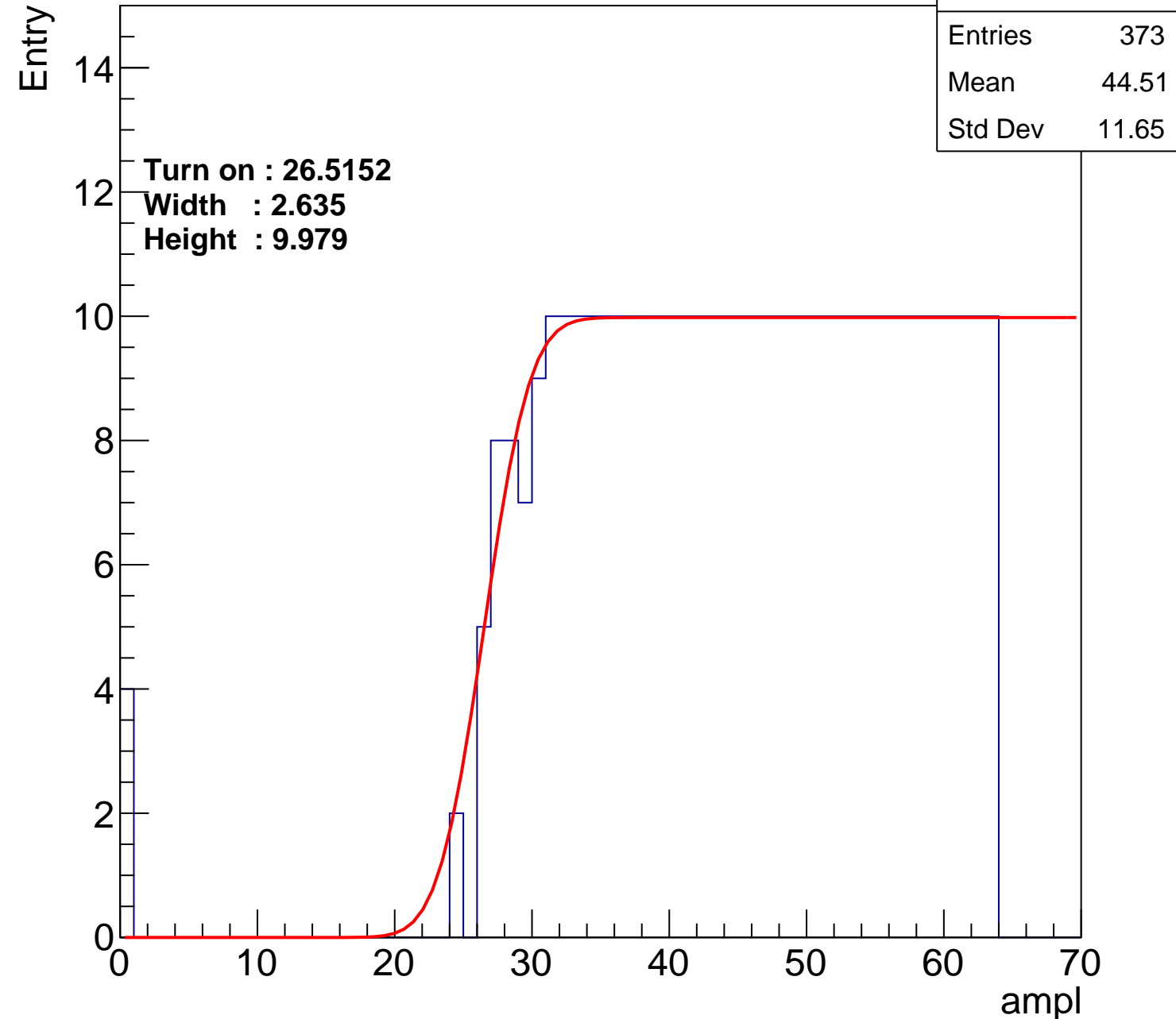
Width : 2.635

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.88
Std Dev	11.53

Turn on : 25.1173

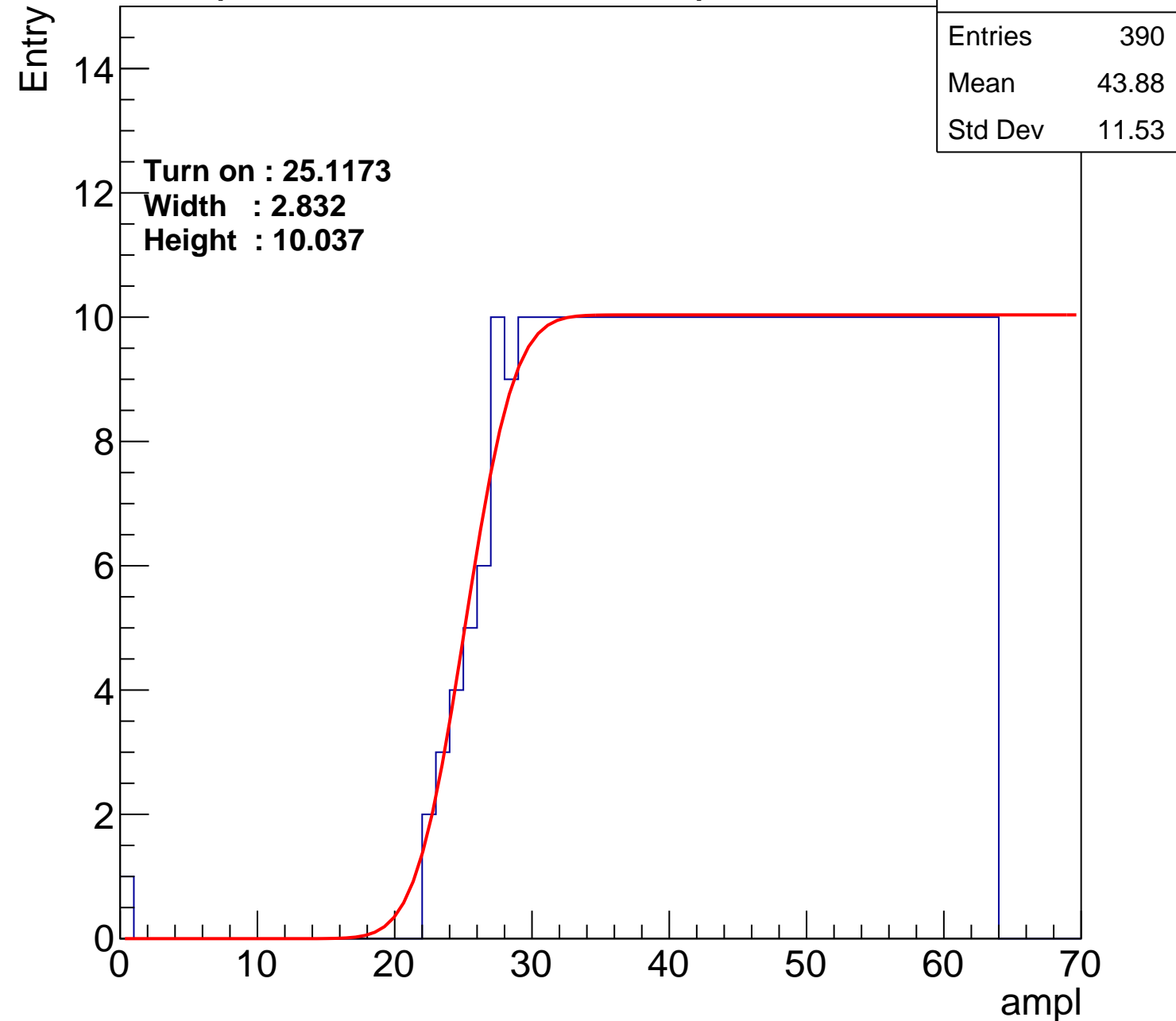
Width : 2.832

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.1921

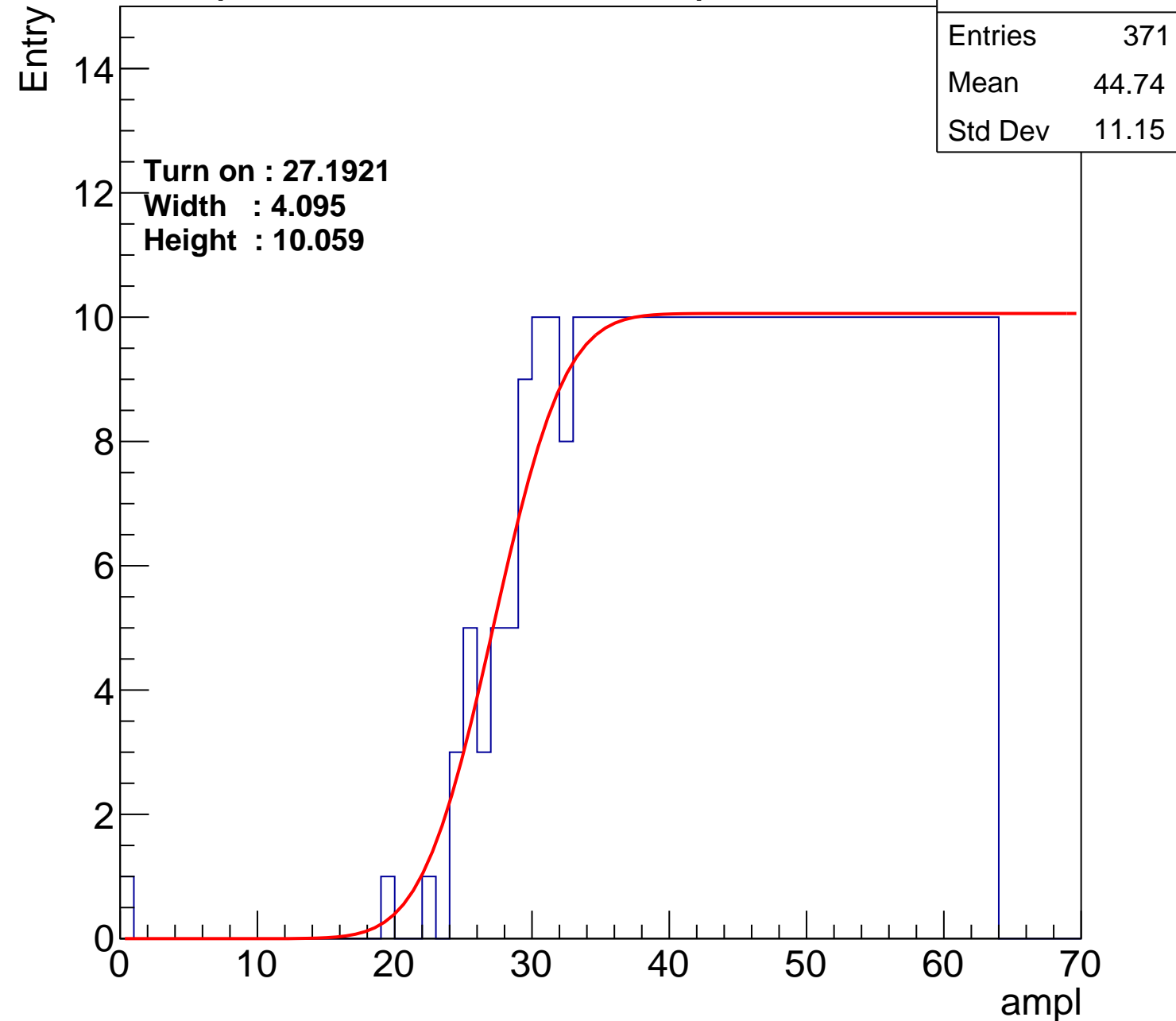
Width : 4.095

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.77
Std Dev	12.06

Turn on : 26.4914

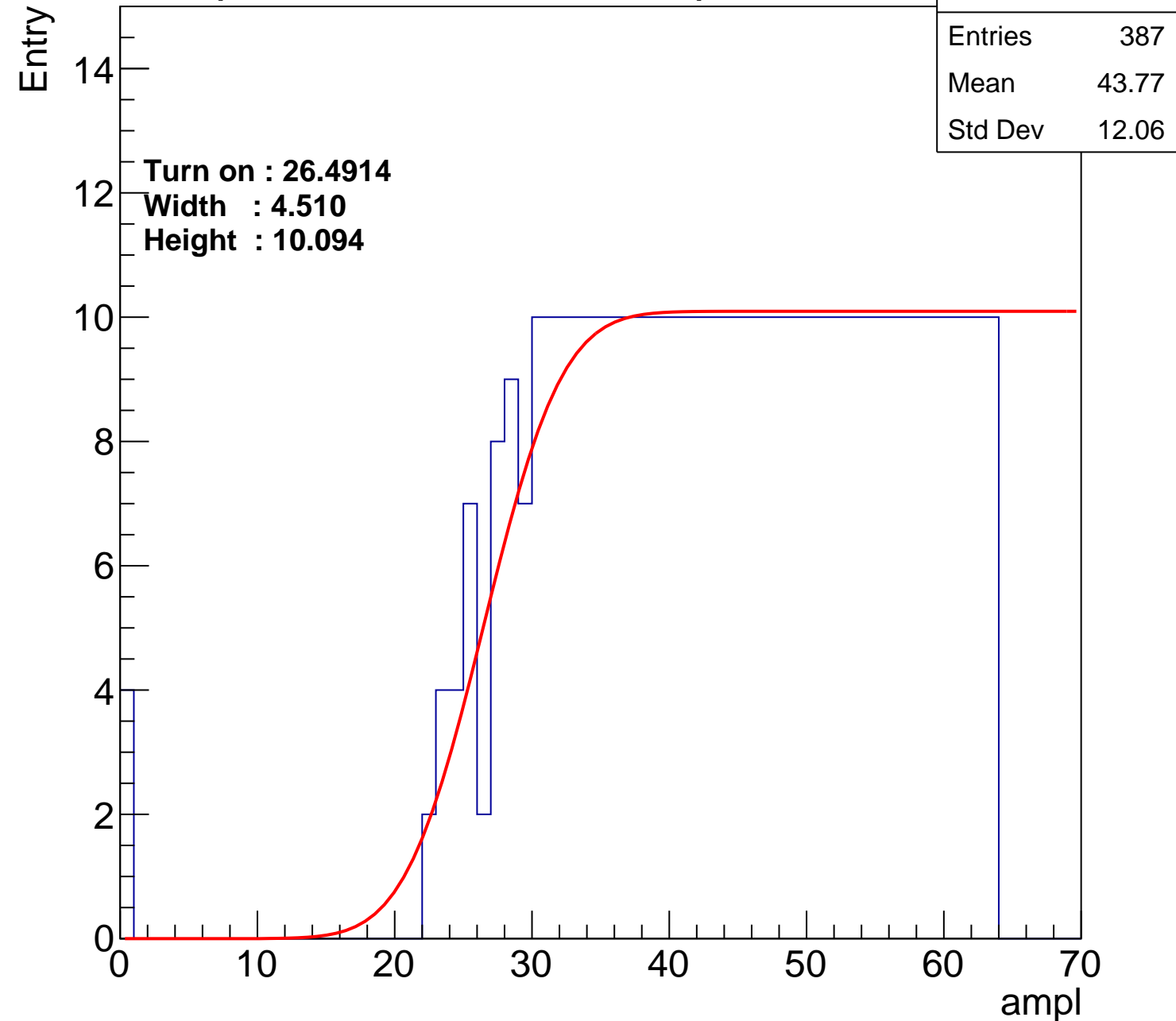
Width : 4.510

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 26.2978

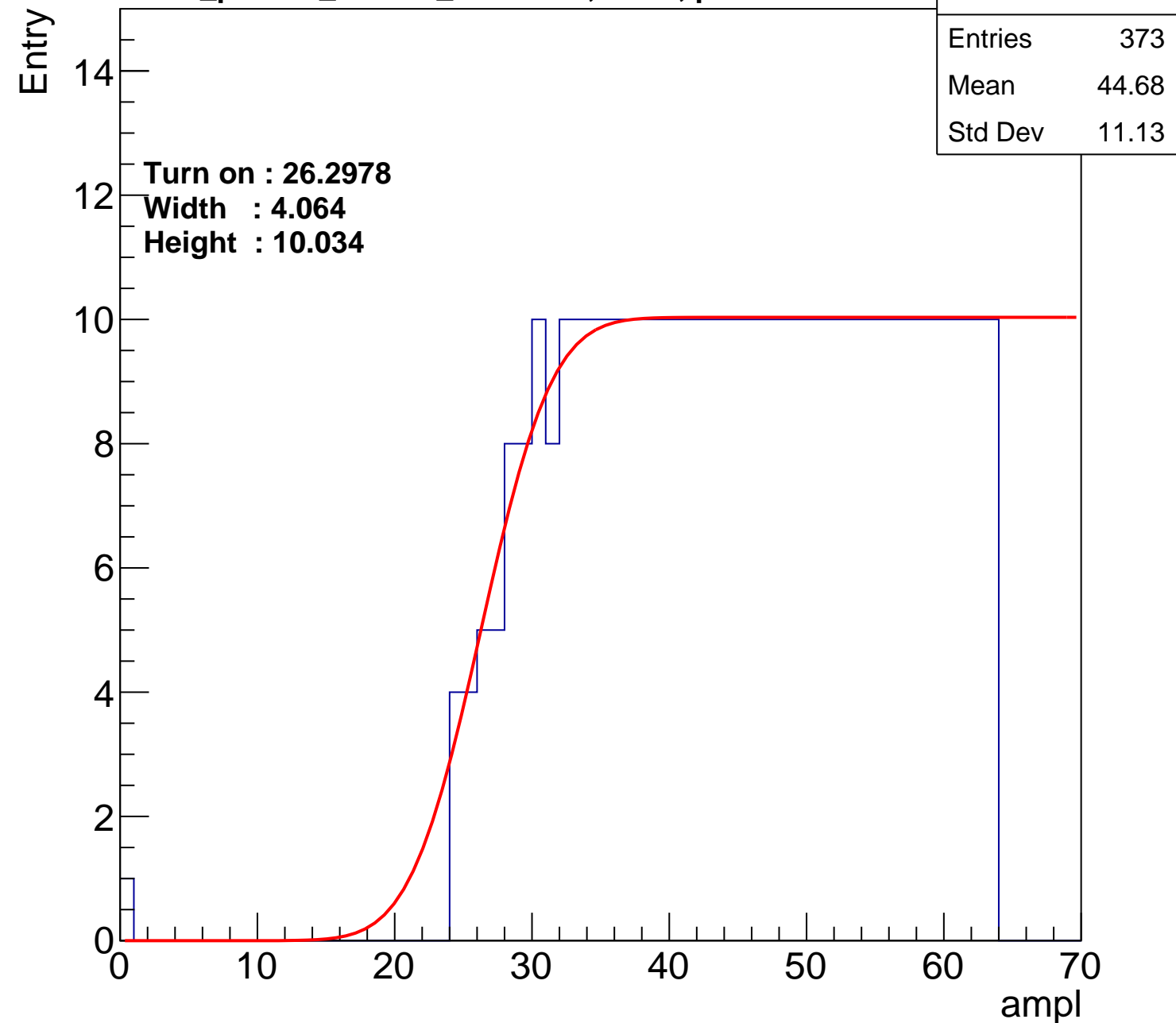
Width : 4.064

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U6-ch127

calib_packv5_042523_0143.root, FC#3, port B3

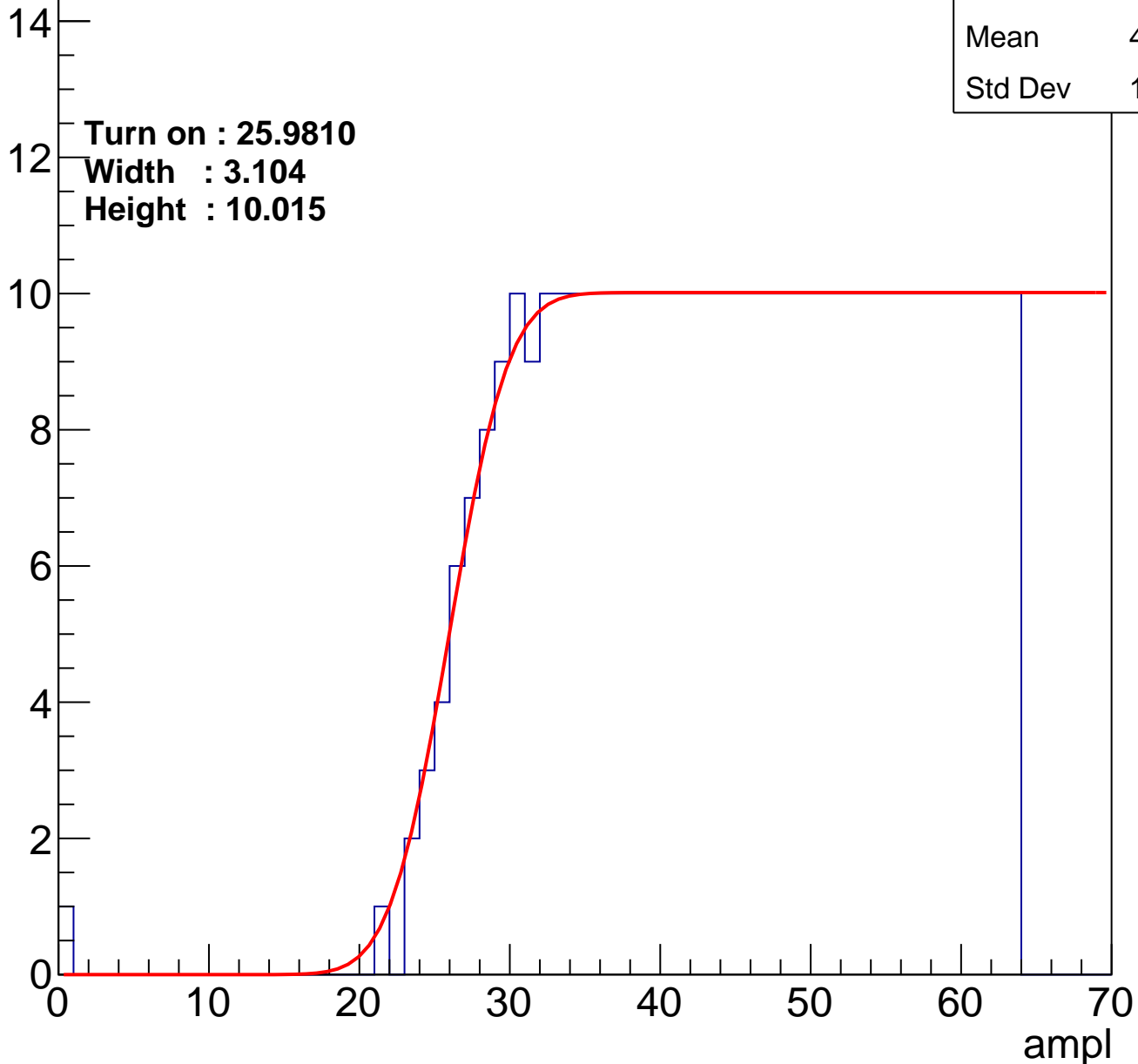
Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 25.9810

Width : 3.104

Height : 10.015

Entry



B1L000S, U6-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 25.9810

Width : 3.104

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl

