

B0L000S, U13-ch0

calib_packv5_042523_0143.root, FC#5, port B1

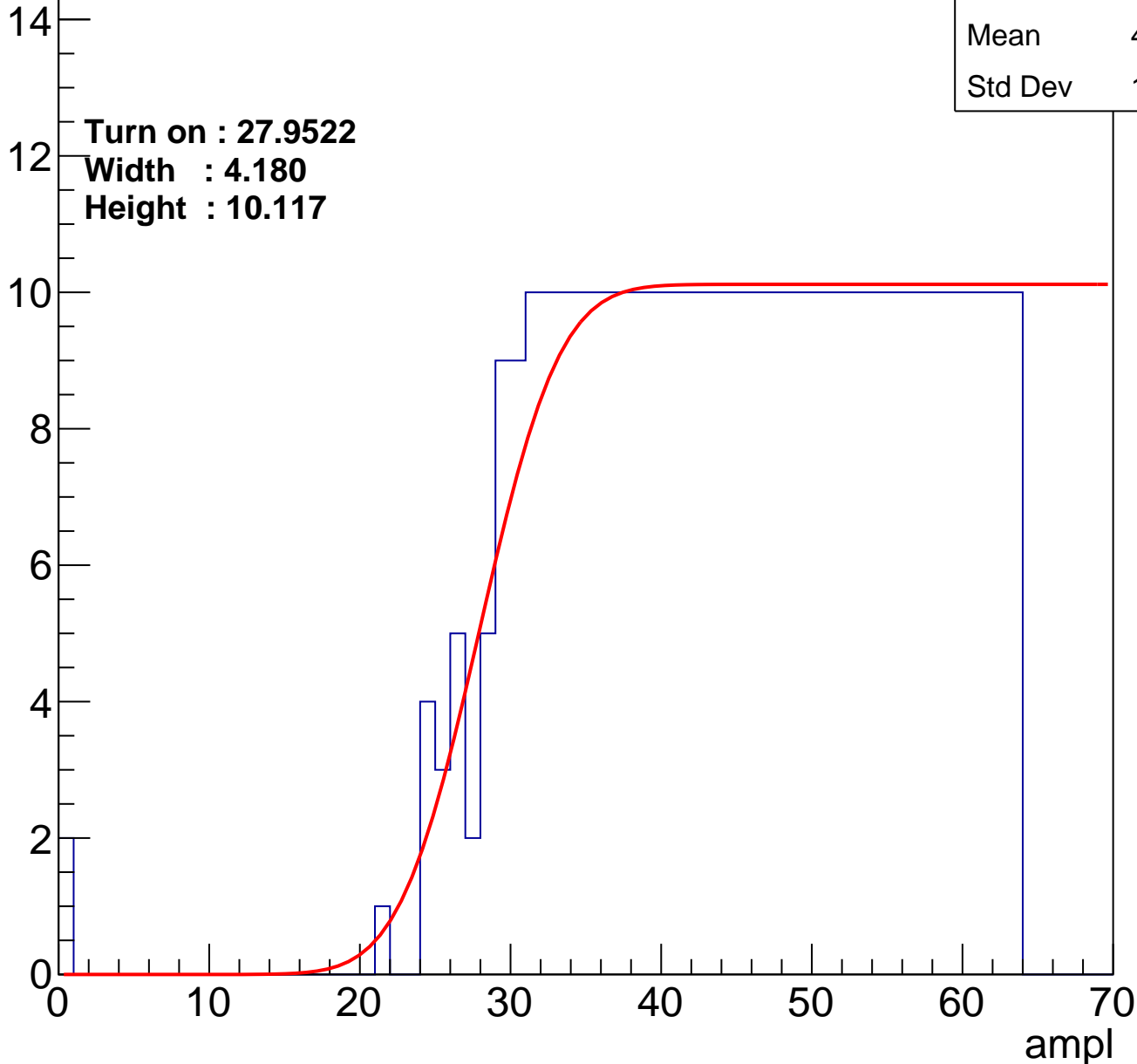
Entry

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.9522

Width : 4.180

Height : 10.117



B0L000S, U13-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.35
Std Dev	10.83

Turn on : 27.7619

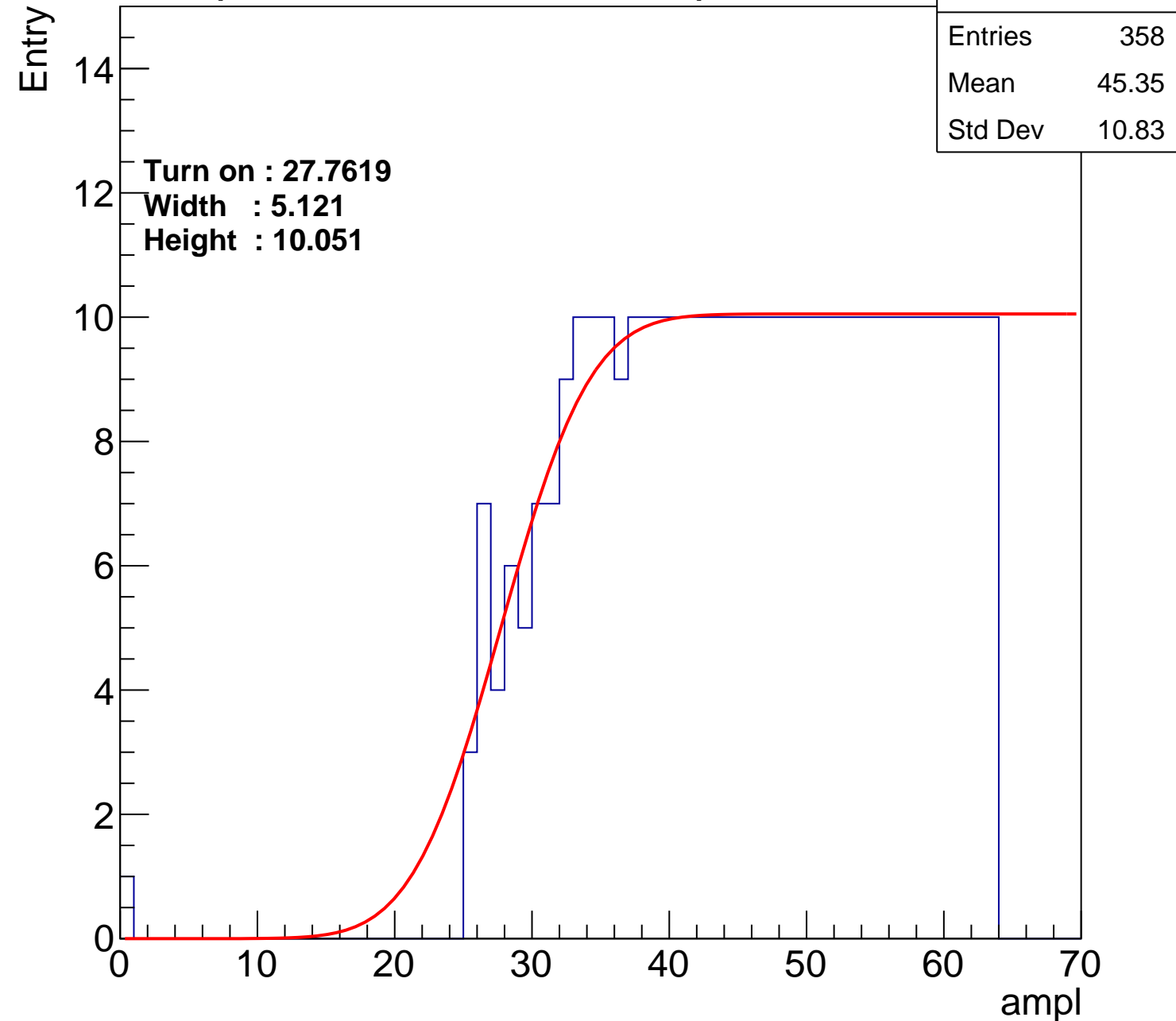
Width : 5.121

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	346
Mean	45.95
Std Dev	10.52

Turn on : 30.0582

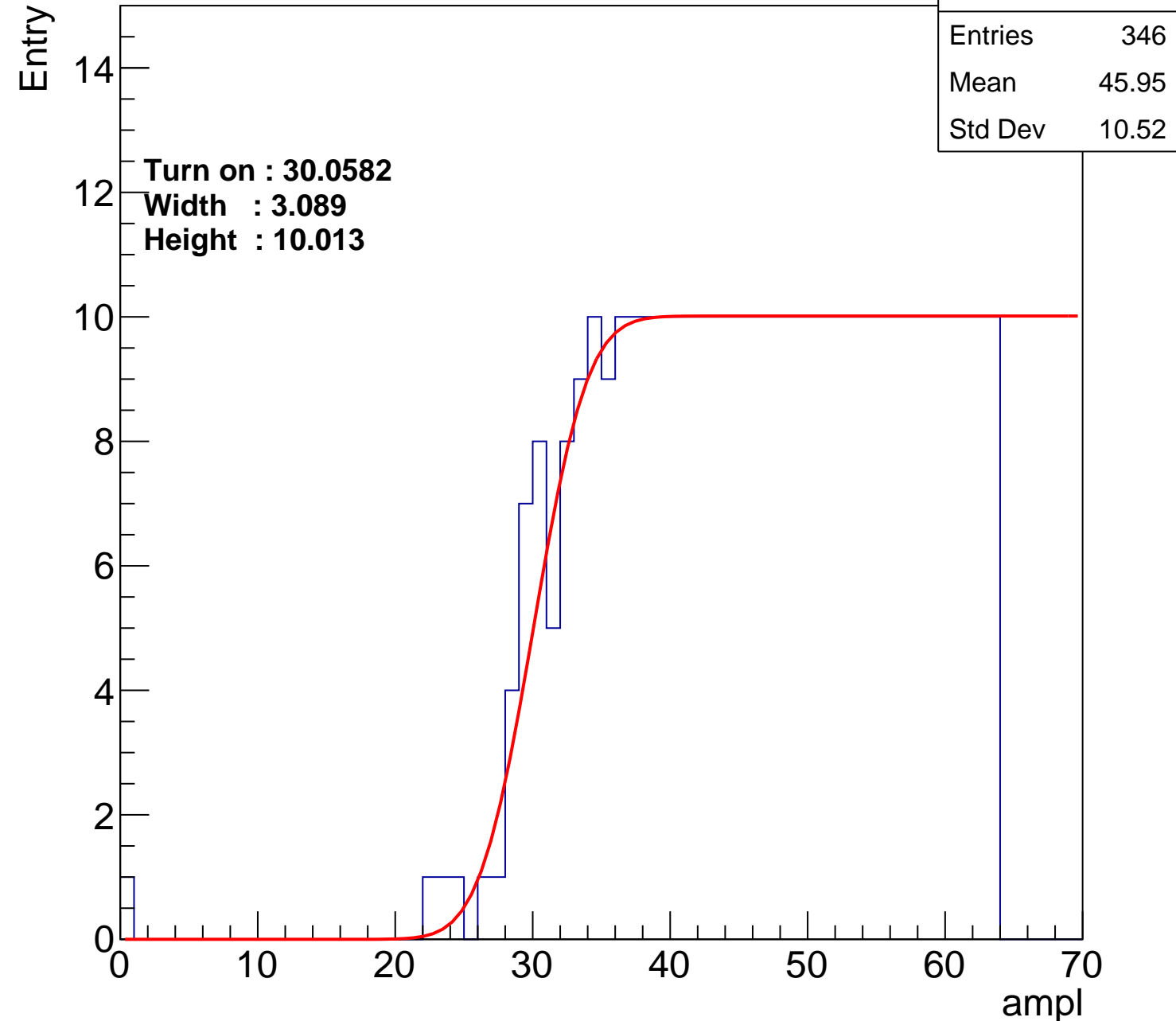
Width : 3.089

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.48
Std Dev	10.89

Turn on : 28.6981

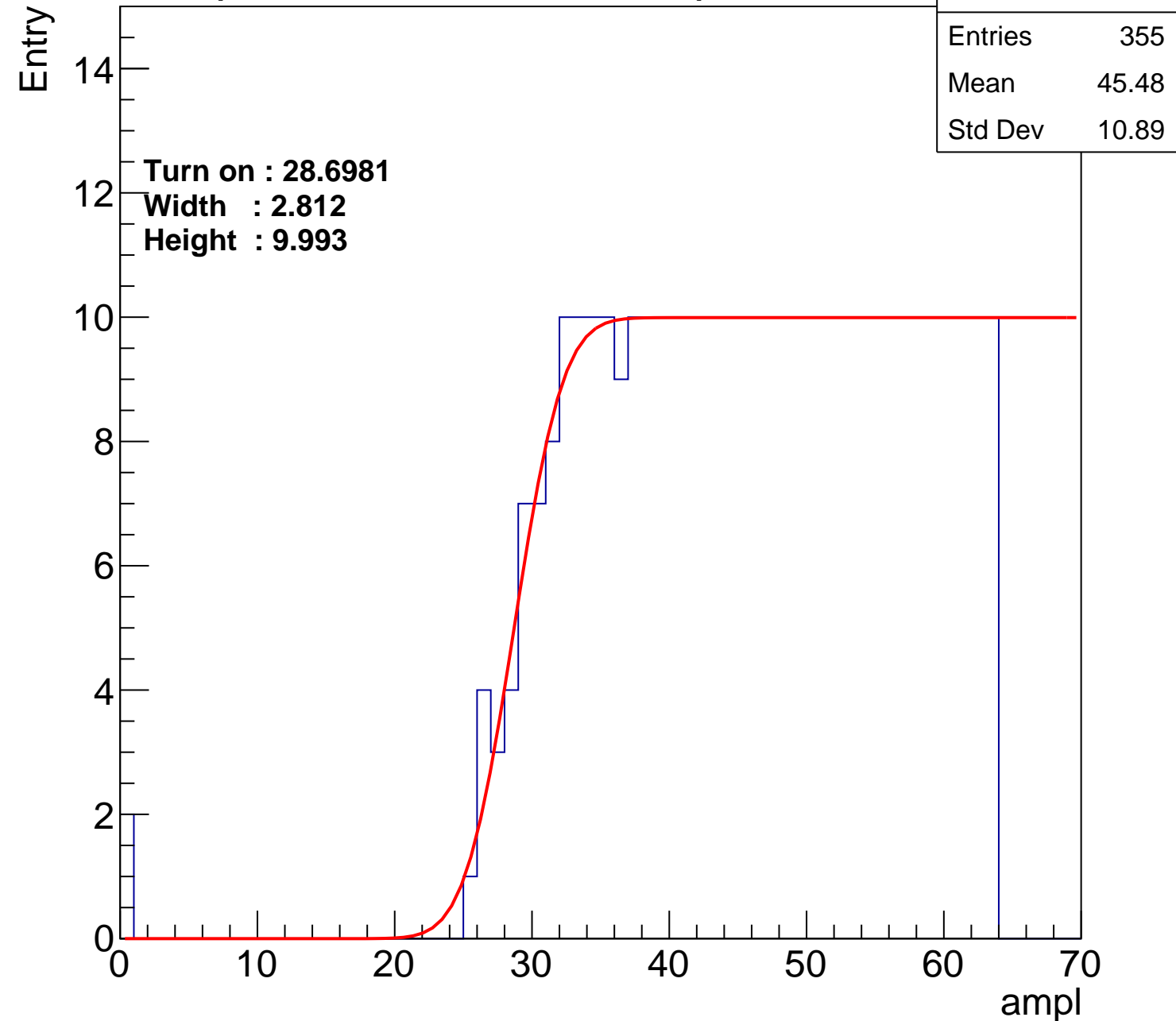
Width : 2.812

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch4

calib_packv5_042523_0143.root, FC#5, port B1

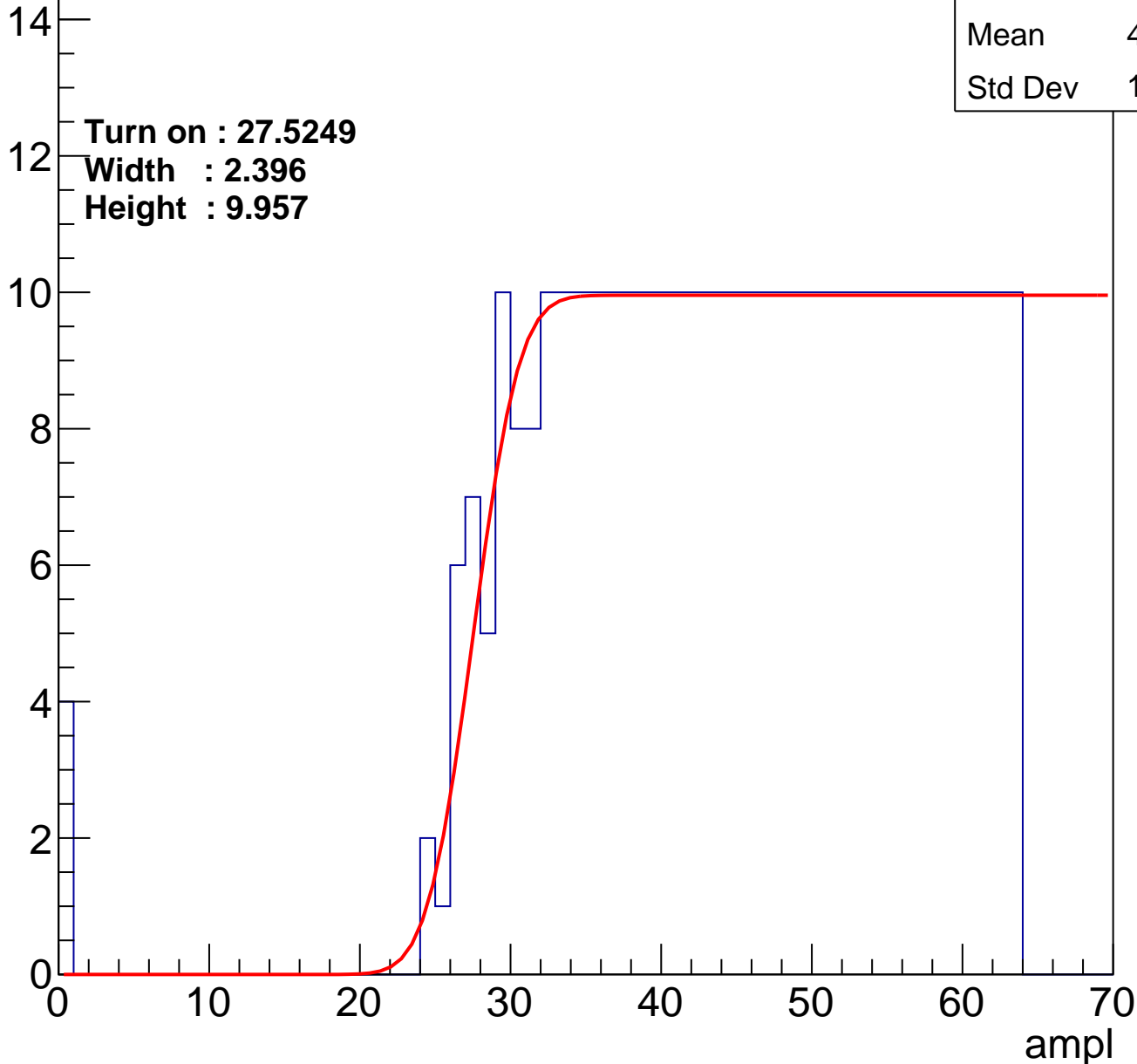
Entry

Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 27.5249

Width : 2.396

Height : 9.957



B0L000S, U13-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.63
Std Dev	10.55

Turn on : 28.8696

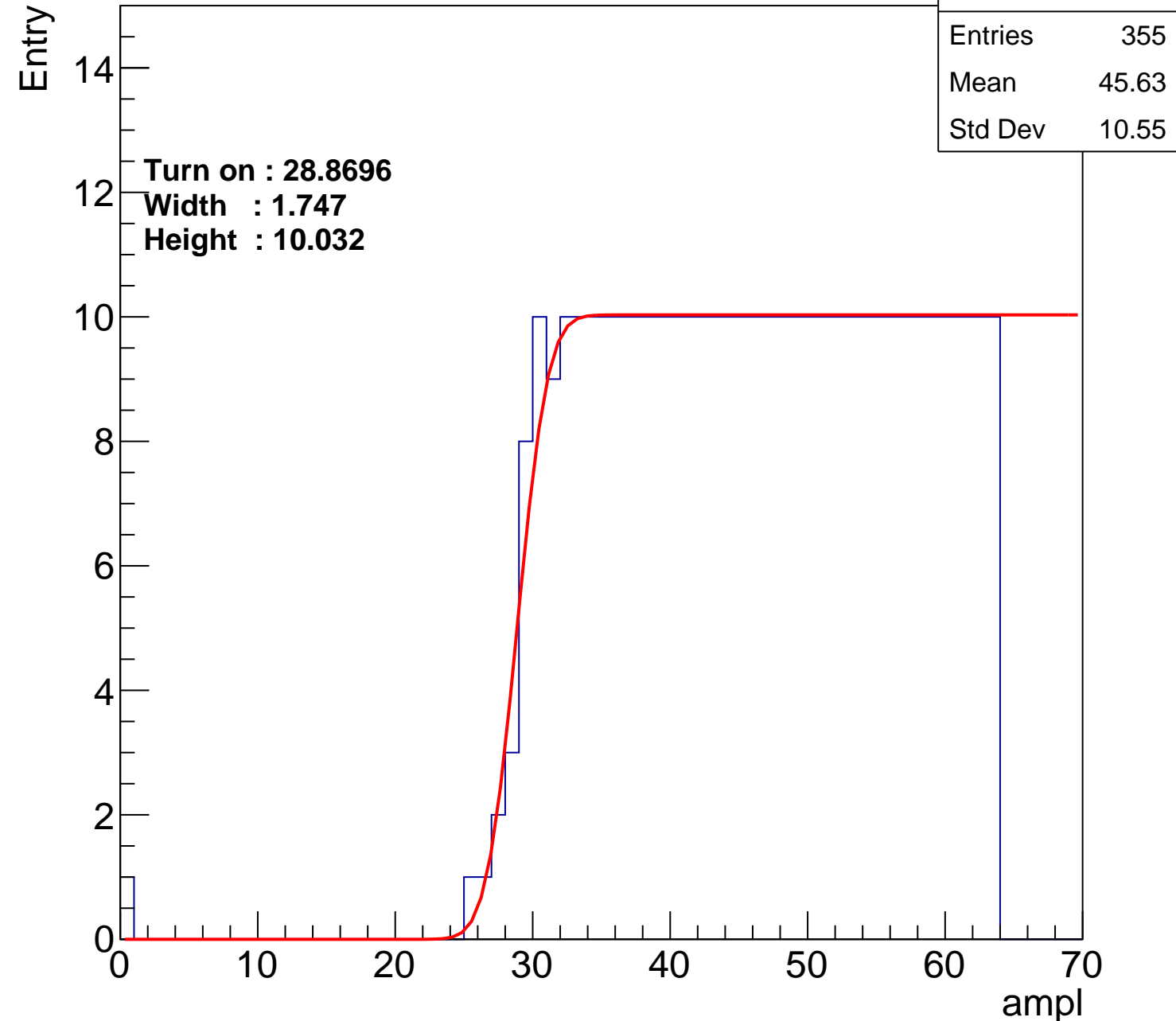
Width : 1.747

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch6

calib_packv5_042523_0143.root, FC#5, port B1

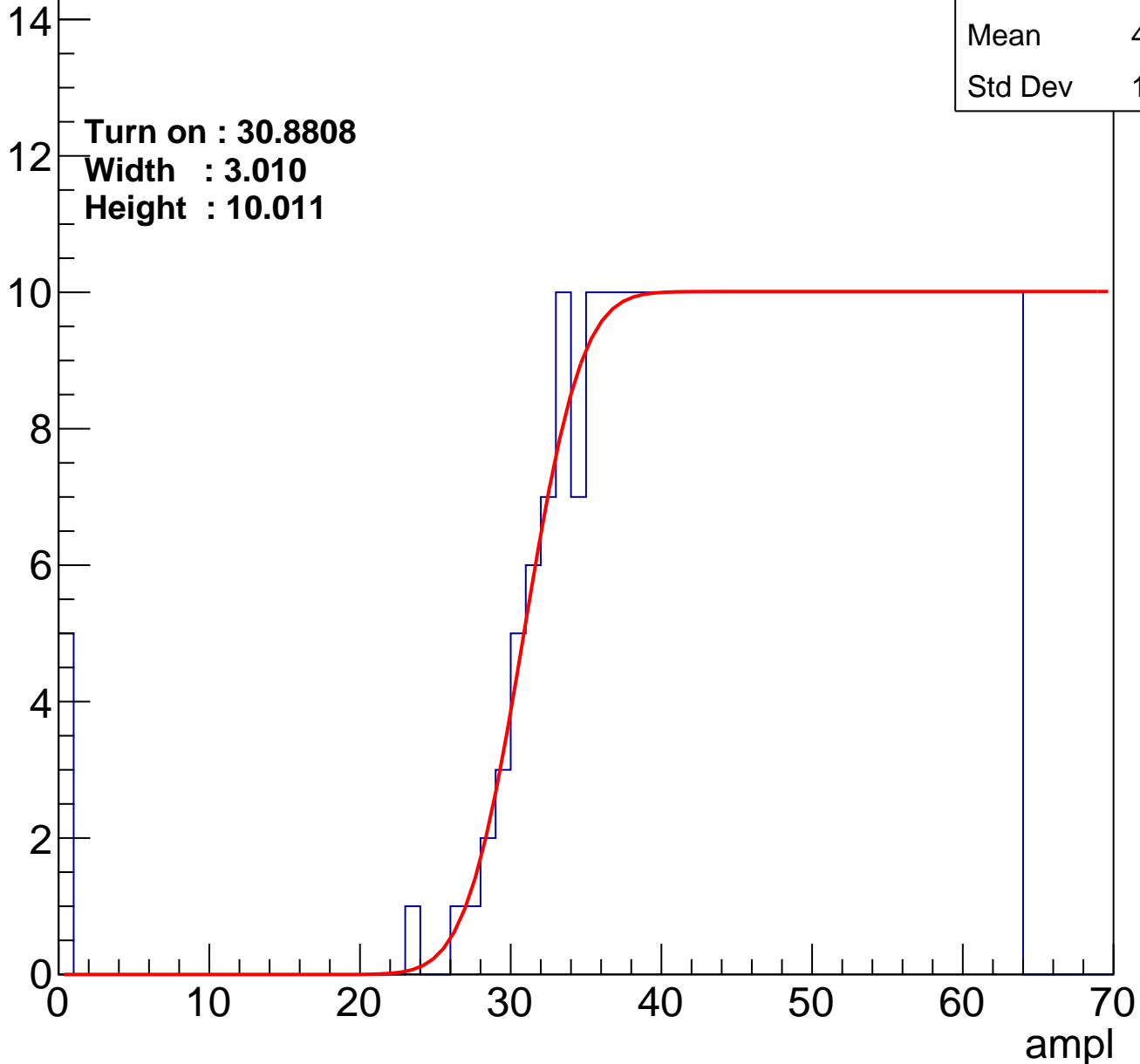
Entries	338
Mean	46.03
Std Dev	11.28

Turn on : 30.8808

Width : 3.010

Height : 10.011

Entry



B0L000S, U13-ch7

calib_packv5_042523_0143.root, FC#5, port B1

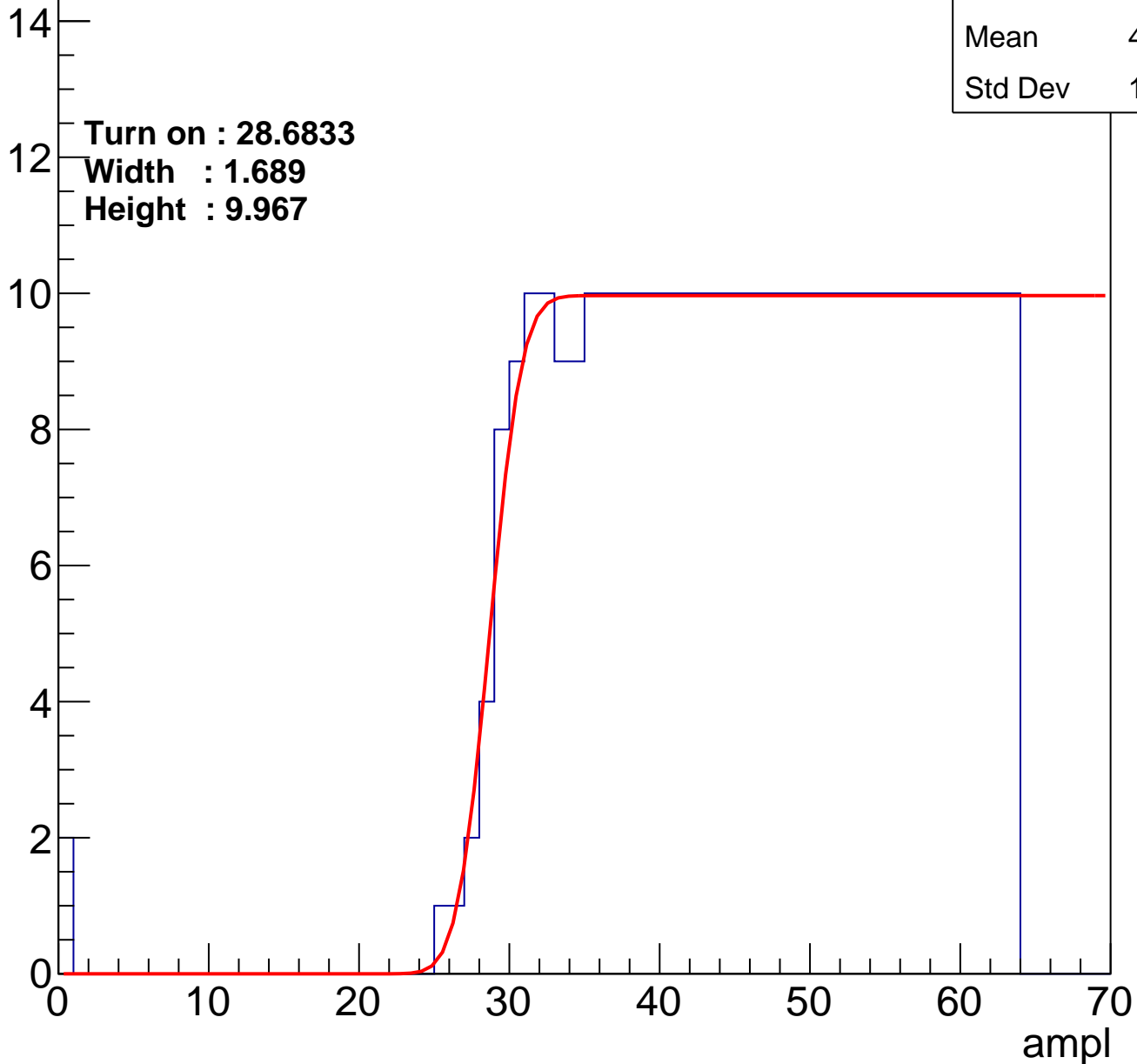
Entry

Entries	355
Mean	45.53
Std Dev	10.82

Turn on : 28.6833

Width : 1.689

Height : 9.967



B0L000S, U13-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	348
Mean	45.77
Std Dev	10.92

Turn on : 29.8272

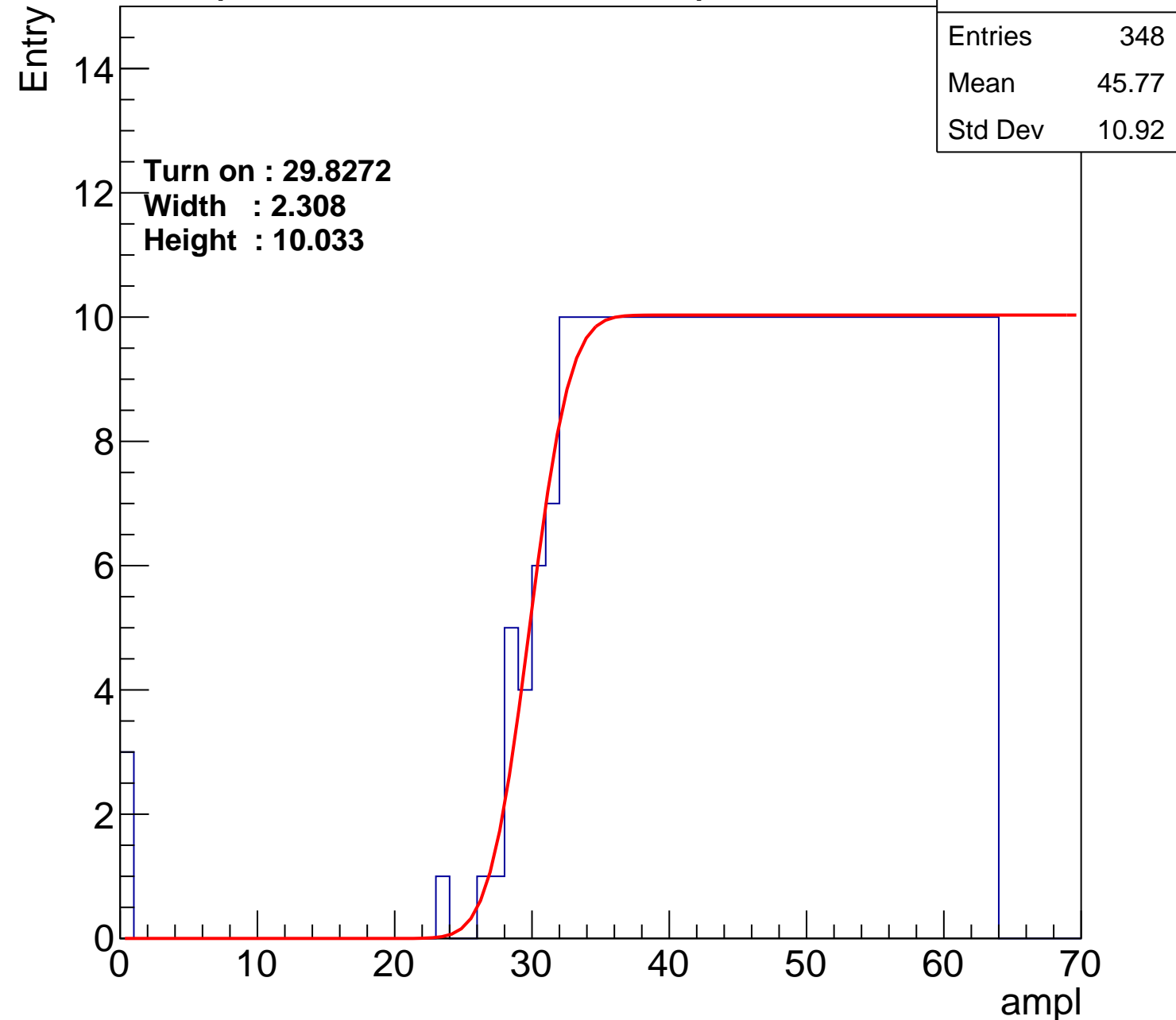
Width : 2.308

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 27.8160

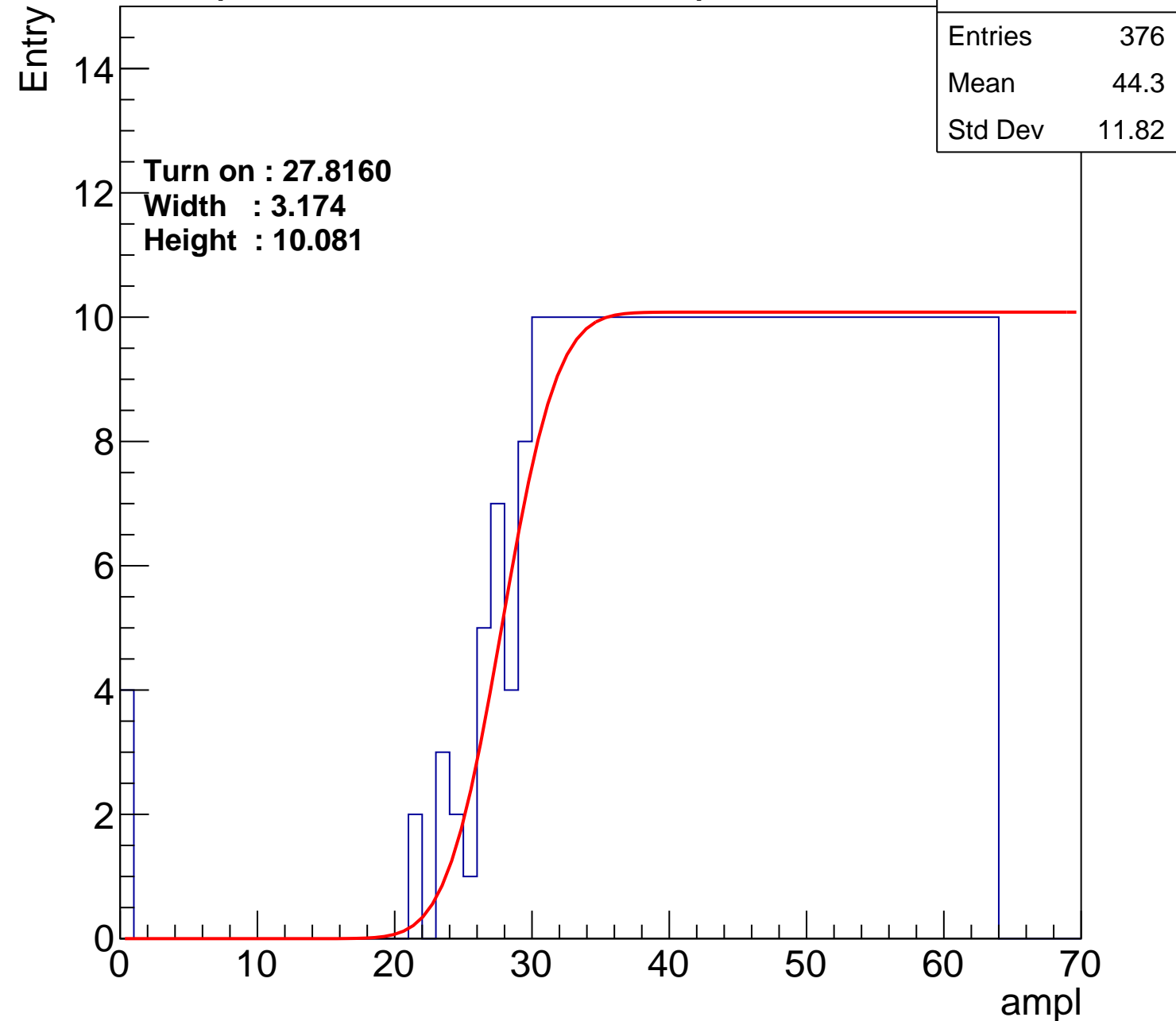
Width : 3.174

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch10

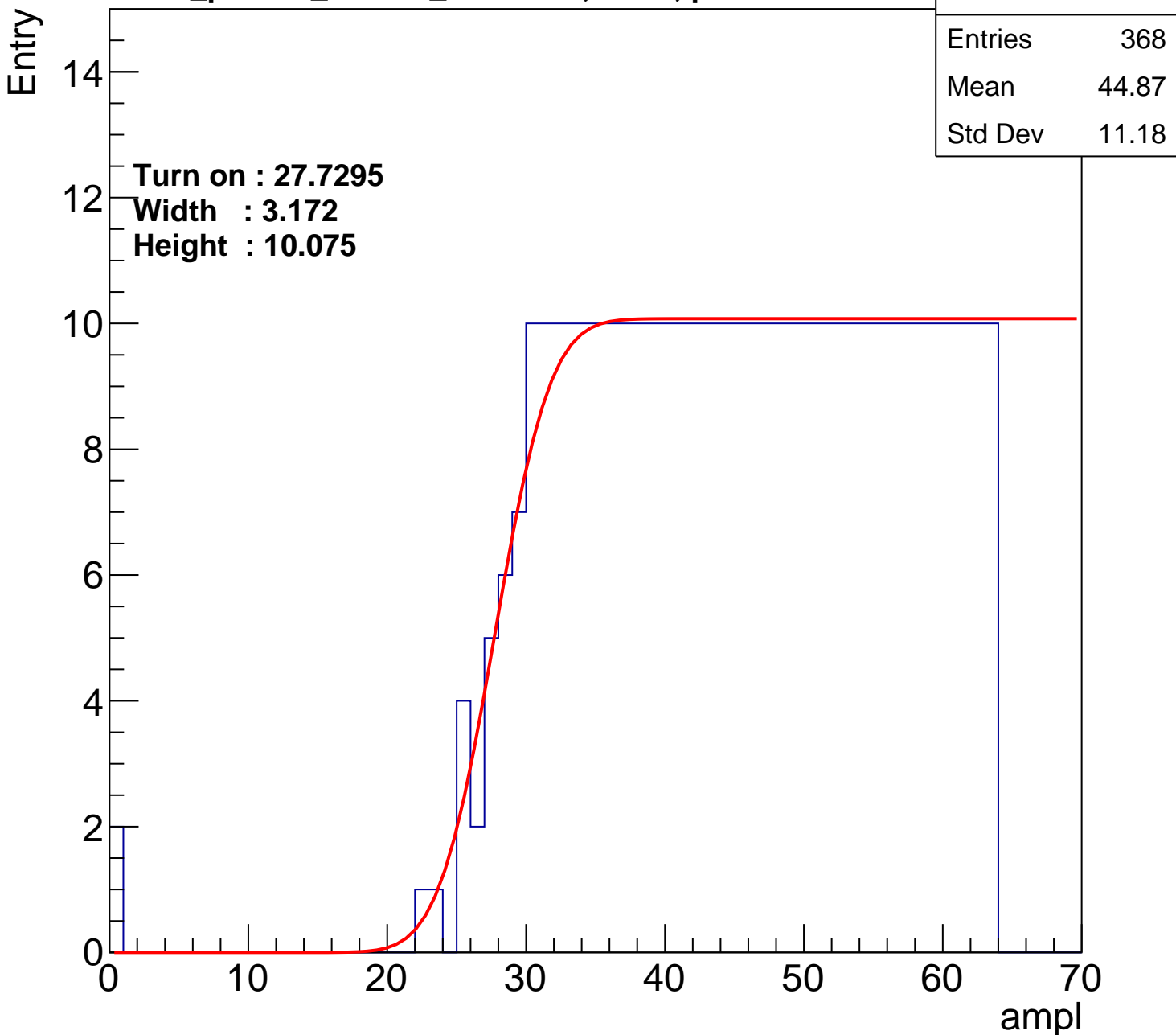
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.7295

Width : 3.172

Height : 10.075

Entries	368
Mean	44.87
Std Dev	11.18



B0L000S, U13-ch11

calib_packv5_042523_0143.root, FC#5, port B1

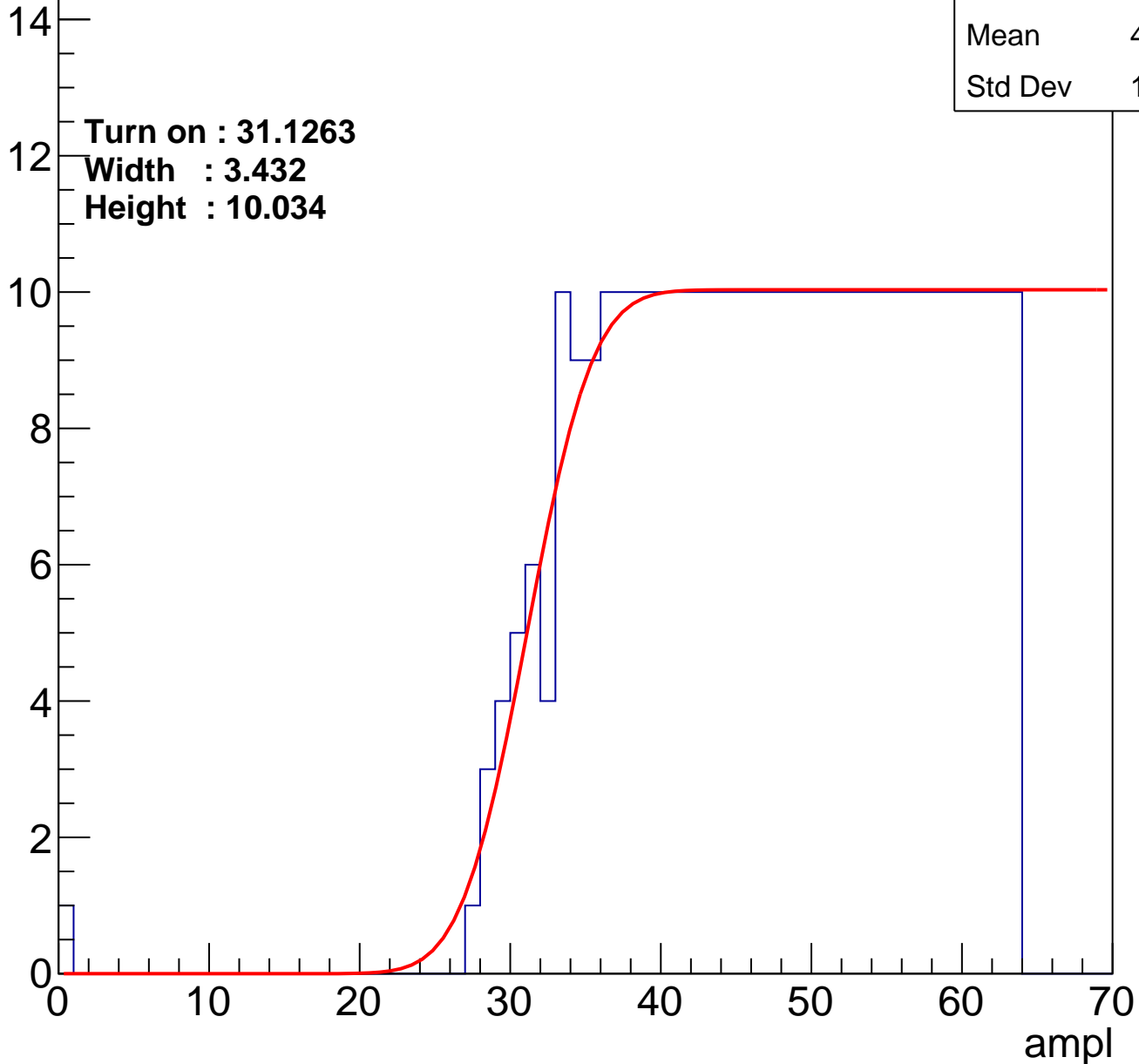
Entries	332
Mean	46.69
Std Dev	10.07

Turn on : 31.1263

Width : 3.432

Height : 10.034

Entry



B0L000S, U13-ch12

calib_packv5_042523_0143.root, FC#5, port B1

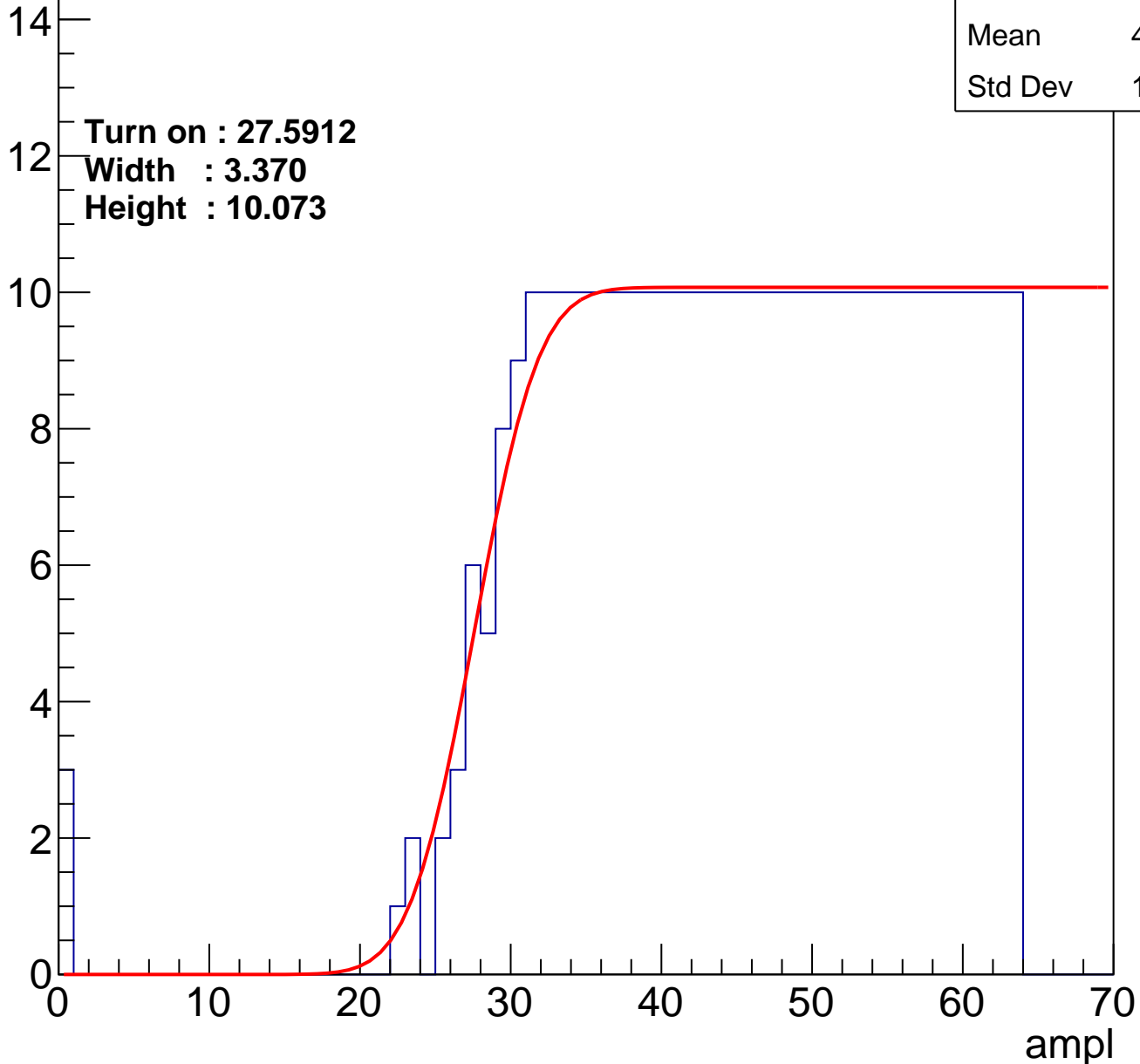
Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.5912

Width : 3.370

Height : 10.073

Entry



B0L000S, U13-ch13

calib_packv5_042523_0143.root, FC#5, port B1

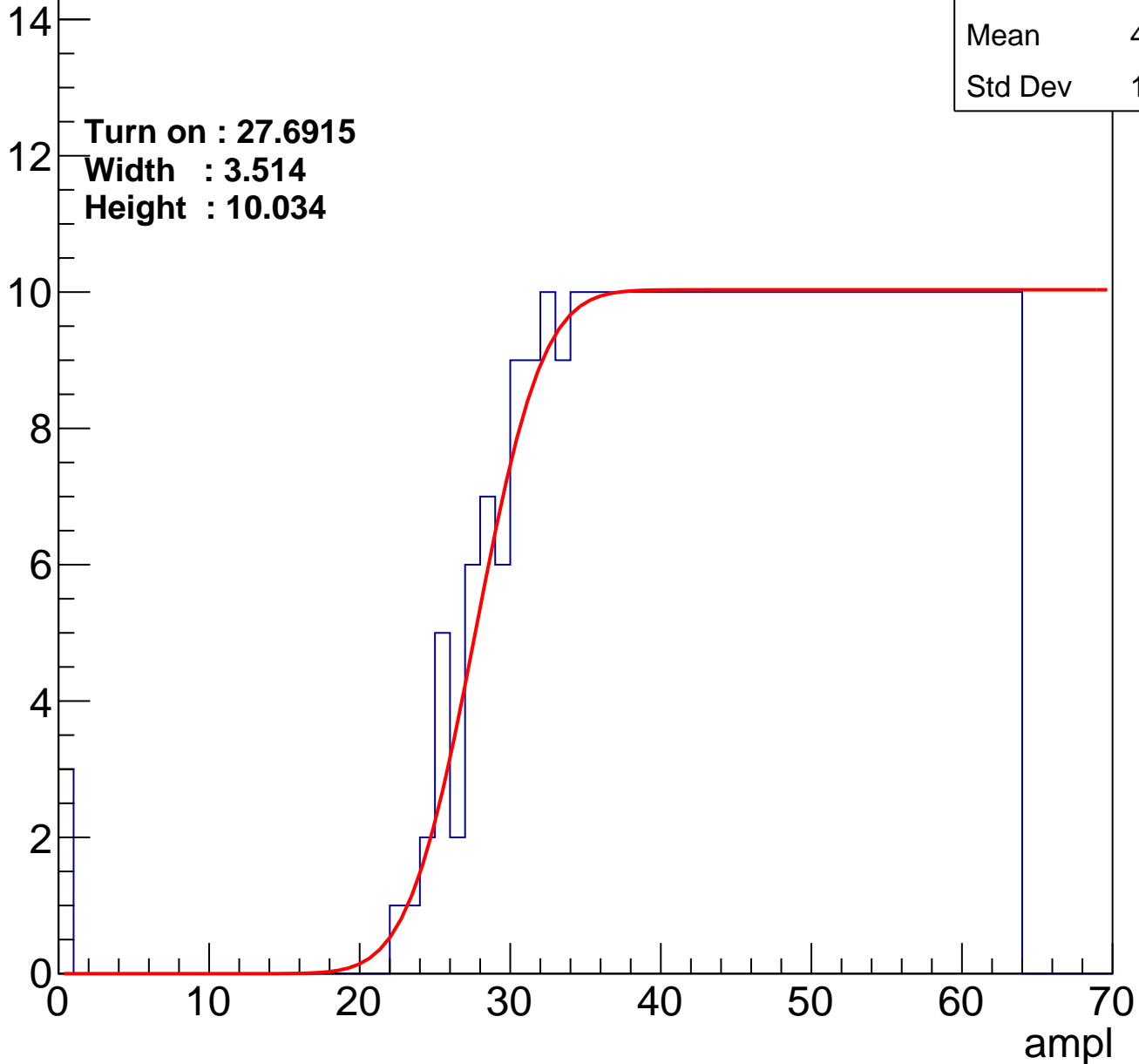
Entries	370
Mean	44.64
Std Dev	11.52

Turn on : 27.6915

Width : 3.514

Height : 10.034

Entry



B0L000S, U13-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.7
Std Dev	11.25

Turn on : 27.4537

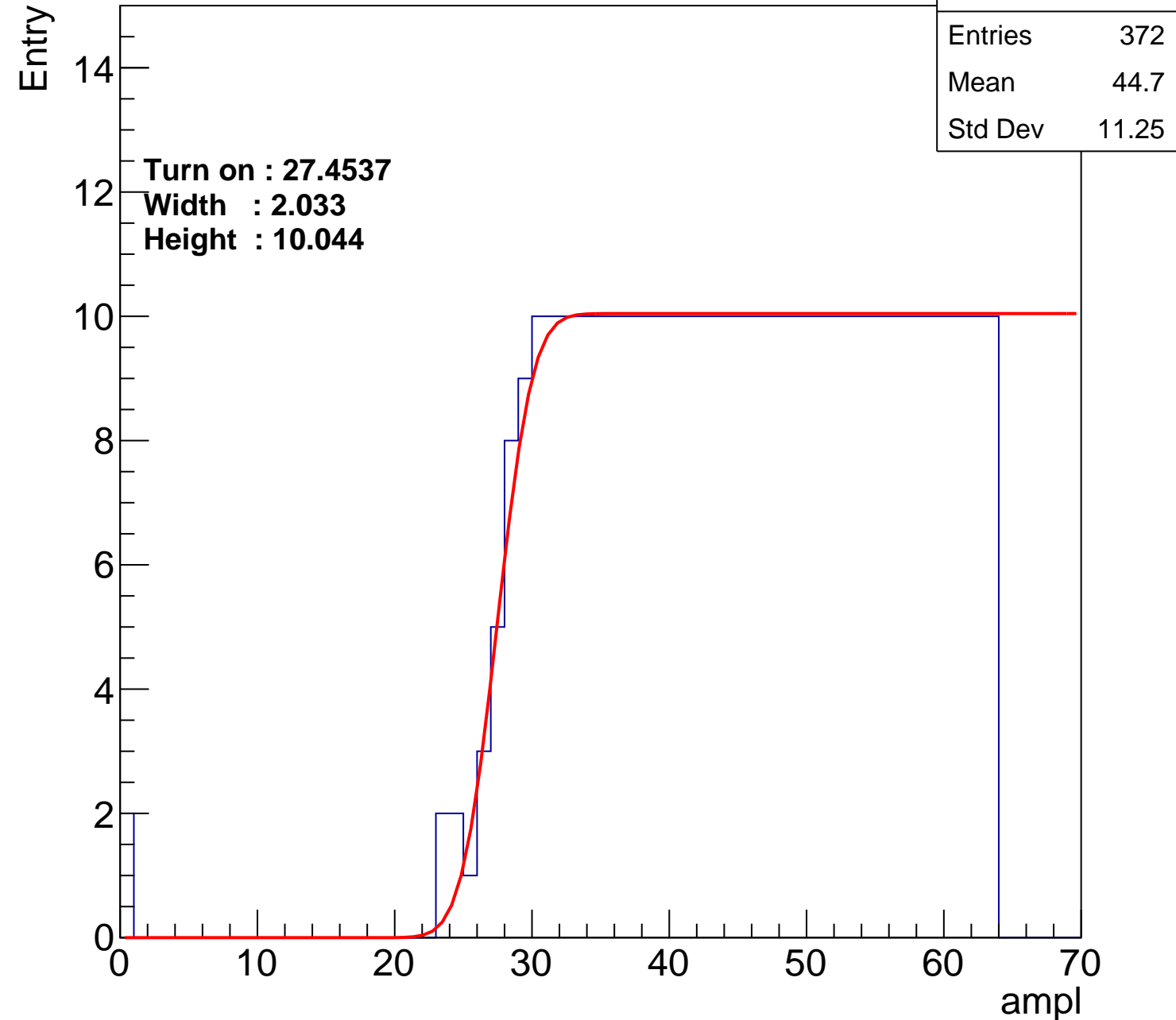
Width : 2.033

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.26
Std Dev	11.08

Turn on : 28.0185

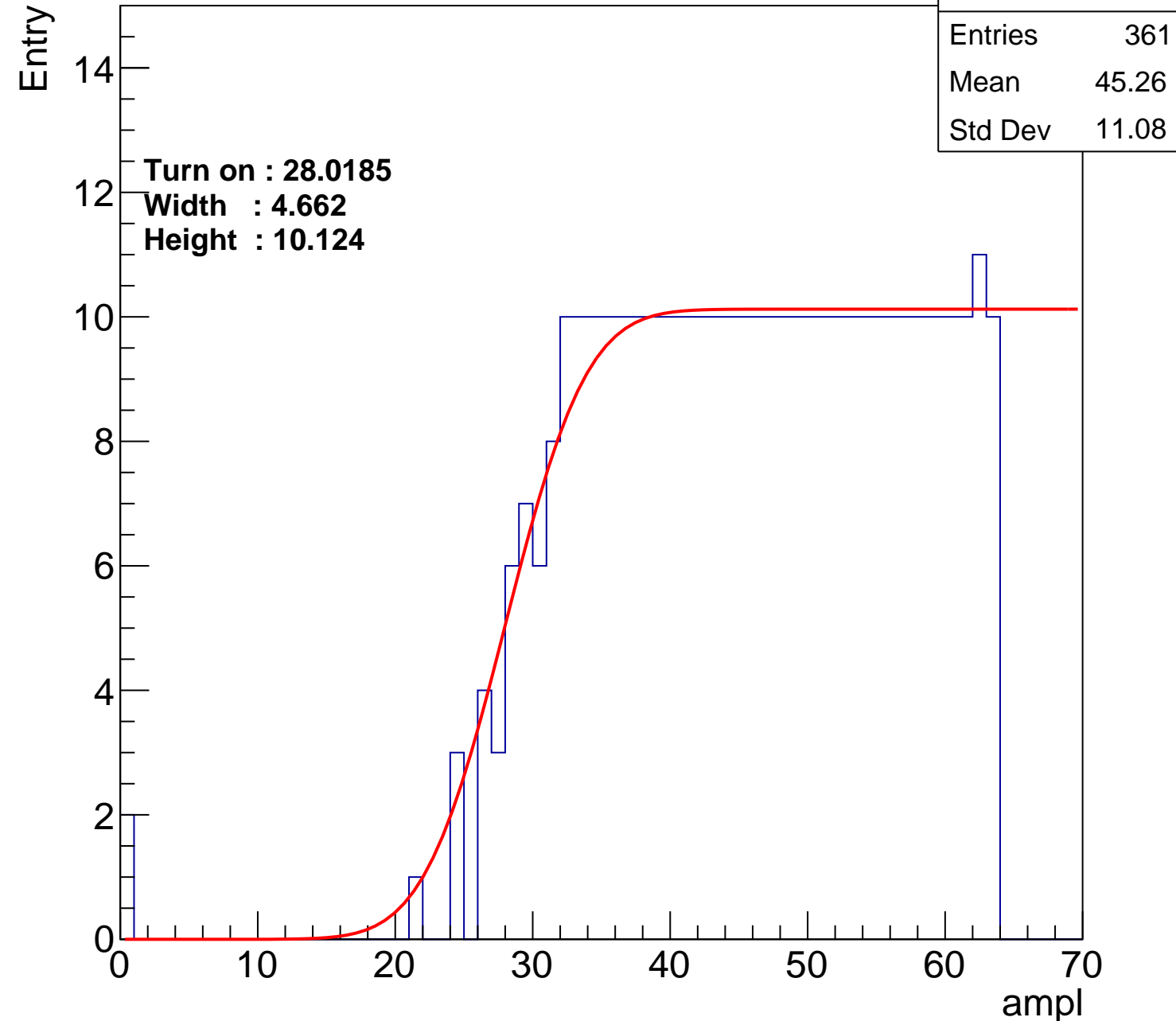
Width : 4.662

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch16

calib_packv5_042523_0143.root, FC#5, port B1

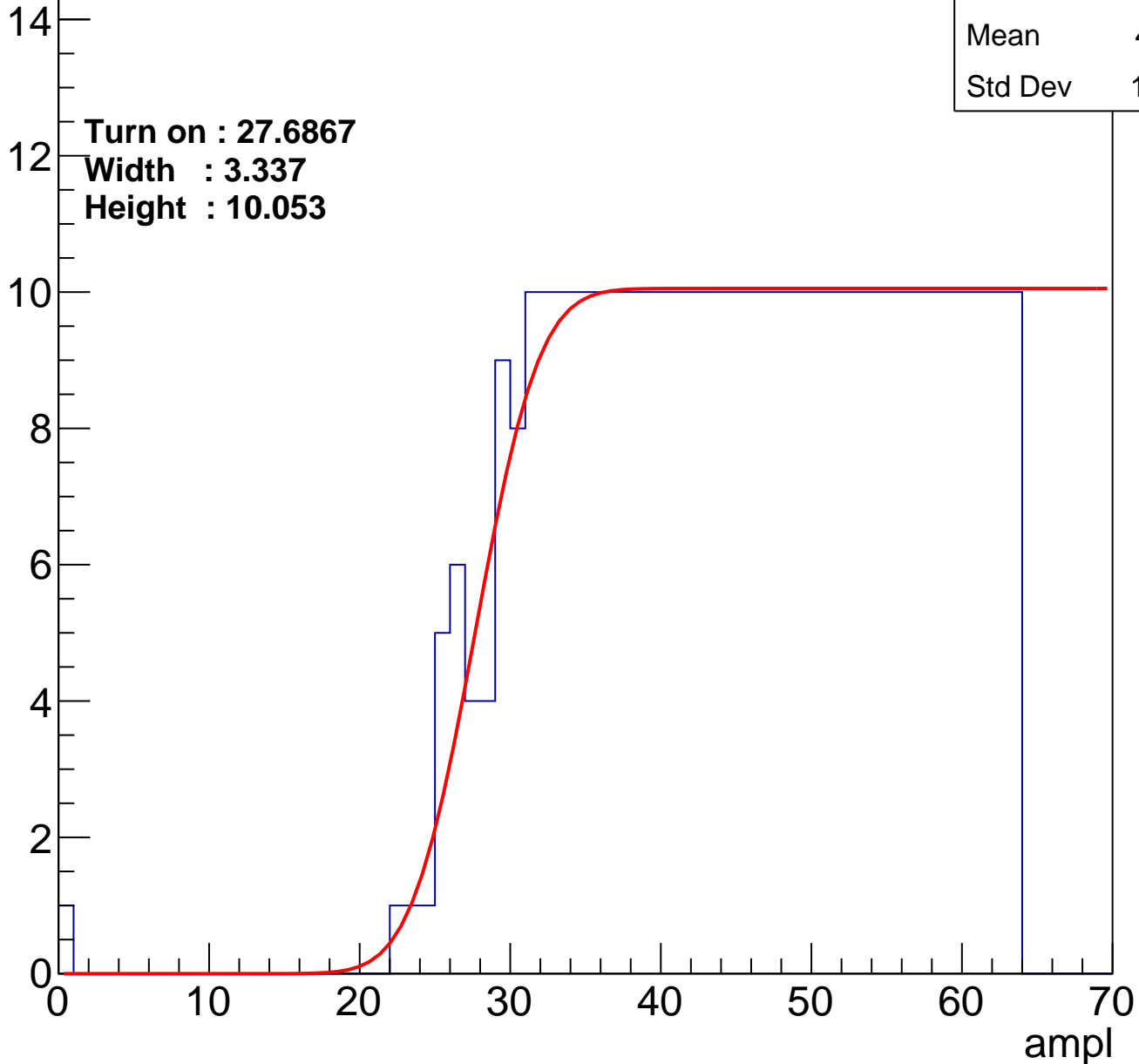
Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.6867

Width : 3.337

Height : 10.053

Entry



B0L000S, U13-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.21
Std Dev	11.61

Turn on : 26.4669

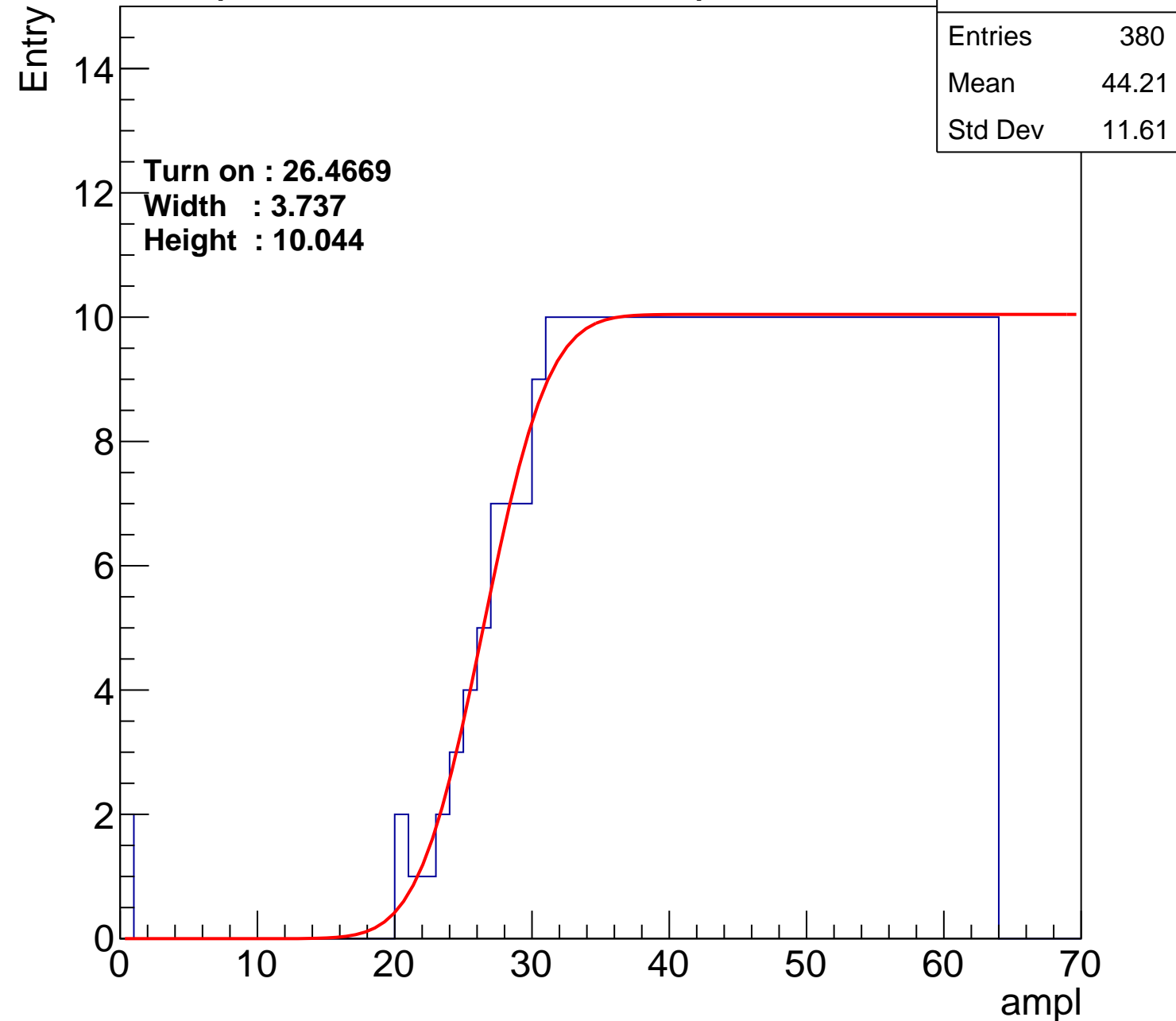
Width : 3.737

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch18

calib_packv5_042523_0143.root, FC#5, port B1

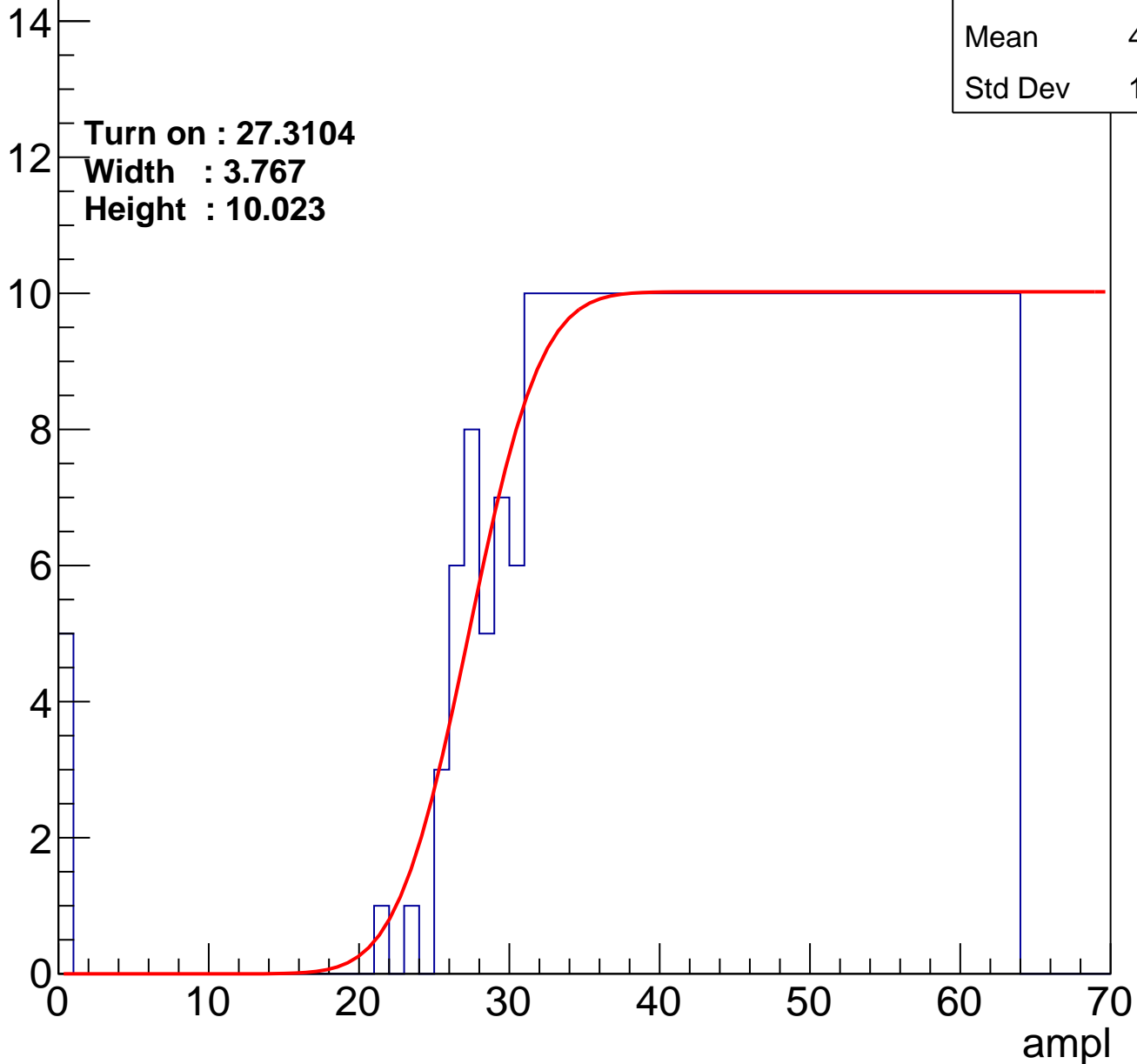
Entries	372
Mean	44.42
Std Dev	11.92

Turn on : 27.3104

Width : 3.767

Height : 10.023

Entry



B0L000S, U13-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.84
Std Dev	10.54

Turn on : 29.5542

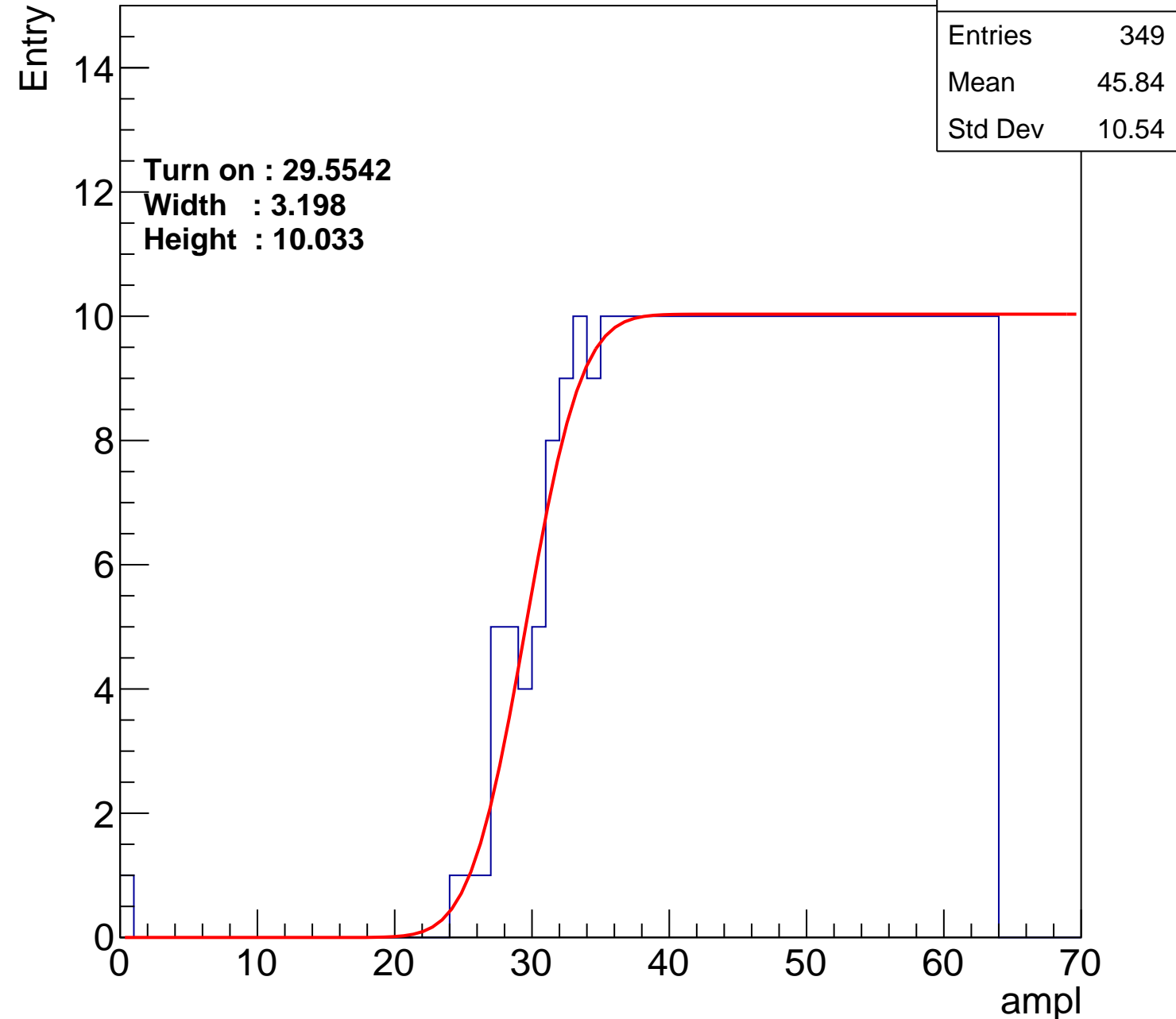
Width : 3.198

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	339
Mean	46.35
Std Dev	10.25

Turn on : 30.3314

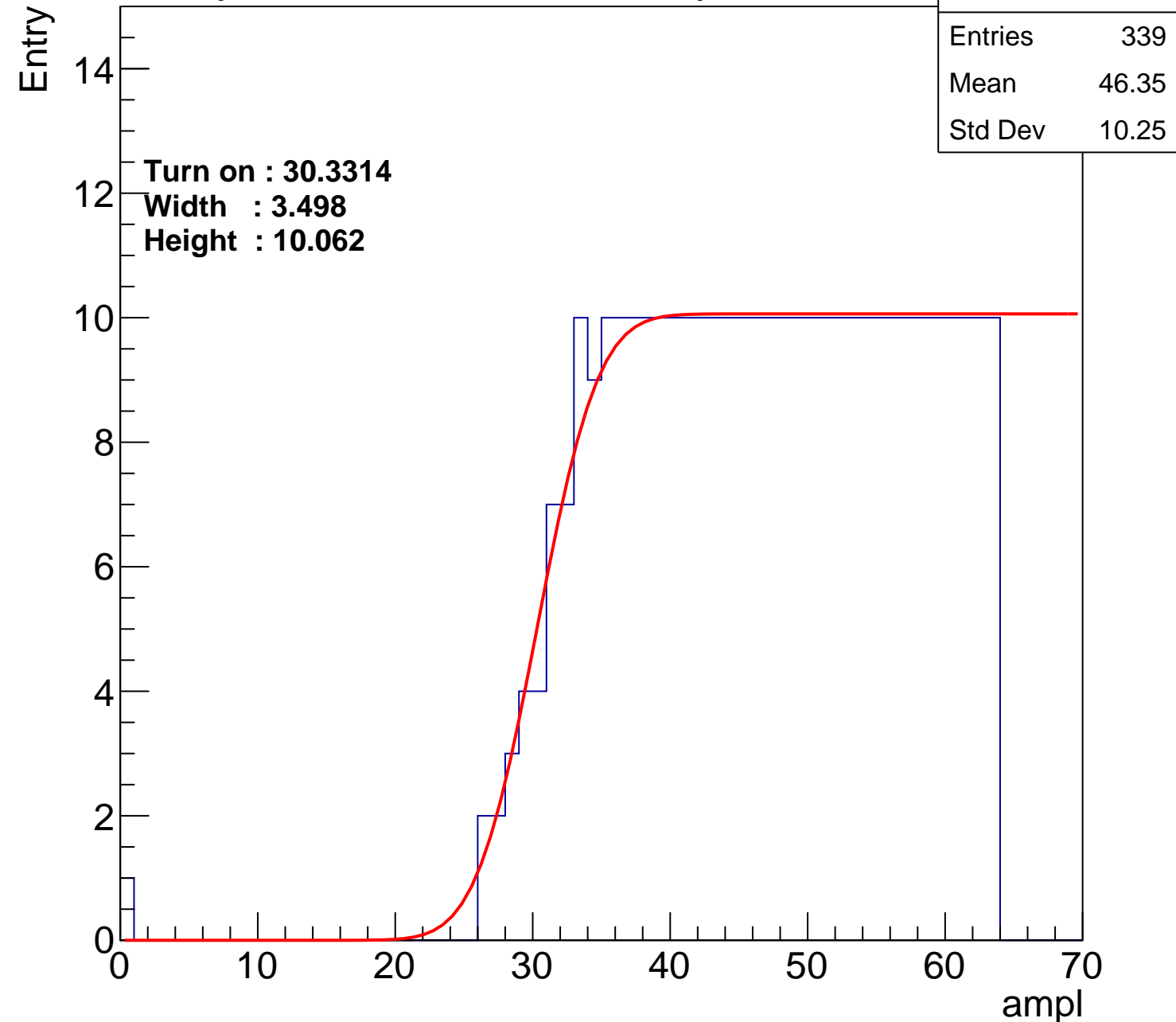
Width : 3.498

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch21

calib_packv5_042523_0143.root, FC#5, port B1

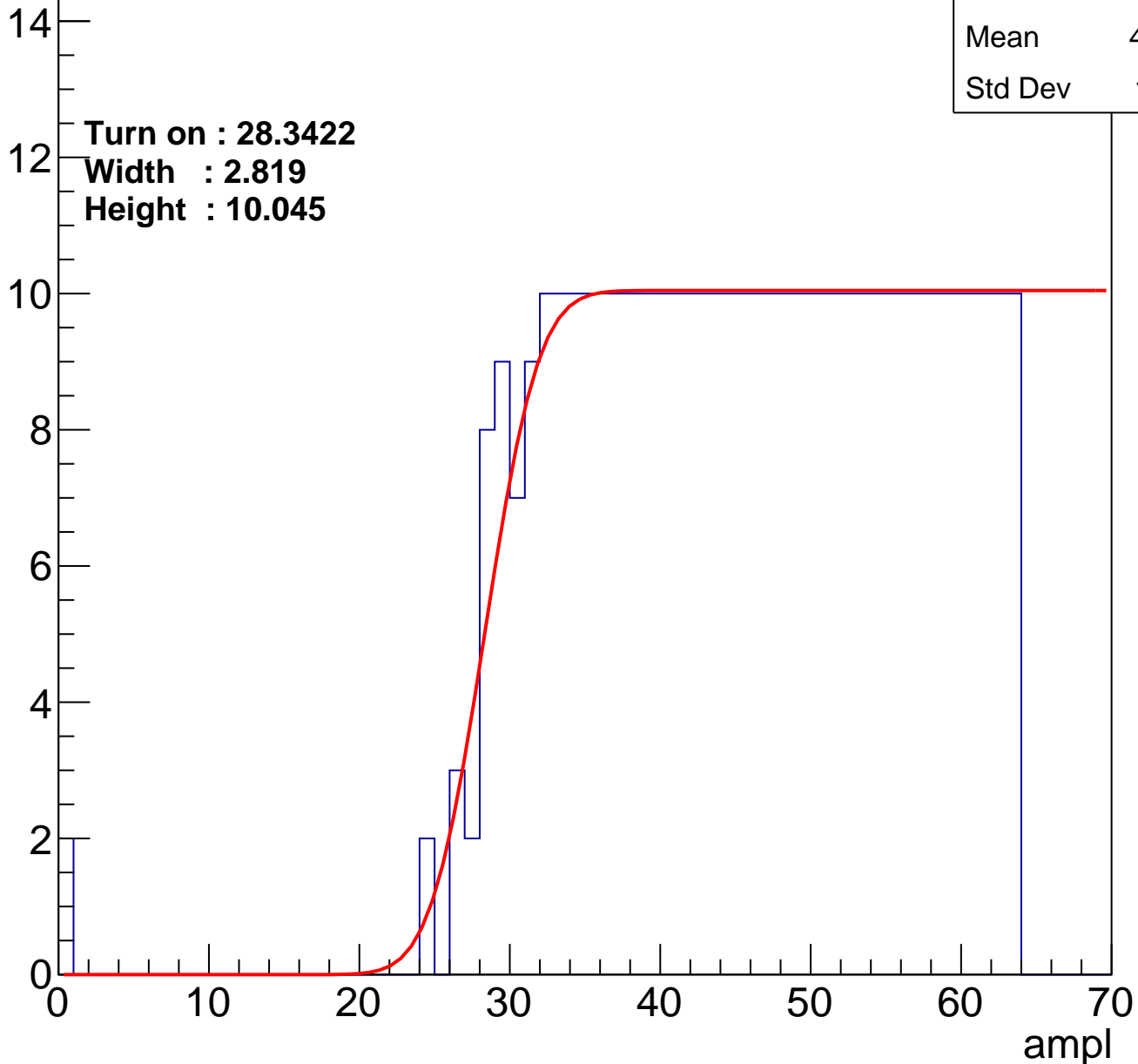
Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 28.3422

Width : 2.819

Height : 10.045

Entry



B0L000S, U13-ch22

calib_packv5_042523_0143.root, FC#5, port B1

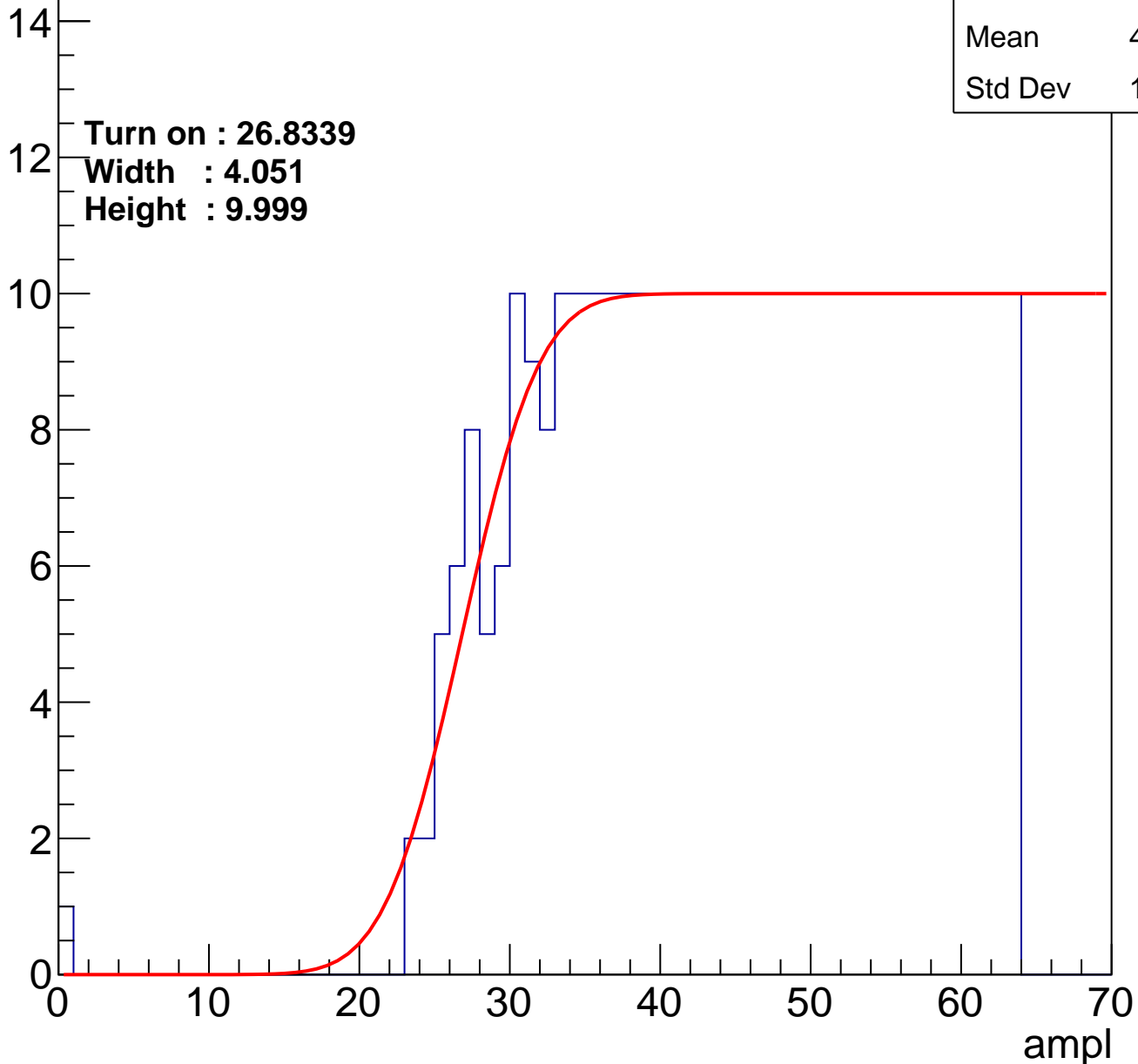
Entries	372
Mean	44.68
Std Dev	11.18

Turn on : 26.8339

Width : 4.051

Height : 9.999

Entry



B0L000S, U13-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.87
Std Dev	11.38

Turn on : 27.5131

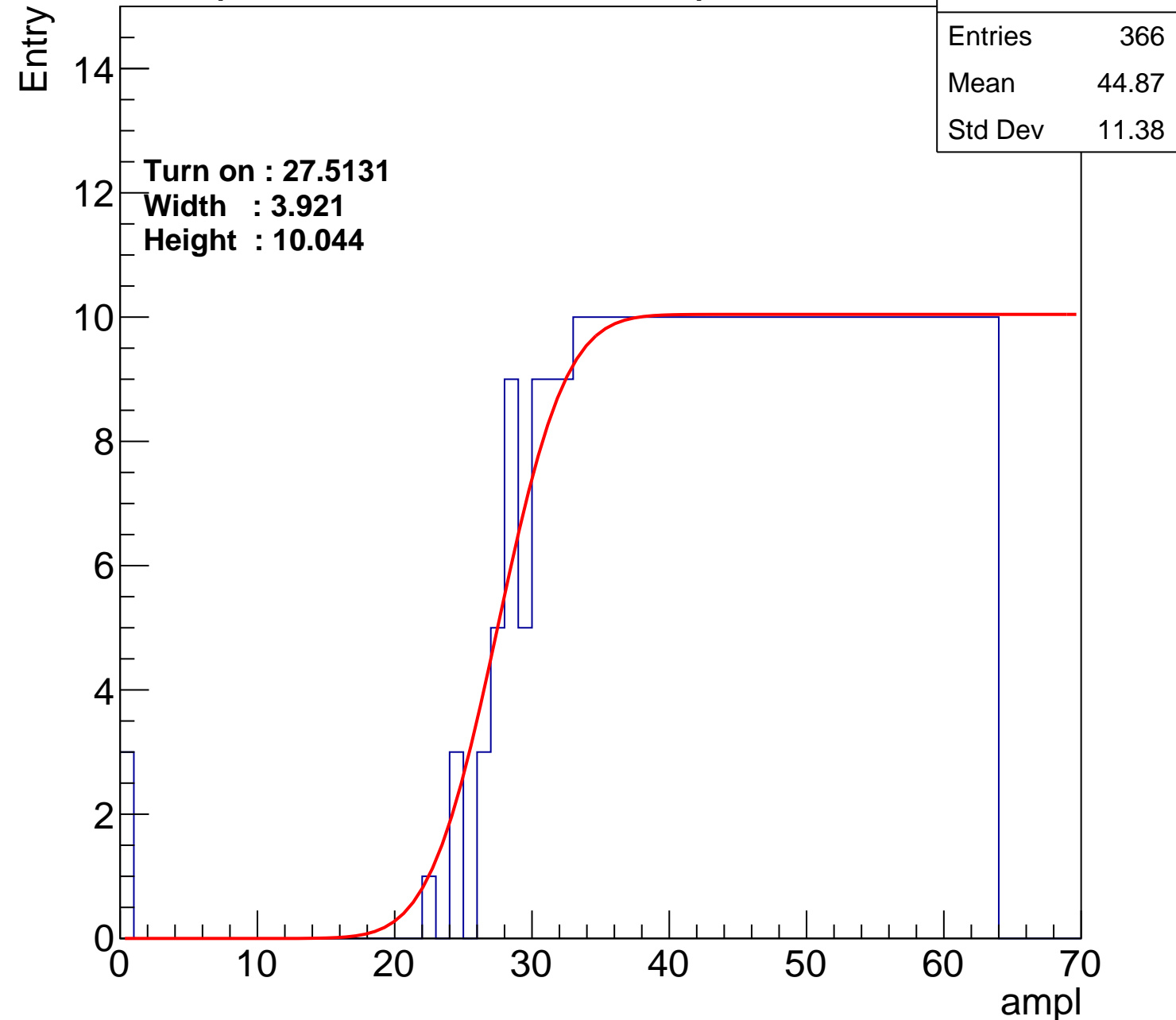
Width : 3.921

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch24

calib_packv5_042523_0143.root, FC#5, port B1

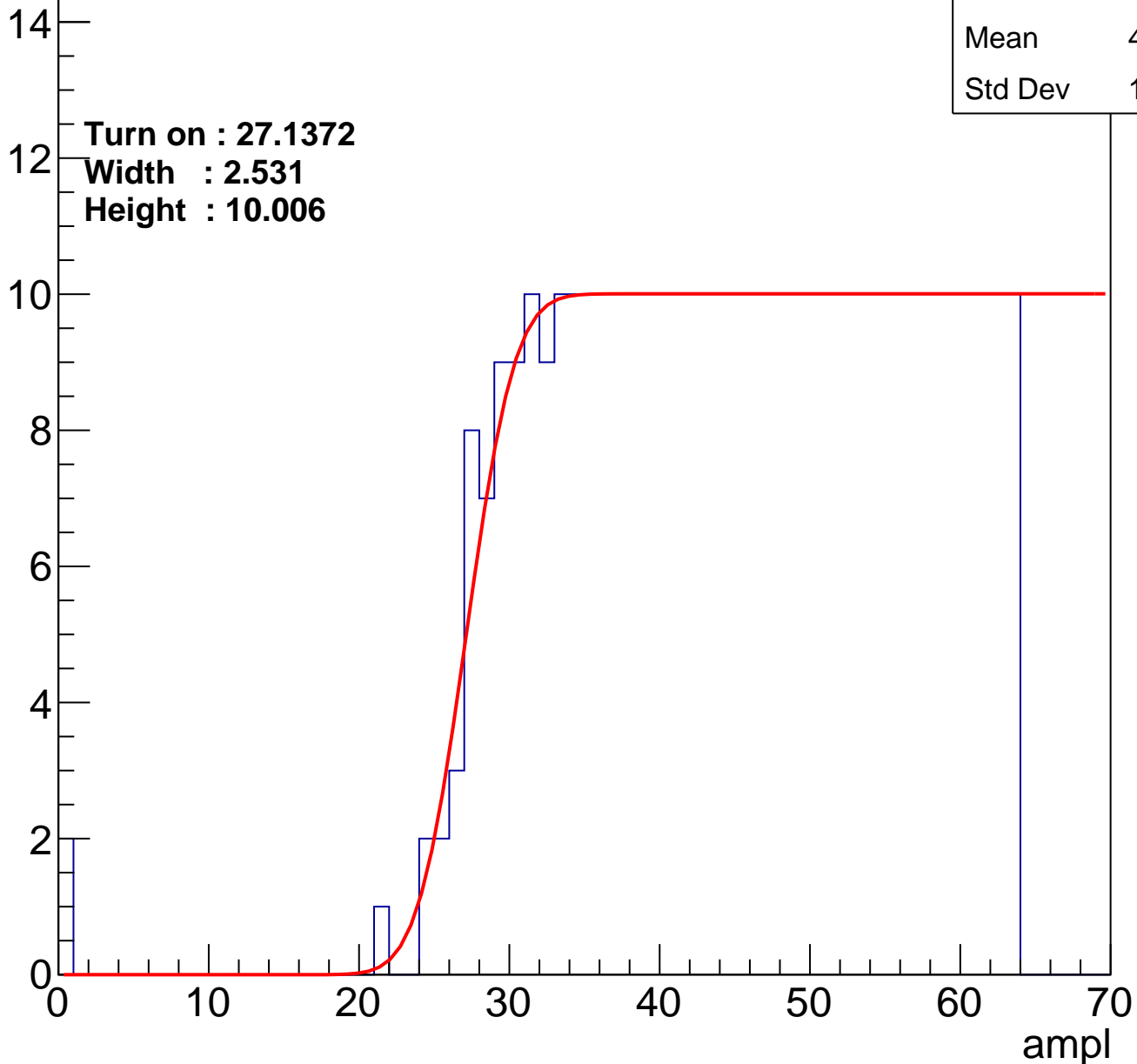
Entries	372
Mean	44.67
Std Dev	11.28

Turn on : 27.1372

Width : 2.531

Height : 10.006

Entry



B0L000S, U13-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.97
Std Dev	11.32

Turn on : 28.0313

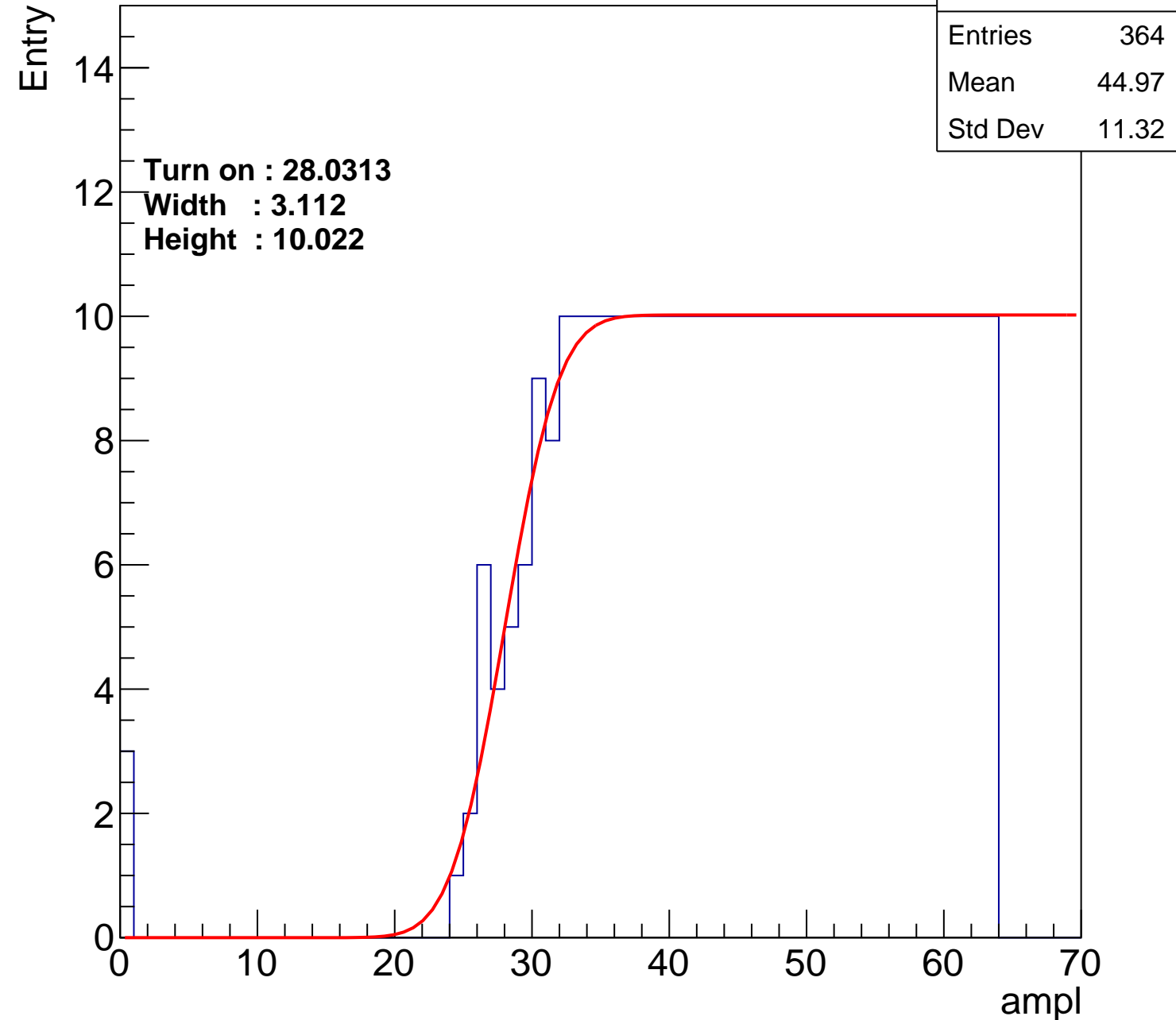
Width : 3.112

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch26

calib_packv5_042523_0143.root, FC#5, port B1

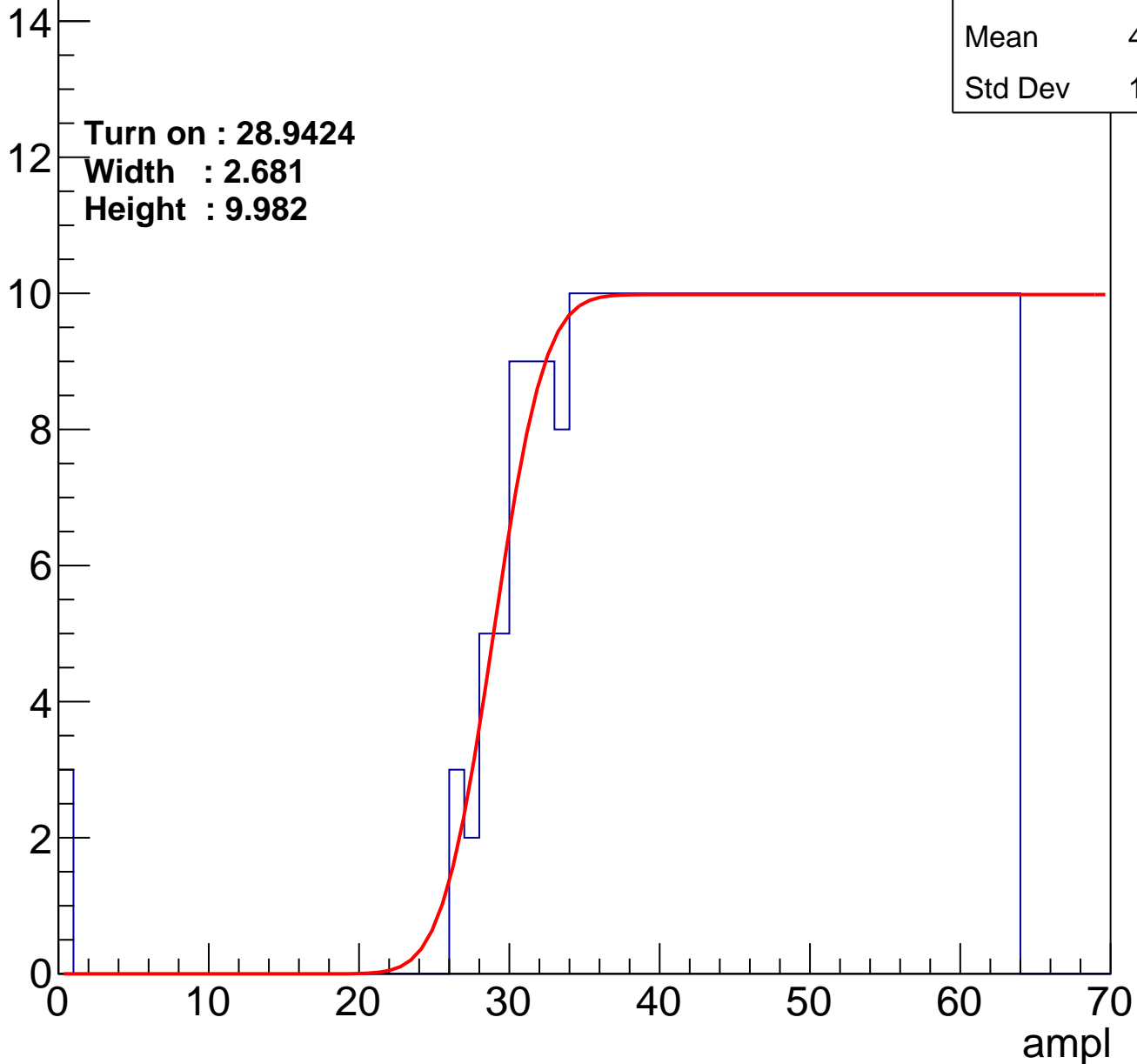
Entries	353
Mean	45.52
Std Dev	11.04

Turn on : 28.9424

Width : 2.681

Height : 9.982

Entry



B0L000S, U13-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.1597

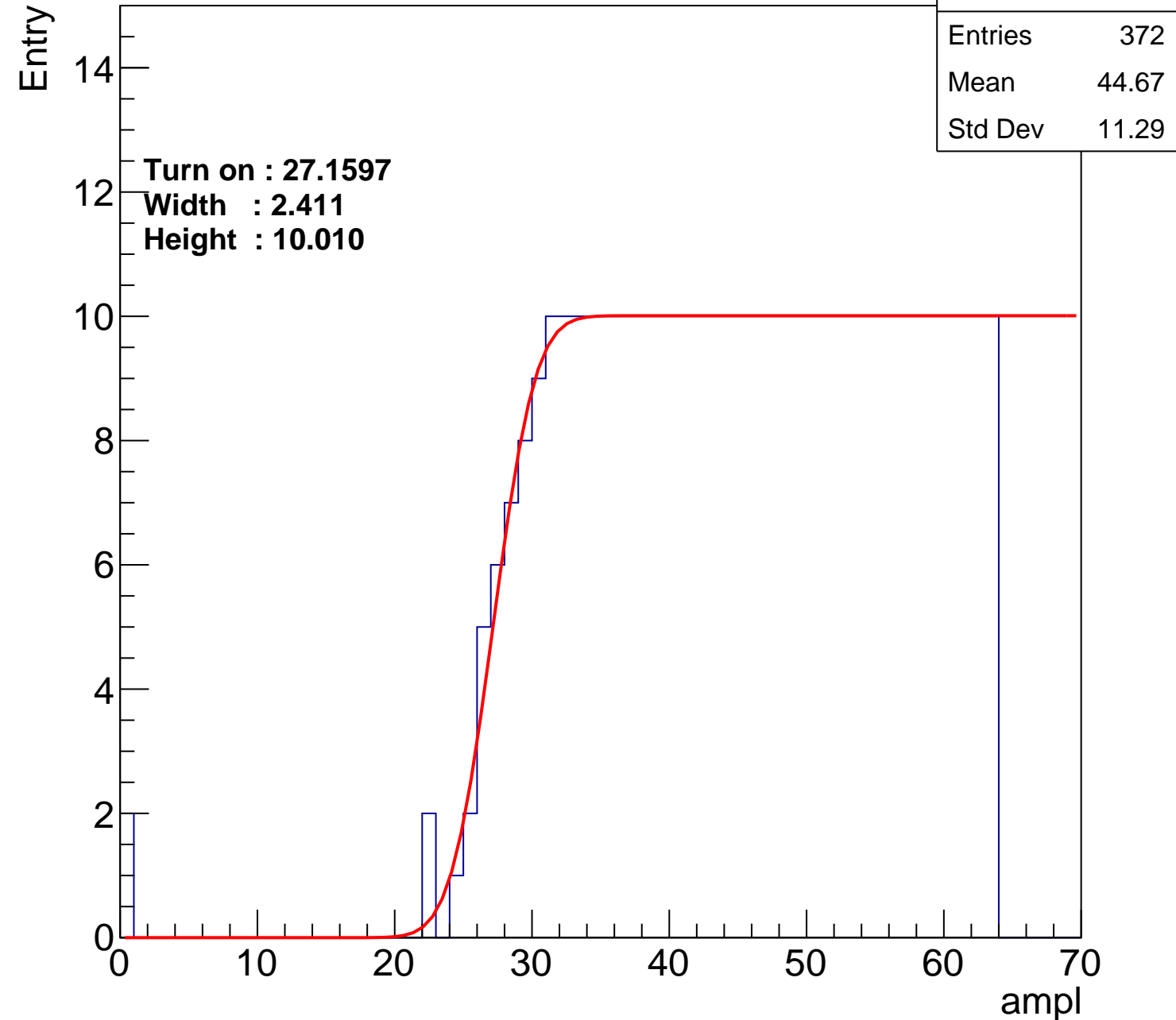
Width : 2.411

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	351
Mean	45.77
Std Dev	10.54

Turn on : 29.3143

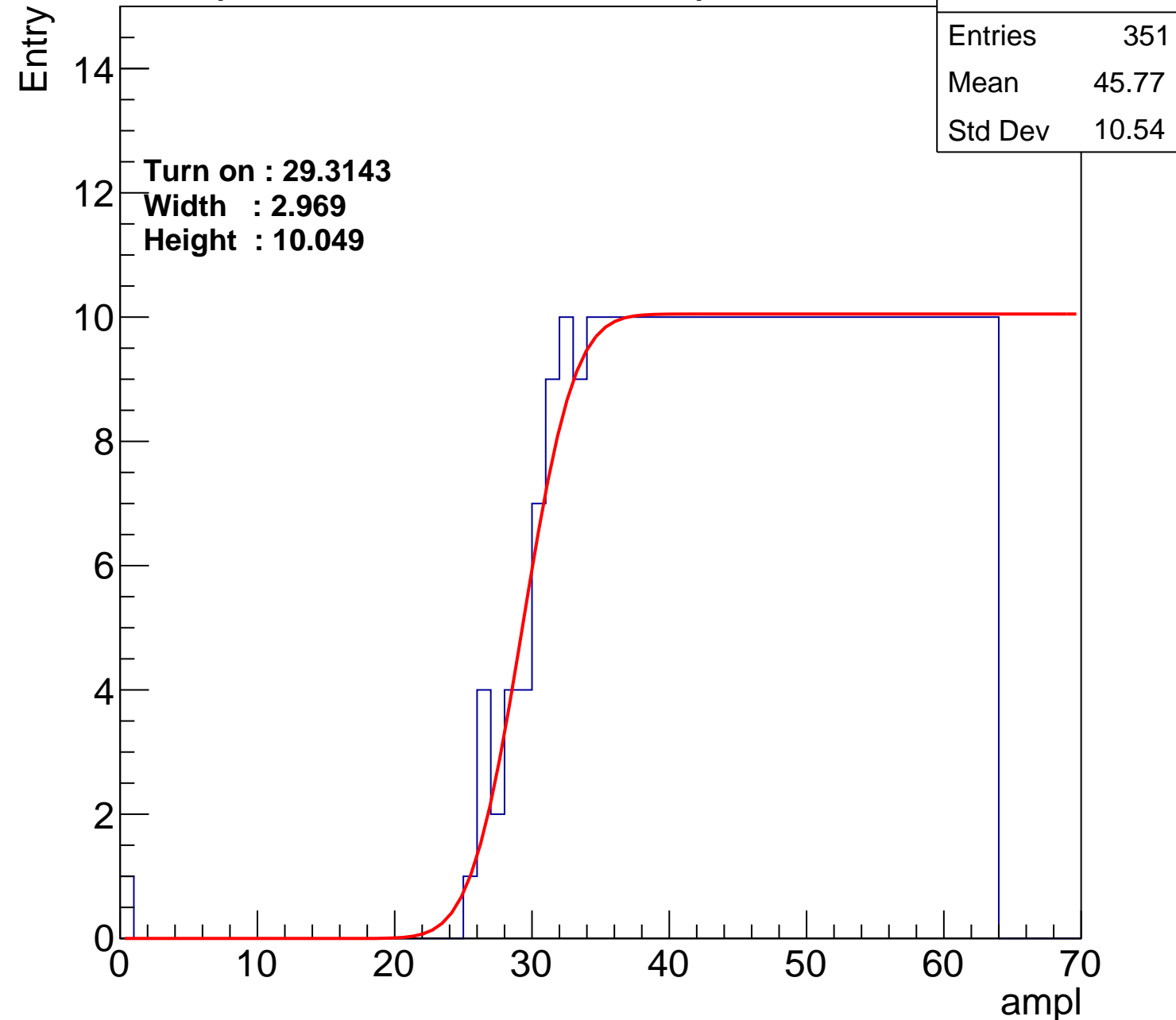
Width : 2.969

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch29

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.28
Std Dev	10.87

Turn on : 28.6797

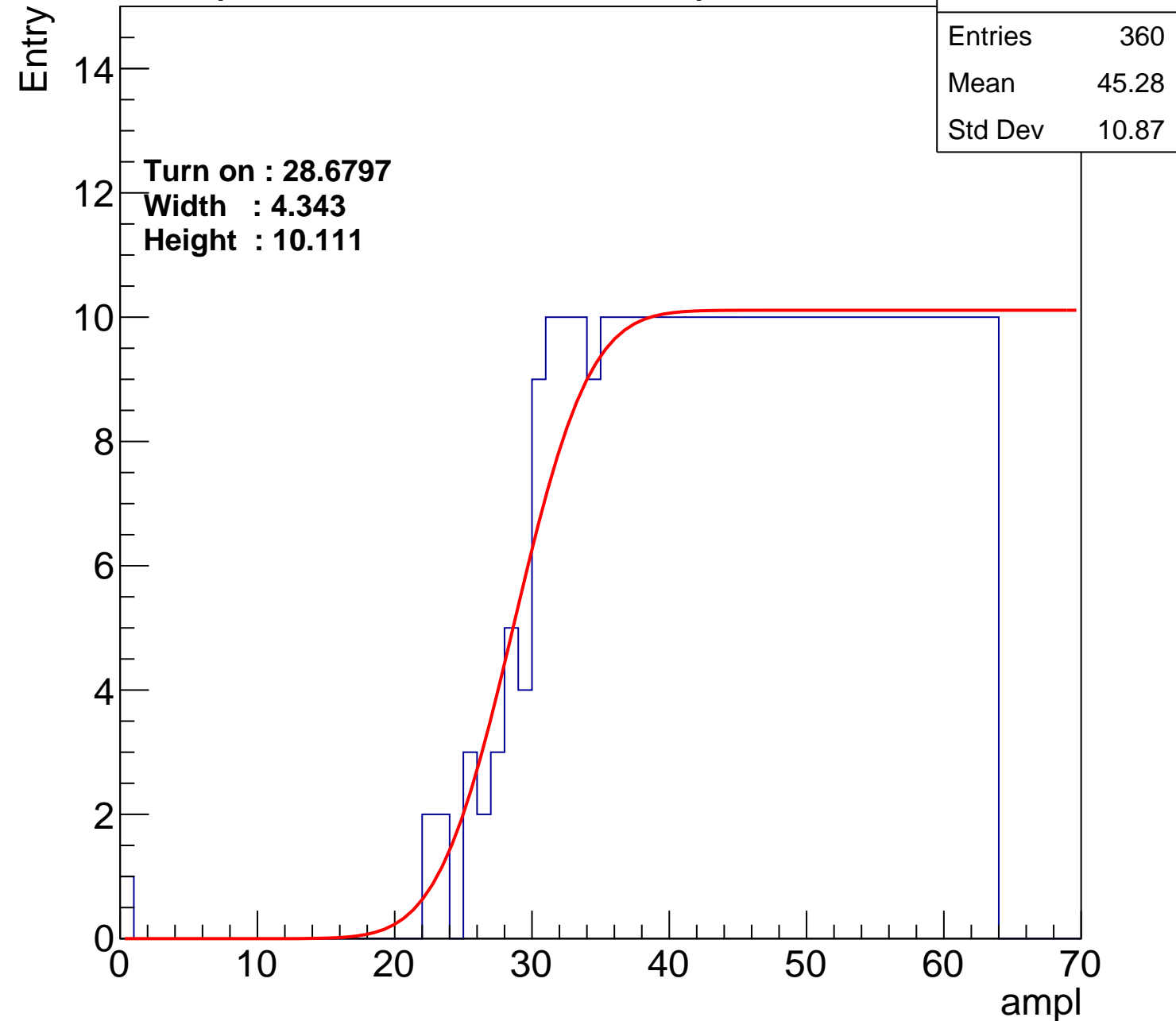
Width : 4.343

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.35
Std Dev	10.89

Turn on : 28.1787

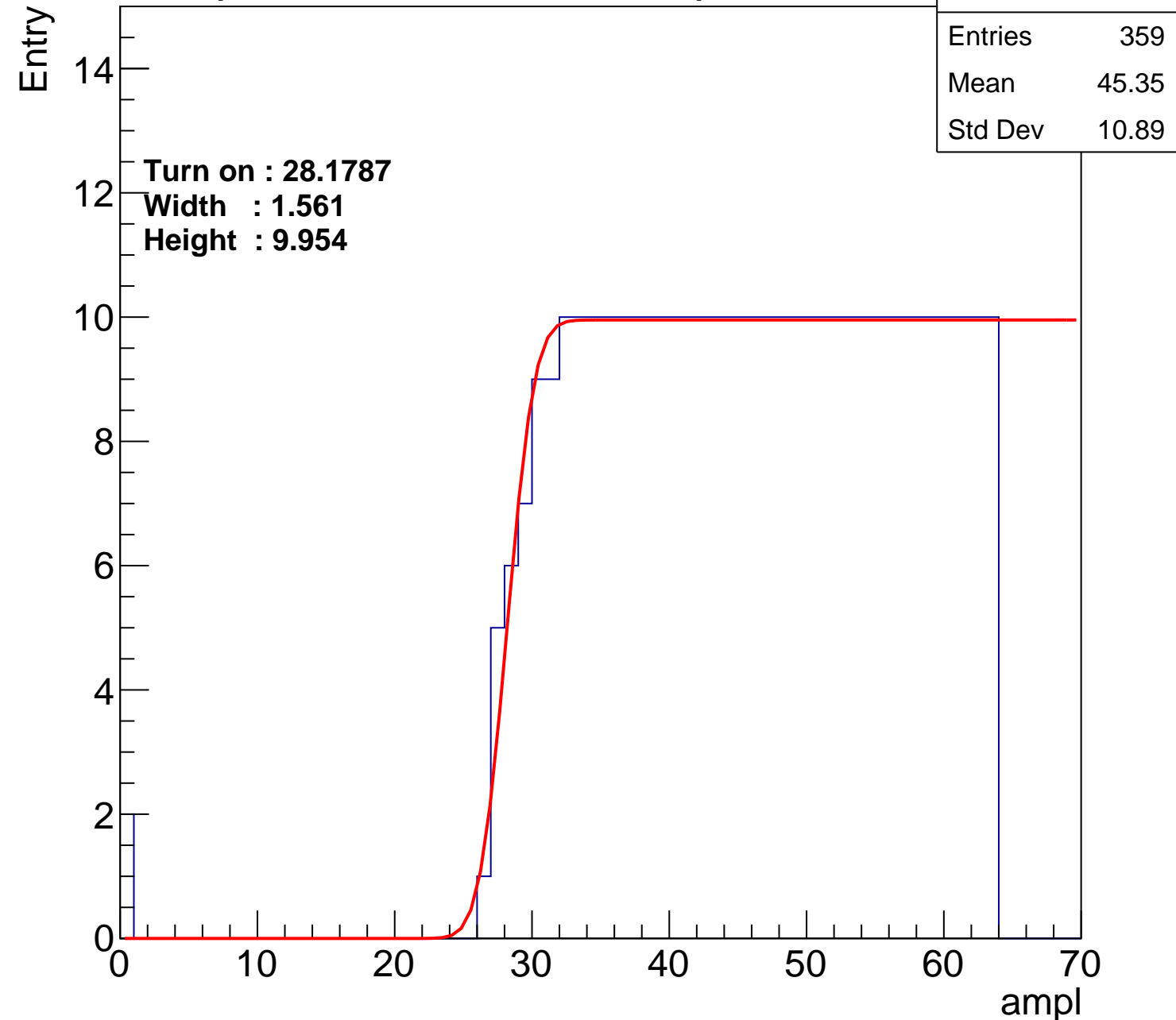
Width : 1.561

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch31

calib_packv5_042523_0143.root, FC#5, port B1

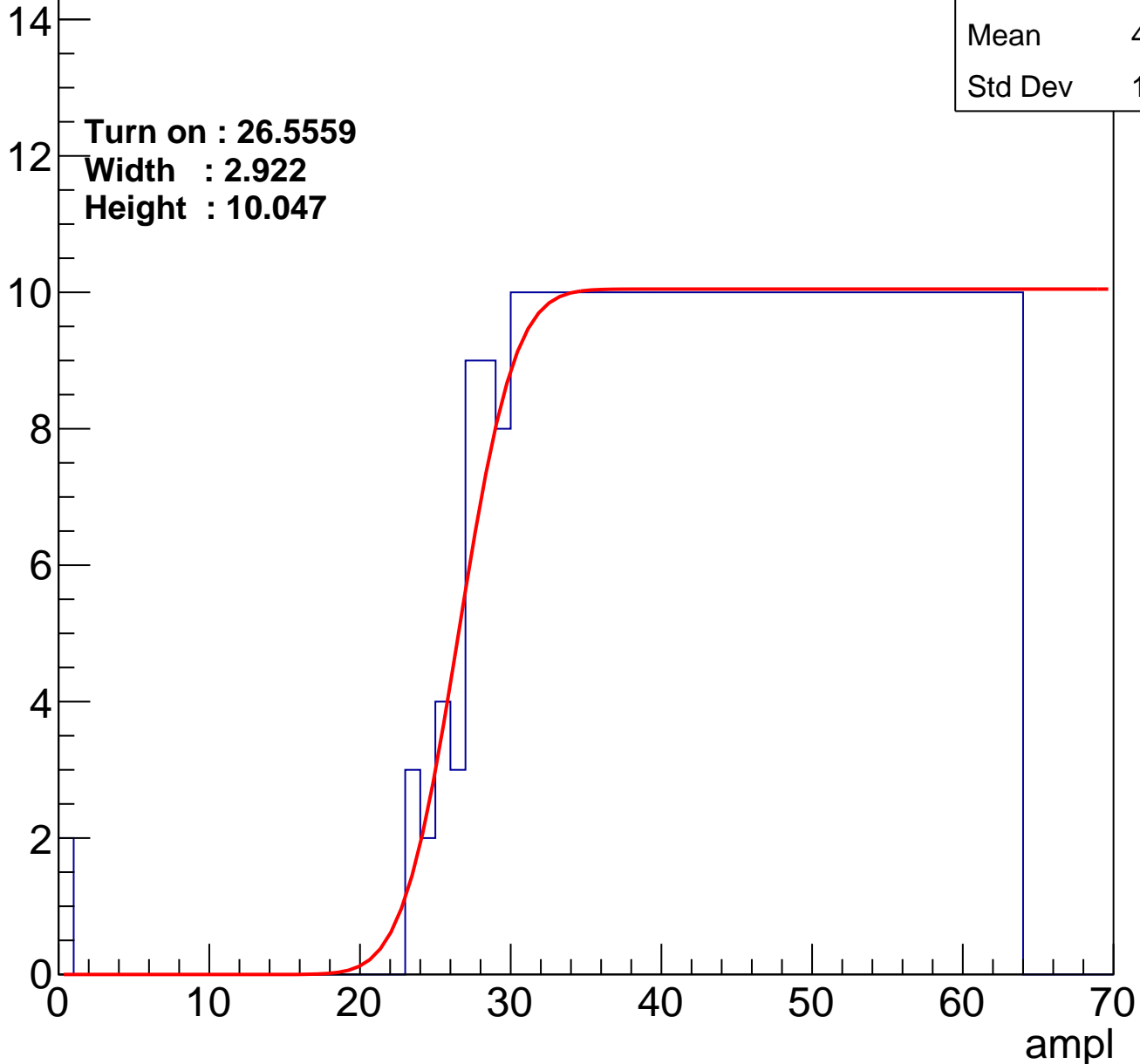
Entries	380
Mean	44.29
Std Dev	11.46

Turn on : 26.5559

Width : 2.922

Height : 10.047

Entry



B0L000S, U13-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.65
Std Dev	11.59

Turn on : 27.6171

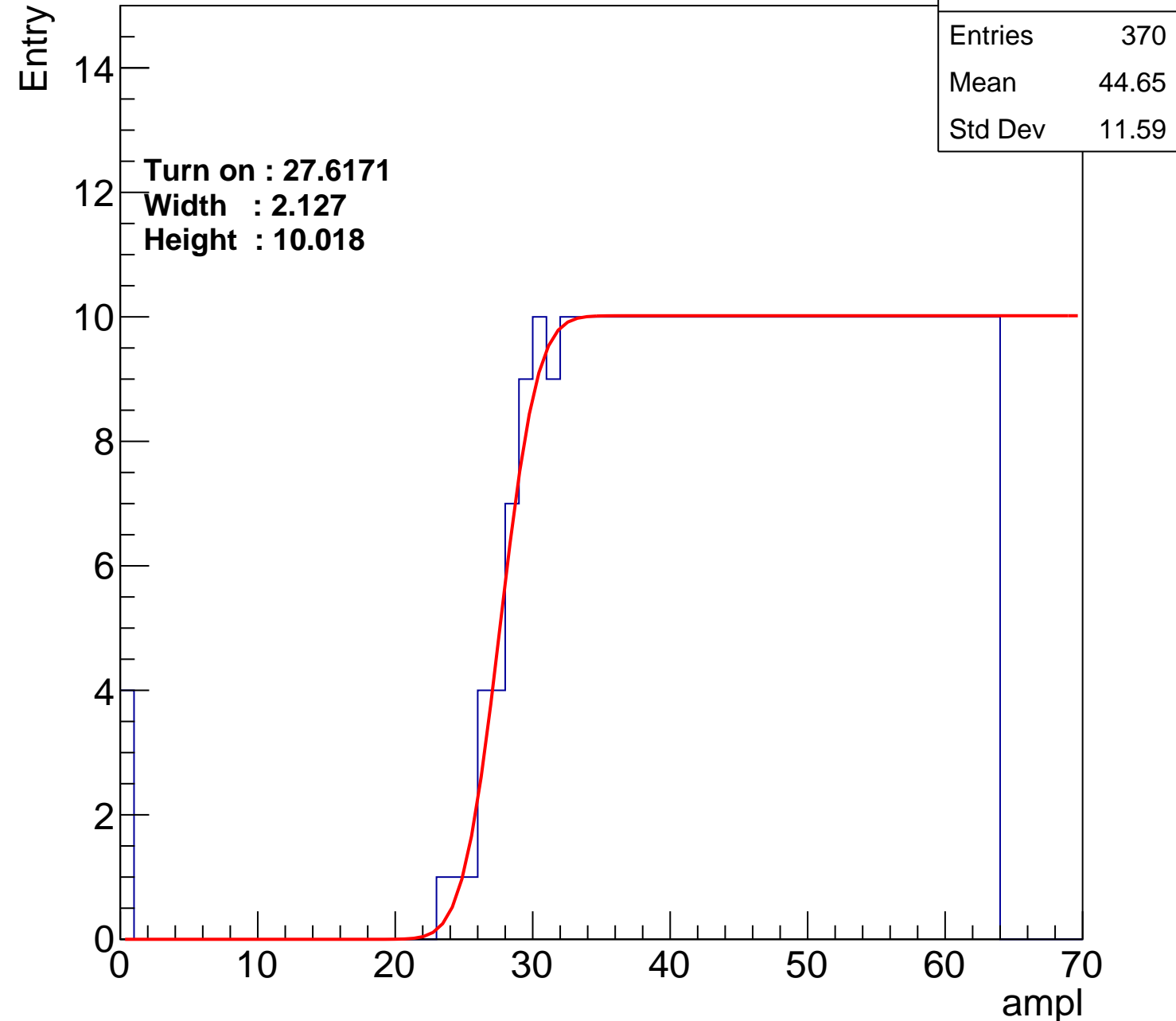
Width : 2.127

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch33

calib_packv5_042523_0143.root, FC#5, port B1

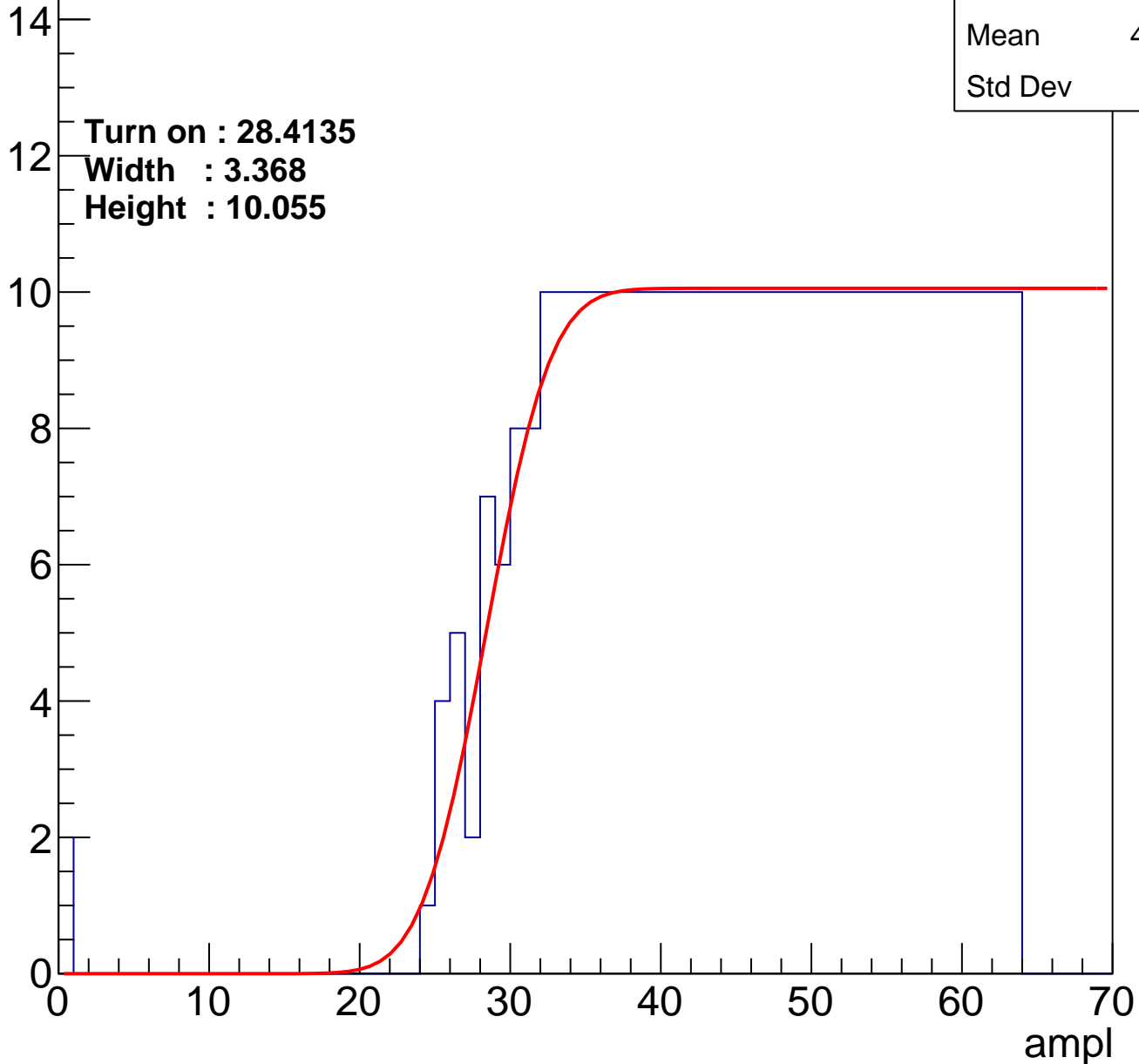
Entries	363
Mean	45.09
Std Dev	11.1

Turn on : 28.4135

Width : 3.368

Height : 10.055

Entry



B0L000S, U13-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.43
Std Dev	10.92

Turn on : 28.4872

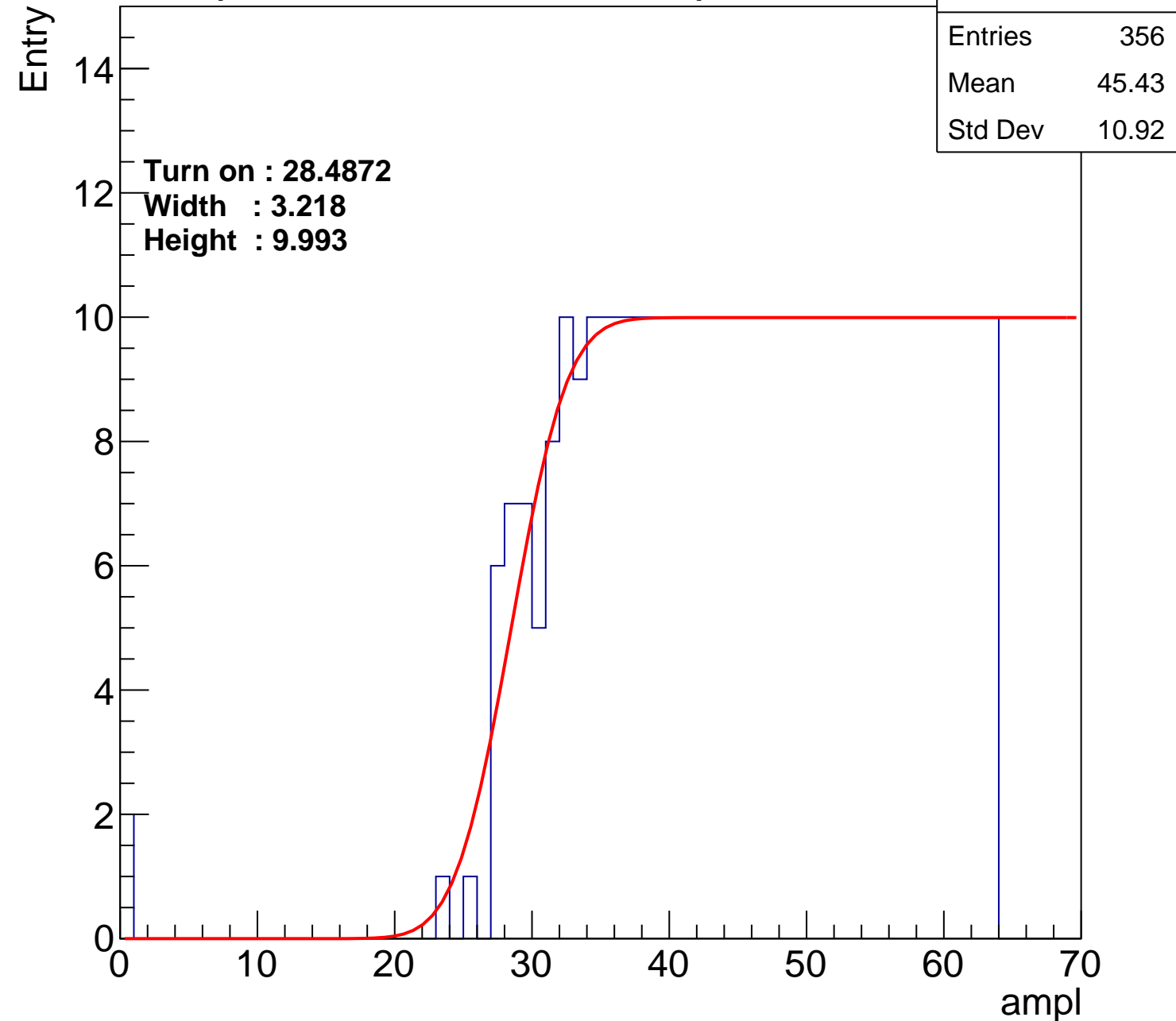
Width : 3.218

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch35

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.67
Std Dev	11.62

Turn on : 28.3396

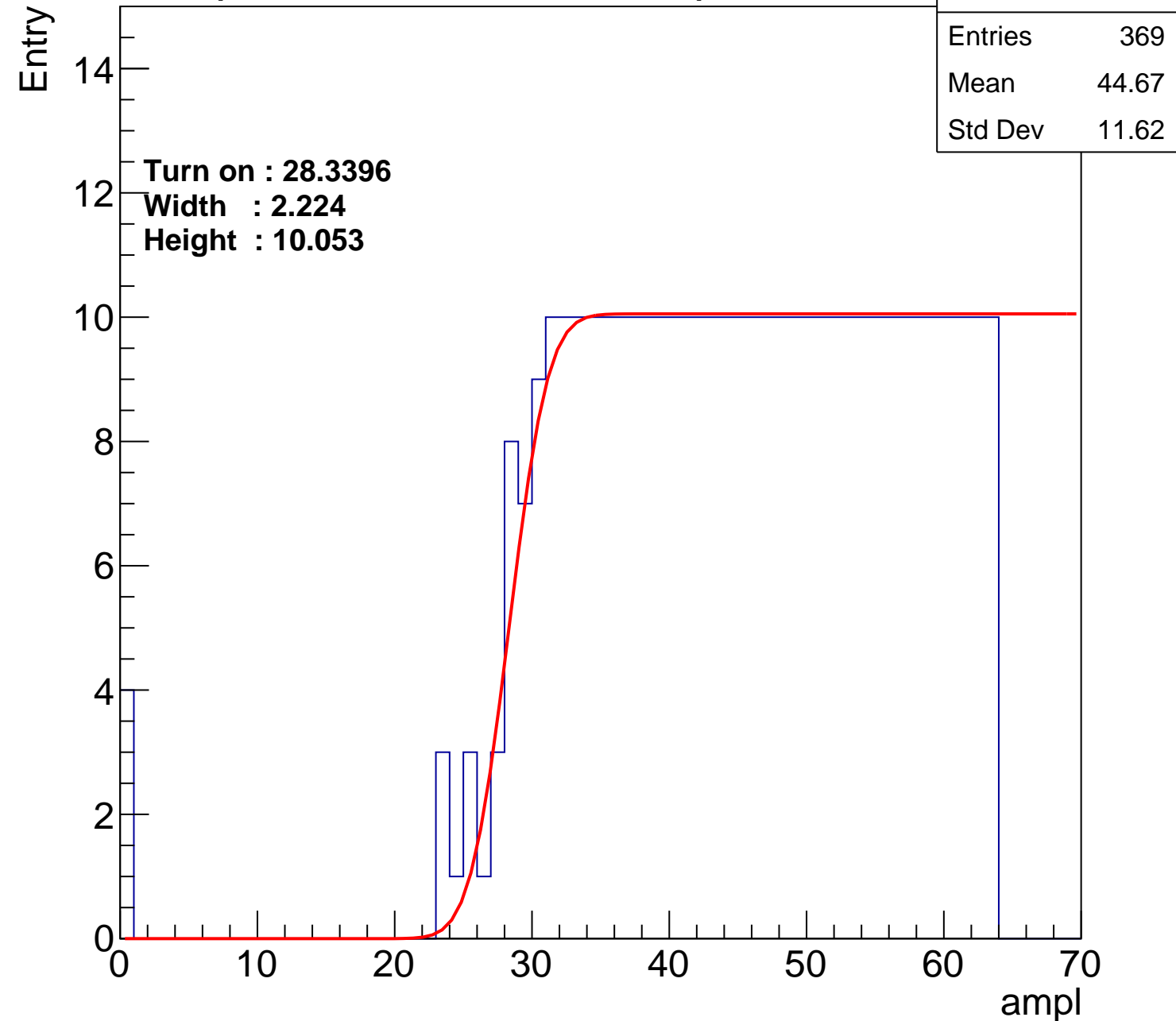
Width : 2.224

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch36

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9333

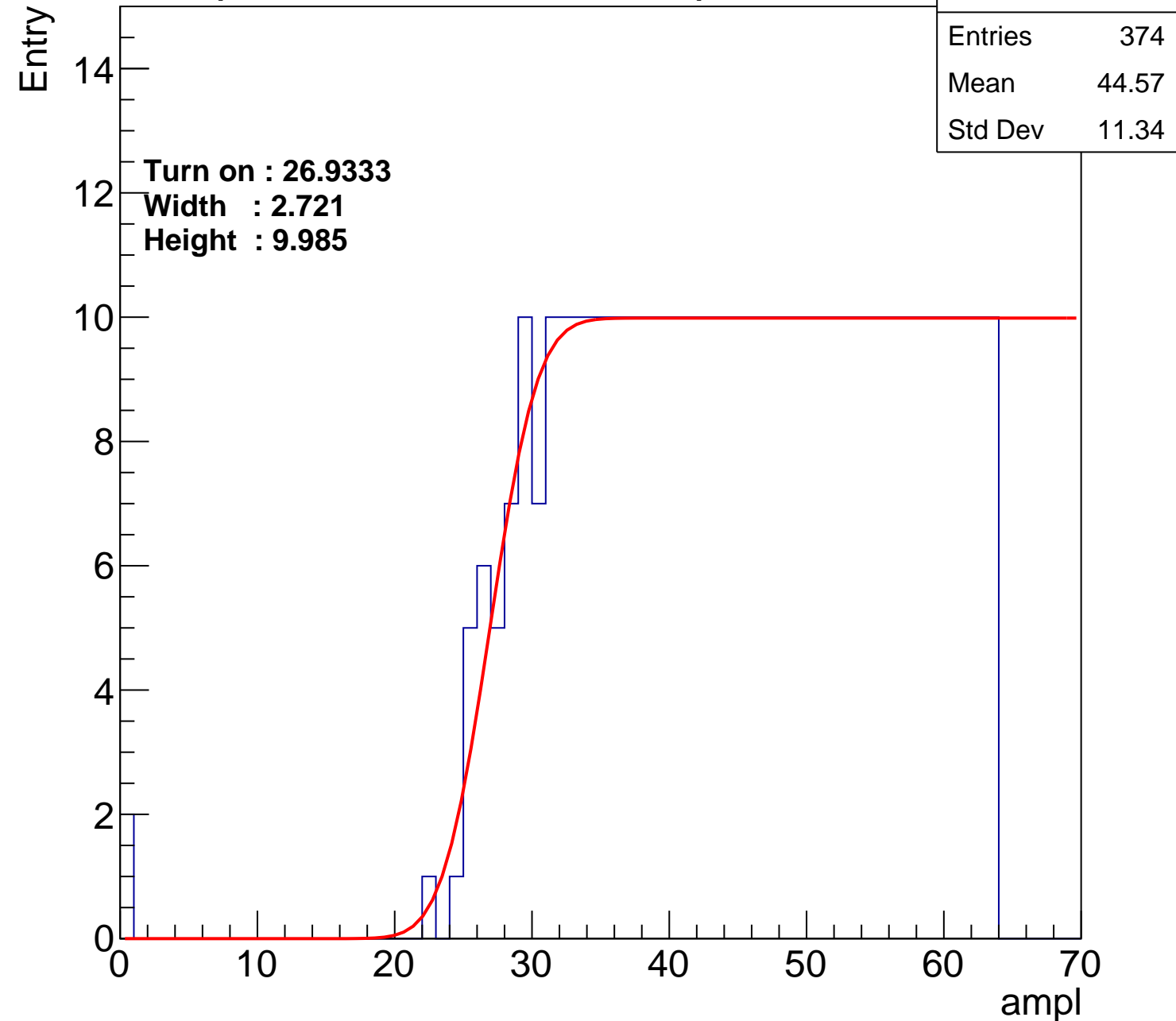
Width : 2.721

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch37

calib_packv5_042523_0143.root, FC#5, port B1

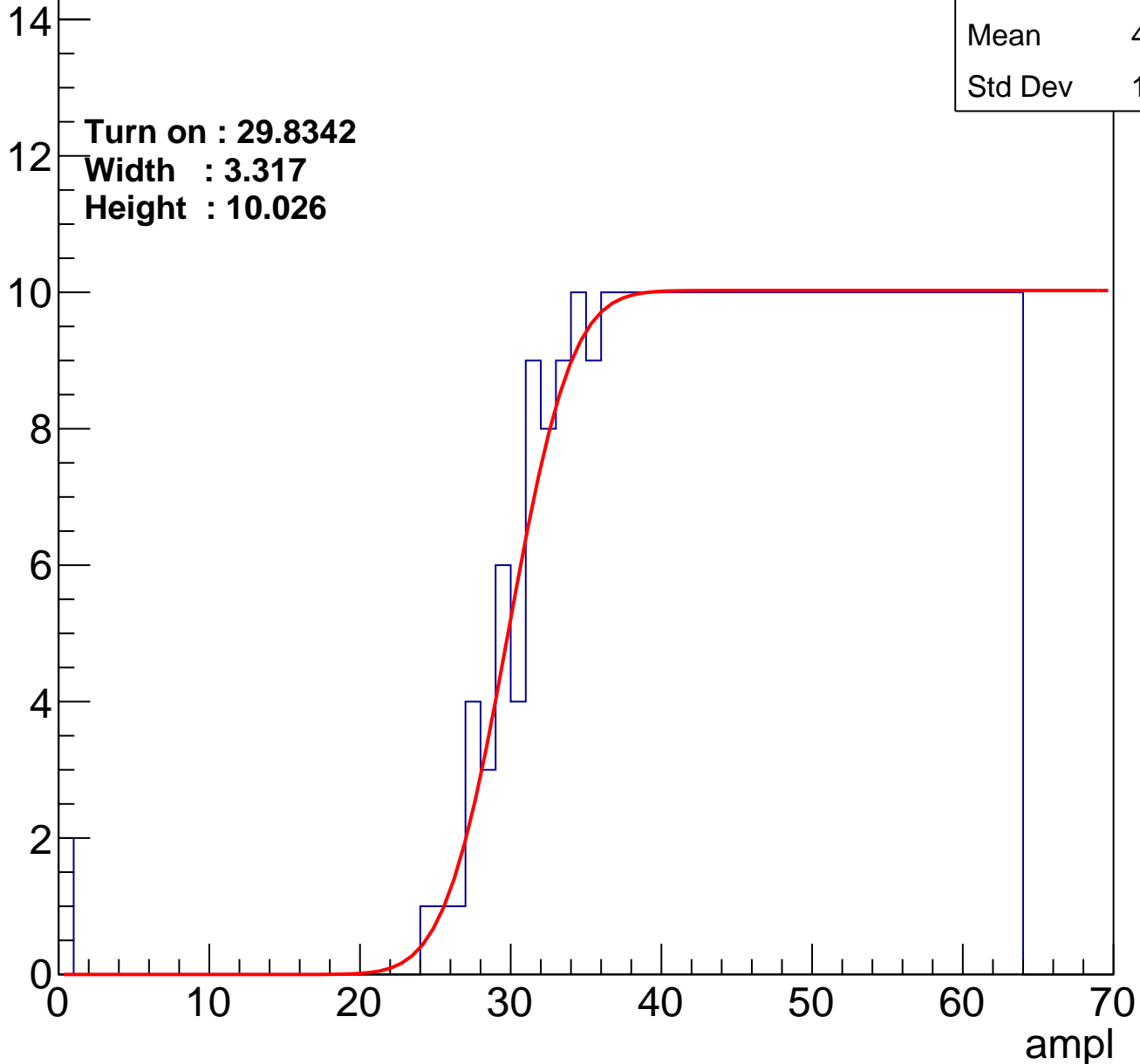
Entries	347
Mean	45.84
Std Dev	10.75

Turn on : 29.8342

Width : 3.317

Height : 10.026

Entry



B0L000S, U13-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.3718

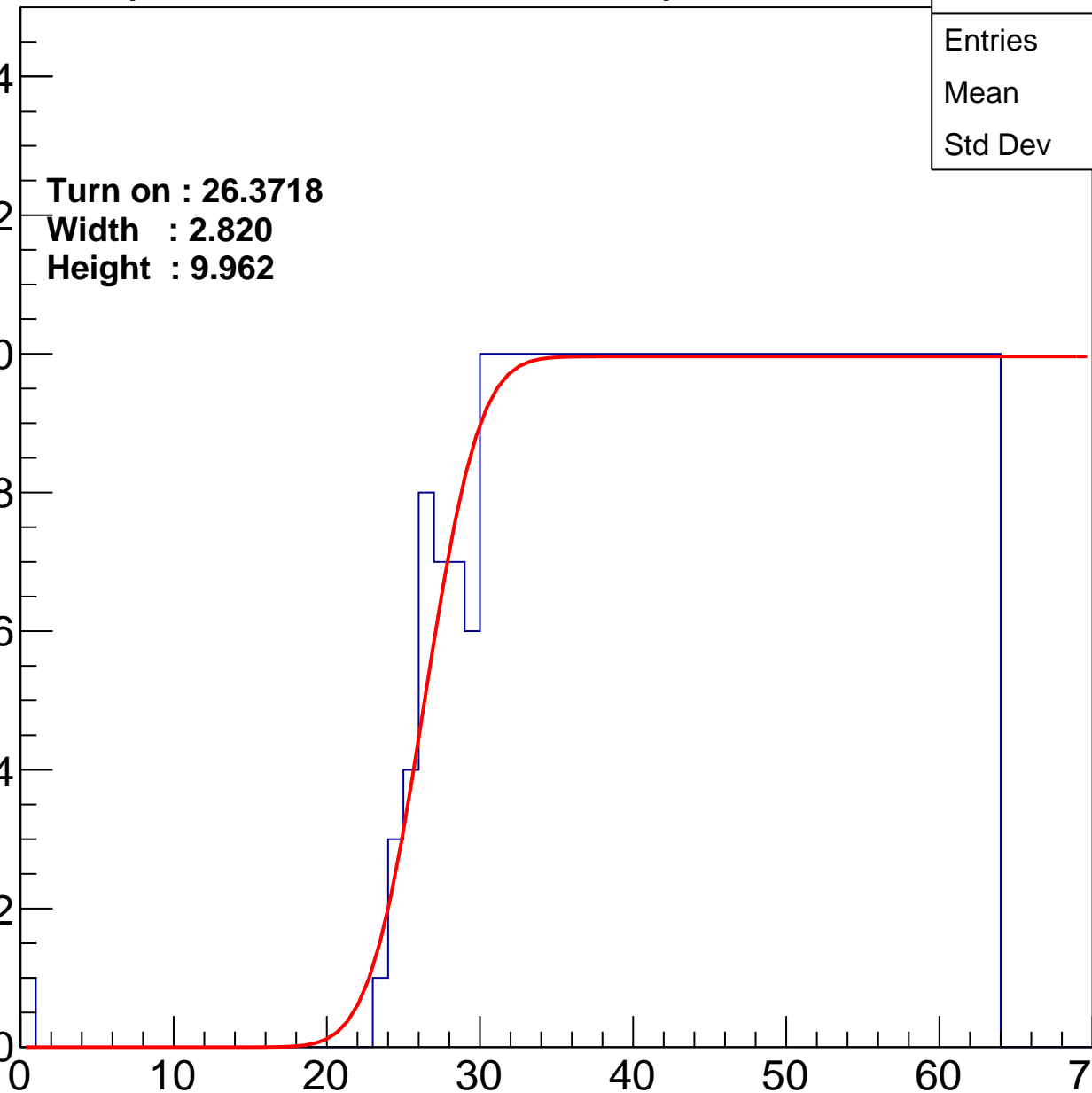
Width : 2.820

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch39

calib_packv5_042523_0143.root, FC#5, port B1

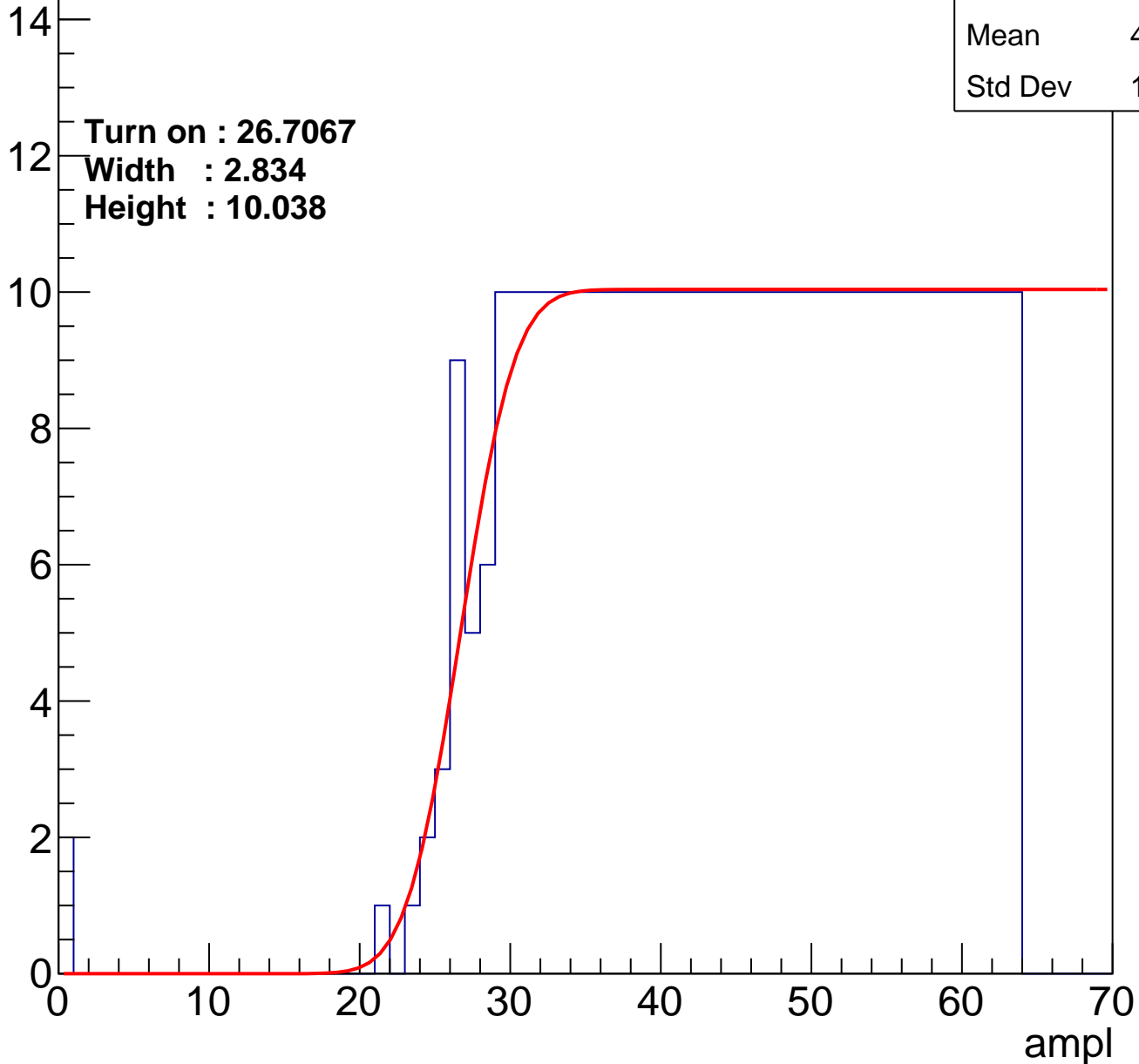
Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.7067

Width : 2.834

Height : 10.038

Entry



B0L000S, U13-ch40

calib_packv5_042523_0143.root, FC#5, port B1

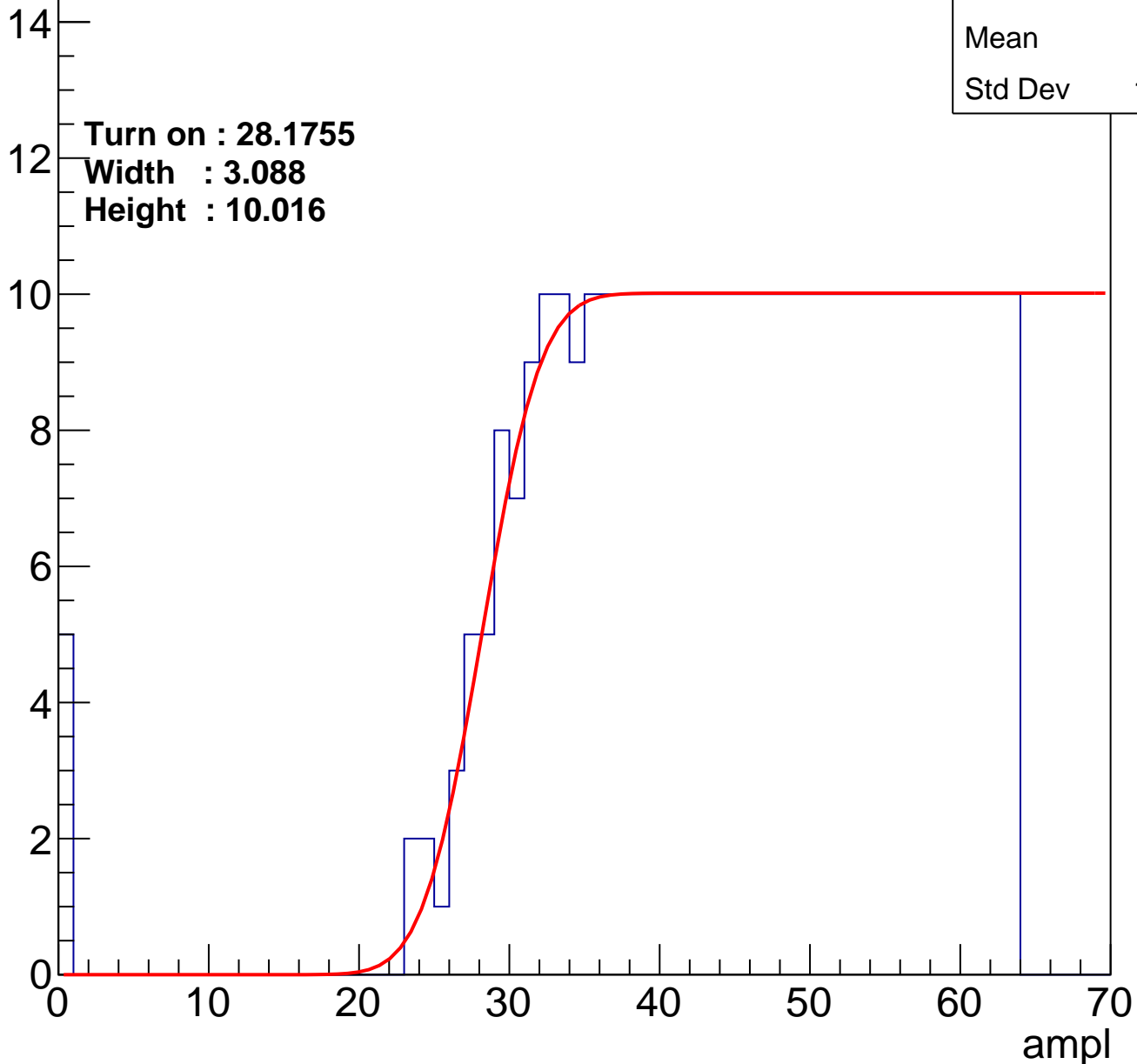
Entries	366
Mean	44.7
Std Dev	11.81

Turn on : 28.1755

Width : 3.088

Height : 10.016

Entry



B0L000S, U13-ch41

calib_packv5_042523_0143.root, FC#5, port B1

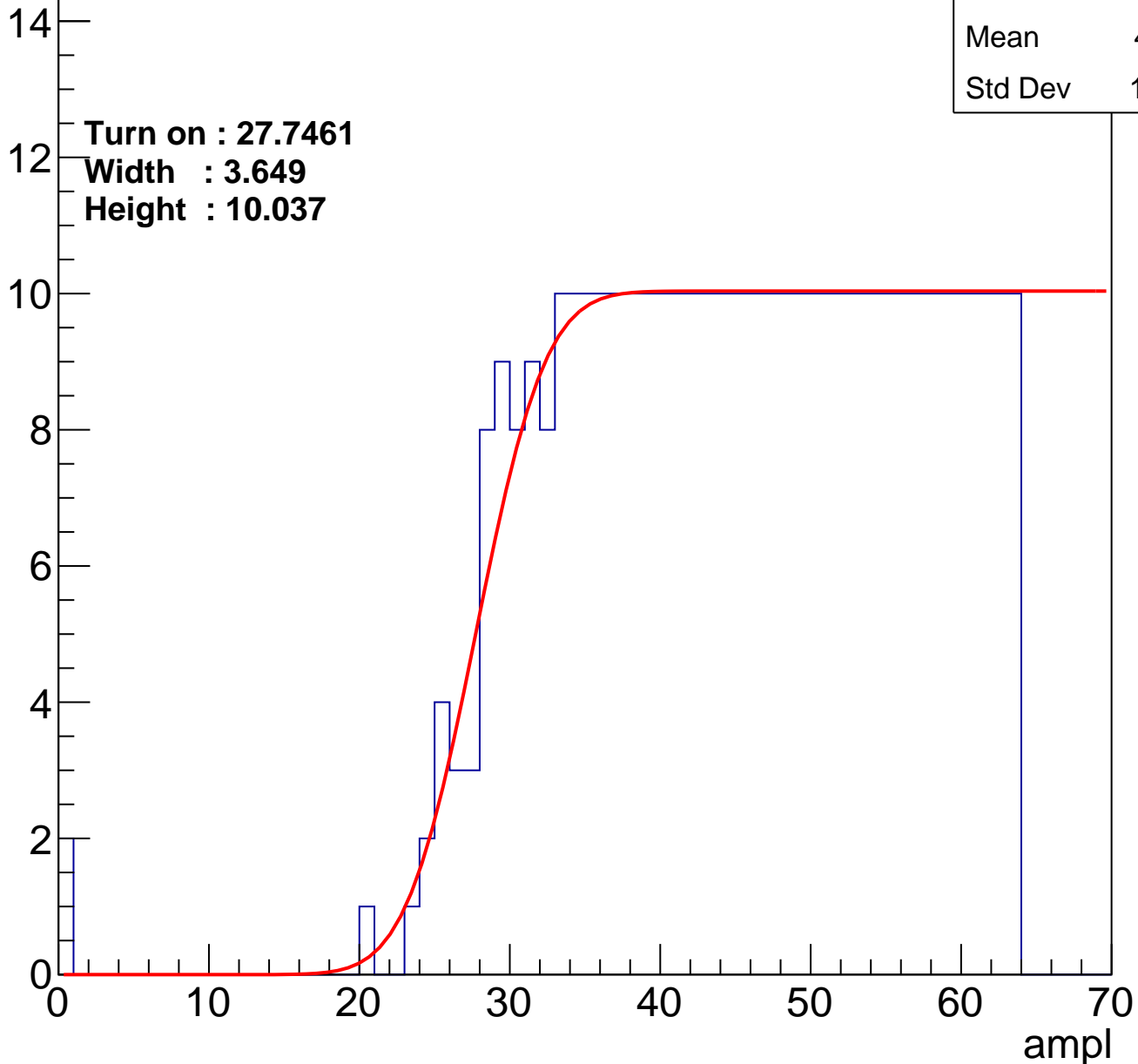
Entry

Entries	368
Mean	44.81
Std Dev	11.28

Turn on : 27.7461

Width : 3.649

Height : 10.037



B0L000S, U13-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.92
Std Dev	11.36

Turn on : 28.4064

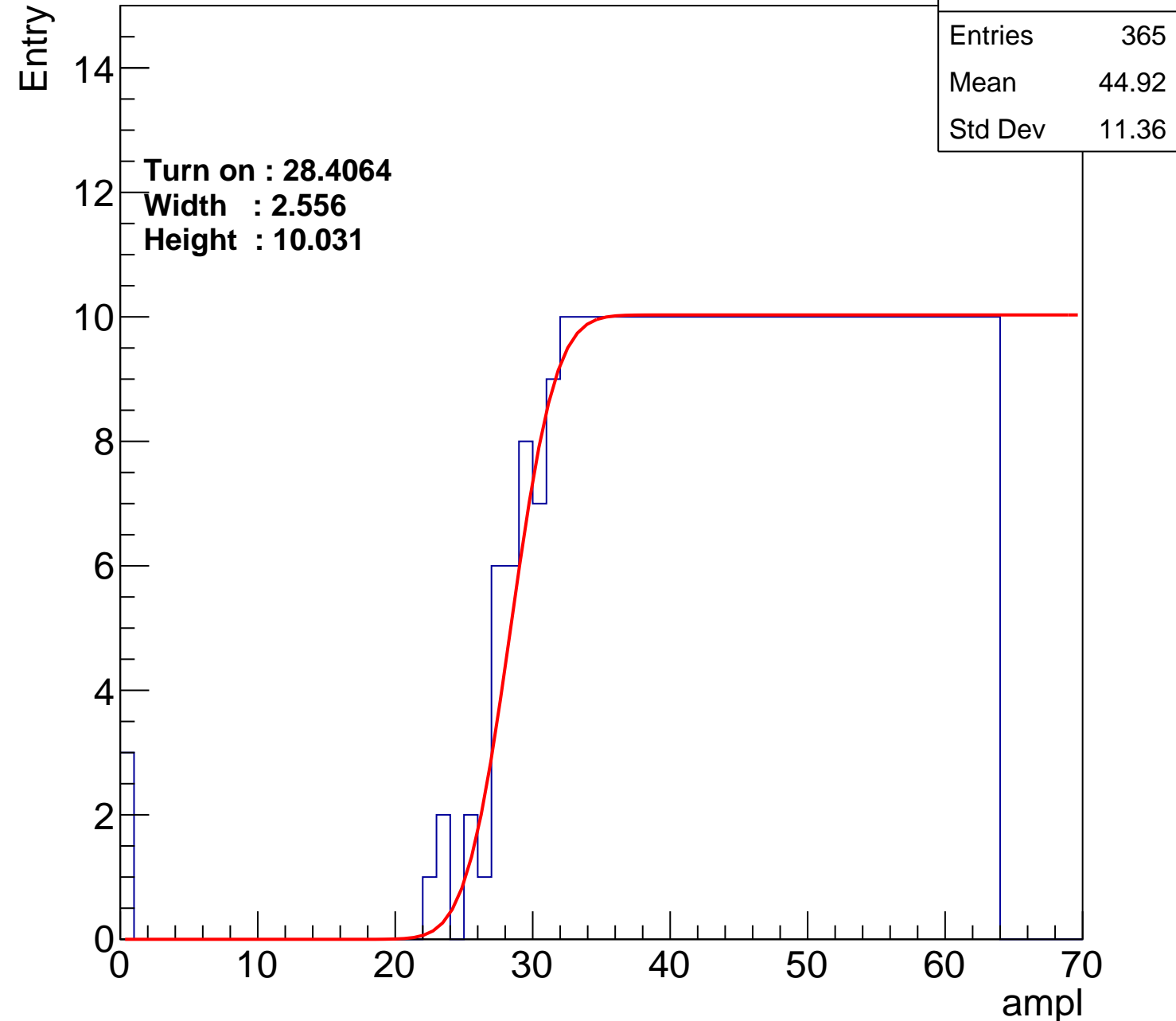
Width : 2.556

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.86
Std Dev	11.24

Turn on : 27.4752

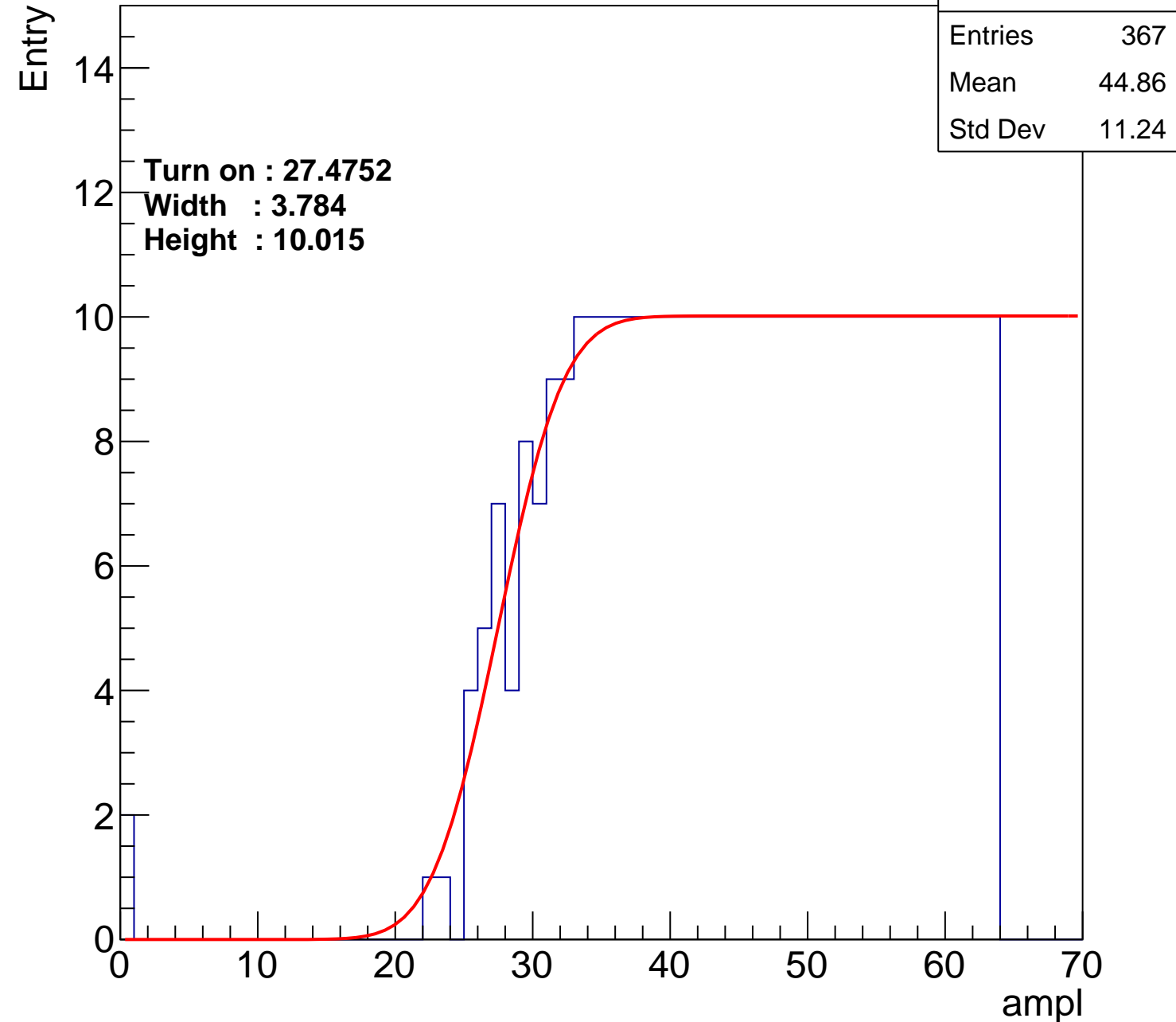
Width : 3.784

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.33
Std Dev	11.19

Turn on : 29.0351

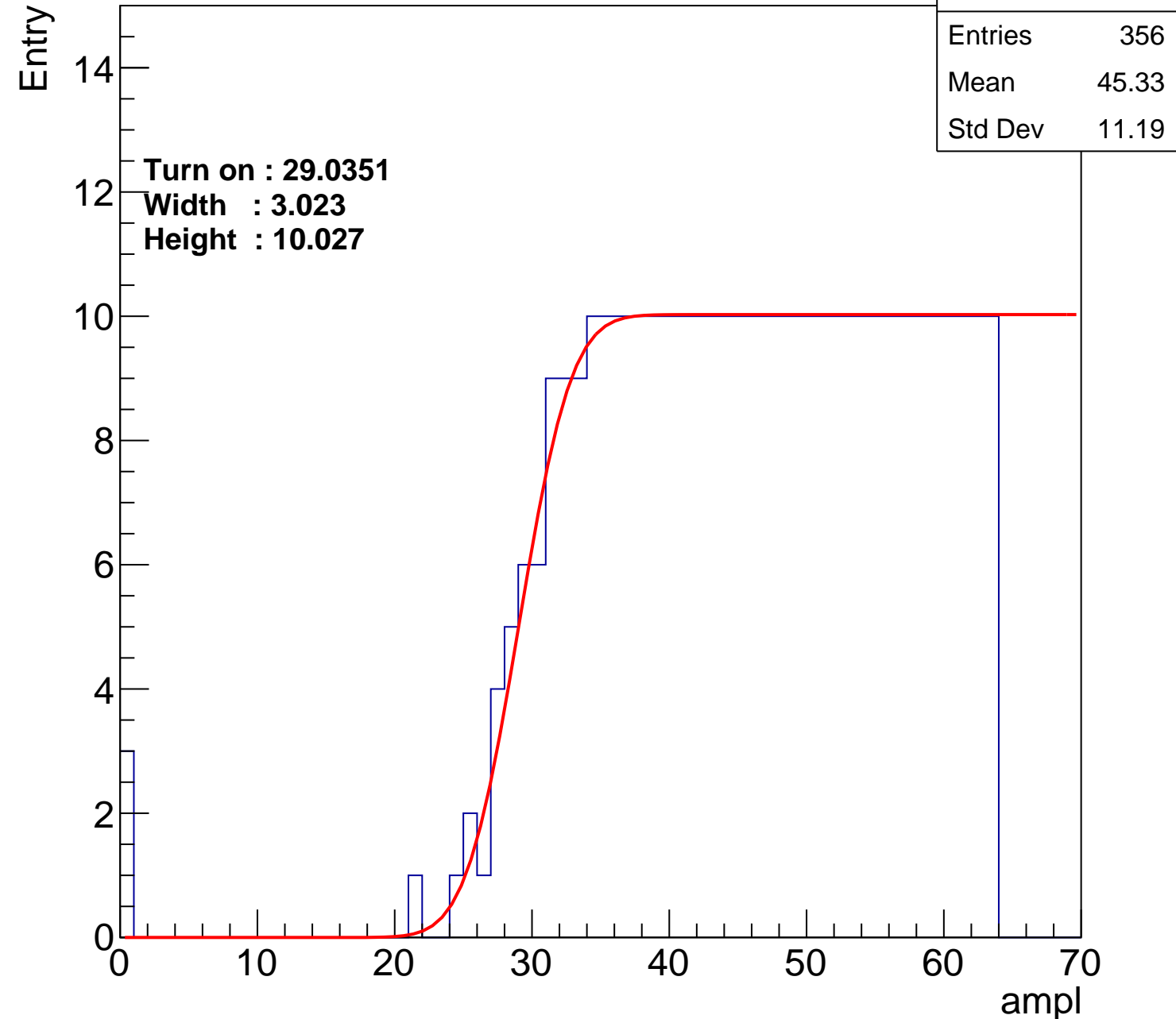
Width : 3.023

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch45

calib_packv5_042523_0143.root, FC#5, port B1

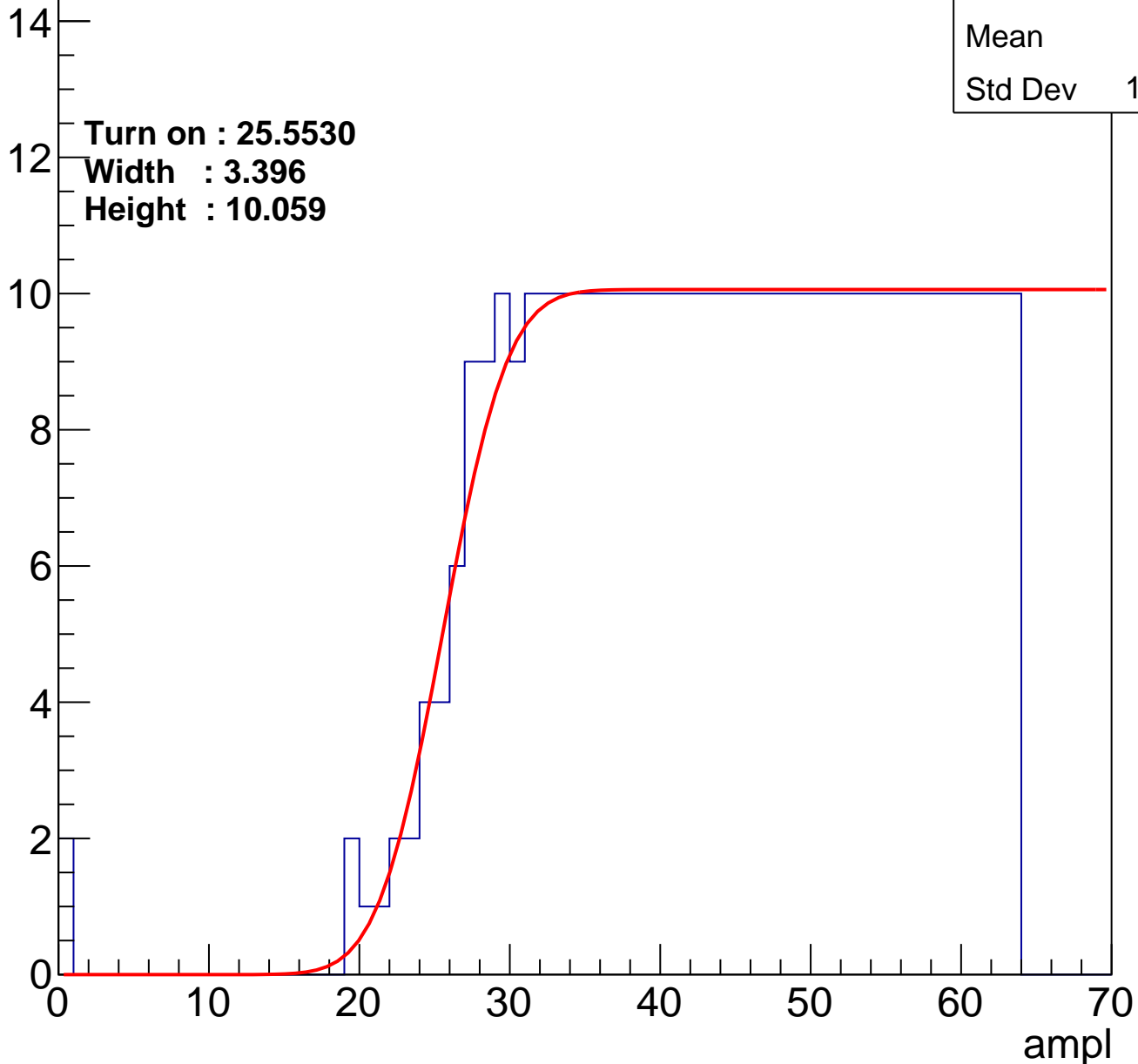
Entries	391
Mean	43.7
Std Dev	11.84

Turn on : 25.5530

Width : 3.396

Height : 10.059

Entry



B0L000S, U13-ch46

calib_packv5_042523_0143.root, FC#5, port B1

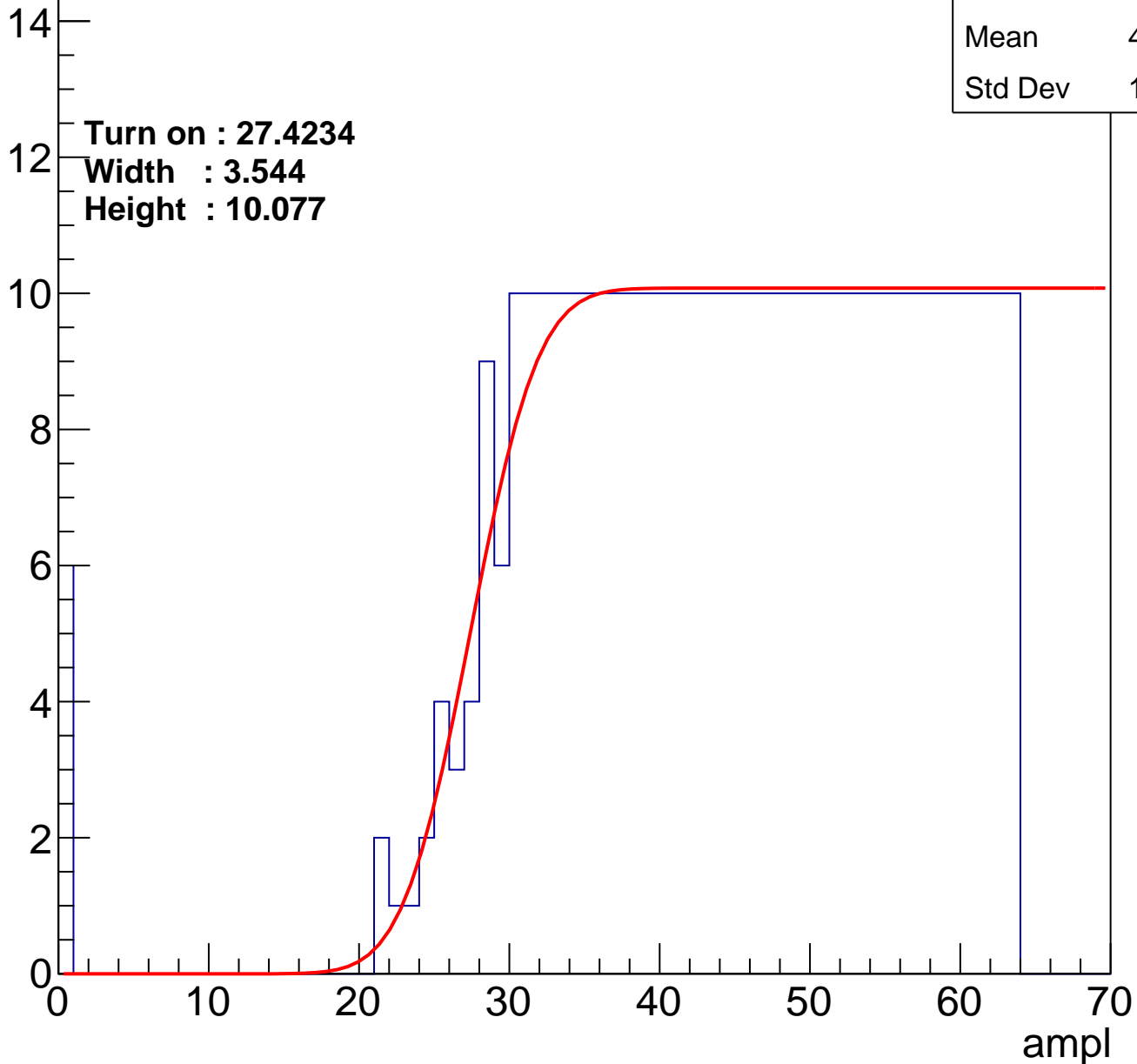
Entries	378
Mean	44.07
Std Dev	12.22

Turn on : 27.4234

Width : 3.544

Height : 10.077

Entry



B0L000S, U13-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.52
Std Dev	11.23

Turn on : 26.6791

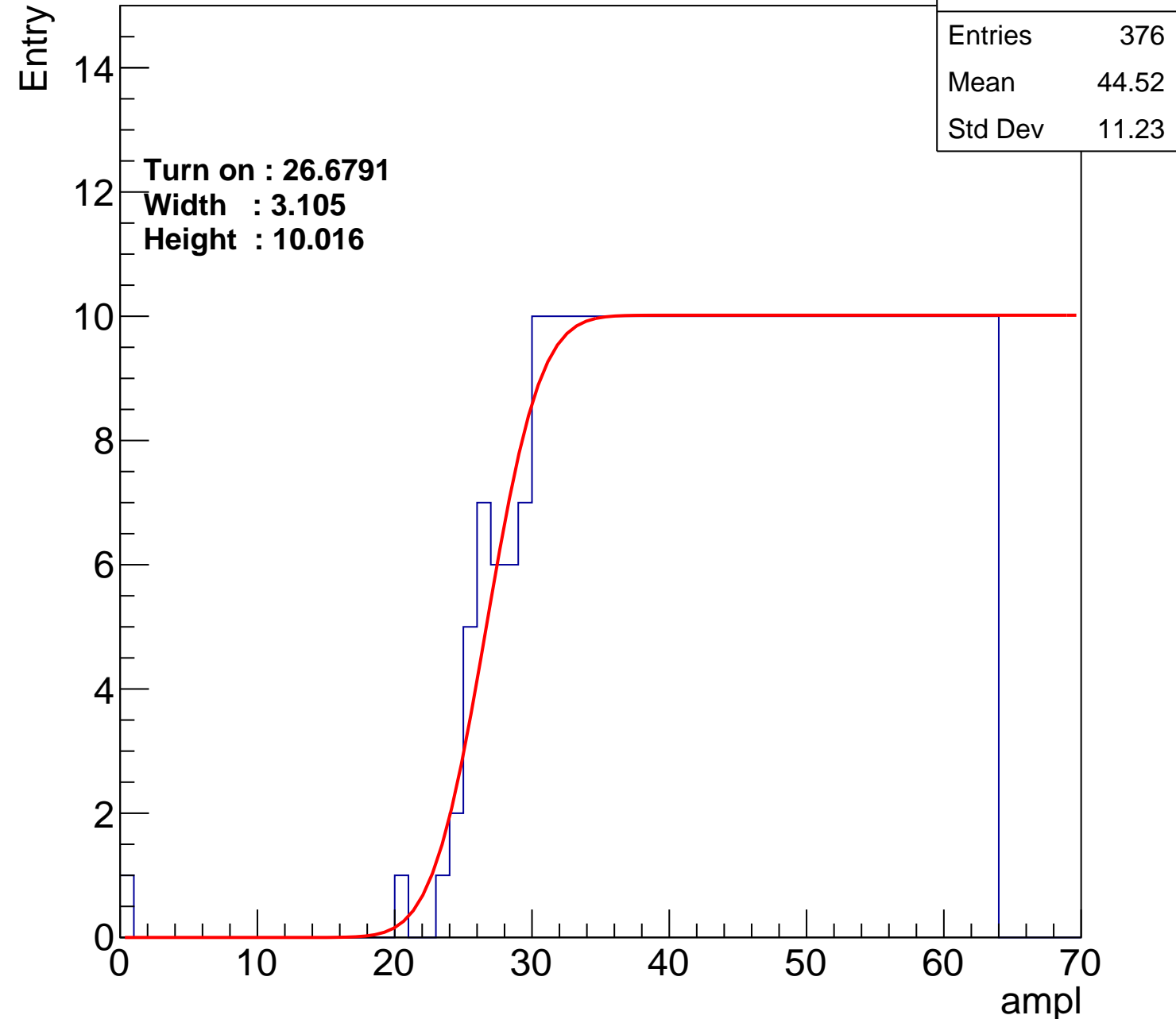
Width : 3.105

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch48

calib_packv5_042523_0143.root, FC#5, port B1

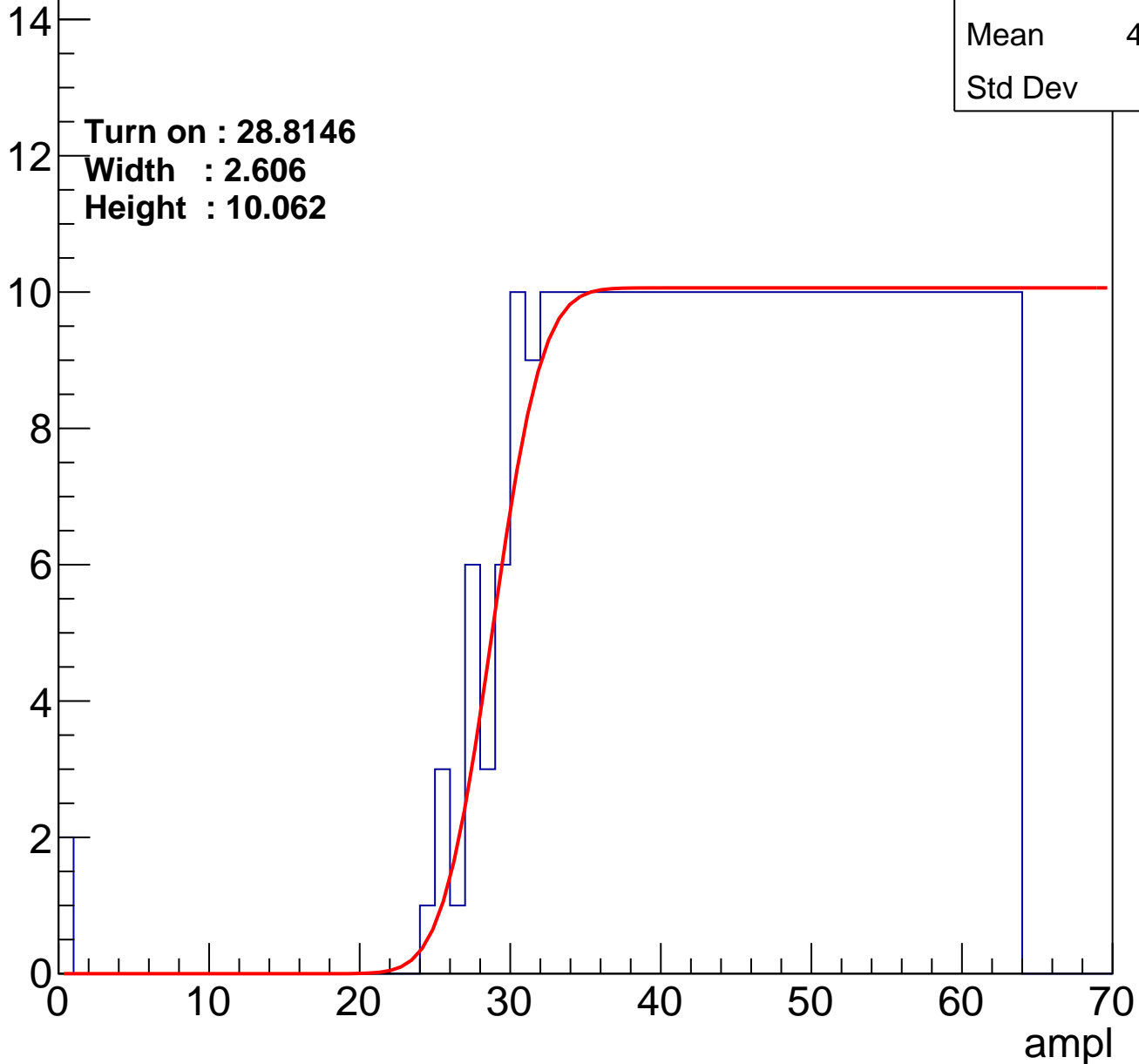
Entries	361
Mean	45.22
Std Dev	11

Turn on : 28.8146

Width : 2.606

Height : 10.062

Entry



B0L000S, U13-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.72
Std Dev	10.78

Turn on : 29.7195

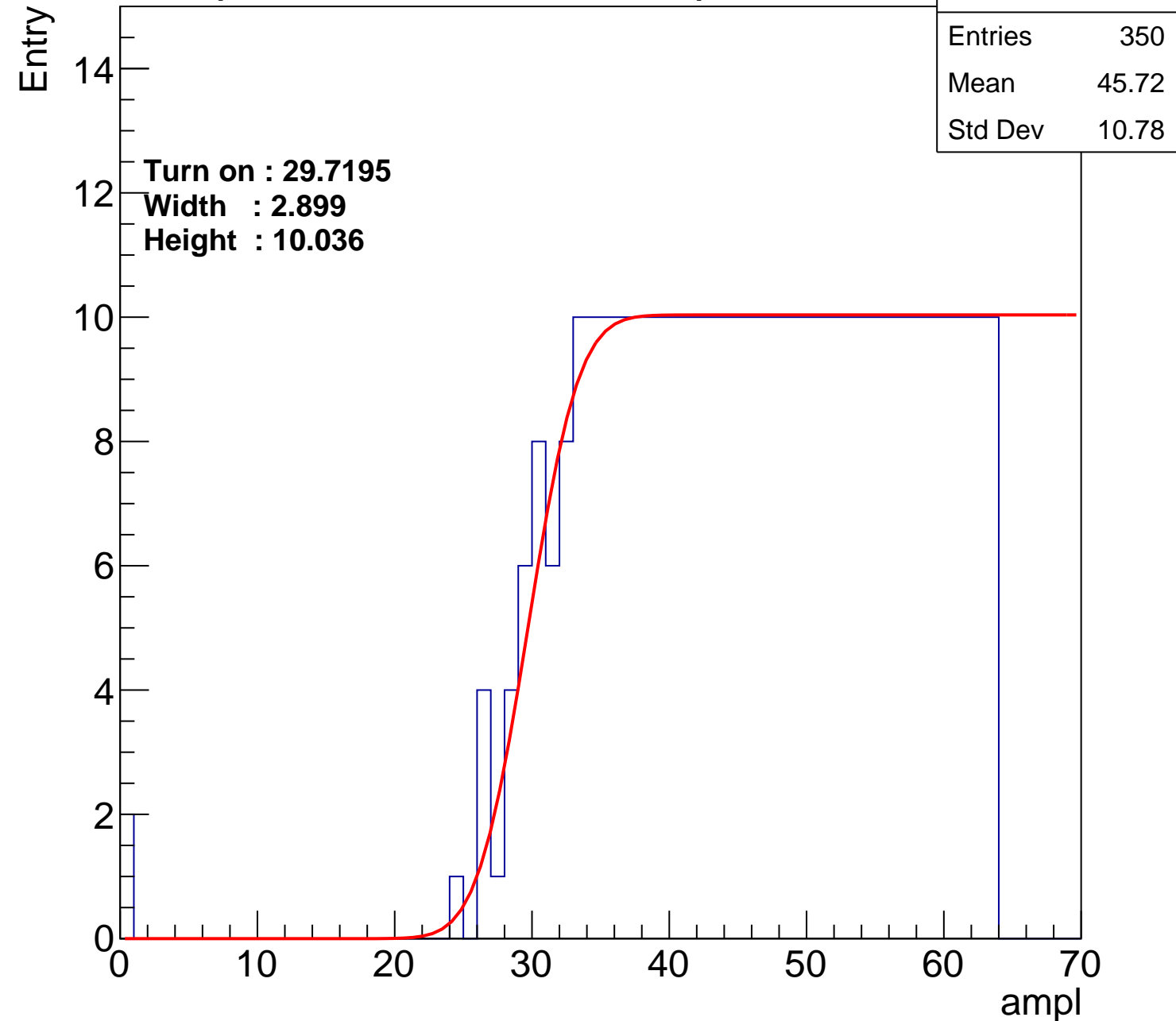
Width : 2.899

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch50

calib_packv5_042523_0143.root, FC#5, port B1

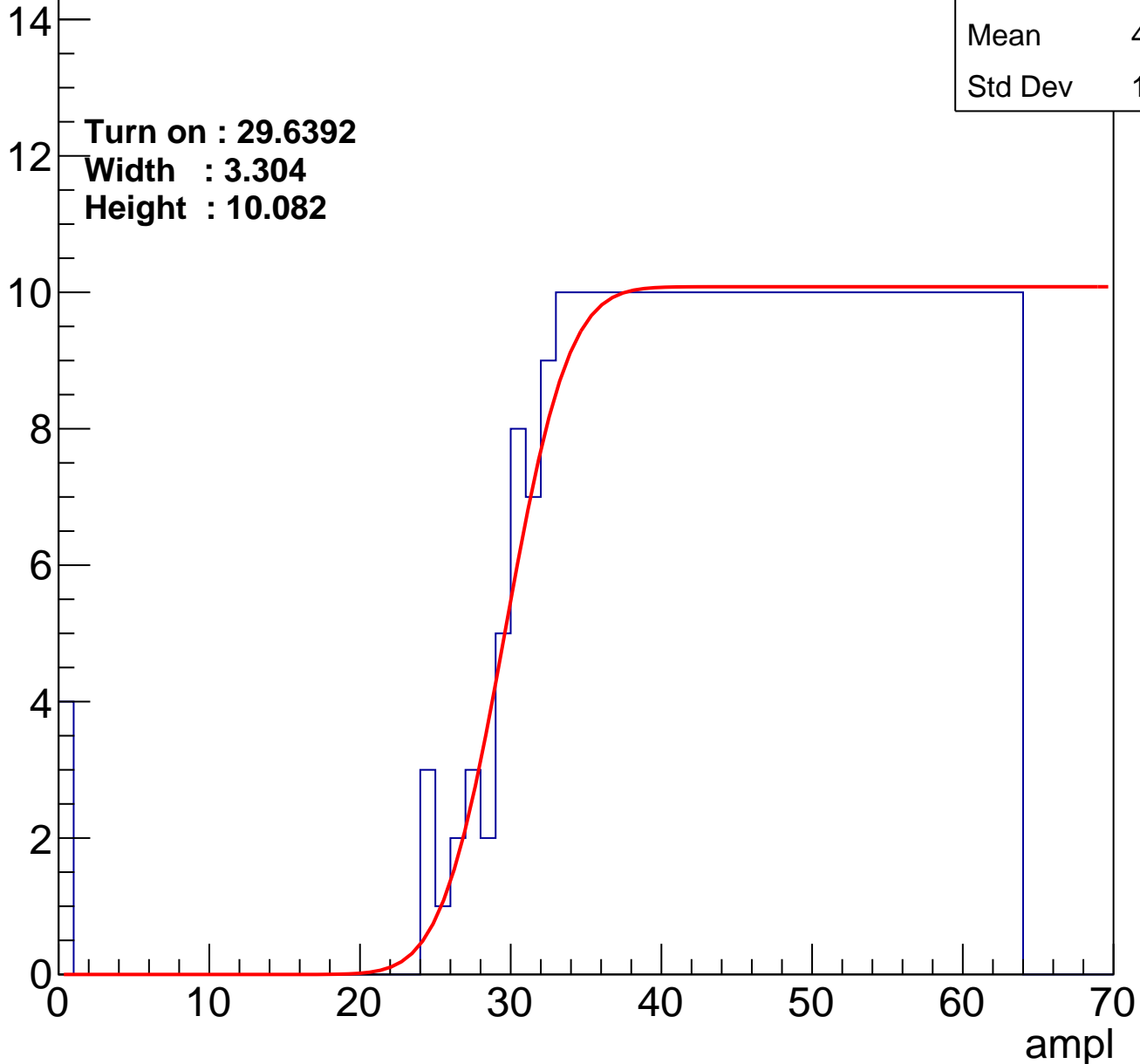
Entries	354
Mean	45.36
Std Dev	11.35

Turn on : 29.6392

Width : 3.304

Height : 10.082

Entry



B0L000S, U13-ch51

calib_packv5_042523_0143.root, FC#5, port B1

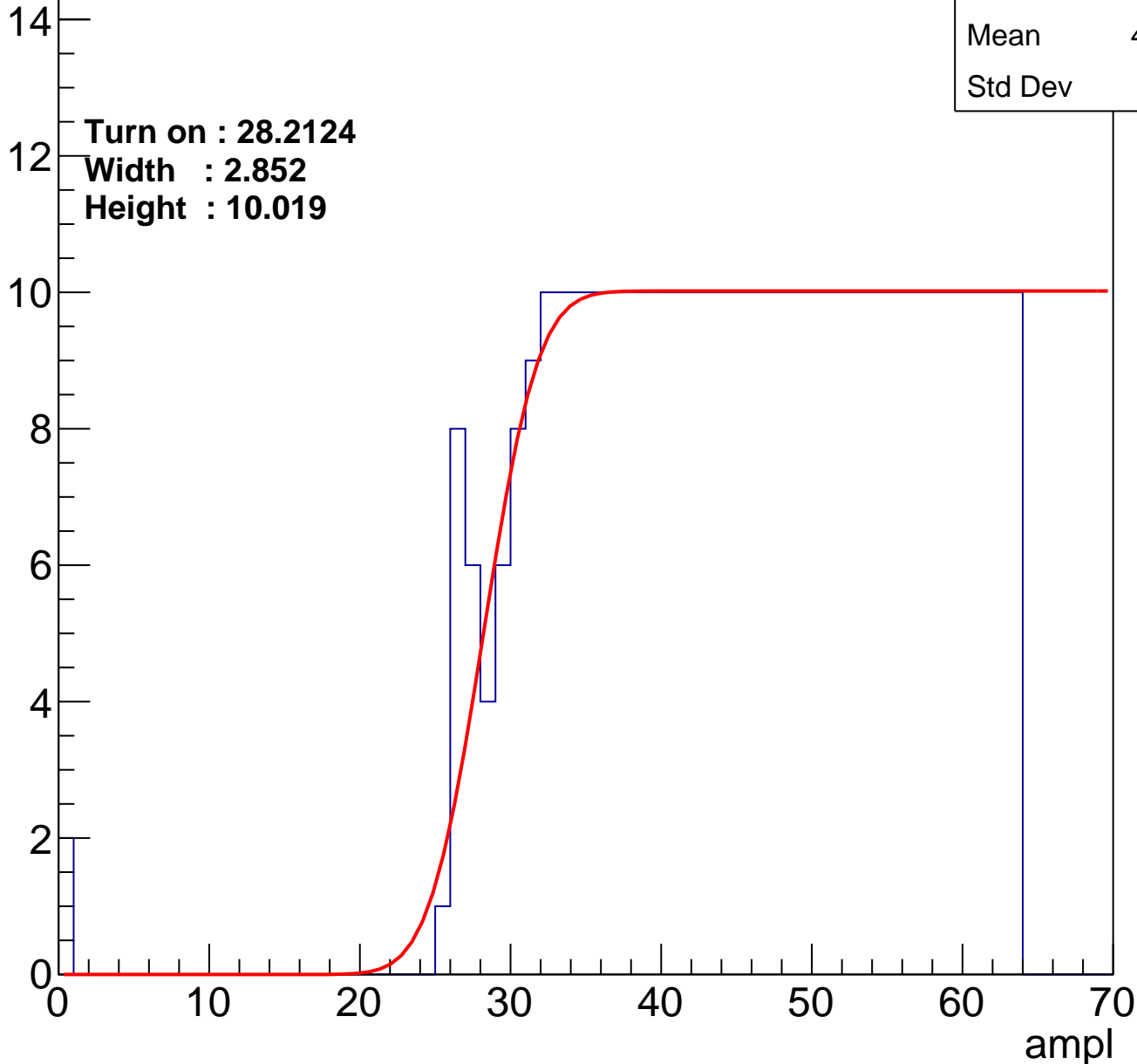
Entries	364
Mean	45.05
Std Dev	11.1

Turn on : 28.2124

Width : 2.852

Height : 10.019

Entry



B0L000S, U13-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.12
Std Dev	11.24

Turn on : 28.1942

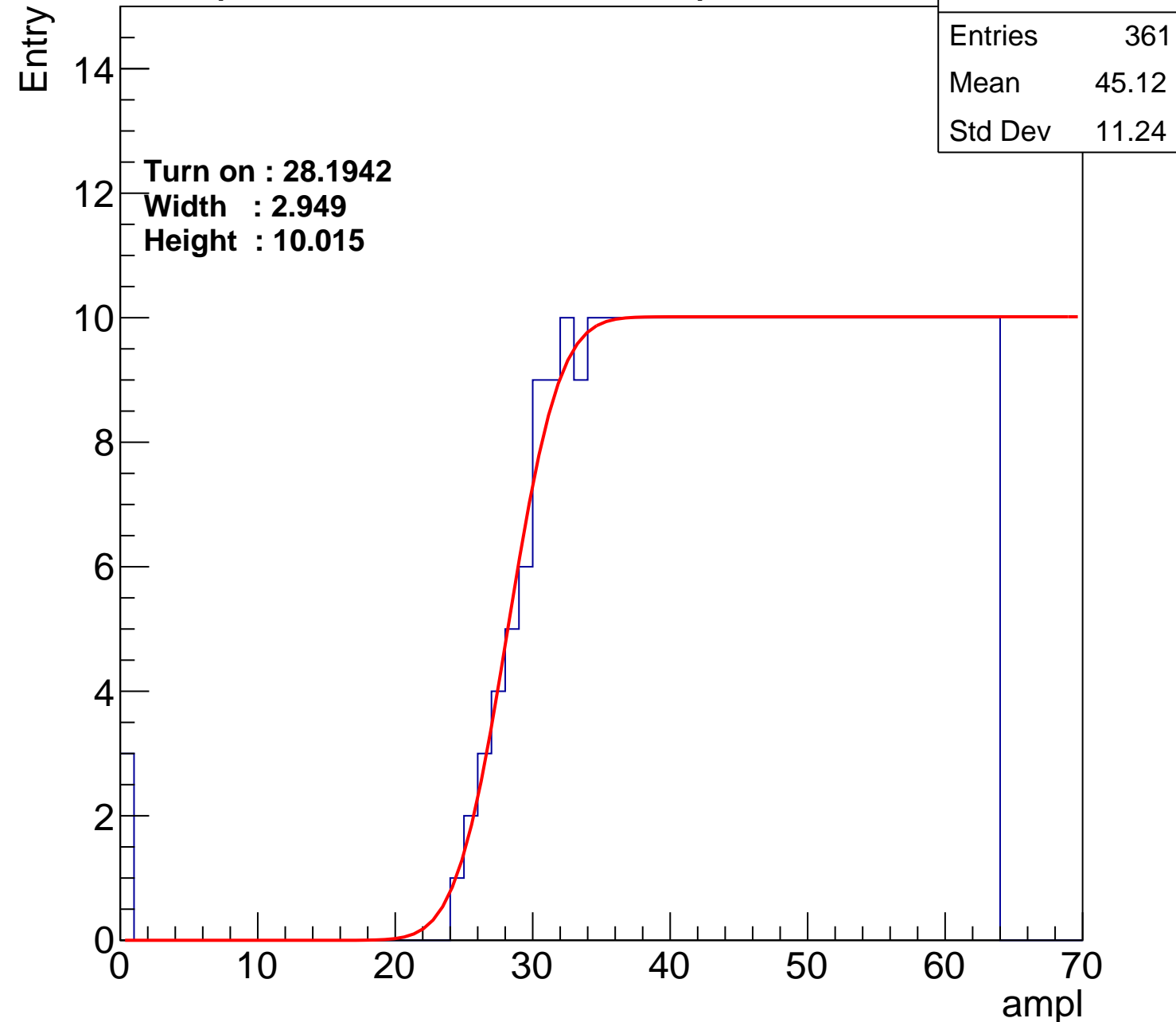
Width : 2.949

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 26.3051

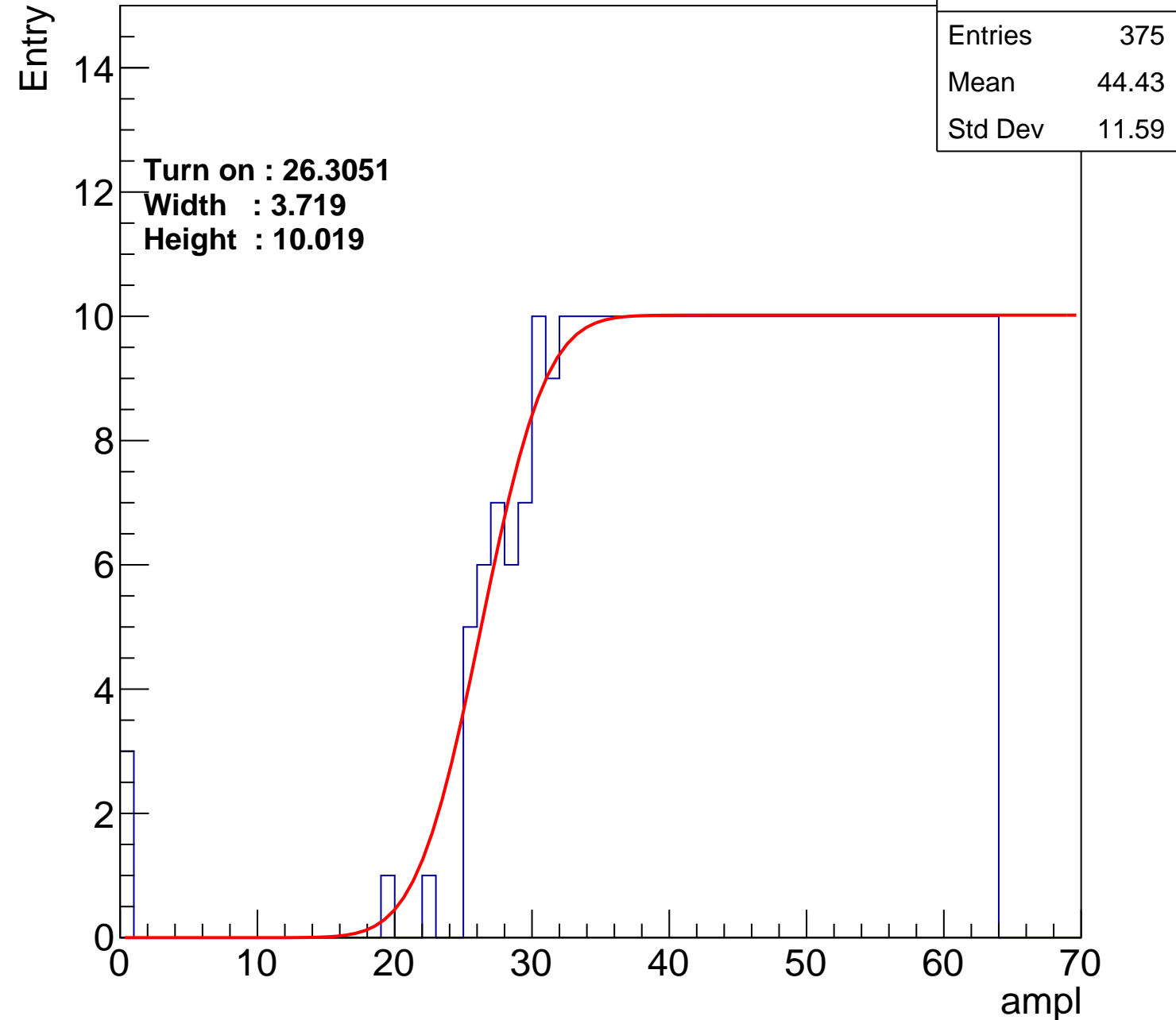
Width : 3.719

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.66
Std Dev	11.49

Turn on : 27.7662

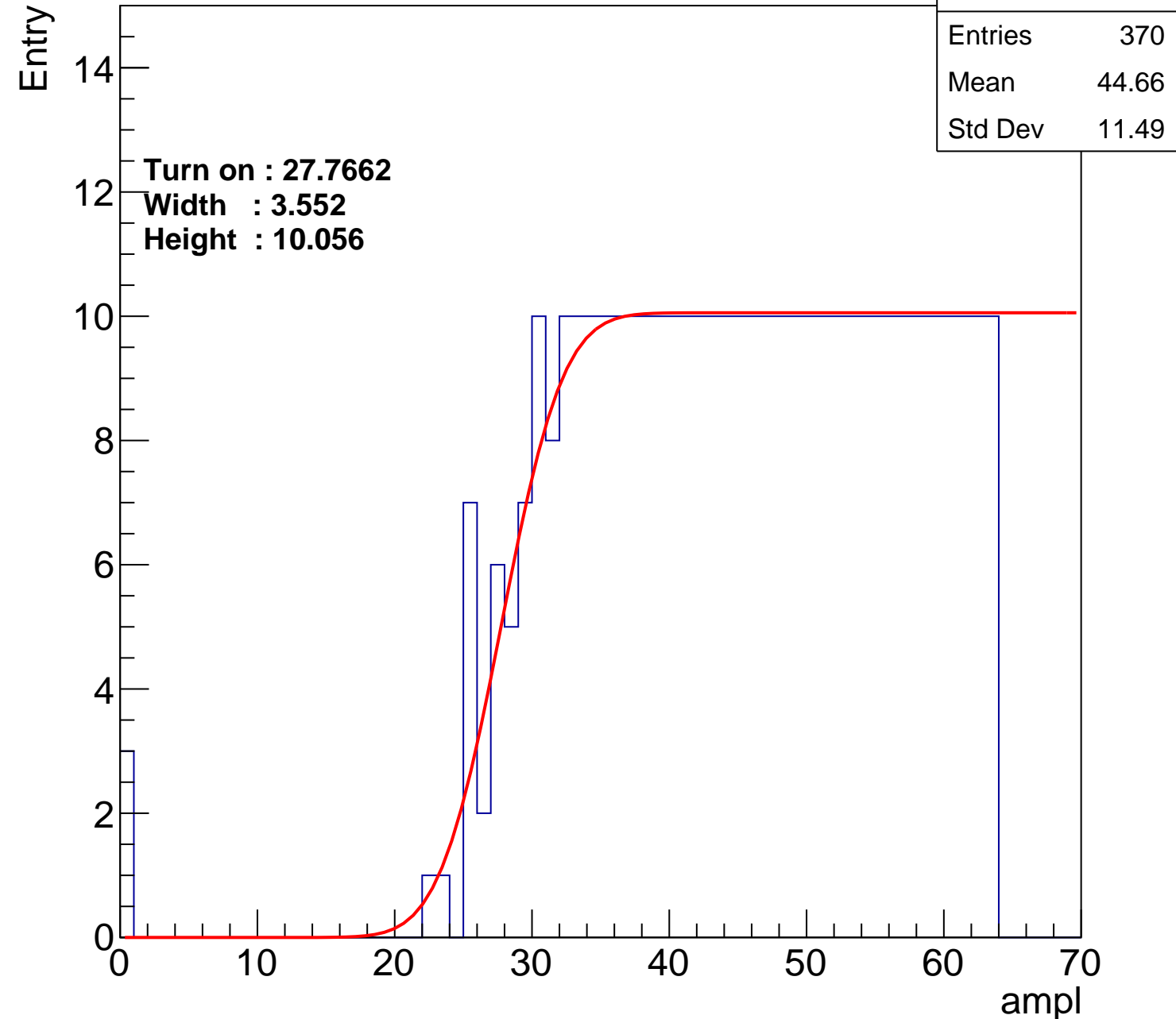
Width : 3.552

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch55

calib_packv5_042523_0143.root, FC#5, port B1

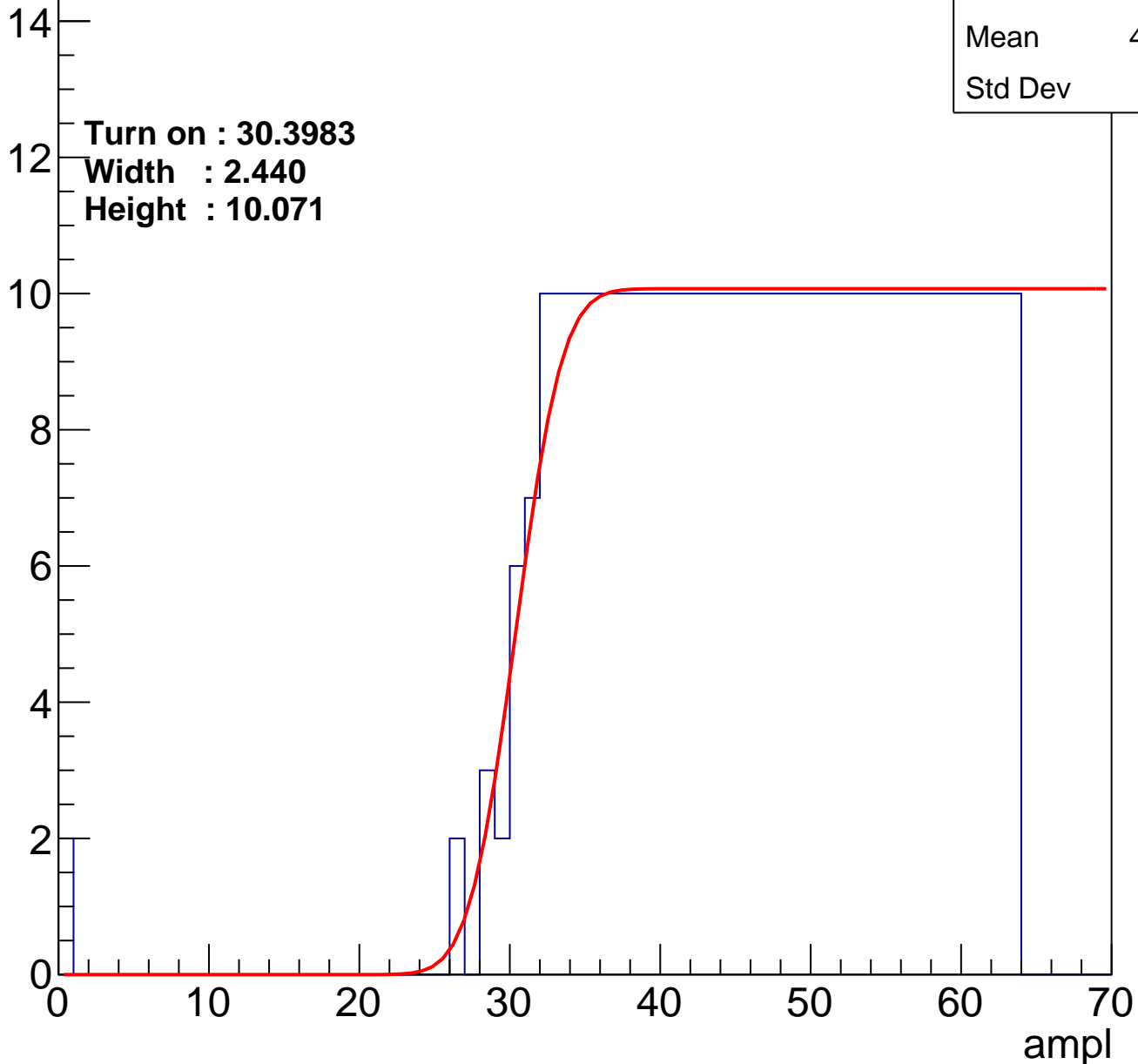
Entries	342
Mean	46.17
Std Dev	10.5

Turn on : 30.3983

Width : 2.440

Height : 10.071

Entry



B0L000S, U13-ch56

calib_packv5_042523_0143.root, FC#5, port B1

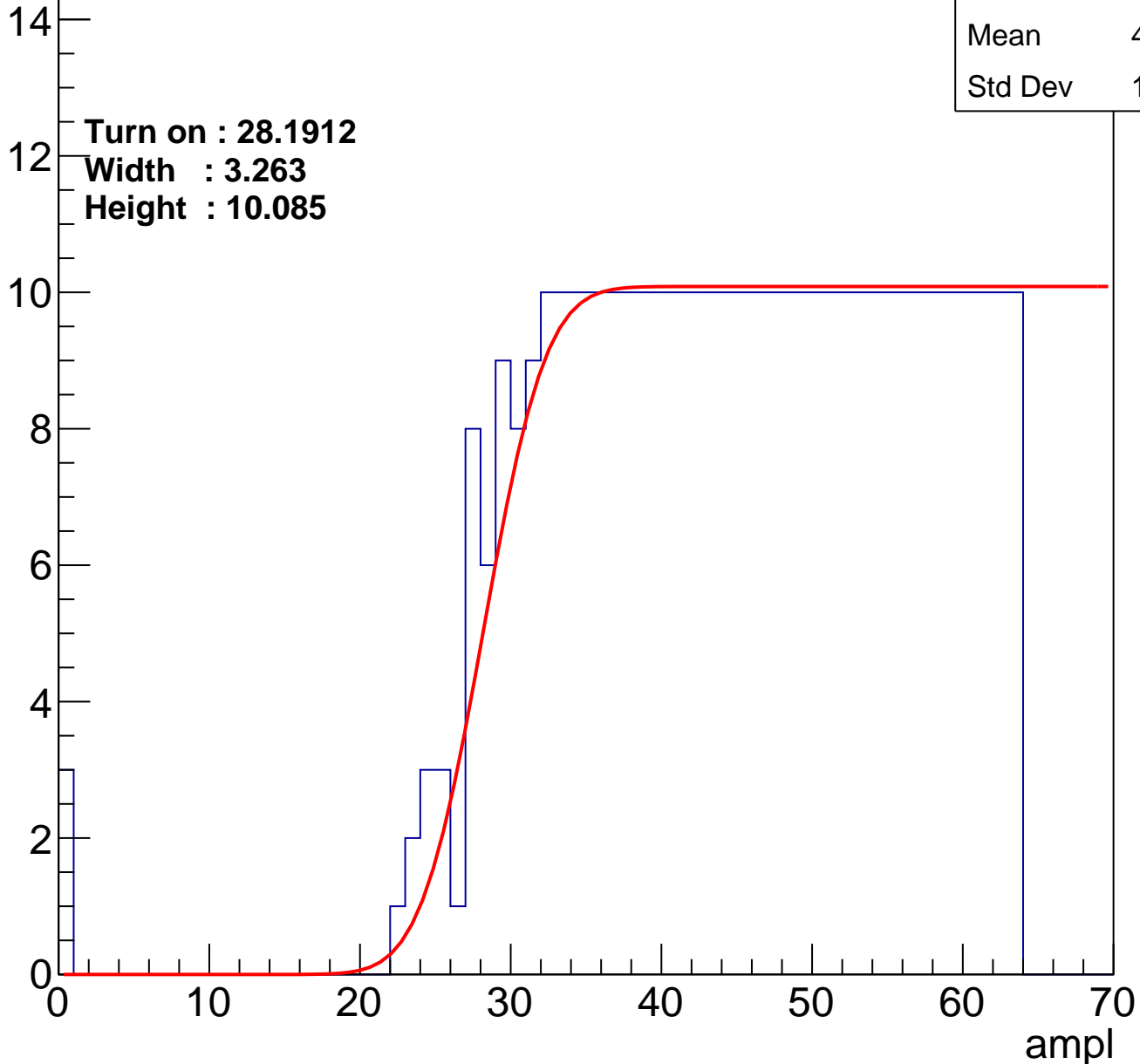
Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 28.1912

Width : 3.263

Height : 10.085

Entry



B0L000S, U13-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	347
Mean	45.78
Std Dev	10.97

Turn on : 29.4297

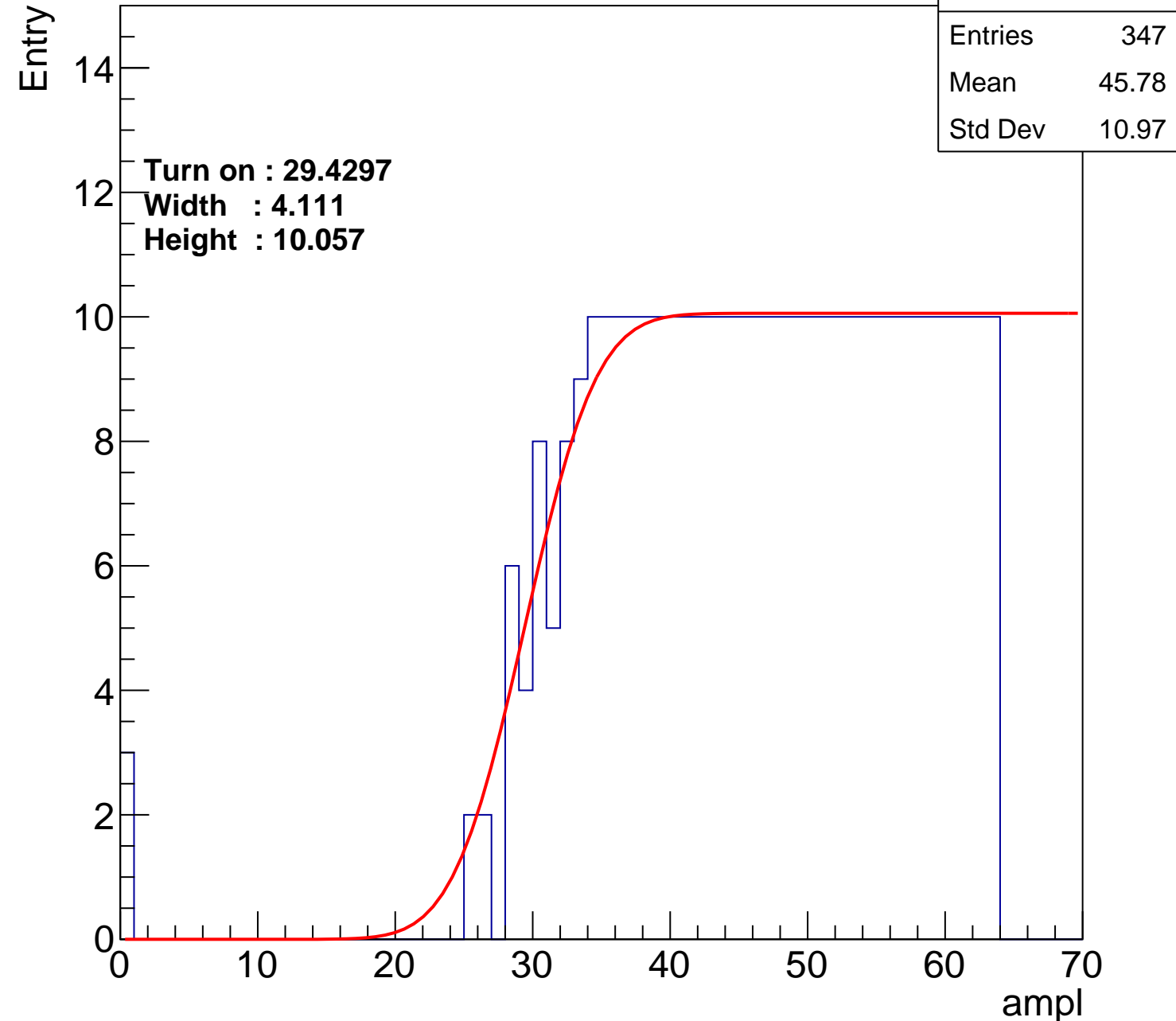
Width : 4.111

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch58

calib_packv5_042523_0143.root, FC#5, port B1

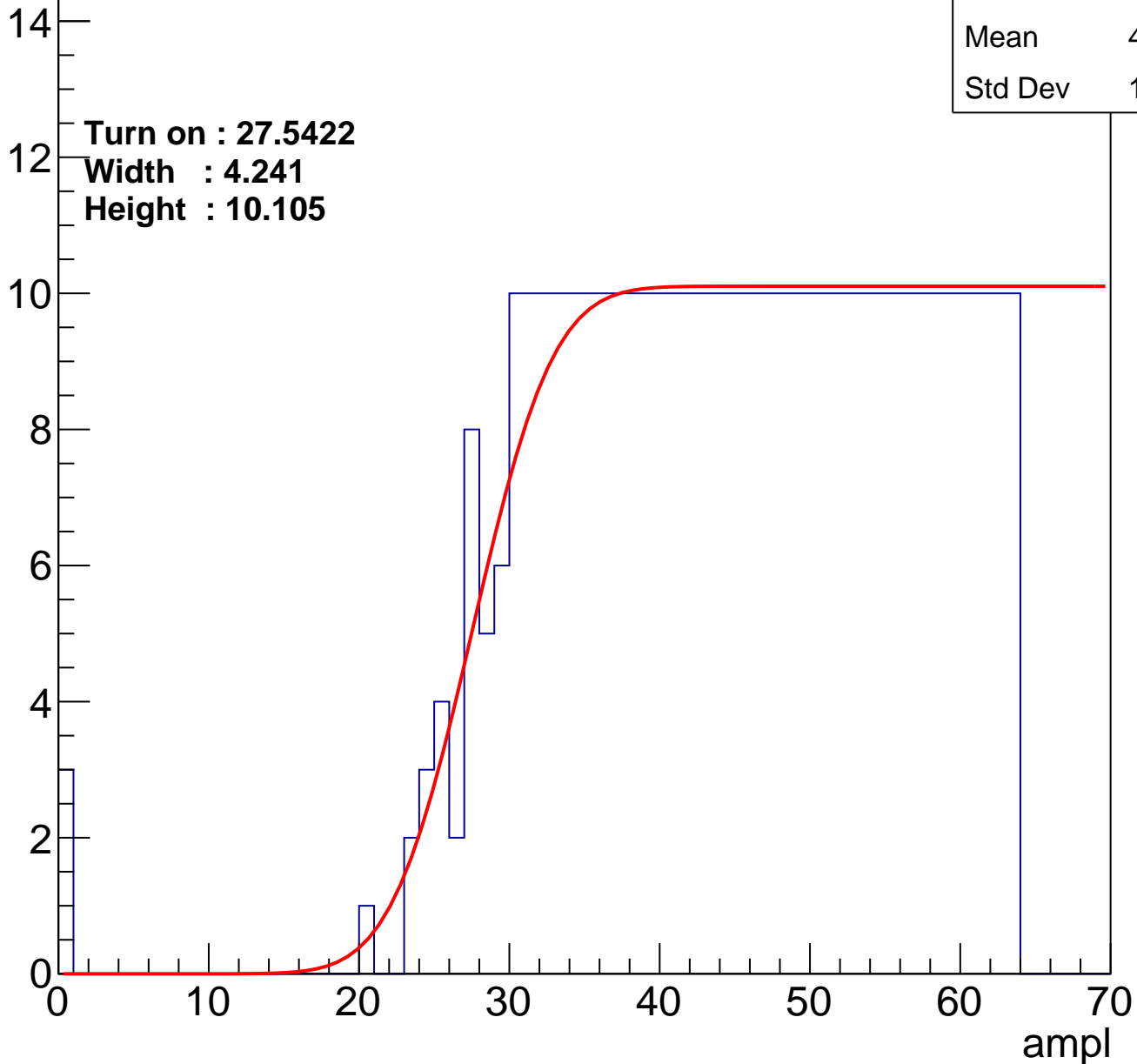
Entries	374
Mean	44.47
Std Dev	11.59

Turn on : 27.5422

Width : 4.241

Height : 10.105

Entry



B0L000S, U13-ch59

calib_packv5_042523_0143.root, FC#5, port B1

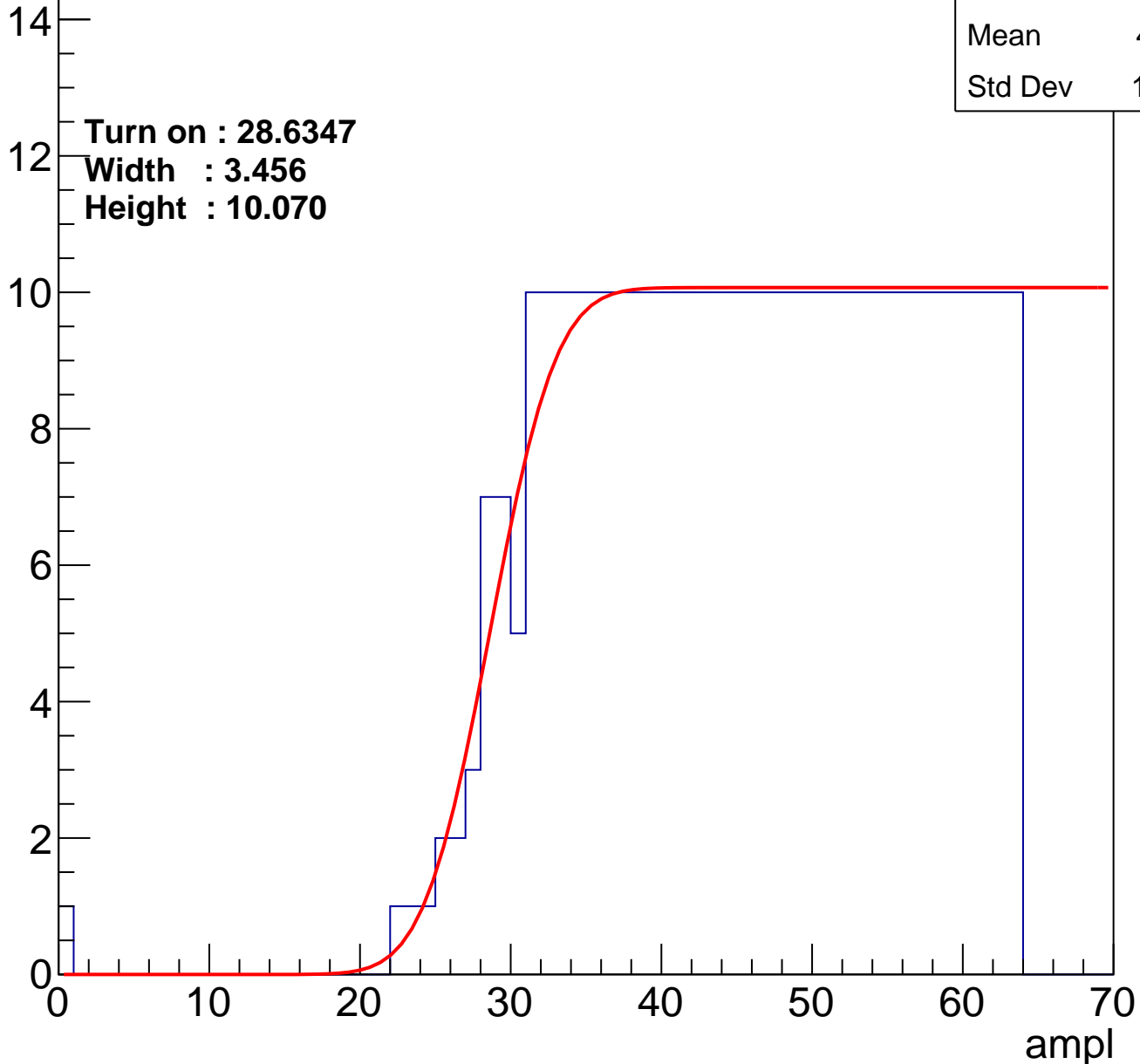
Entries	360
Mean	45.31
Std Dev	10.82

Turn on : 28.6347

Width : 3.456

Height : 10.070

Entry



B0L000S, U13-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	44.99
Std Dev	11.69

Turn on : 28.4472

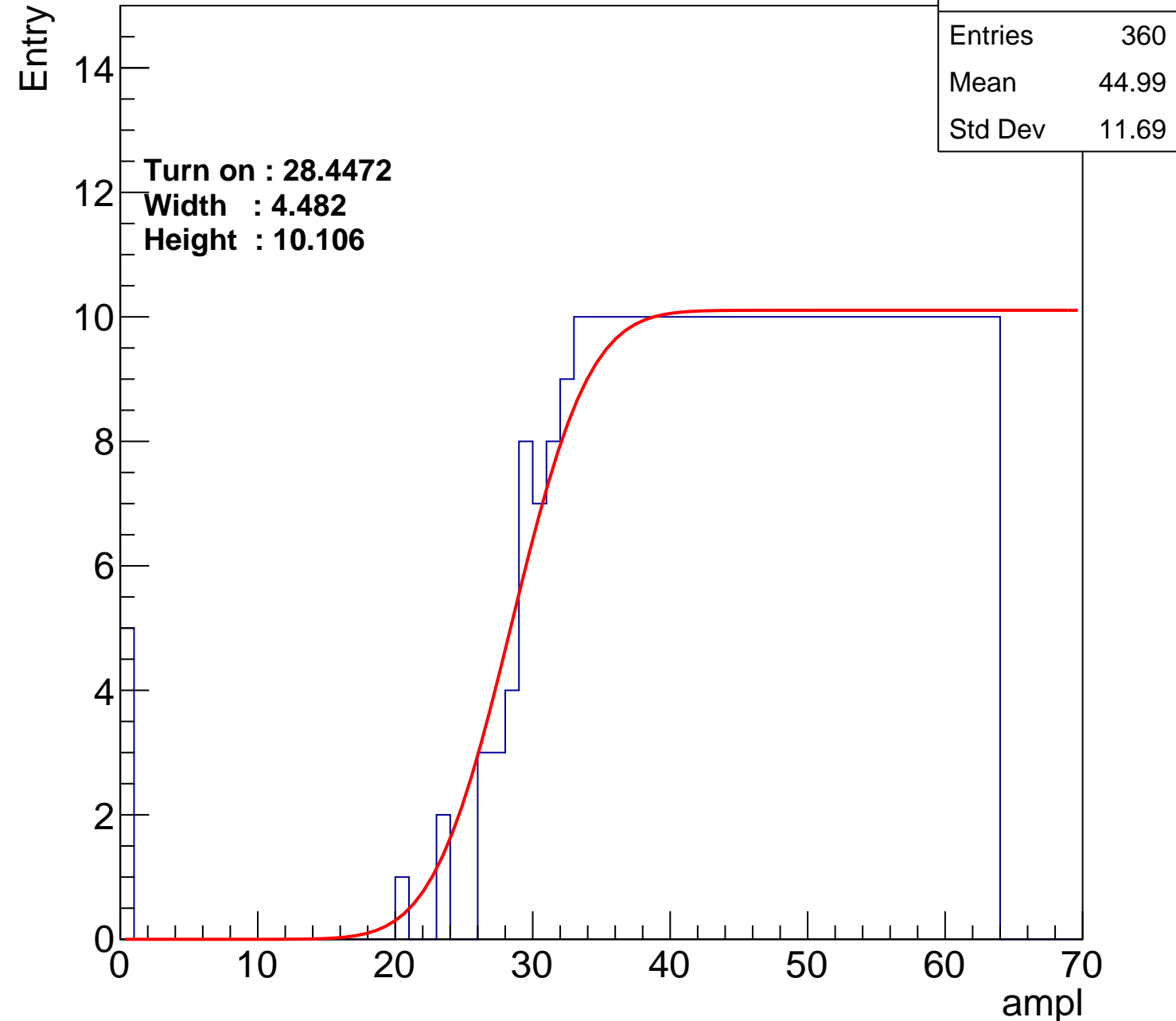
Width : 4.482

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.37
Std Dev	10.75

Turn on : 28.0673

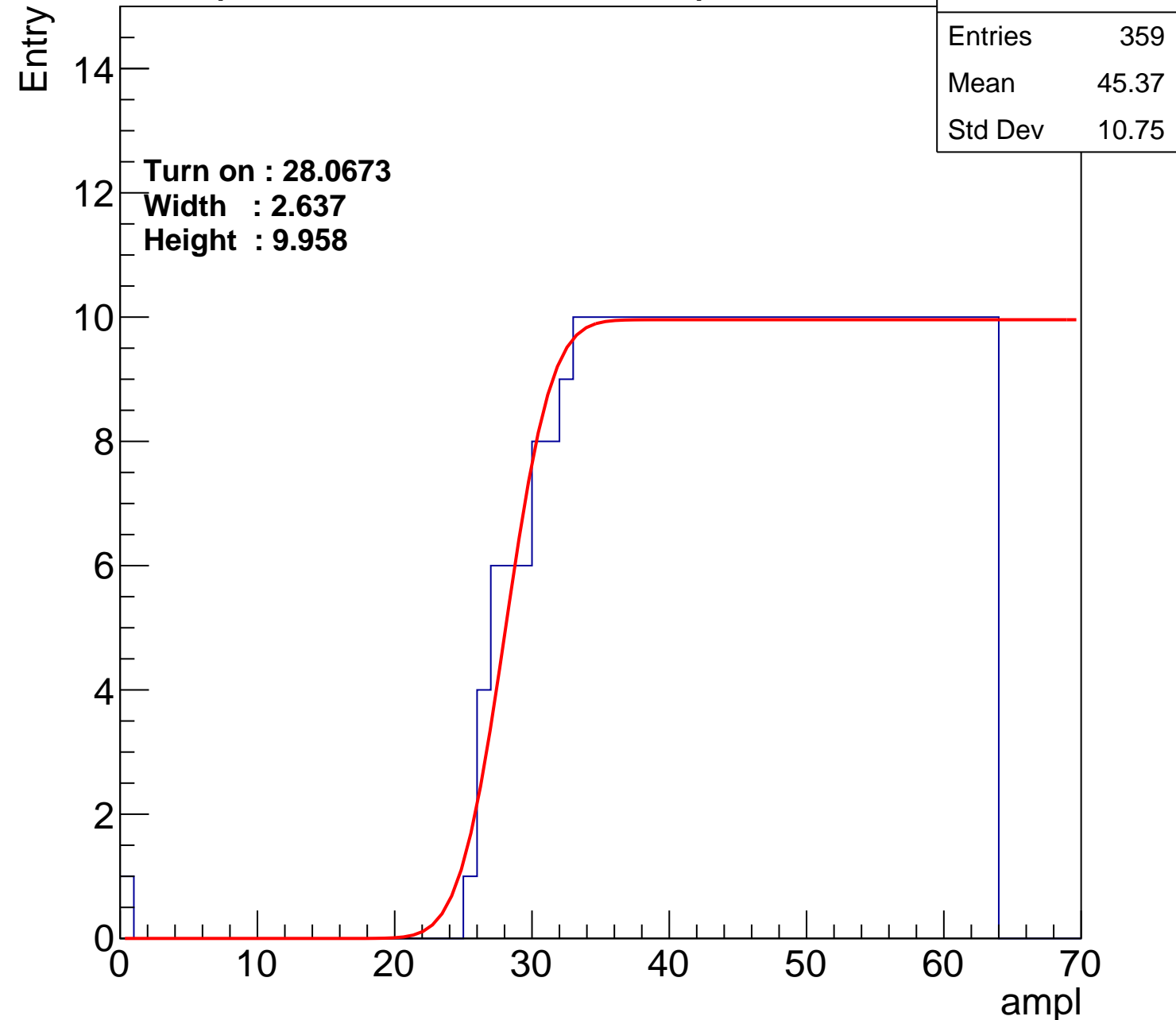
Width : 2.637

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	345
Mean	45.92
Std Dev	10.85

Turn on : 29.9723

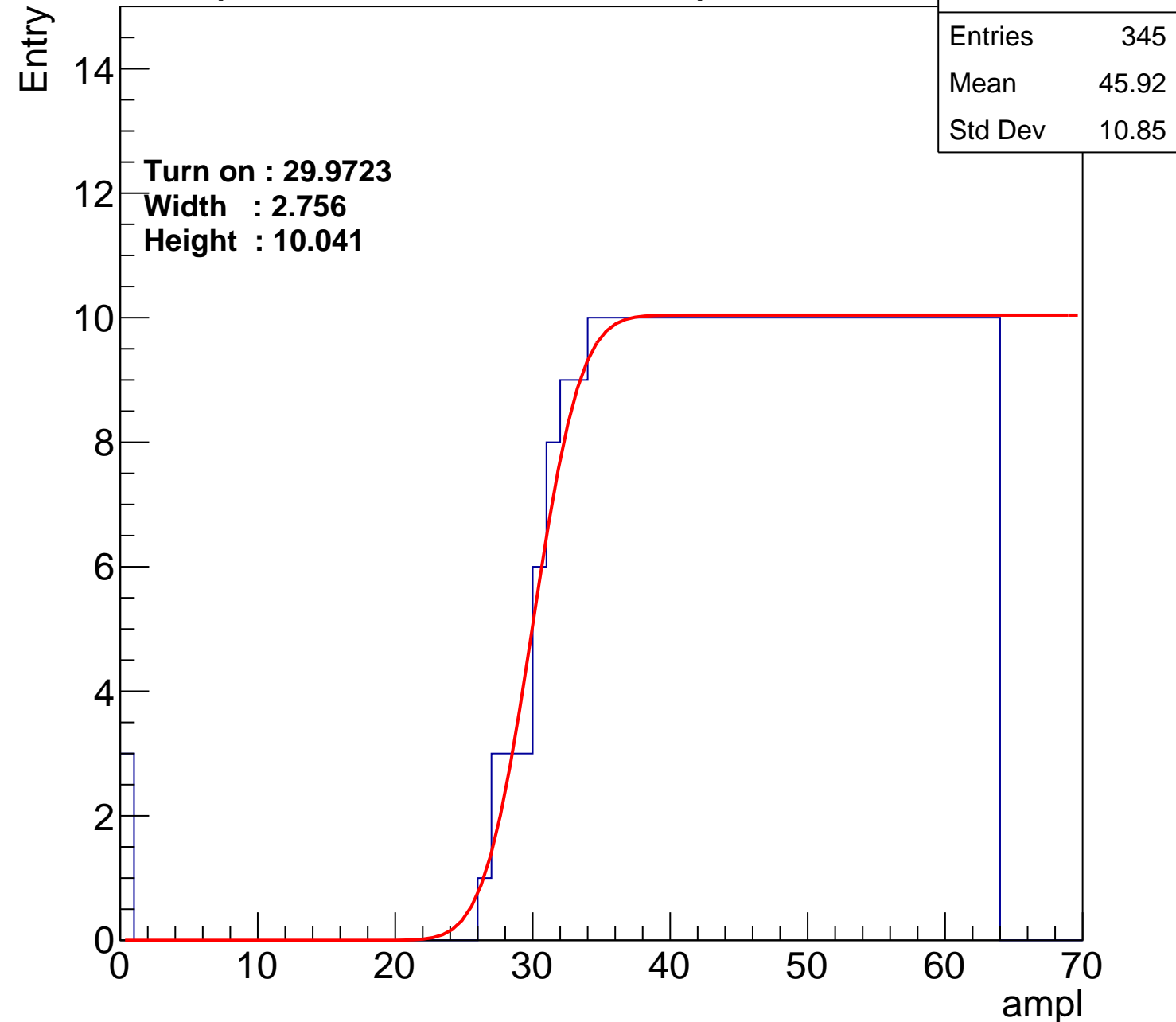
Width : 2.756

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.27
Std Dev	11.08

Turn on : 28.3831

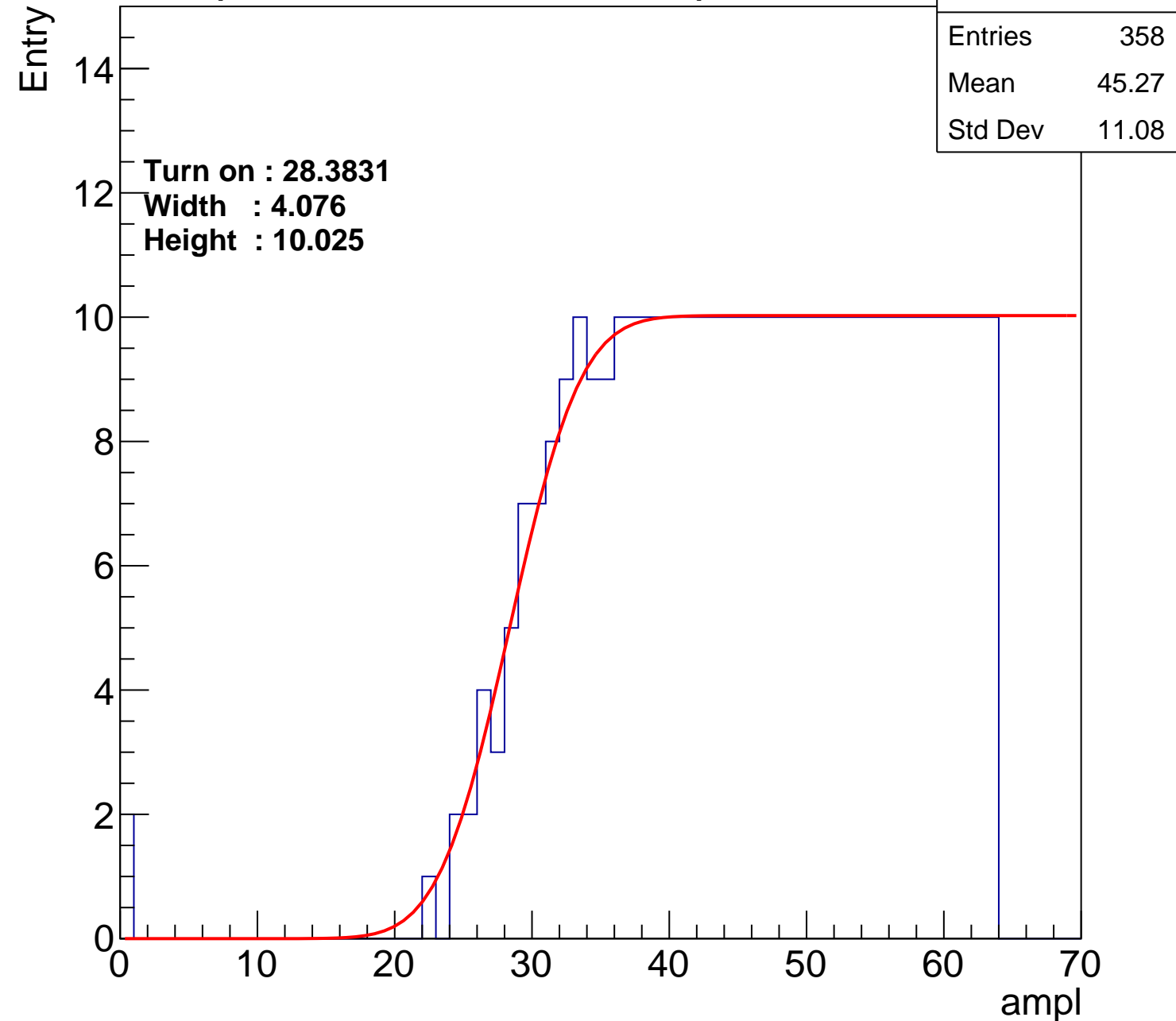
Width : 4.076

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch64

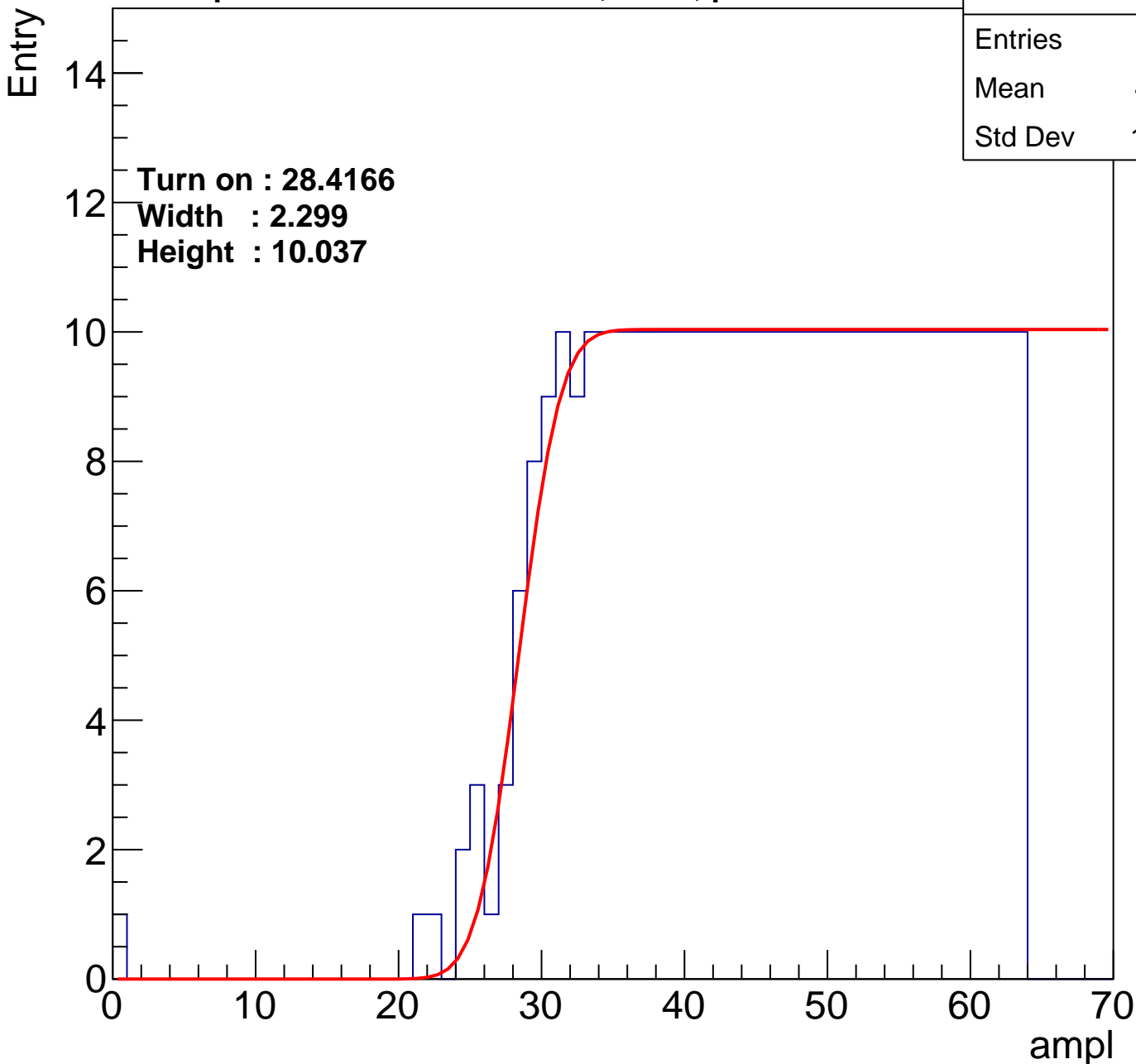
calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 28.4166

Width : 2.299

Height : 10.037



B0L000S, U13-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.8474

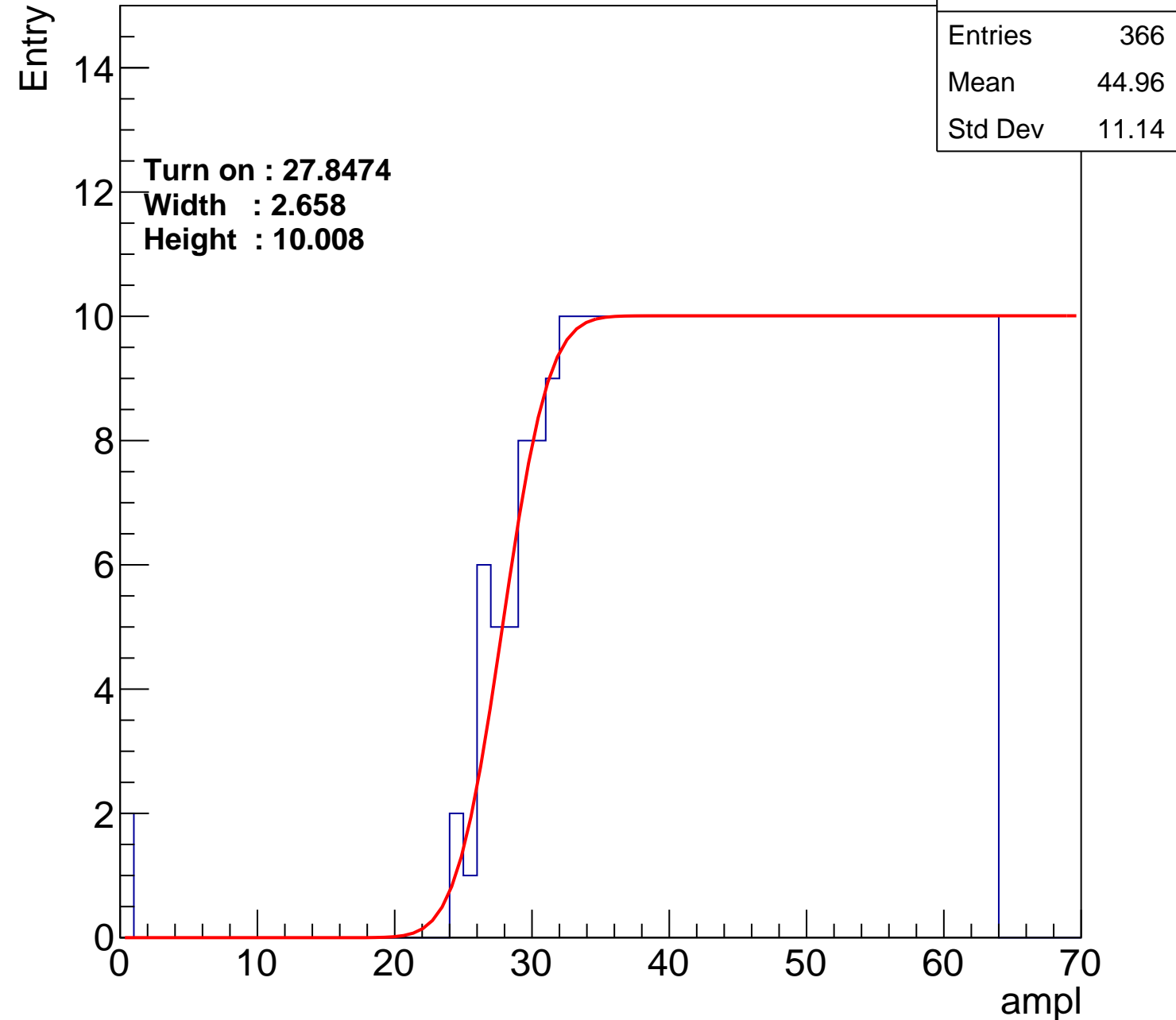
Width : 2.658

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch66

calib_packv5_042523_0143.root, FC#5, port B1

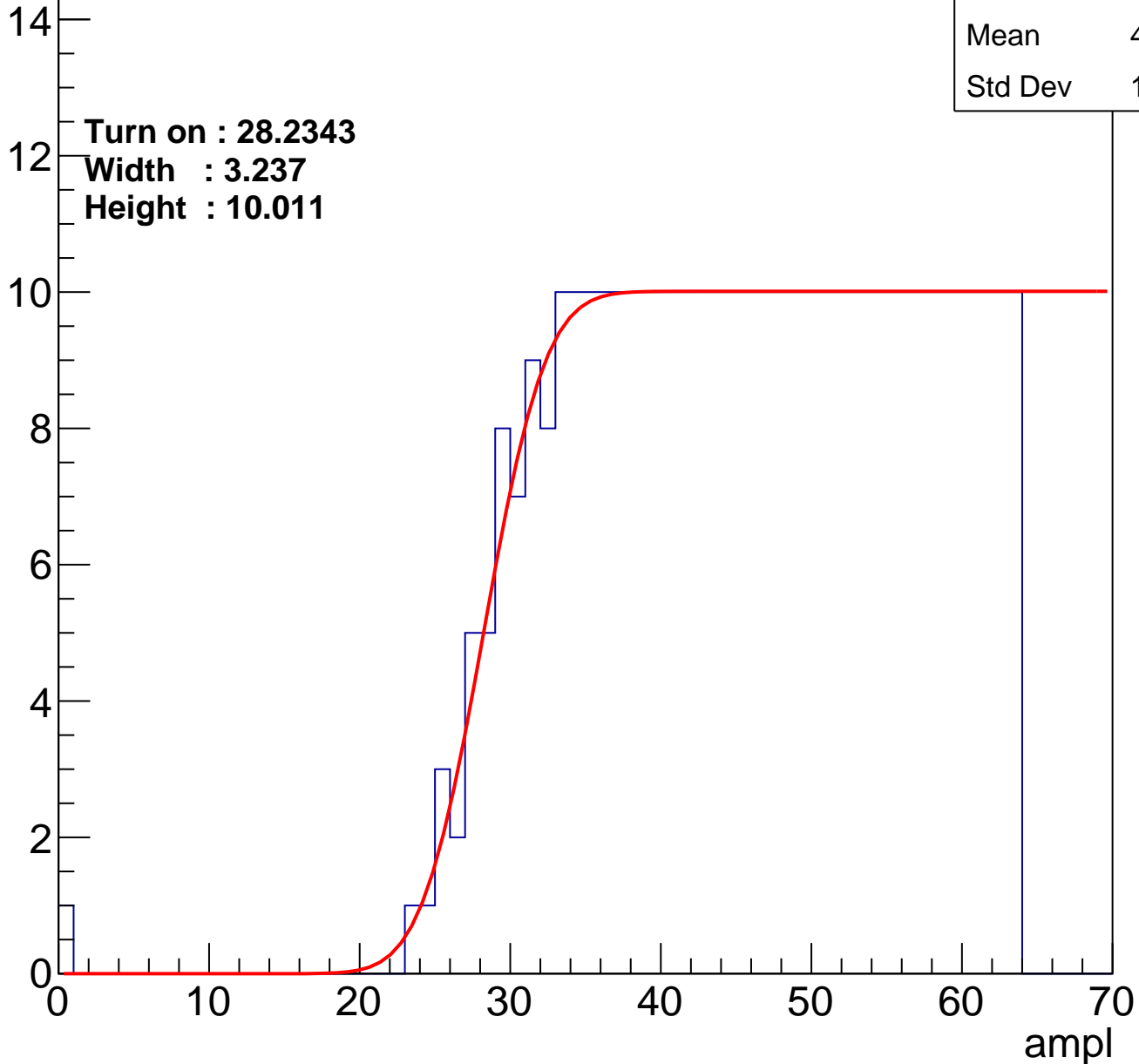
Entries	360
Mean	45.29
Std Dev	10.83

Turn on : 28.2343

Width : 3.237

Height : 10.011

Entry



B0L000S, U13-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.3
Std Dev	10.92

Turn on : 28.2301

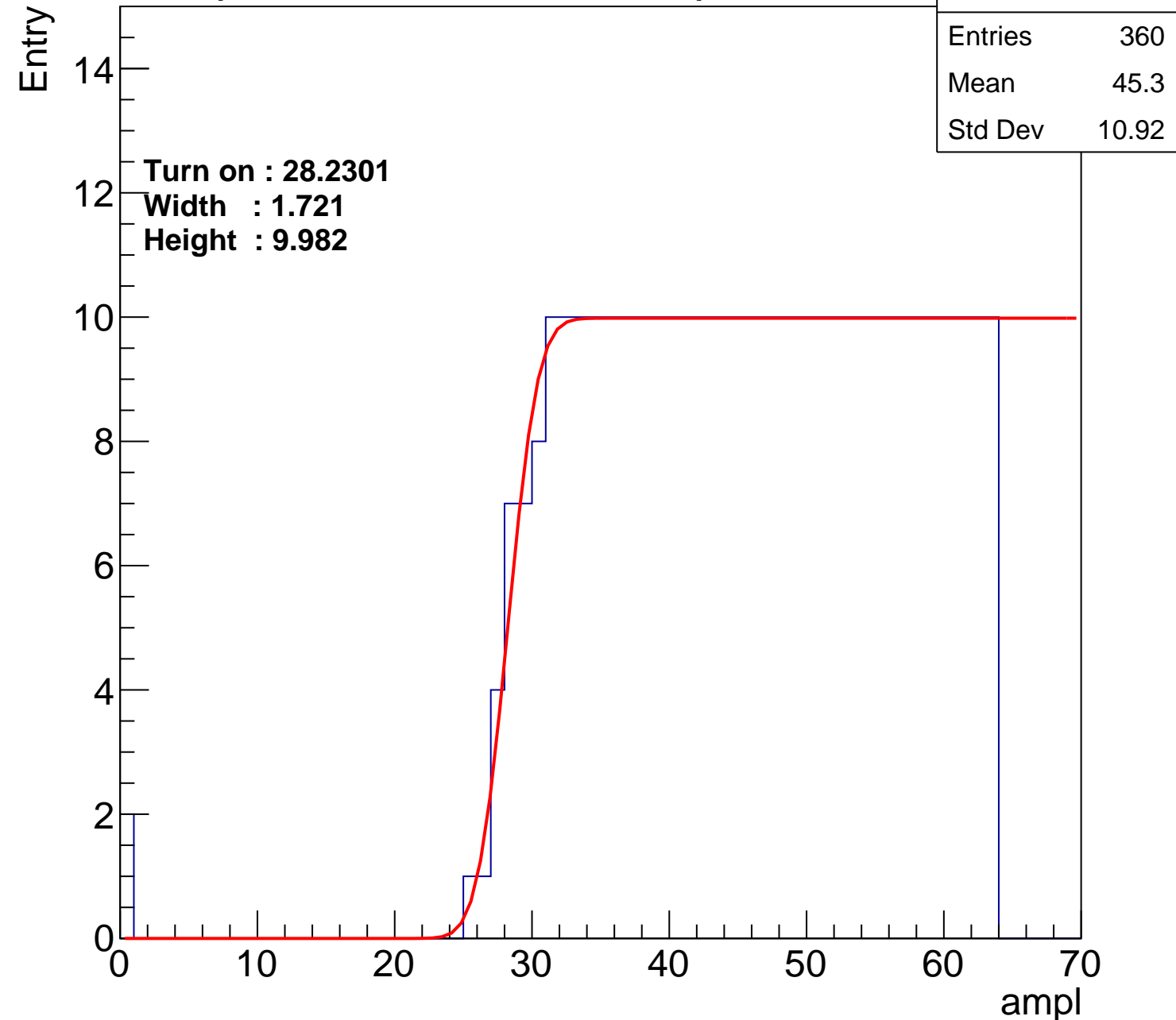
Width : 1.721

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.84
Std Dev	11.17

Turn on : 27.4558

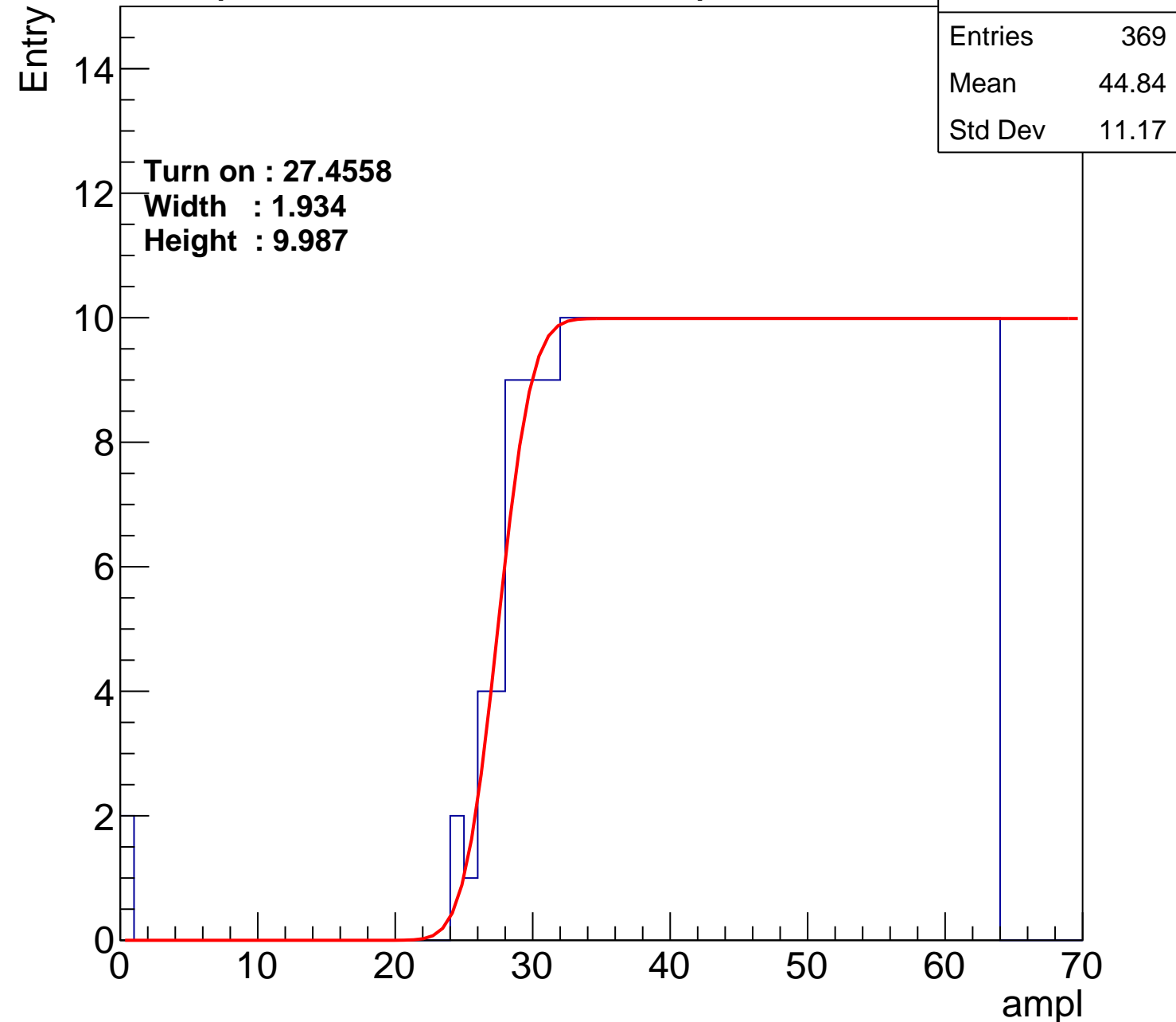
Width : 1.934

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.93
Std Dev	11.13

Turn on : 27.3644

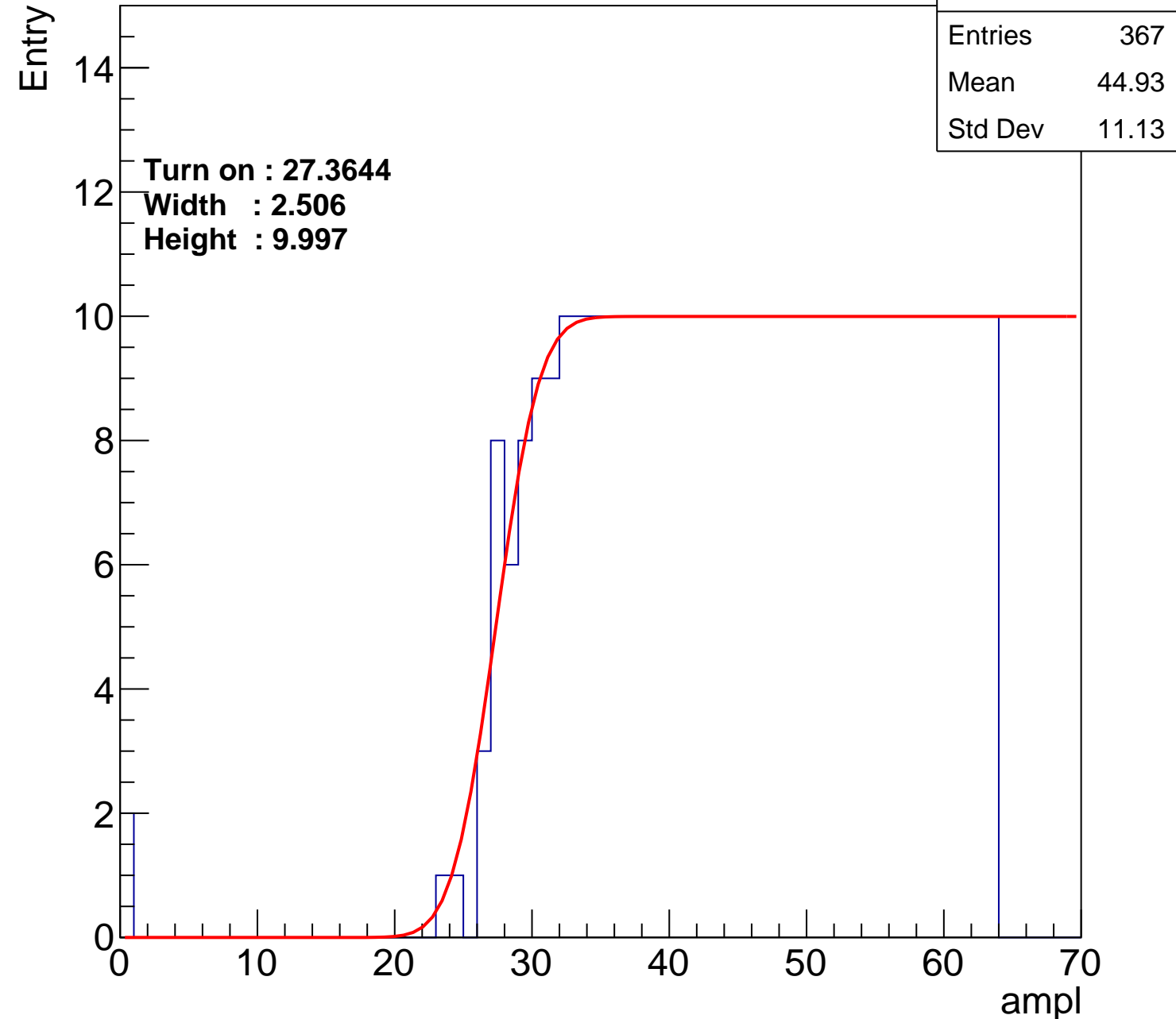
Width : 2.506

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch70

calib_packv5_042523_0143.root, FC#5, port B1

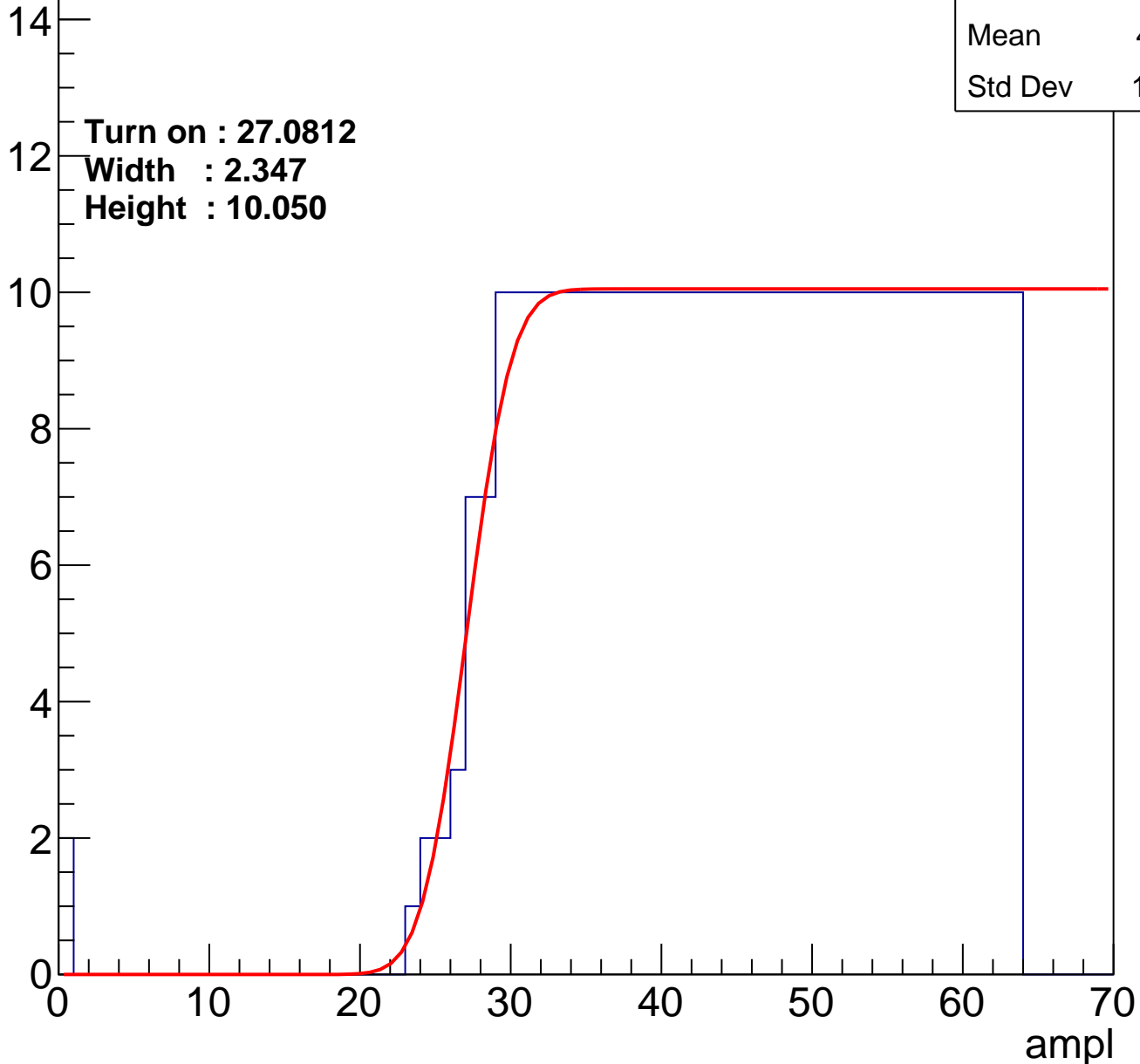
Entries	374
Mean	44.61
Std Dev	11.28

Turn on : 27.0812

Width : 2.347

Height : 10.050

Entry



B0L000S, U13-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	339
Mean	46.29
Std Dev	10.47

Turn on : 30.1385

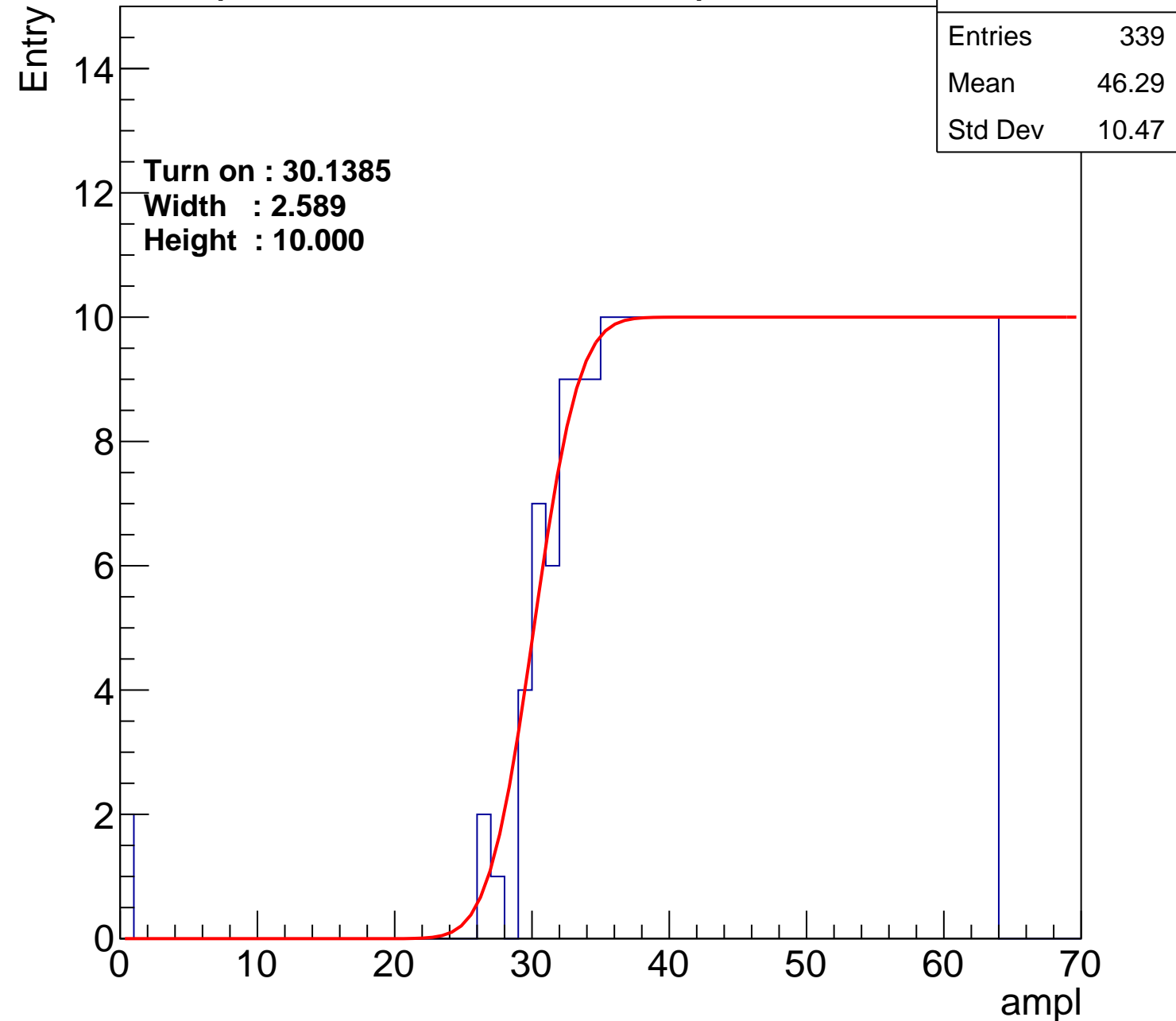
Width : 2.589

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 26.2204

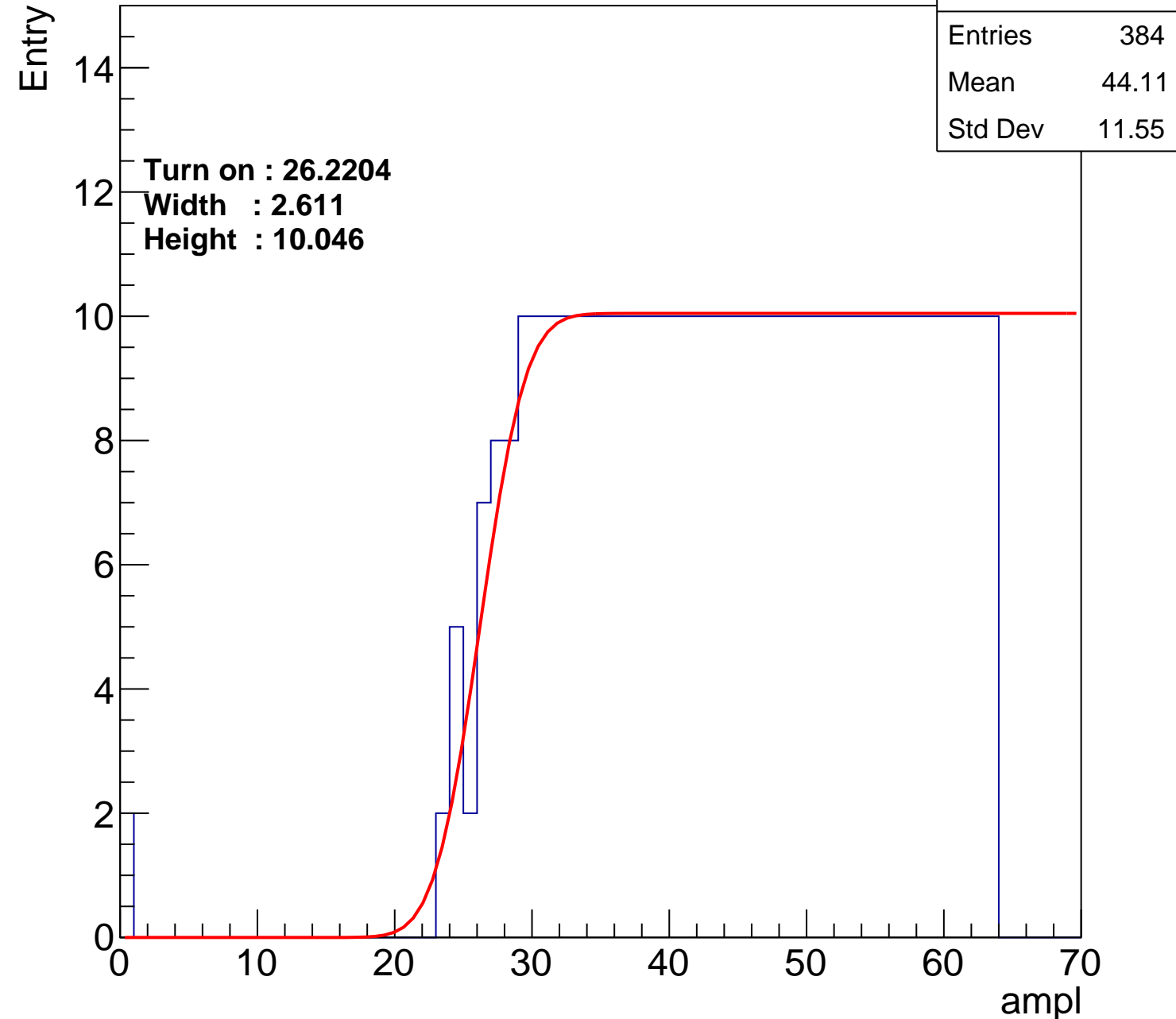
Width : 2.611

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch73

calib_packv5_042523_0143.root, FC#5, port B1

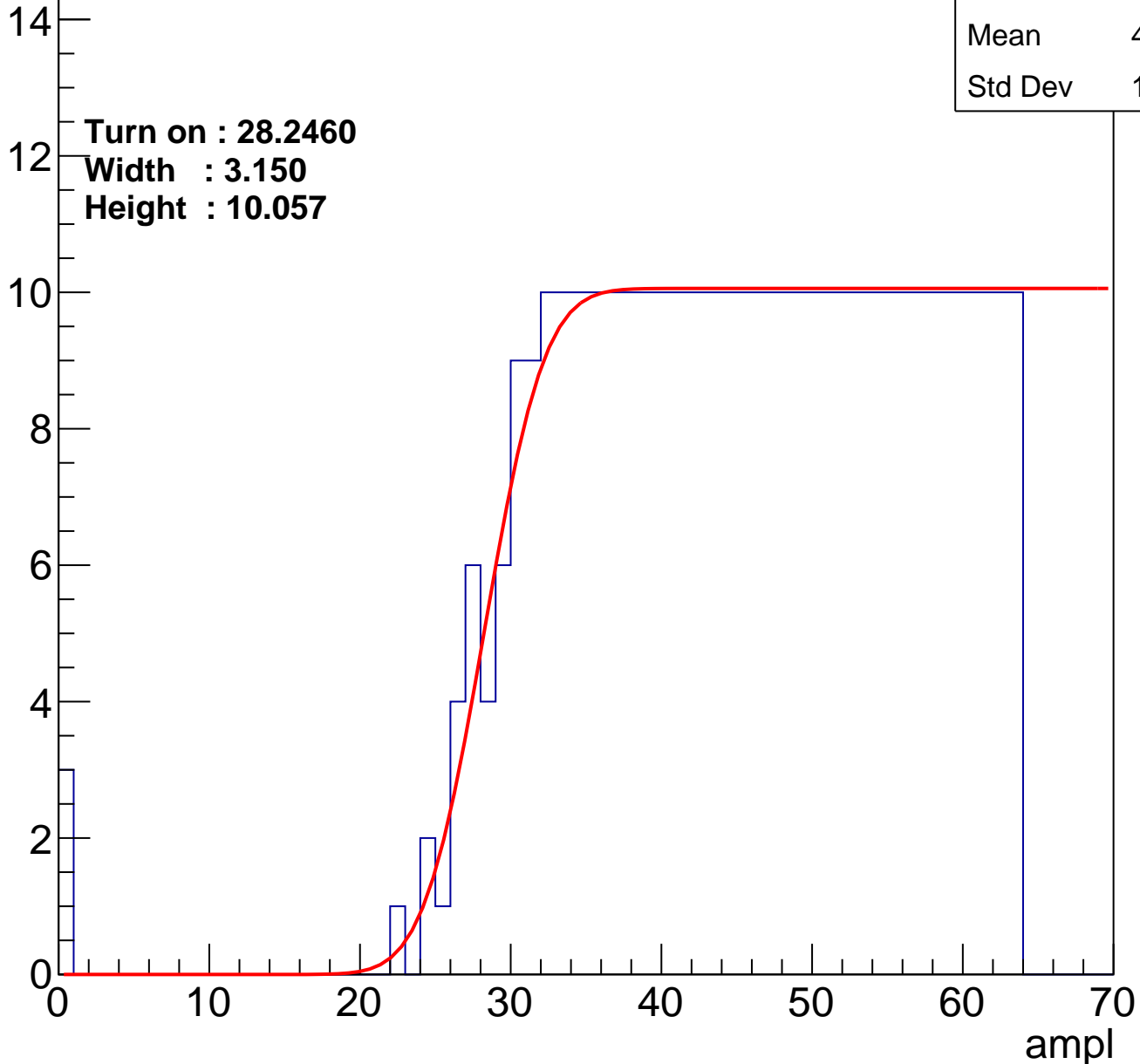
Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.2460

Width : 3.150

Height : 10.057

Entry



B0L000S, U13-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.1659

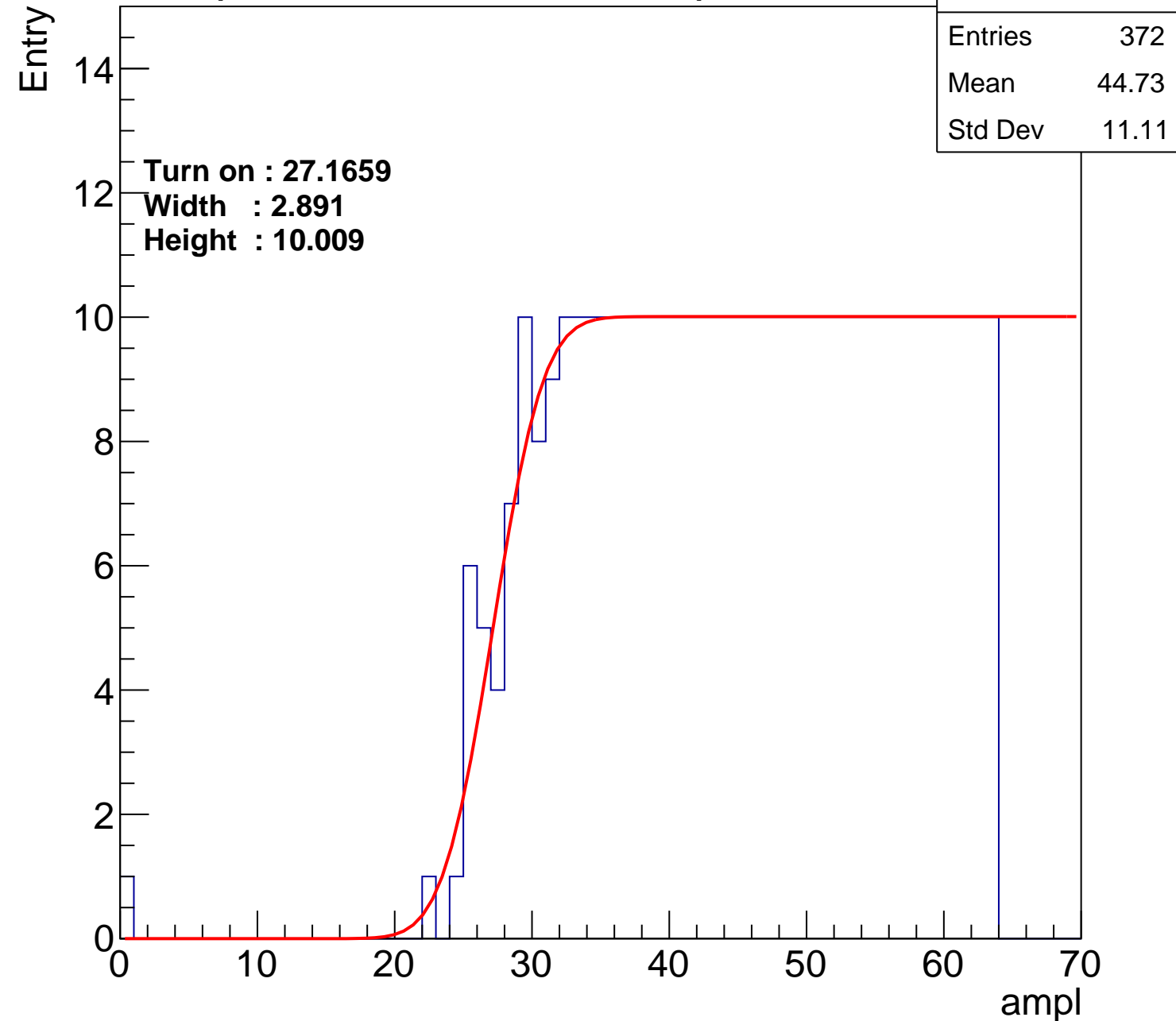
Width : 2.891

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.73
Std Dev	10.76

Turn on : 29.3099

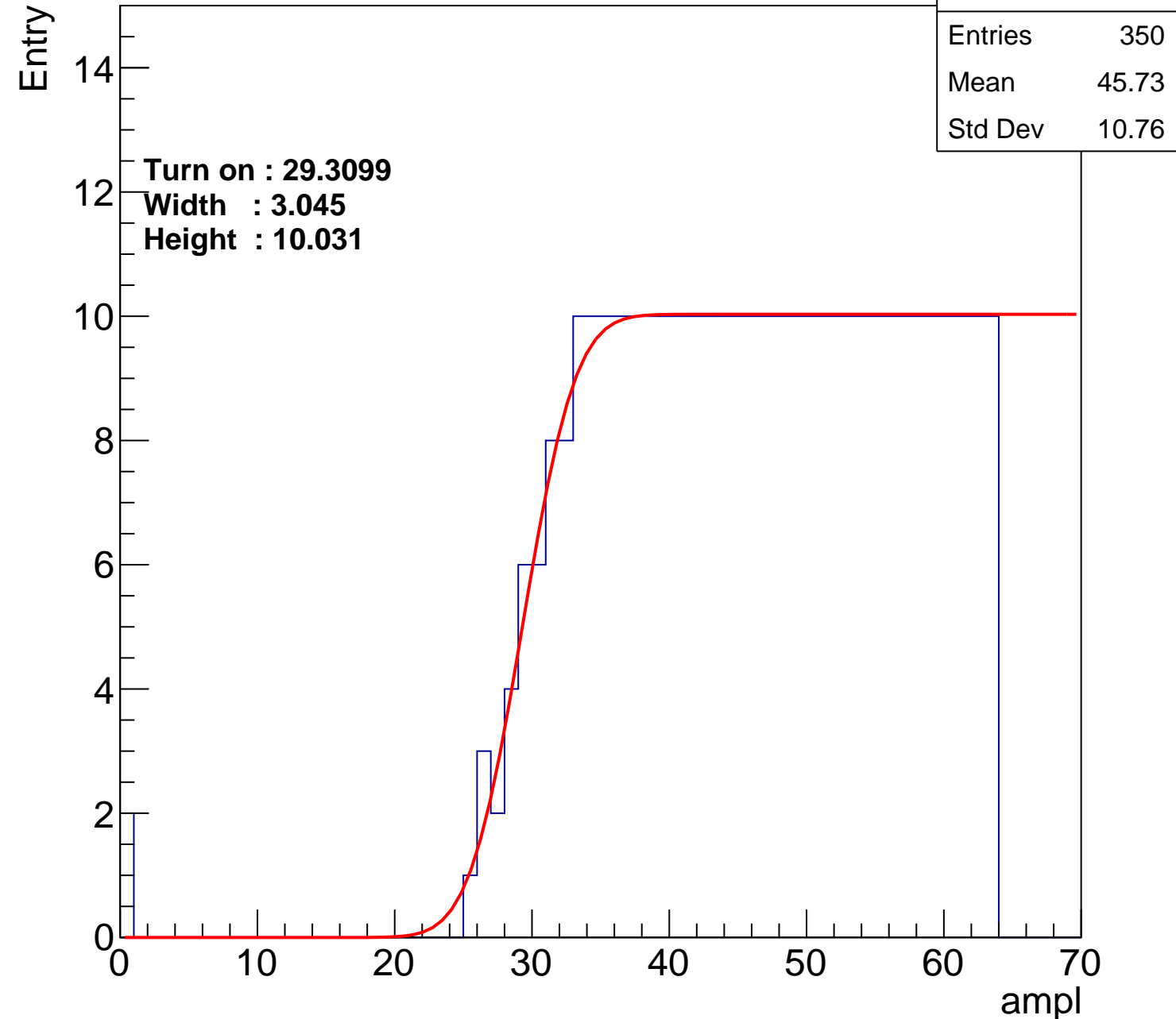
Width : 3.045

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch76

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.3
Std Dev	10.77

Turn on : 28.1806

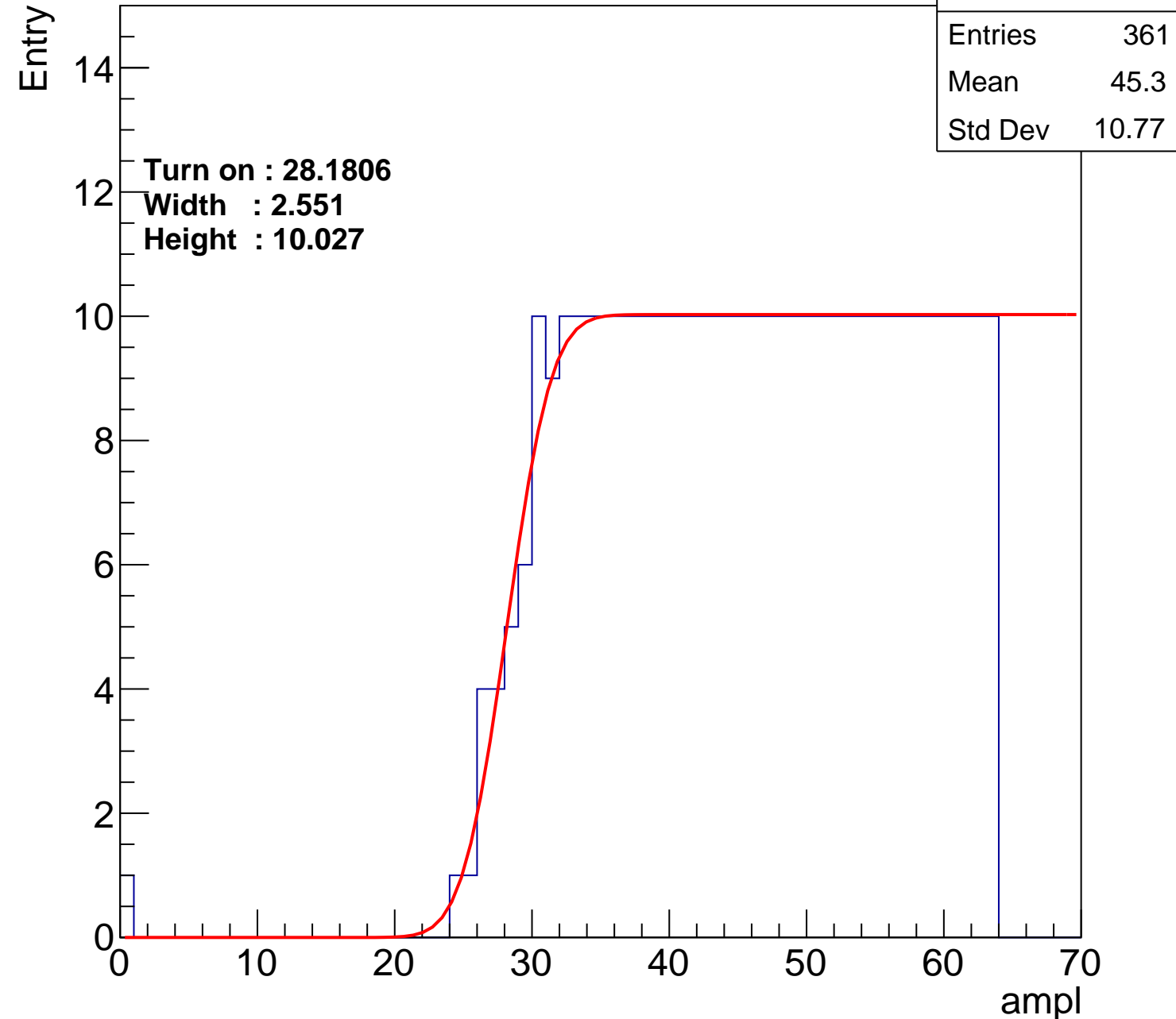
Width : 2.551

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.42
Std Dev	11.14

Turn on : 28.9267

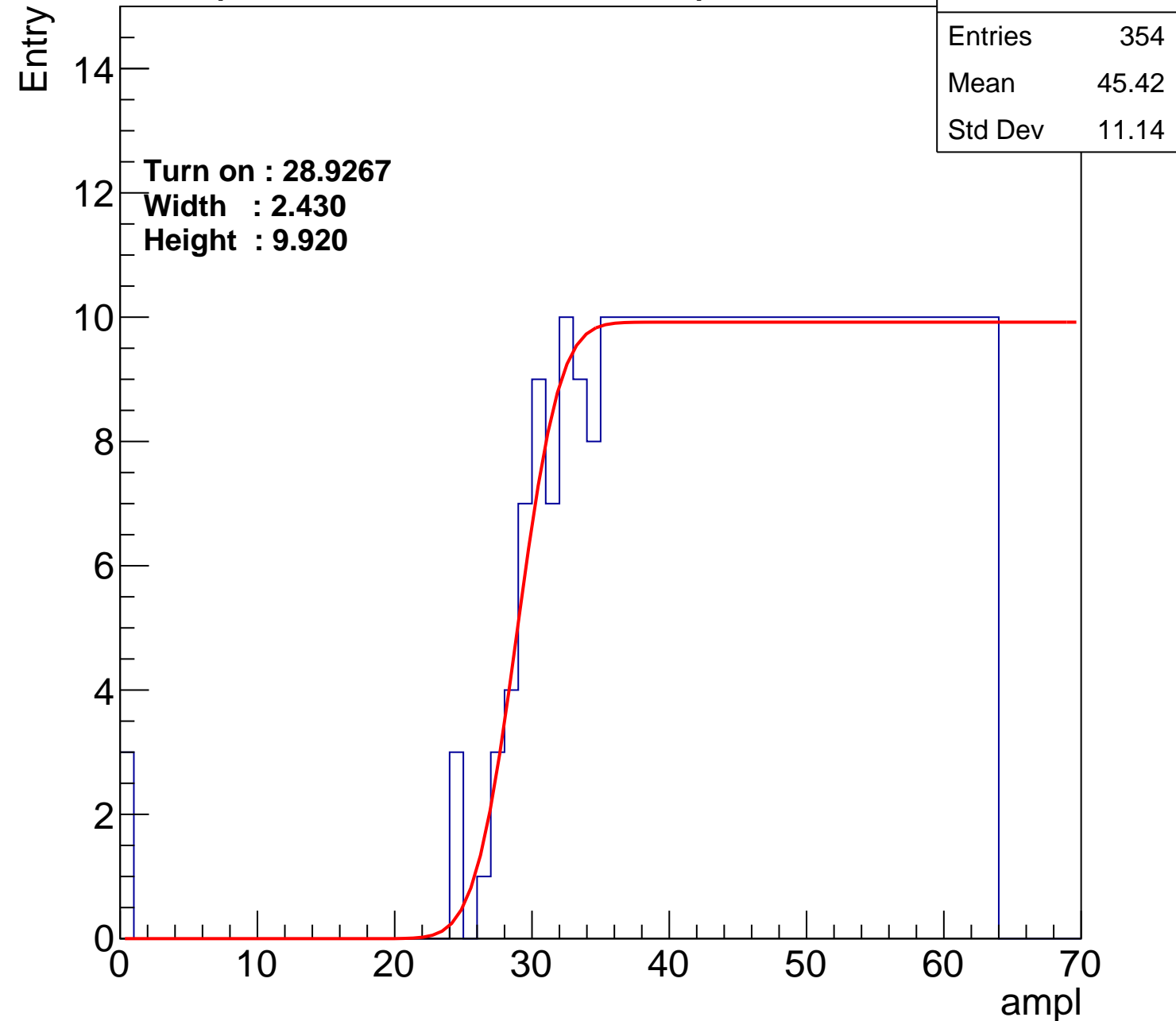
Width : 2.430

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.33
Std Dev	11.04

Turn on : 29.1447

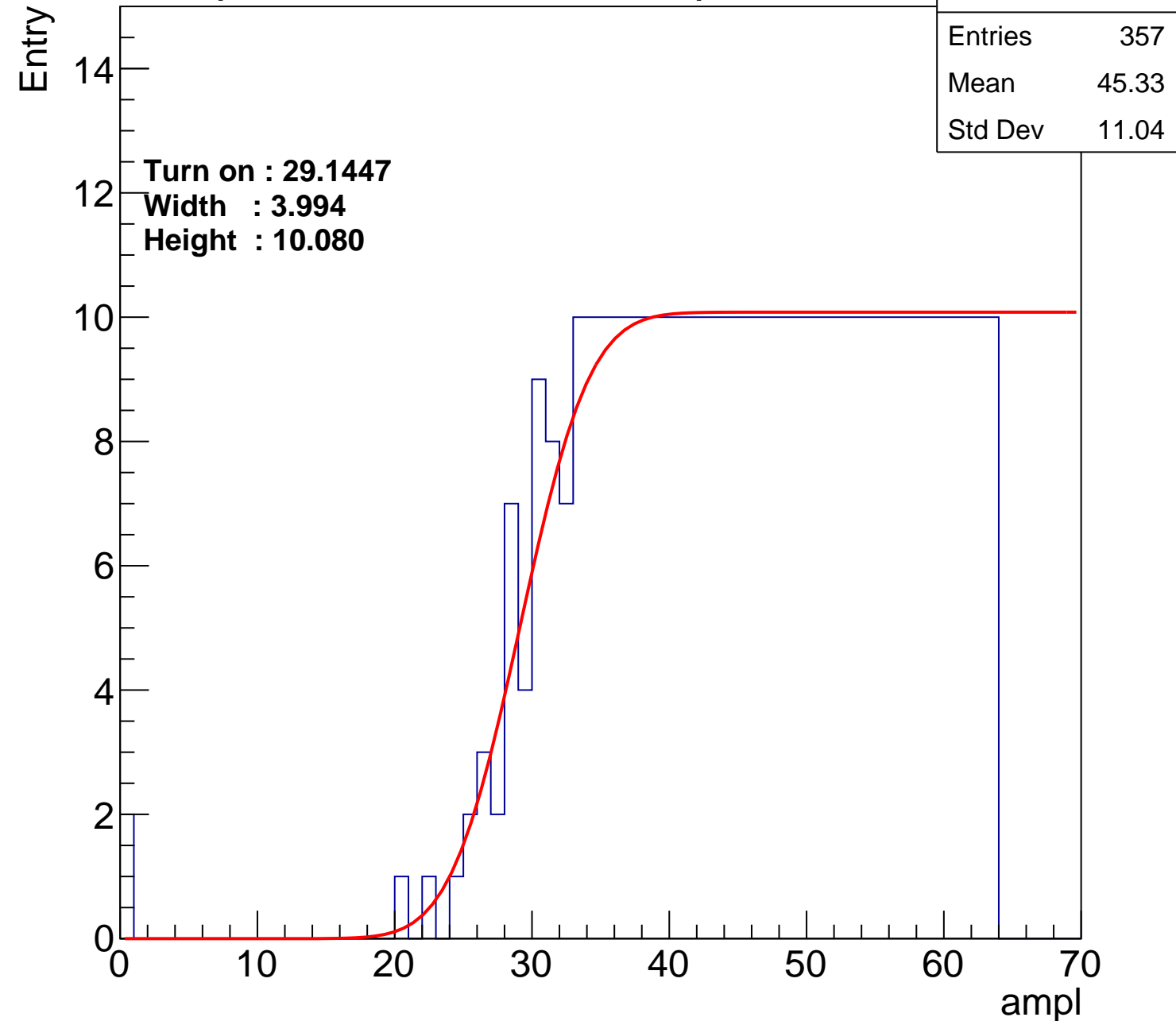
Width : 3.994

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	346
Mean	45.92
Std Dev	10.69

Turn on : 29.9318

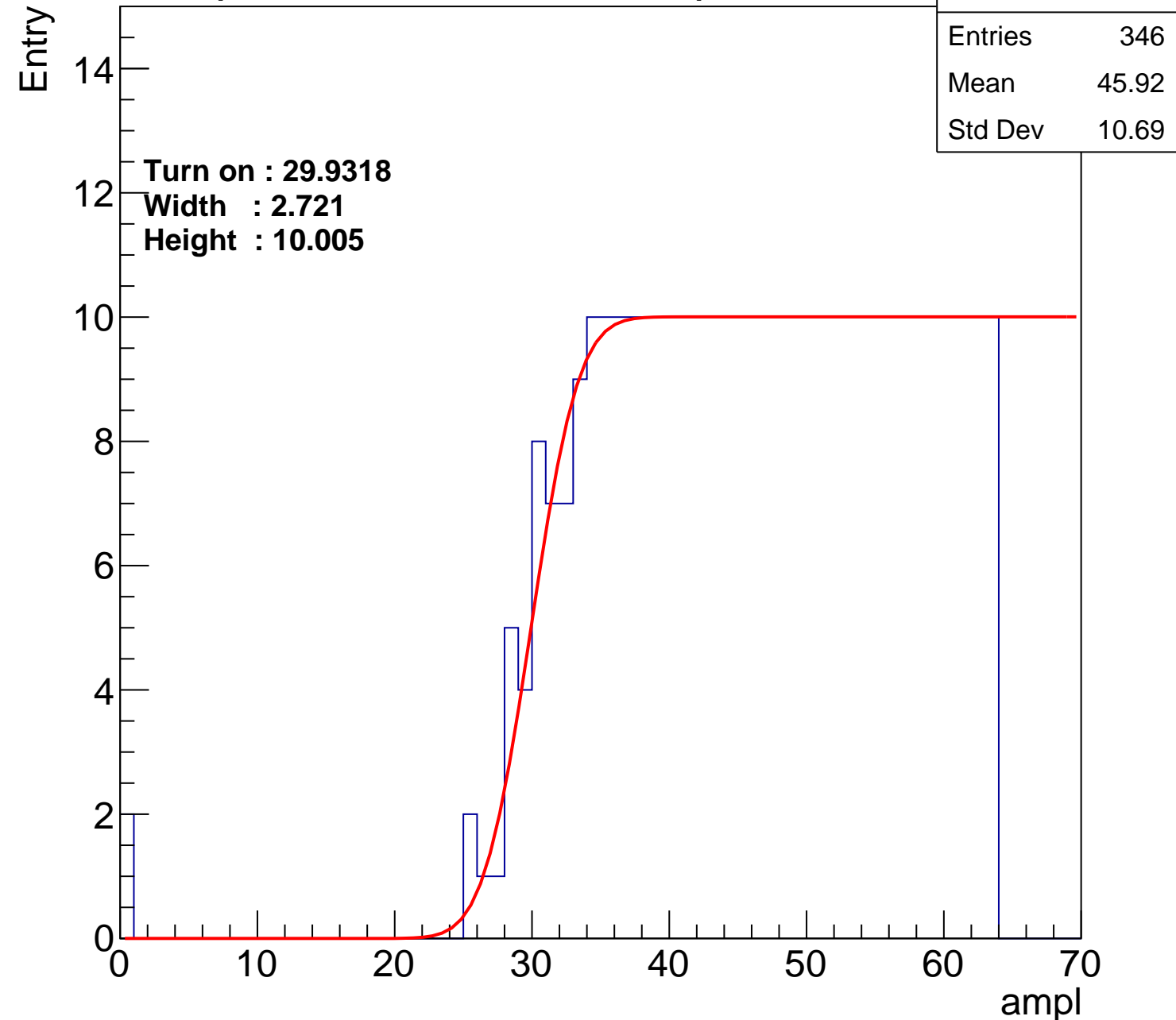
Width : 2.721

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.41
Std Dev	11.38

Turn on : 30.2677

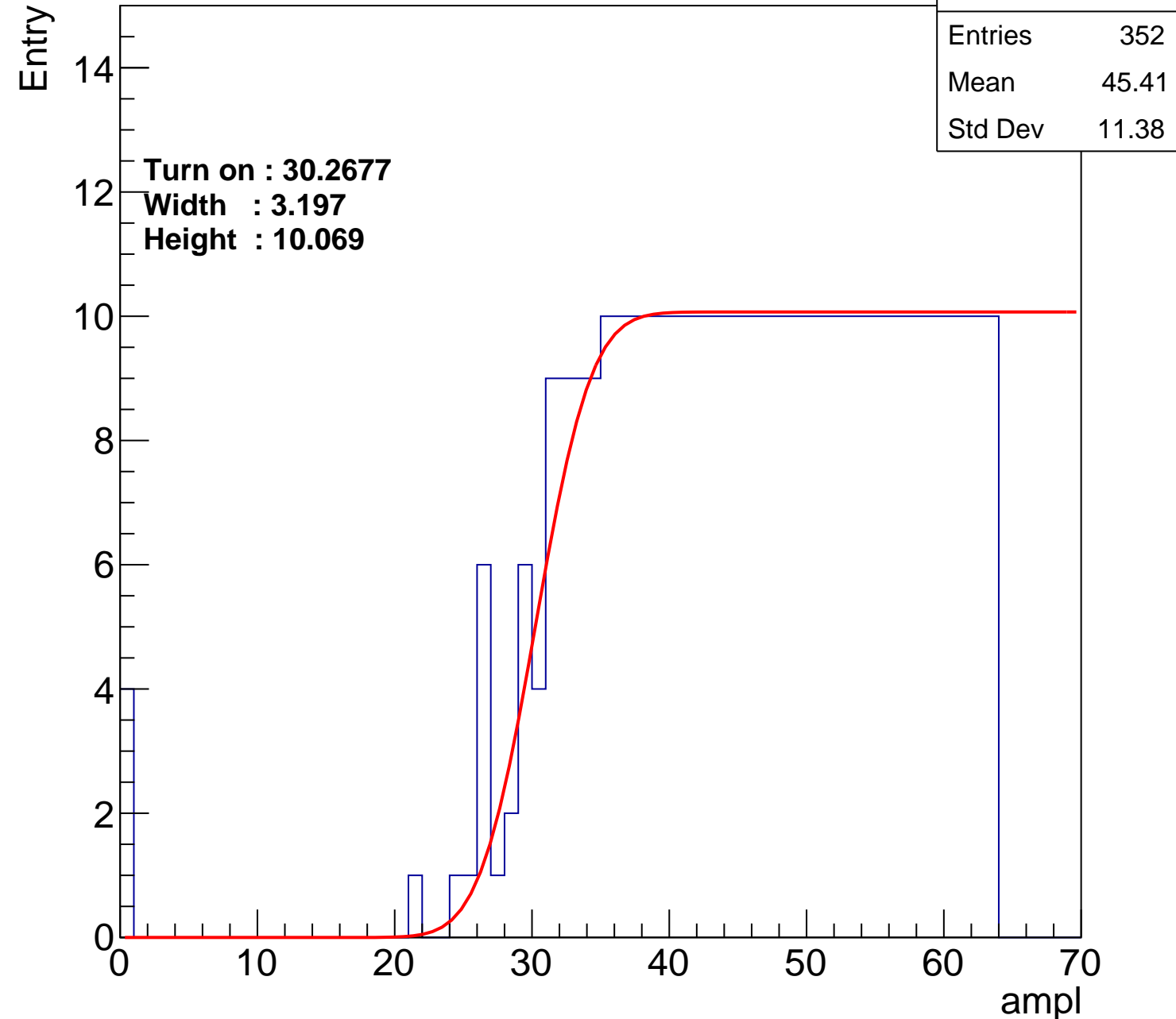
Width : 3.197

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.72
Std Dev	10.58

Turn on : 29.4503

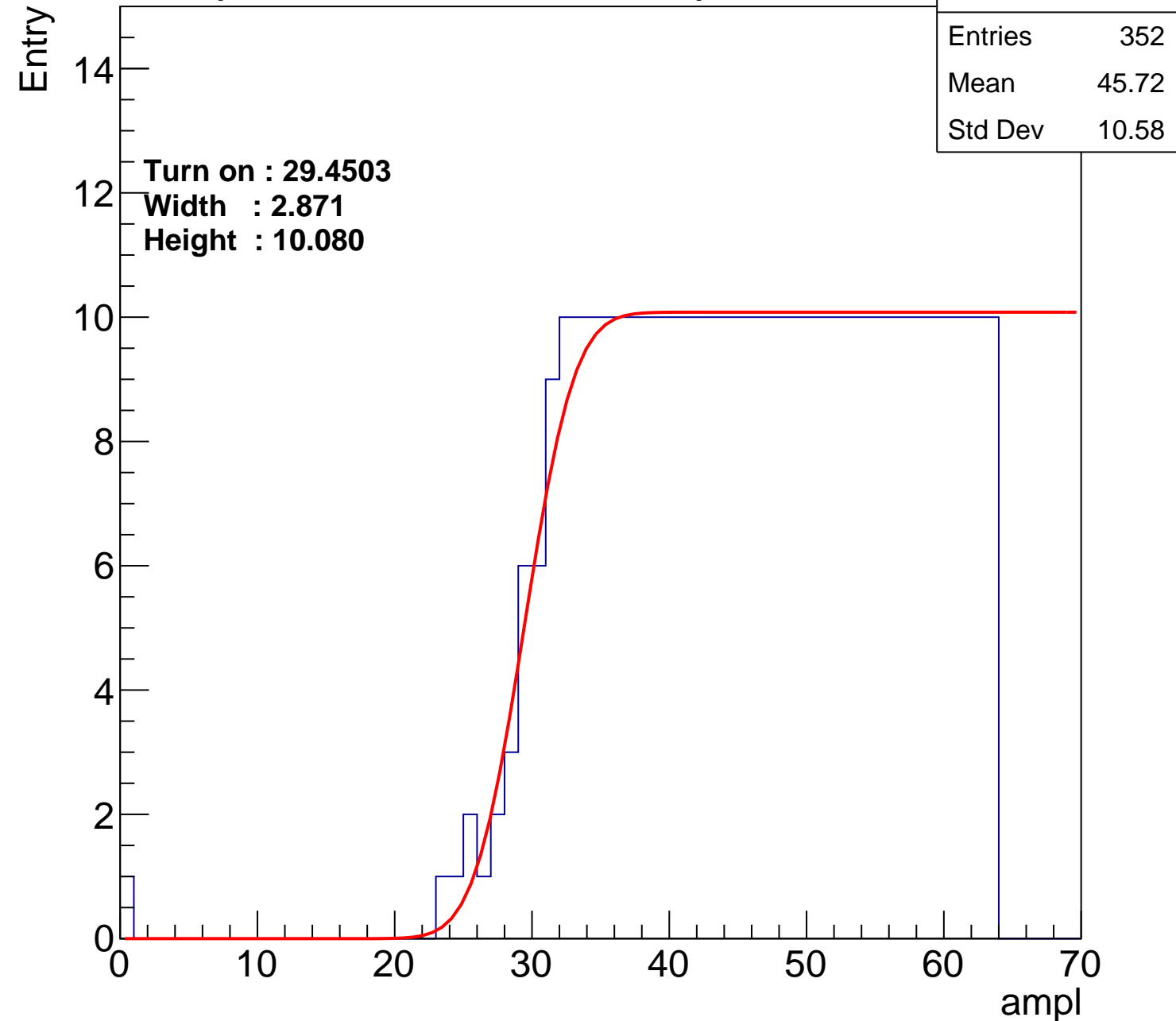
Width : 2.871

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.74
Std Dev	11.08

Turn on : 27.4543

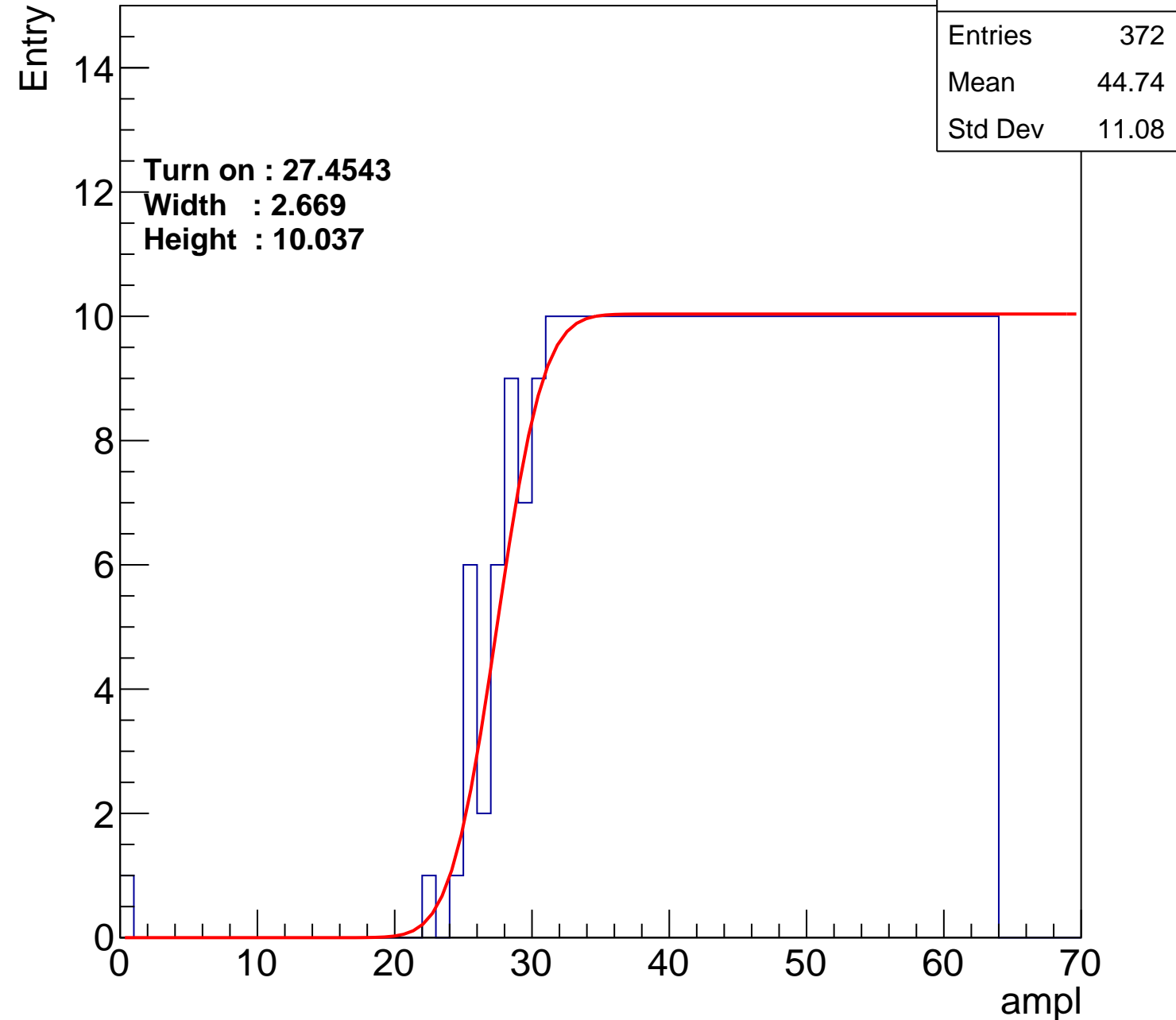
Width : 2.669

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch83

calib_packv5_042523_0143.root, FC#5, port B1

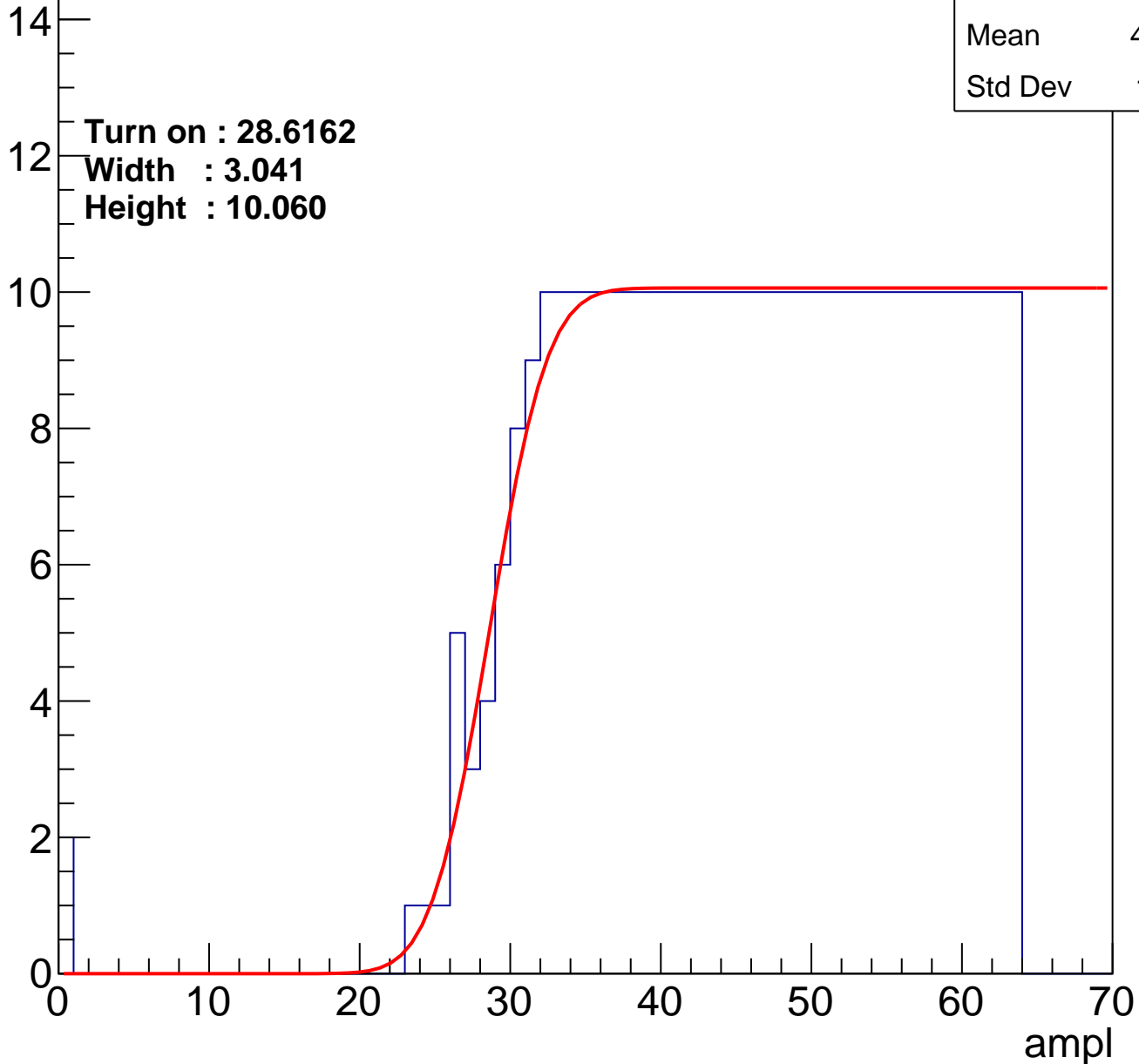
Entries	360
Mean	45.24
Std Dev	11.01

Turn on : 28.6162

Width : 3.041

Height : 10.060

Entry



B0L000S, U13-ch84

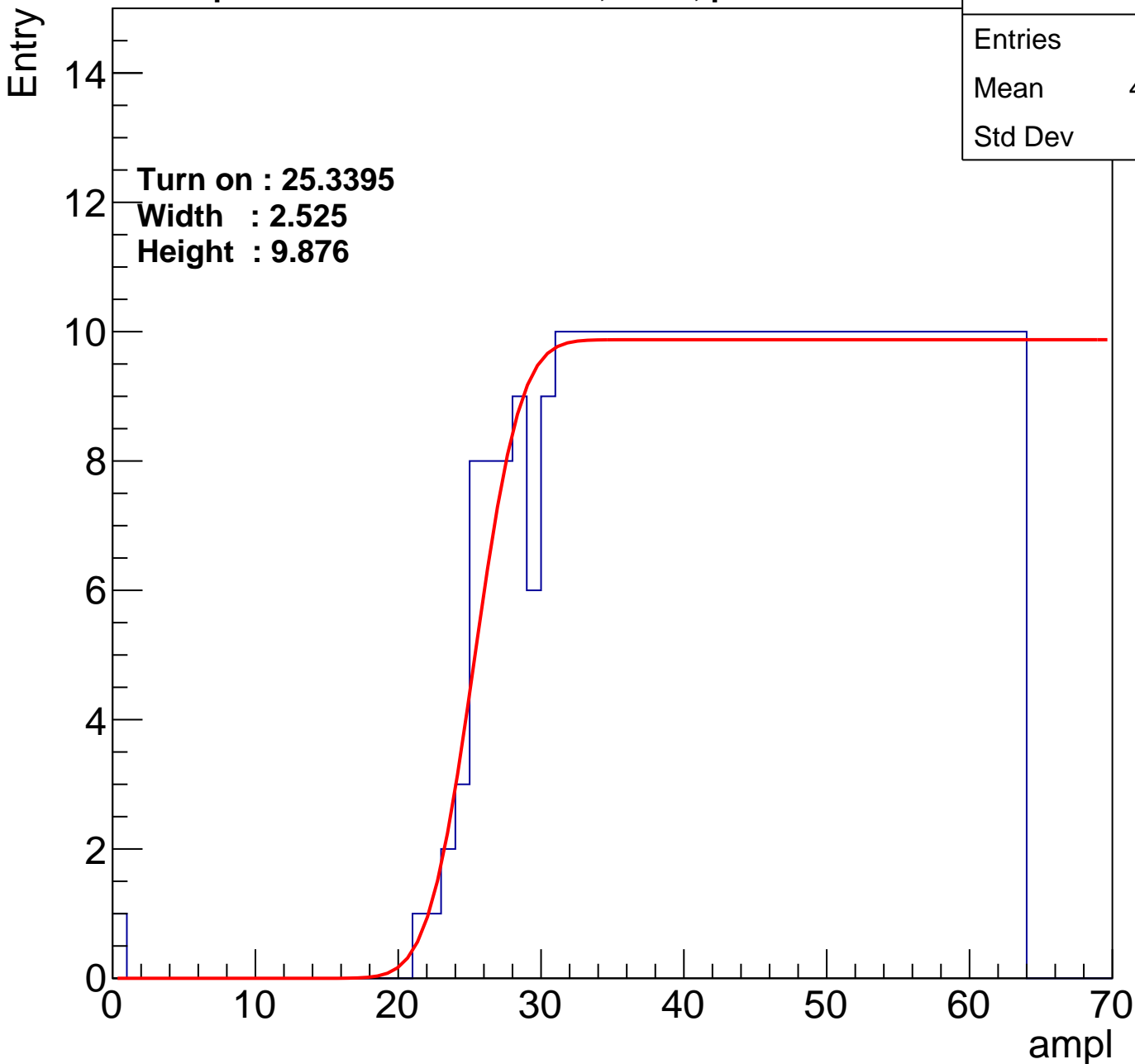
calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	44.02
Std Dev	11.51

Turn on : 25.3395

Width : 2.525

Height : 9.876



B0L000S, U13-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 27.2795

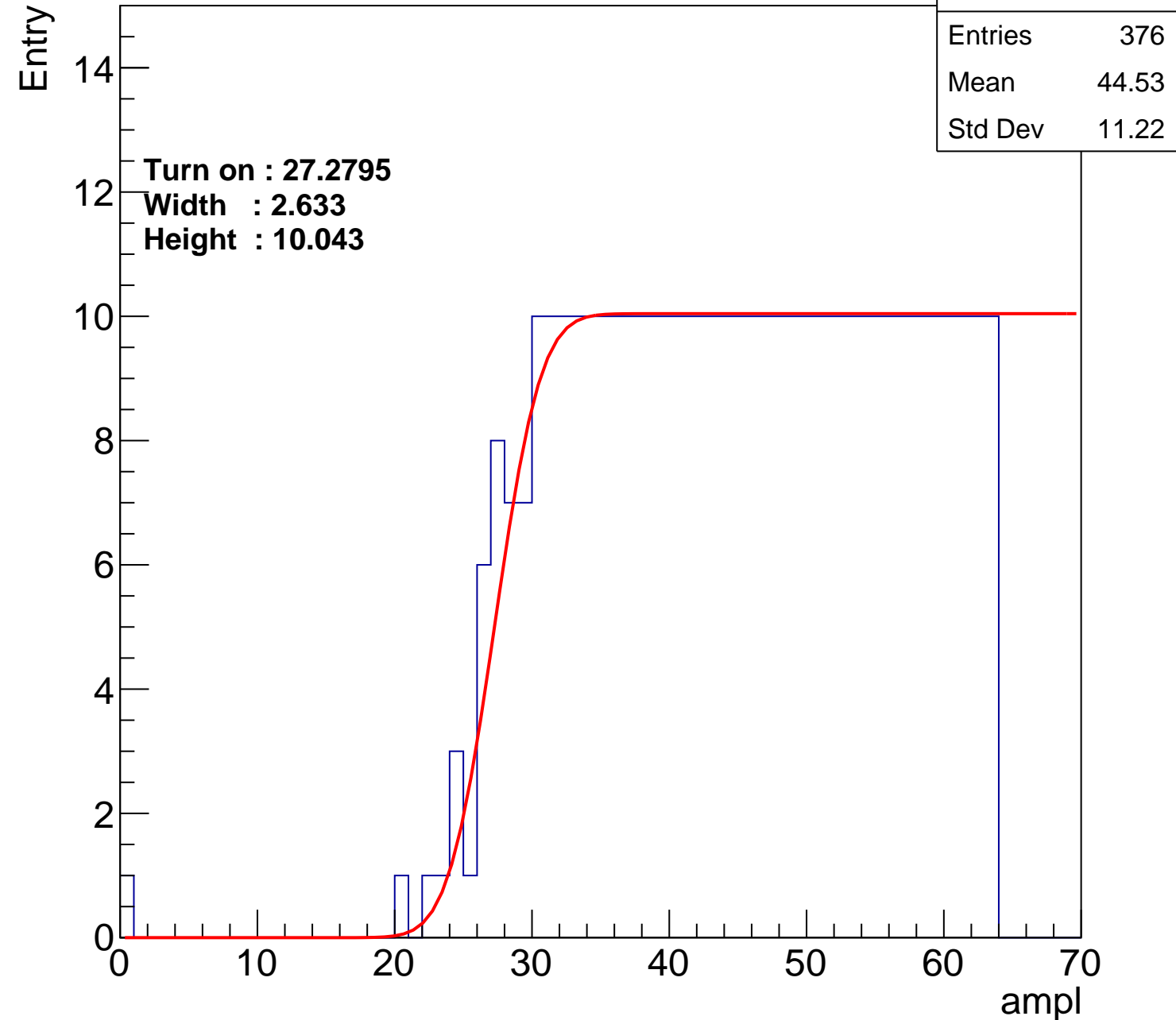
Width : 2.633

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 26.0298

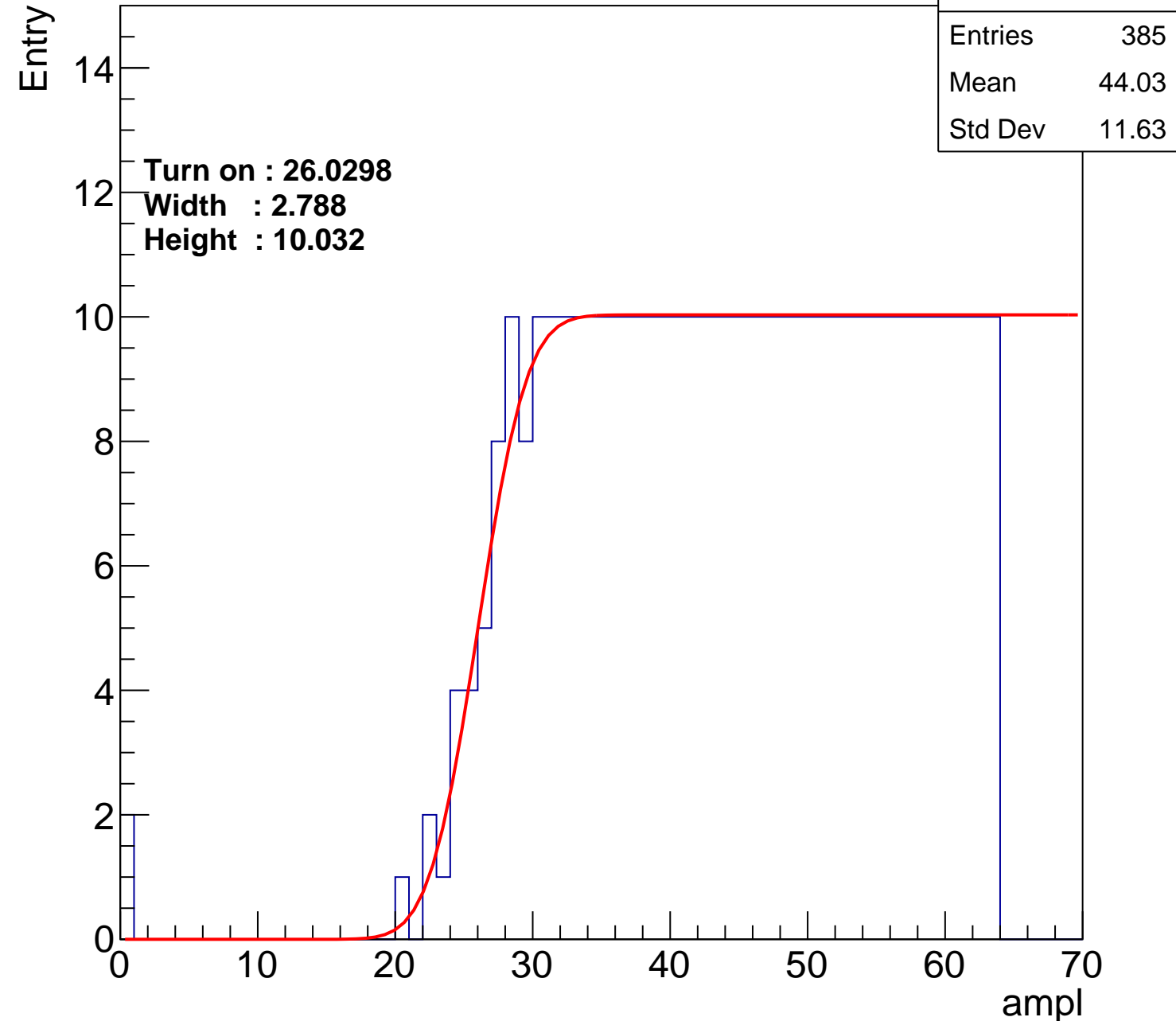
Width : 2.788

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.07
Std Dev	10.94

Turn on : 27.4325

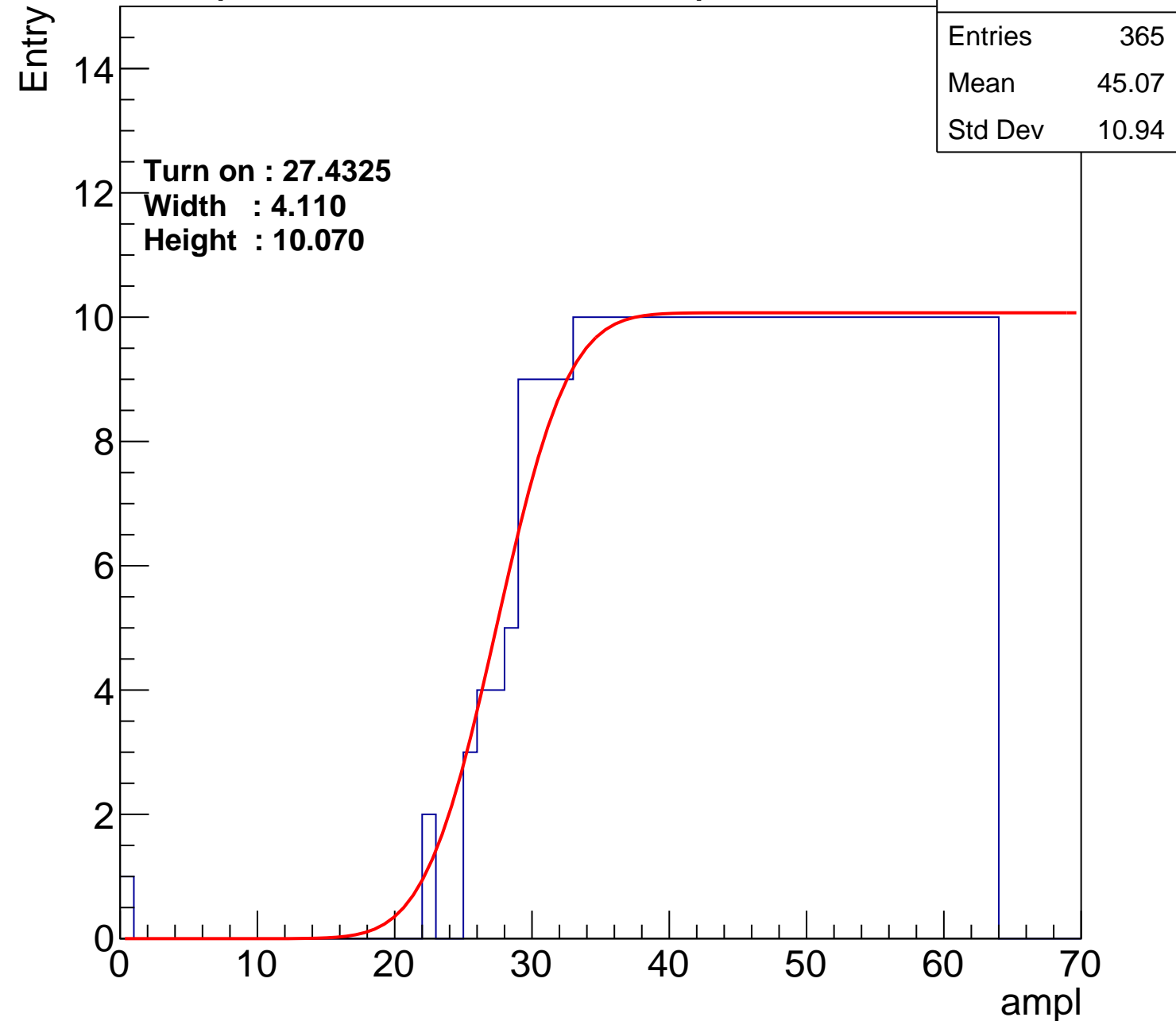
Width : 4.110

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.32
Std Dev	11.2

Turn on : 28.8052

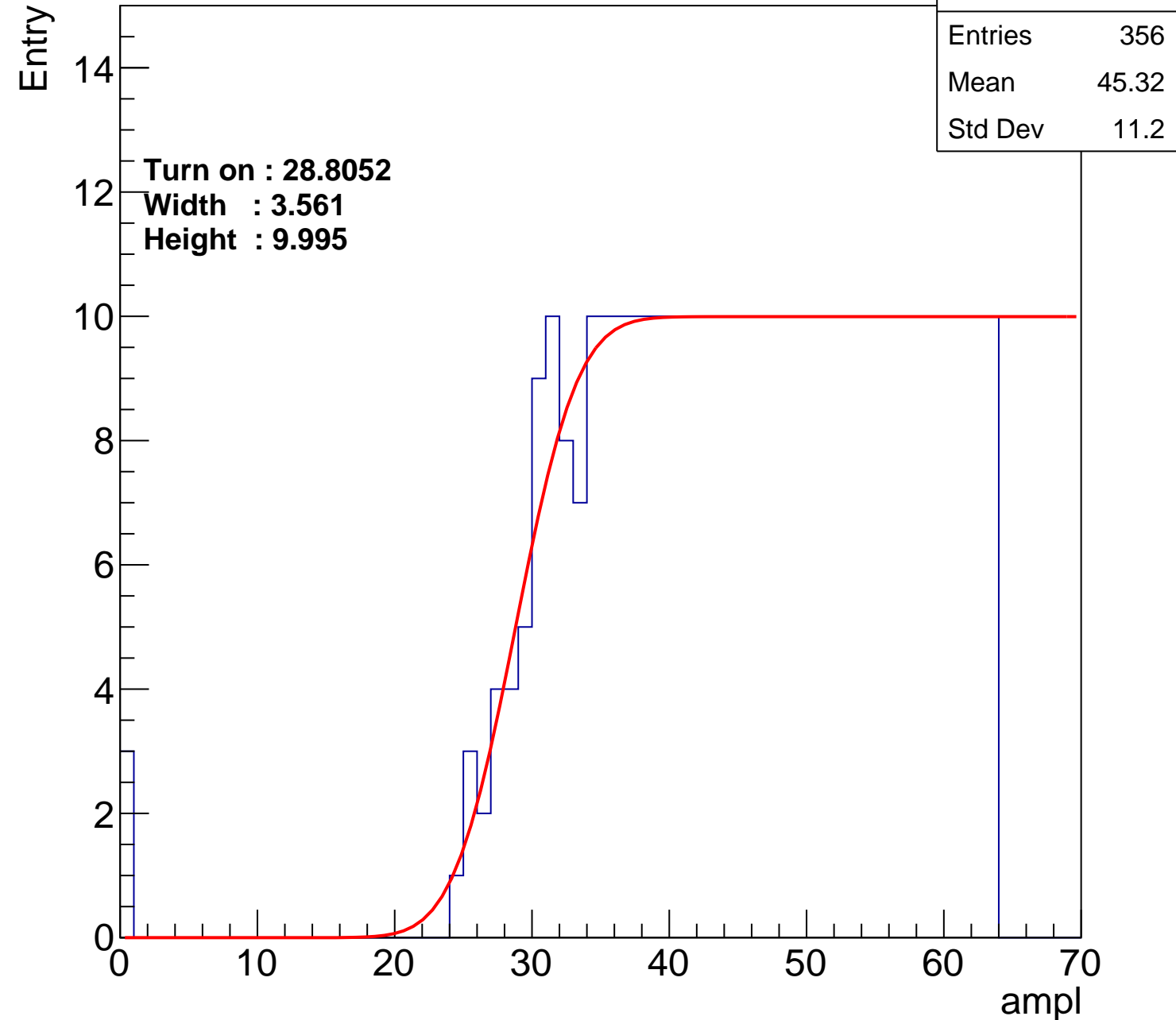
Width : 3.561

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch89

calib_packv5_042523_0143.root, FC#5, port B1

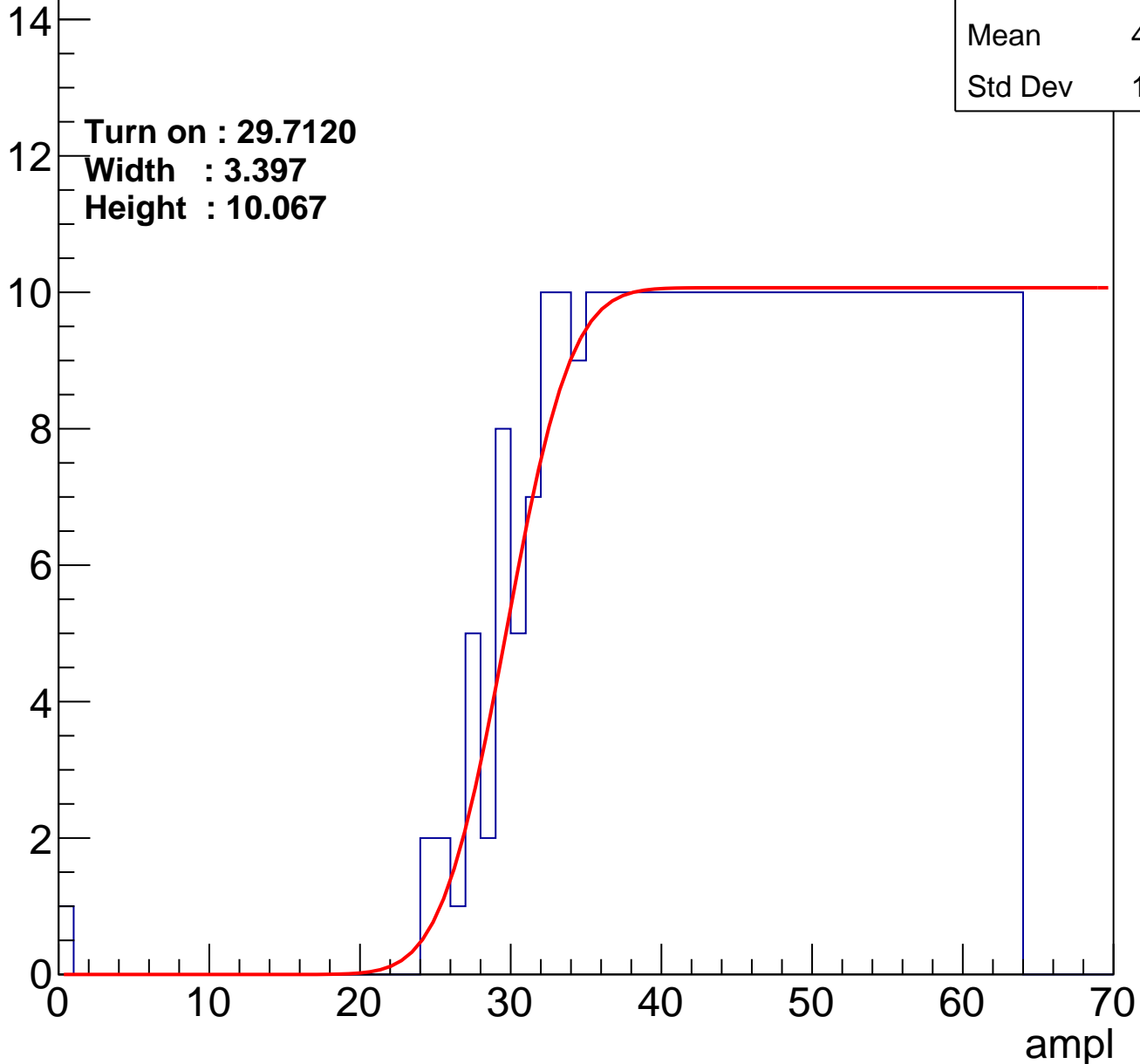
Entries	352
Mean	45.68
Std Dev	10.63

Turn on : 29.7120

Width : 3.397

Height : 10.067

Entry



B0L000S, U13-ch90

calib_packv5_042523_0143.root, FC#5, port B1

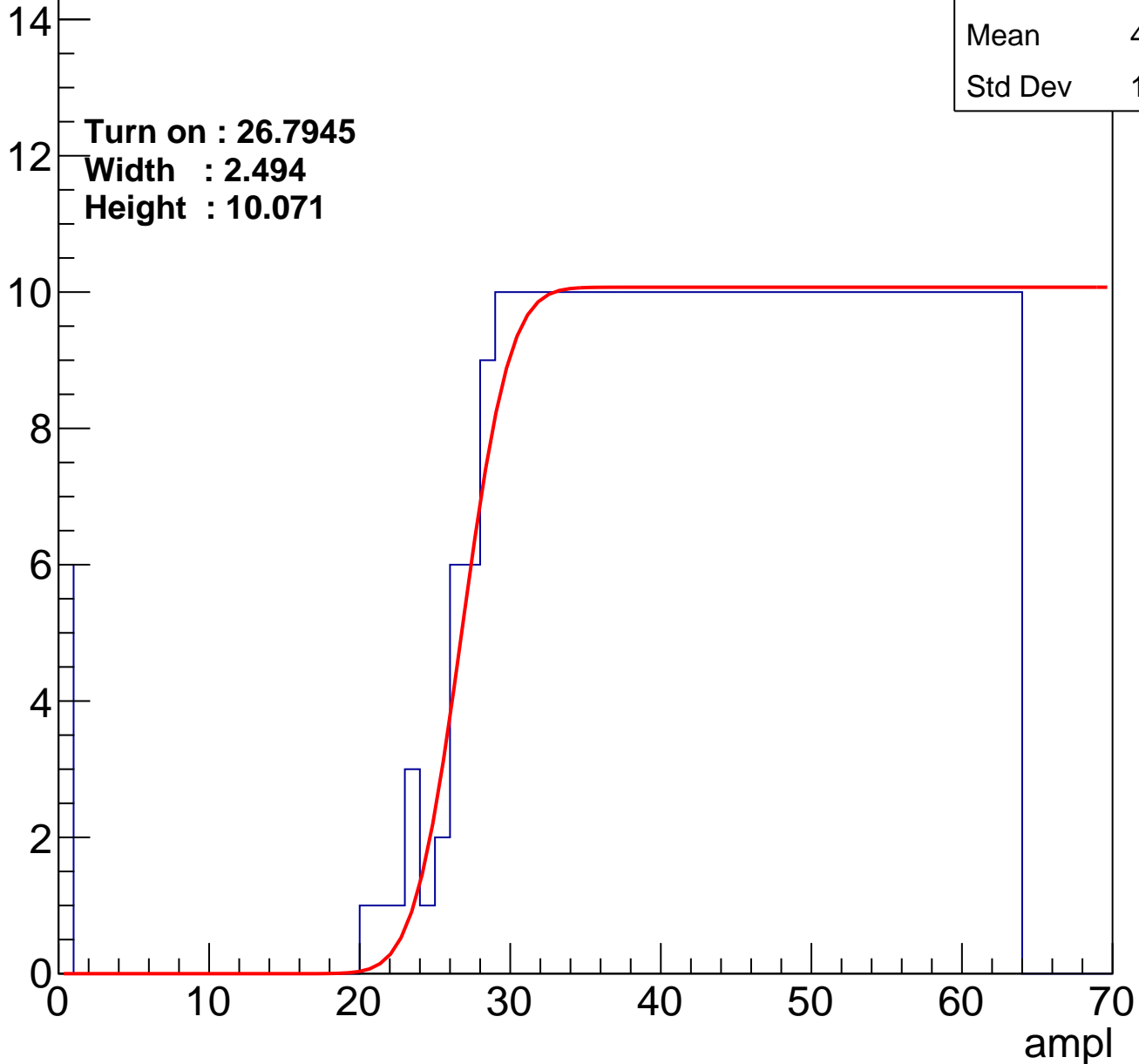
Entries	386
Mean	43.72
Std Dev	12.33

Turn on : 26.7945

Width : 2.494

Height : 10.071

Entry



B0L000S, U13-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.54
Std Dev	11.44

Turn on : 27.2607

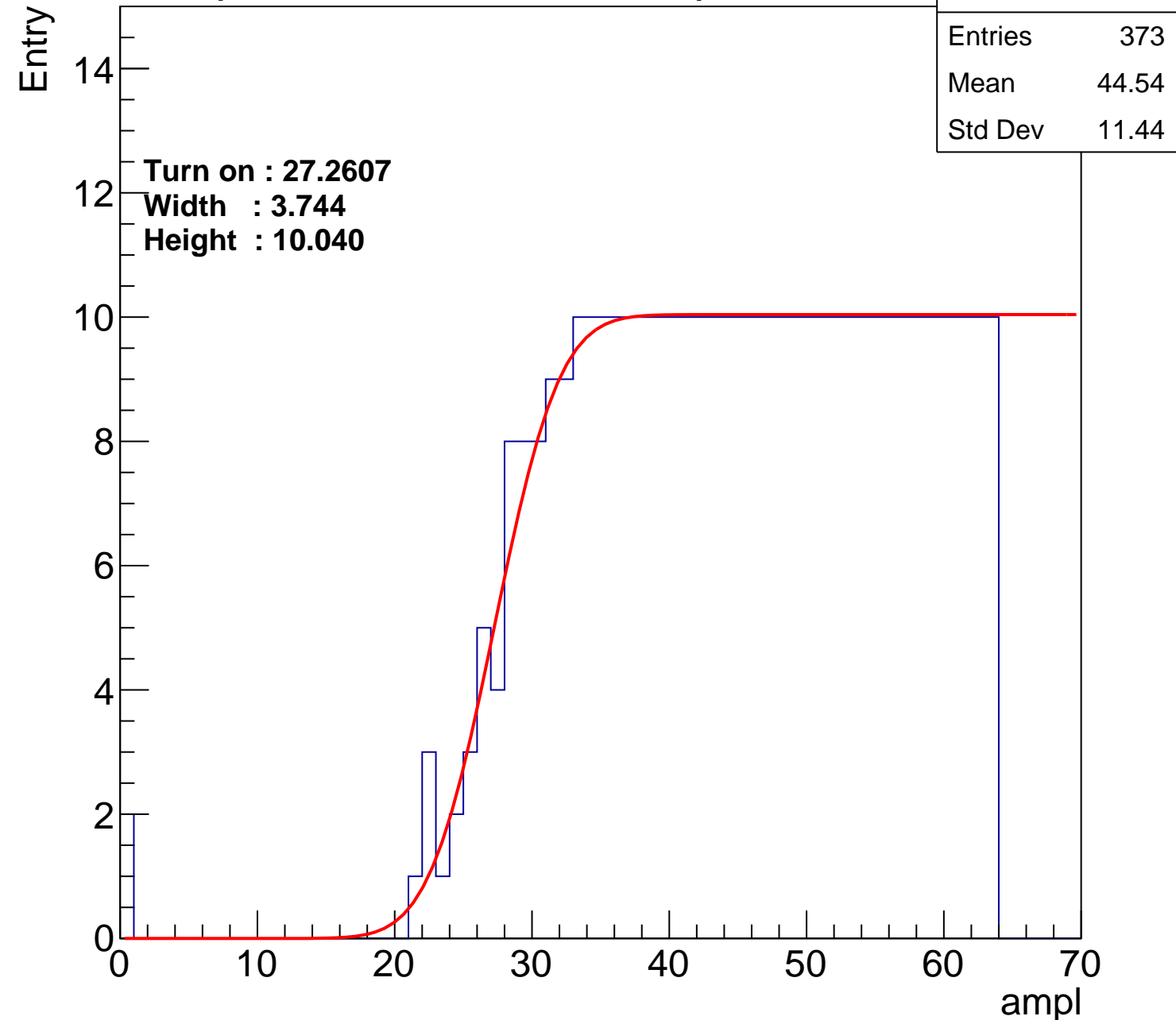
Width : 3.744

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.8837

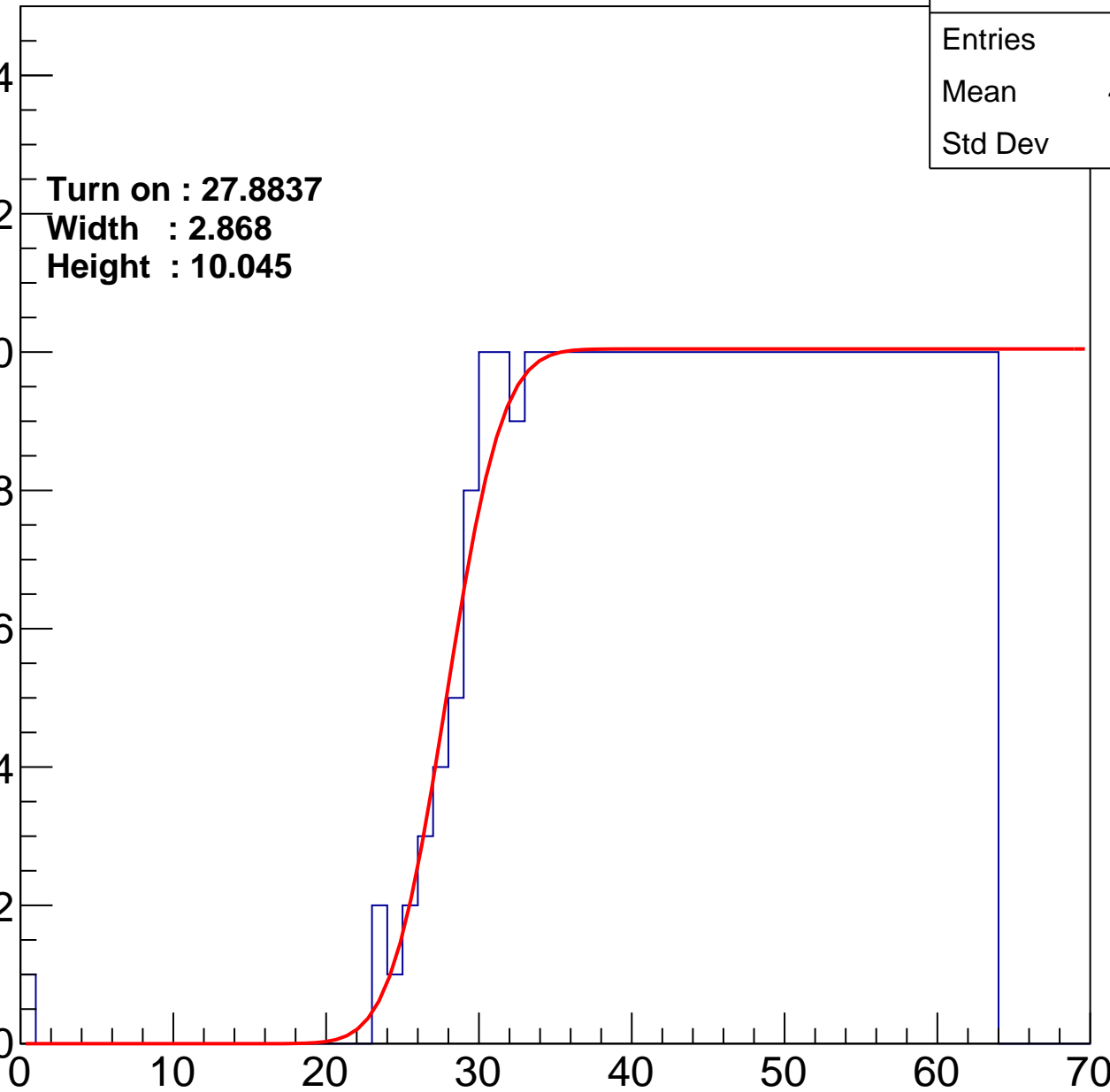
Width : 2.868

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch93

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.2348

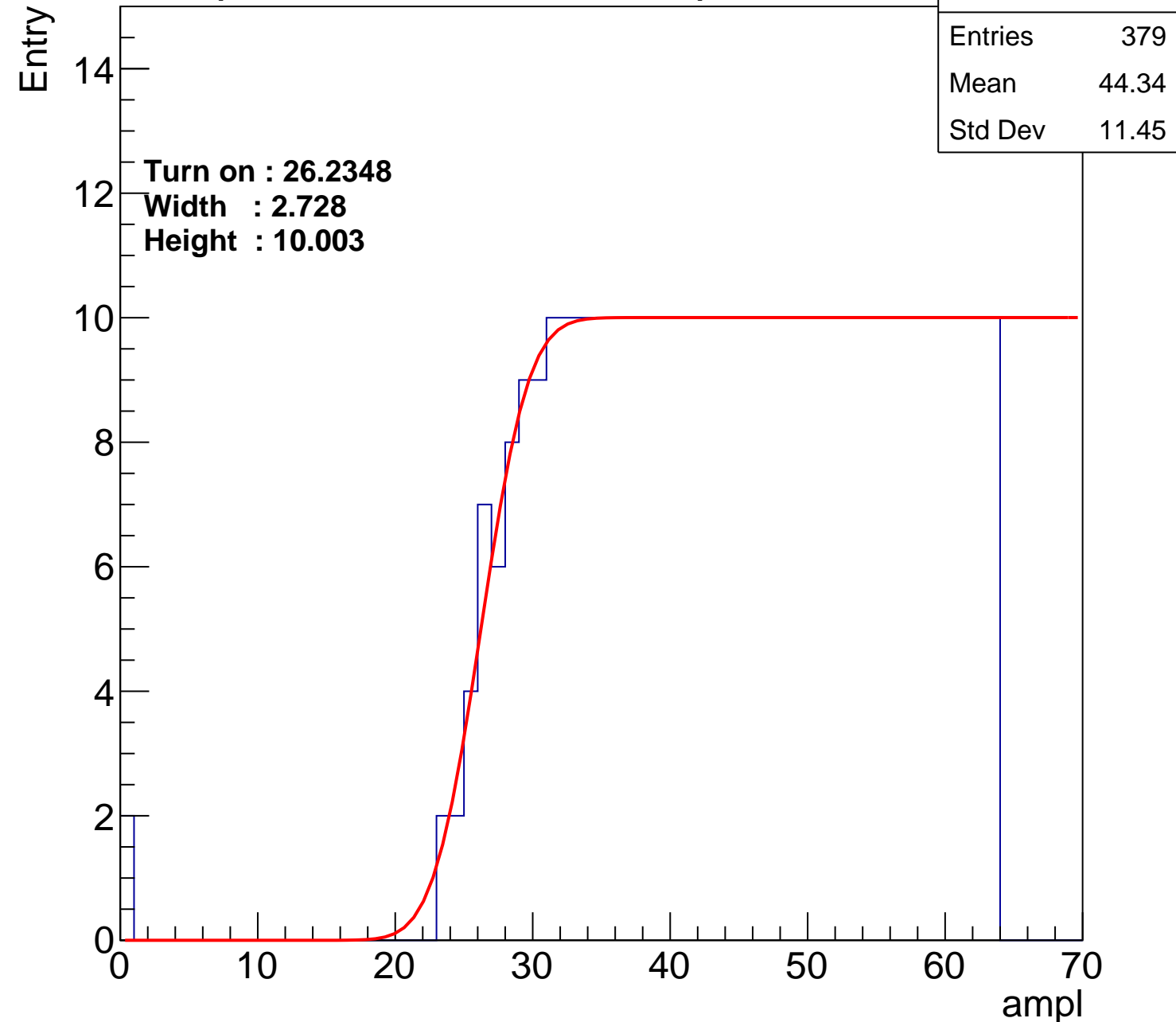
Width : 2.728

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch94

calib_packv5_042523_0143.root, FC#5, port B1

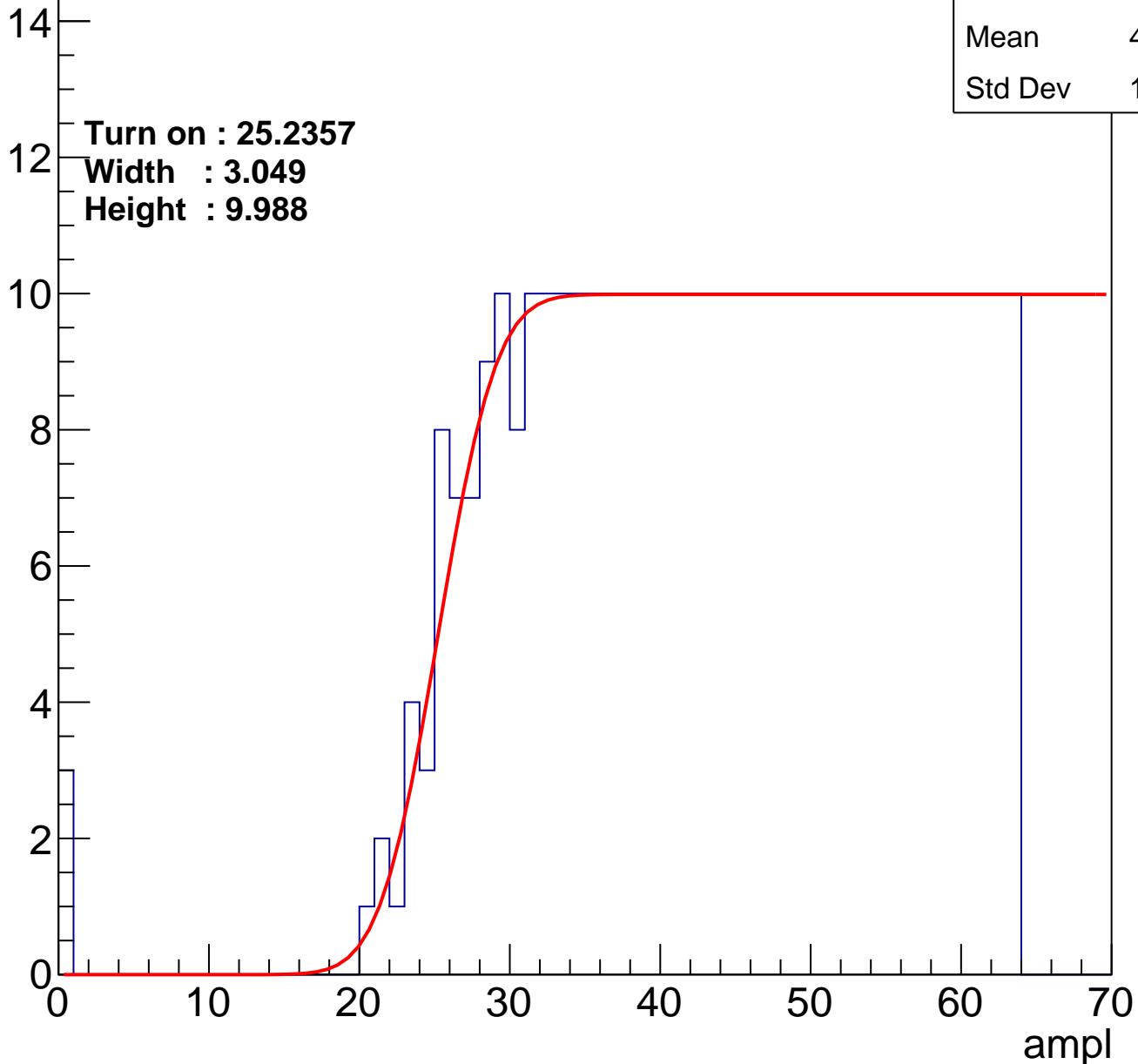
Entries	393
Mean	43.54
Std Dev	12.04

Turn on : 25.2357

Width : 3.049

Height : 9.988

Entry



B0L000S, U13-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	44.96
Std Dev	11.48

Turn on : 28.2037

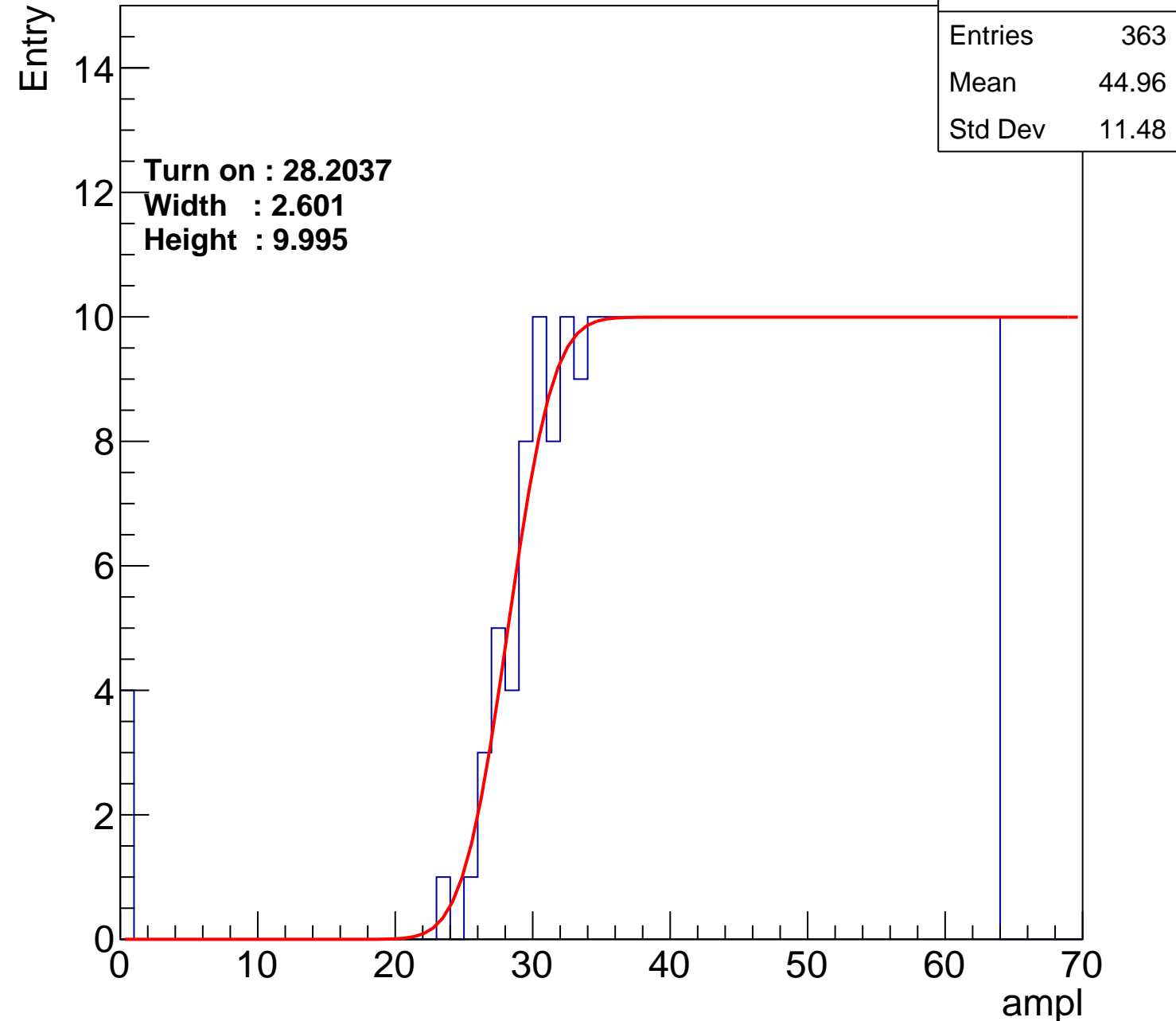
Width : 2.601

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.65
Std Dev	11.45

Turn on : 27.2758

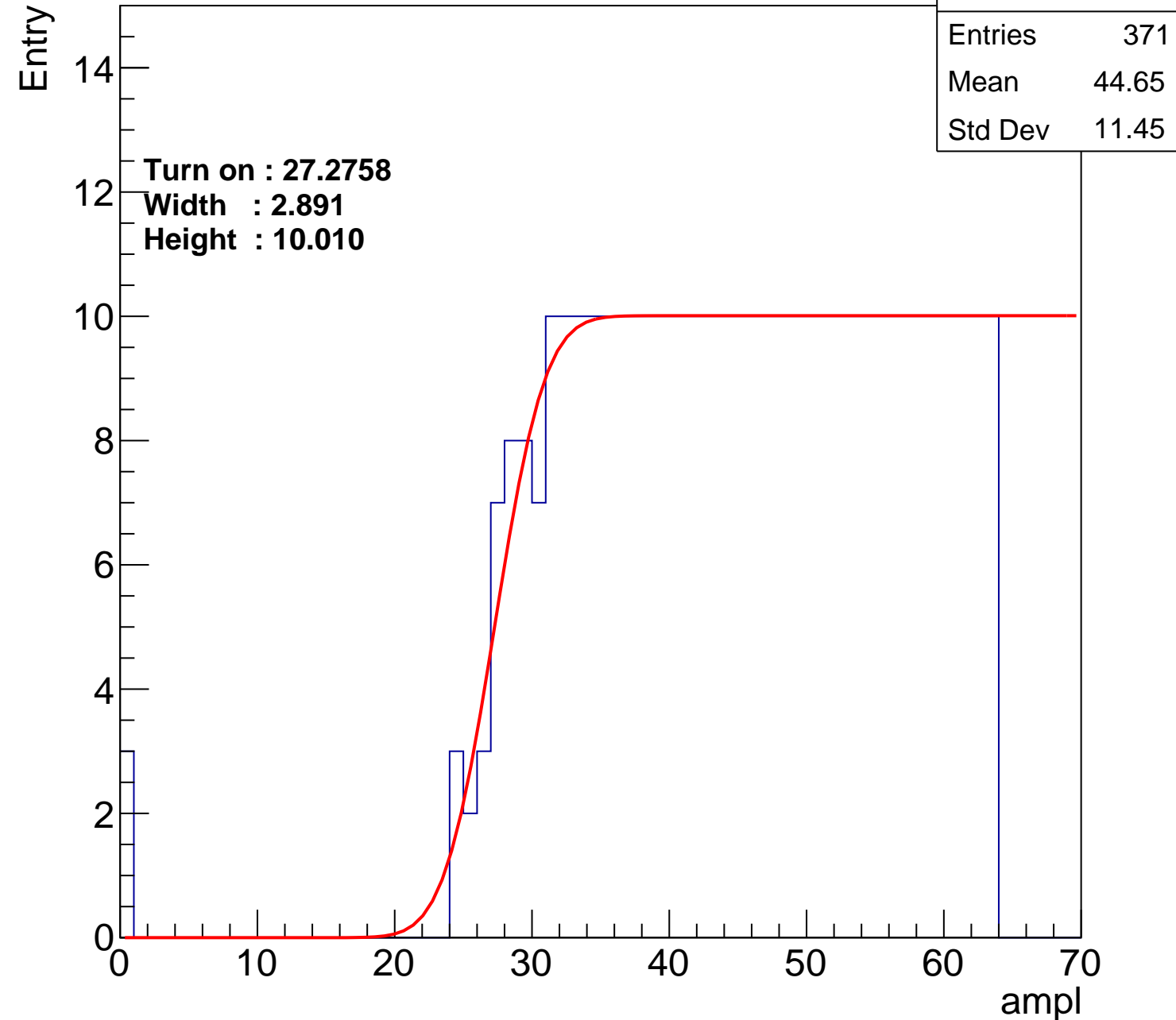
Width : 2.891

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.1
Std Dev	11.86

Turn on : 26.5656

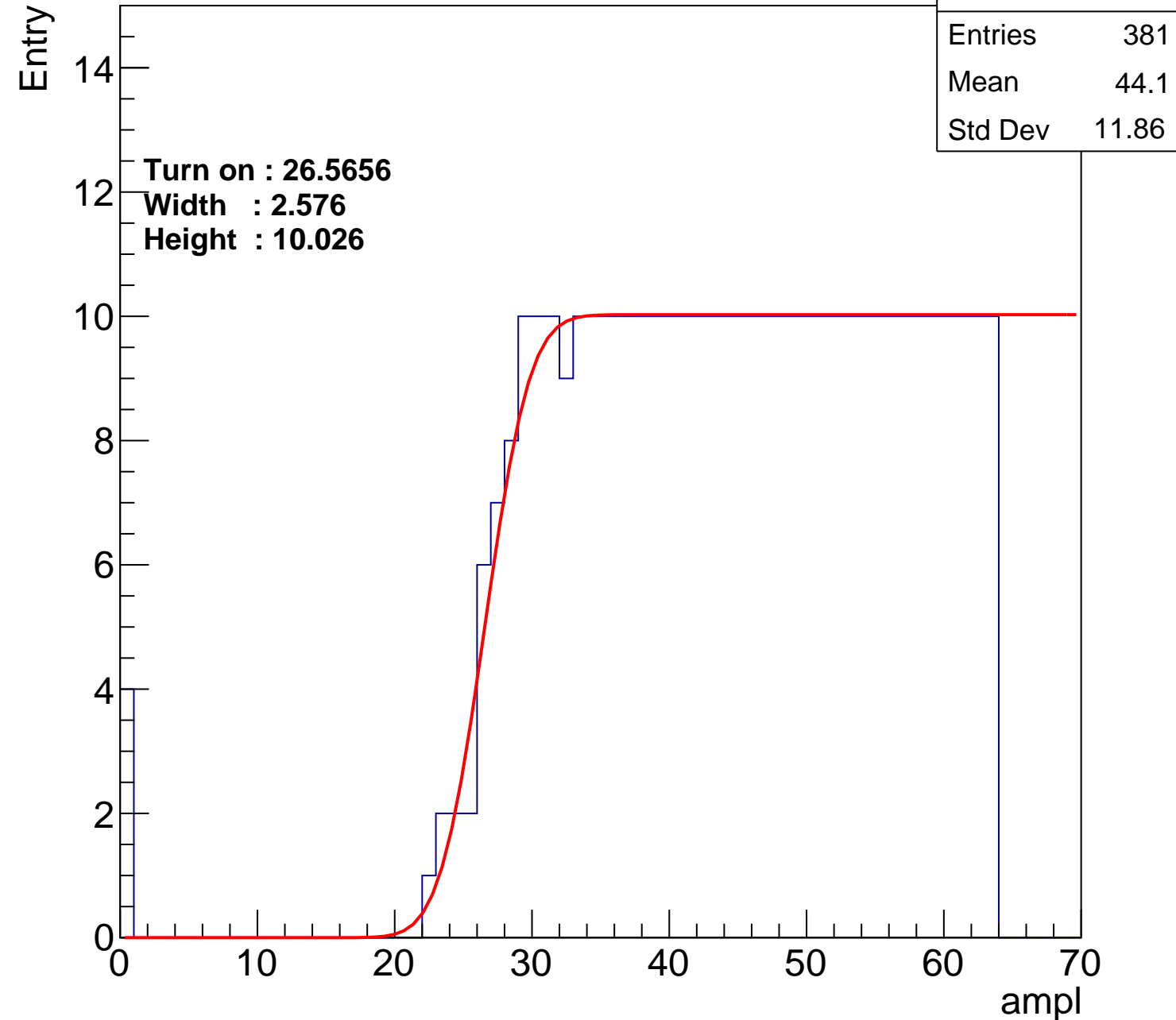
Width : 2.576

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.4847
Width : 2.225
Height : 9.961



B0L000S, U13-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.85
Std Dev	11.09

Turn on : 28.1274

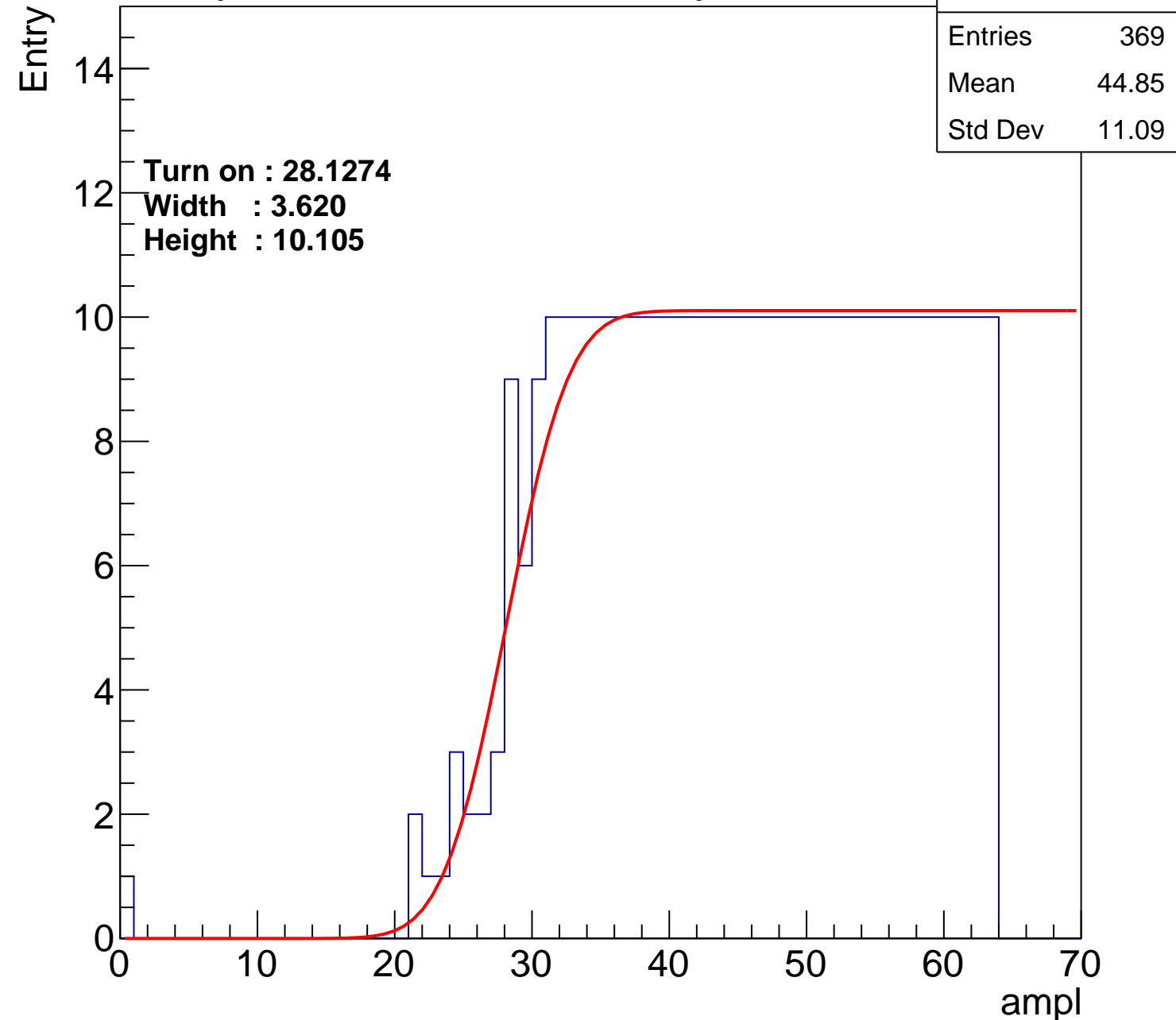
Width : 3.620

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch100

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.56
Std Dev	10.67

Turn on : 28.5196

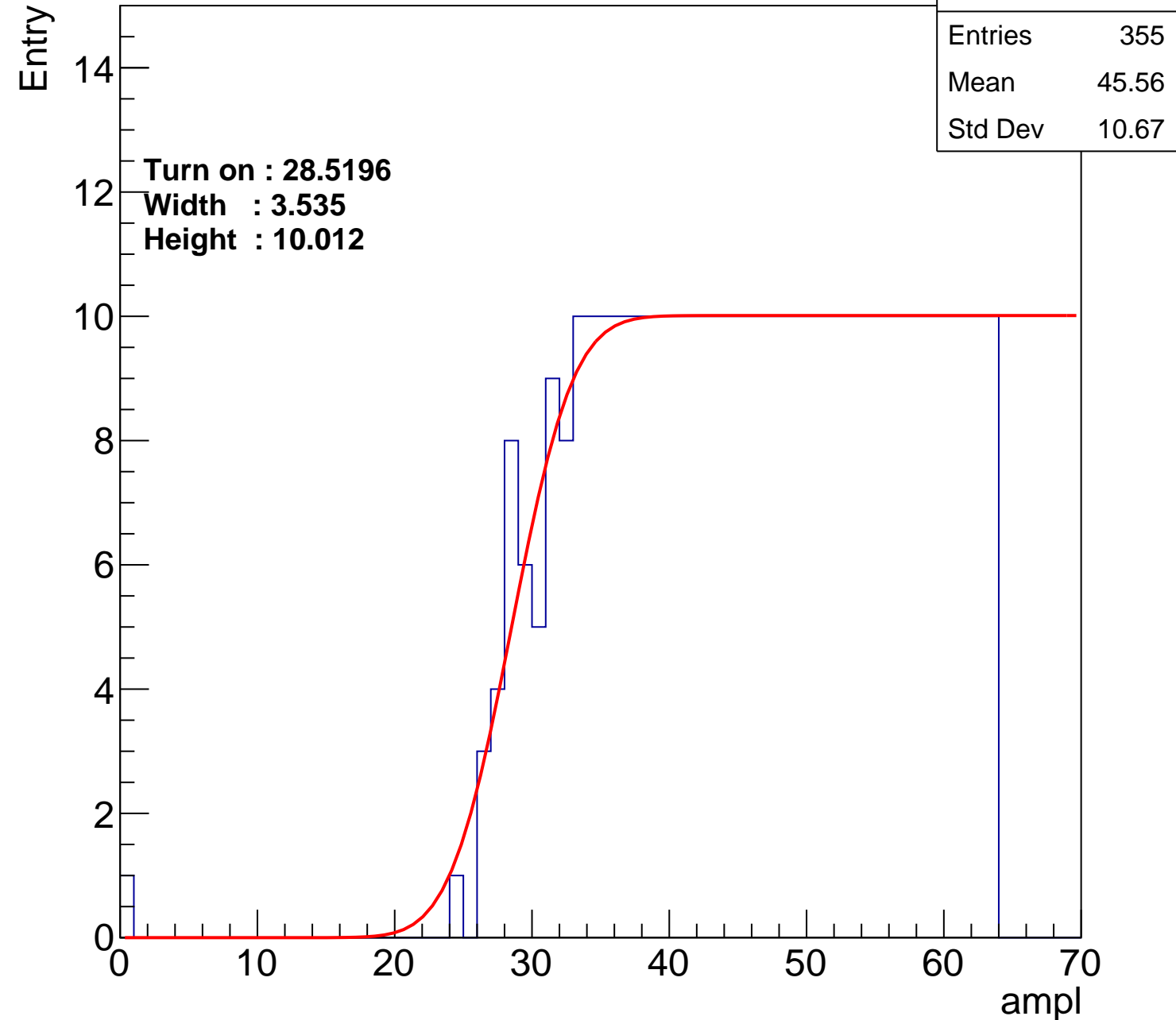
Width : 3.535

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.24
Std Dev	11.63

Turn on : 27.2091

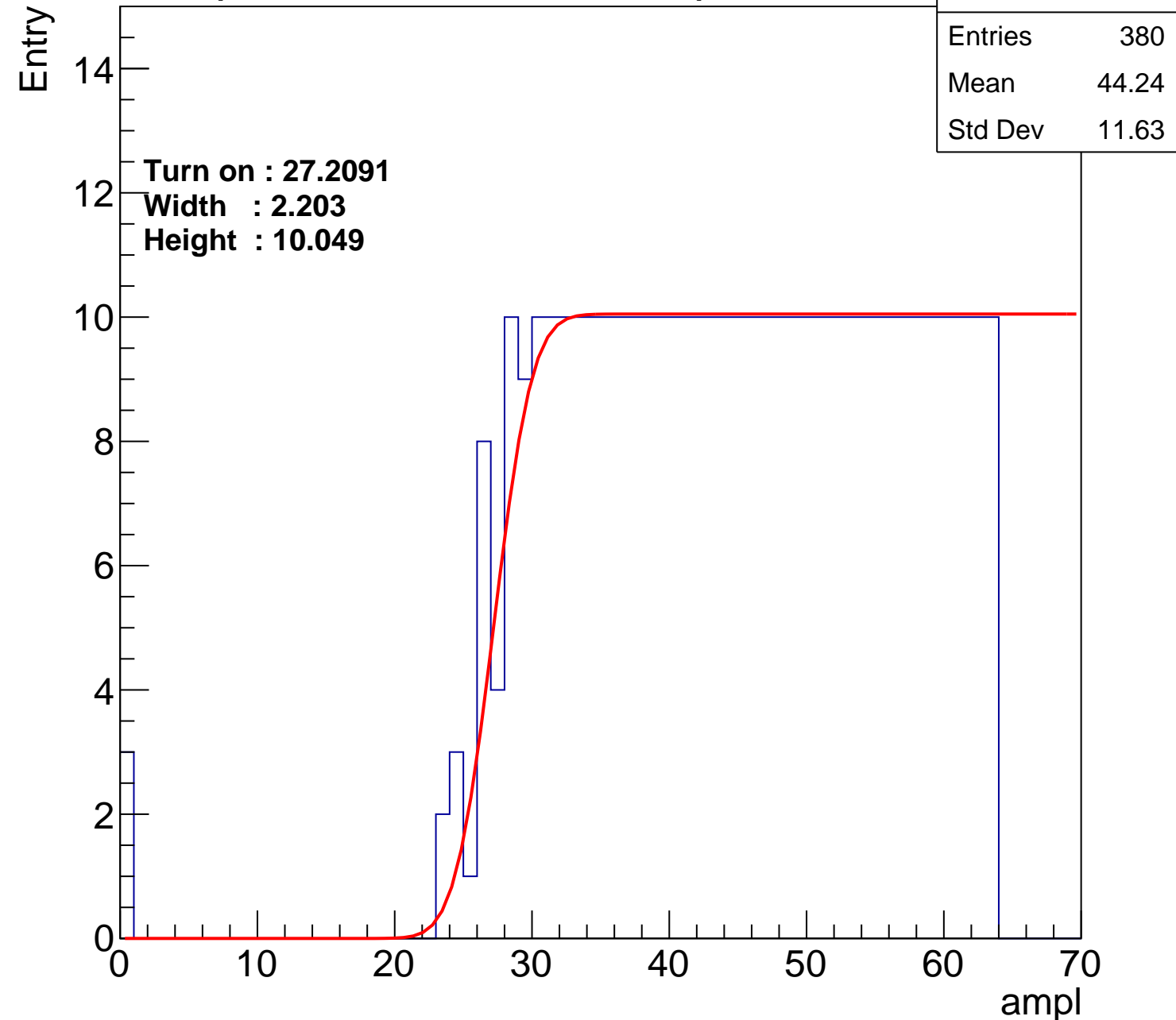
Width : 2.203

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.57
Std Dev	10.66

Turn on : 29.0177

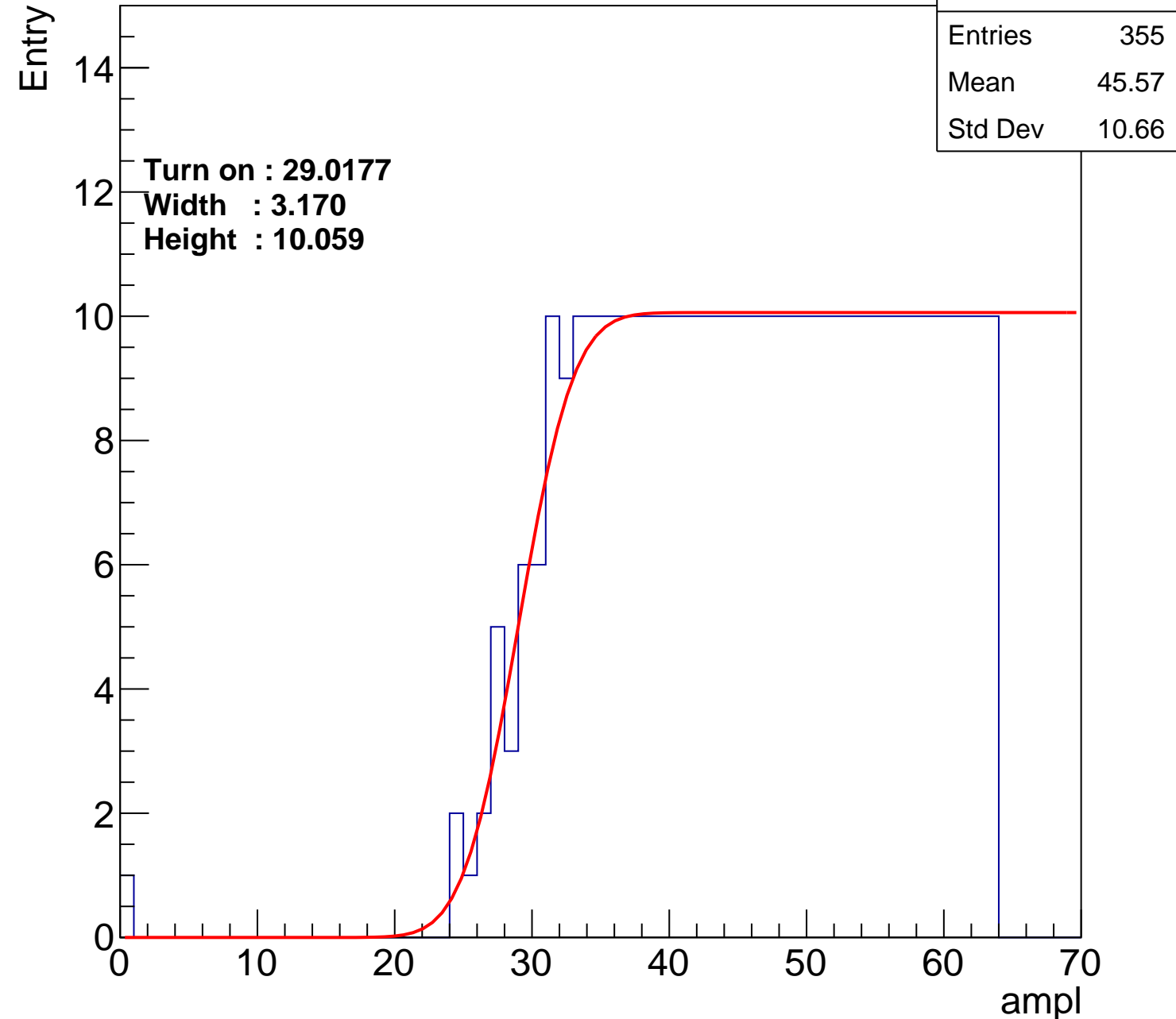
Width : 3.170

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch103

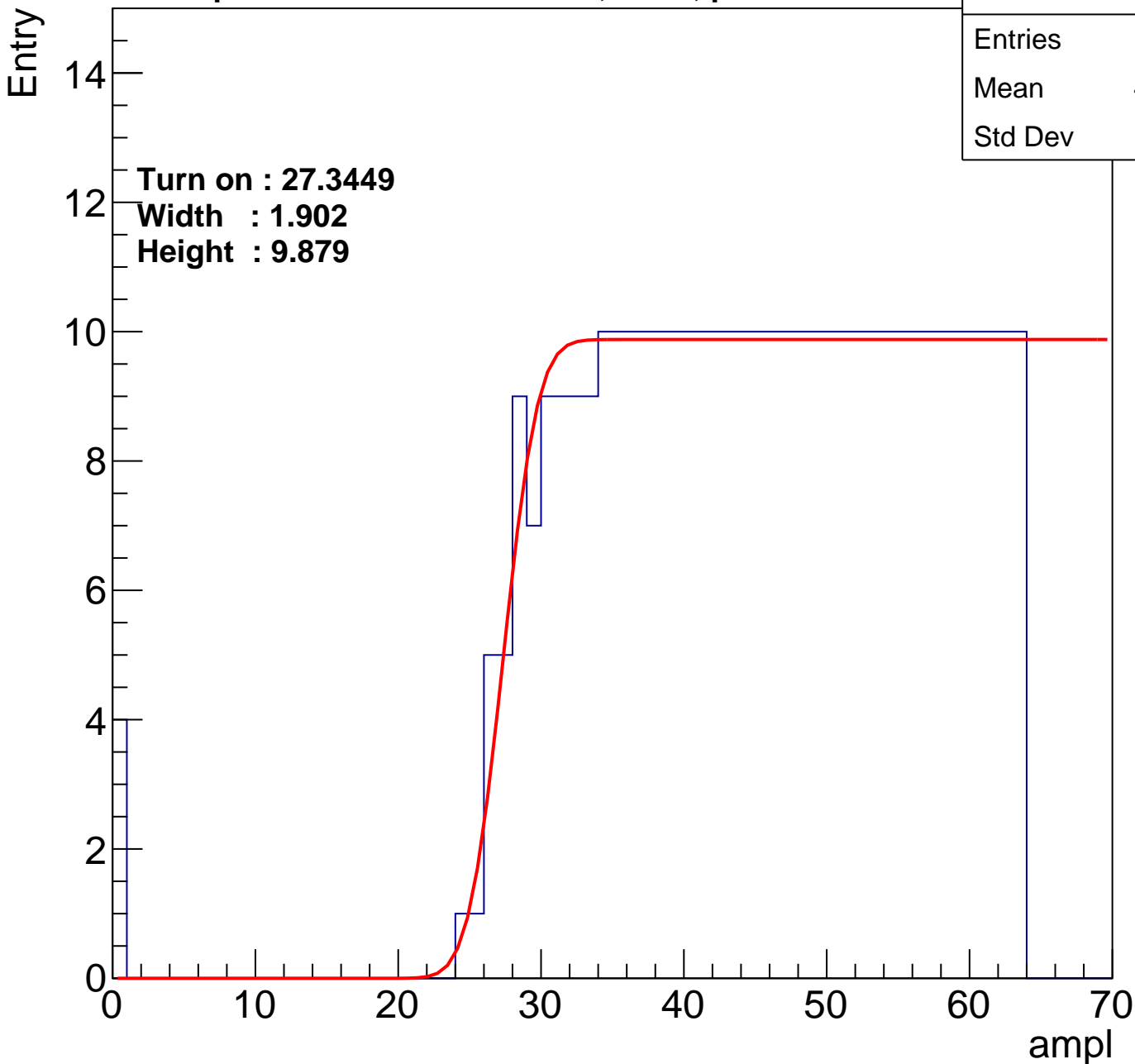
calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.71
Std Dev	11.6

Turn on : 27.3449

Width : 1.902

Height : 9.879



B0L000S, U13-ch104

calib_packv5_042523_0143.root, FC#5, port B1

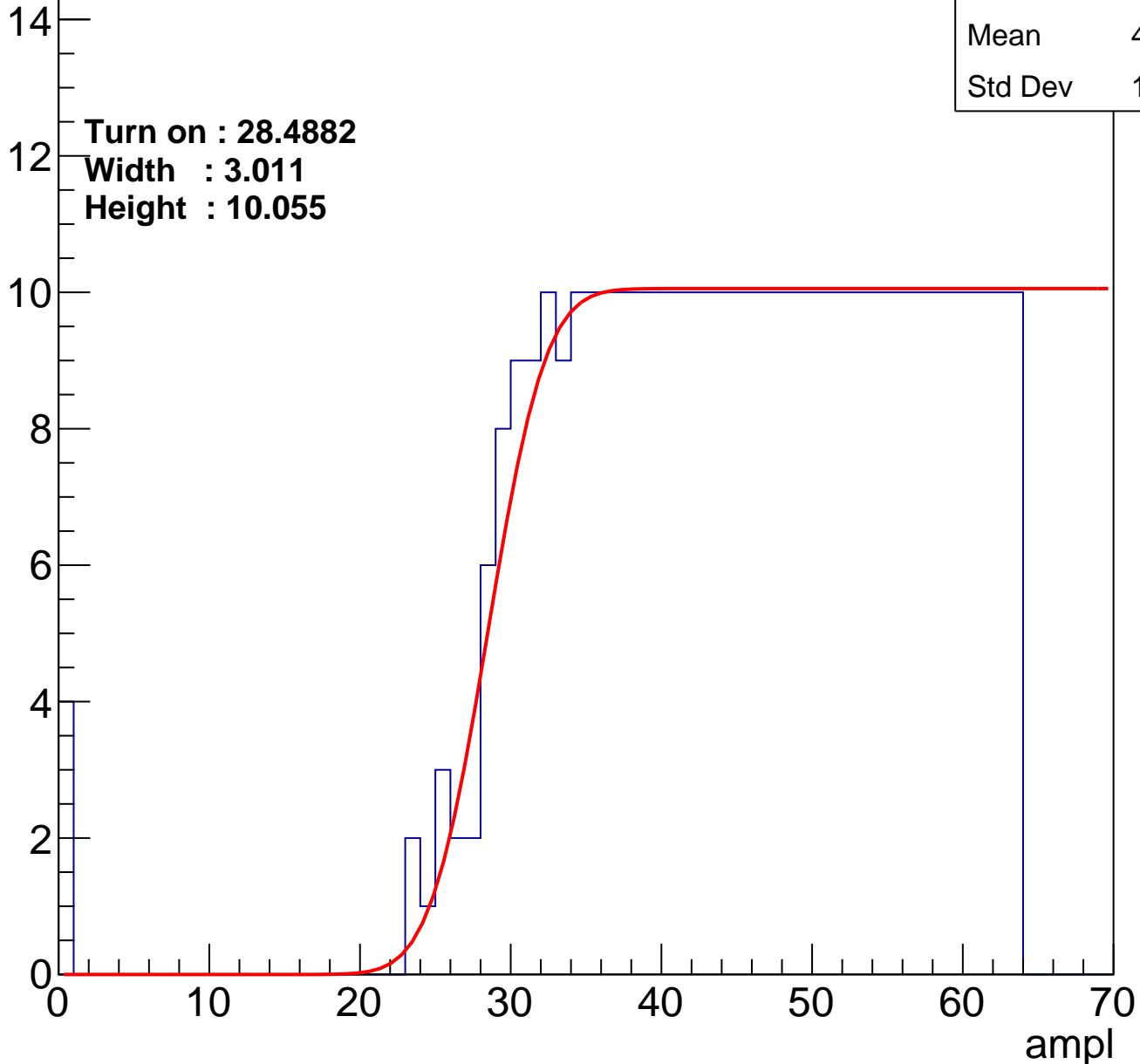
Entries	365
Mean	44.84
Std Dev	11.56

Turn on : 28.4882

Width : 3.011

Height : 10.055

Entry



B0L000S, U13-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.62
Std Dev	11.87

Turn on : 27.7796

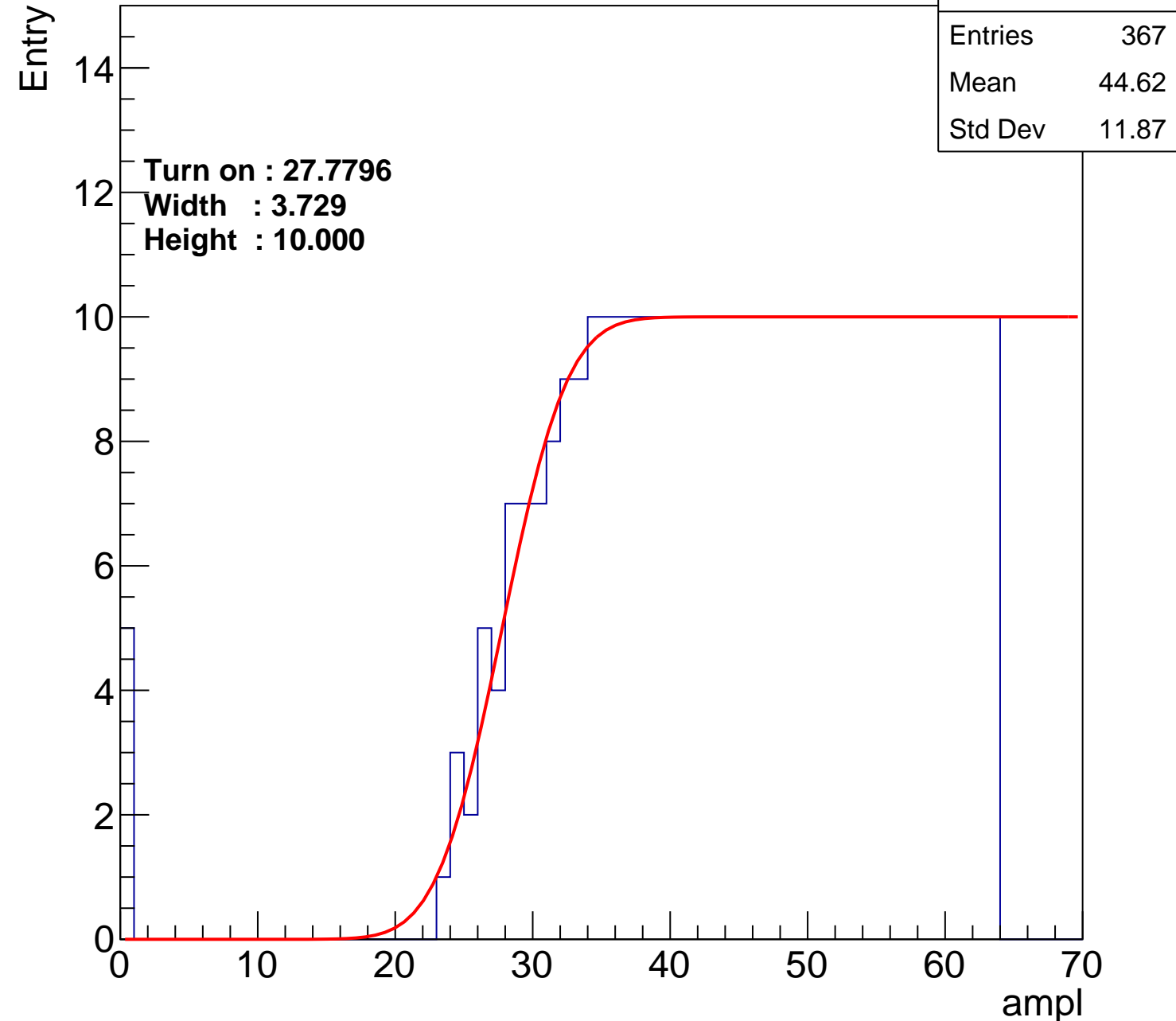
Width : 3.729

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.1158

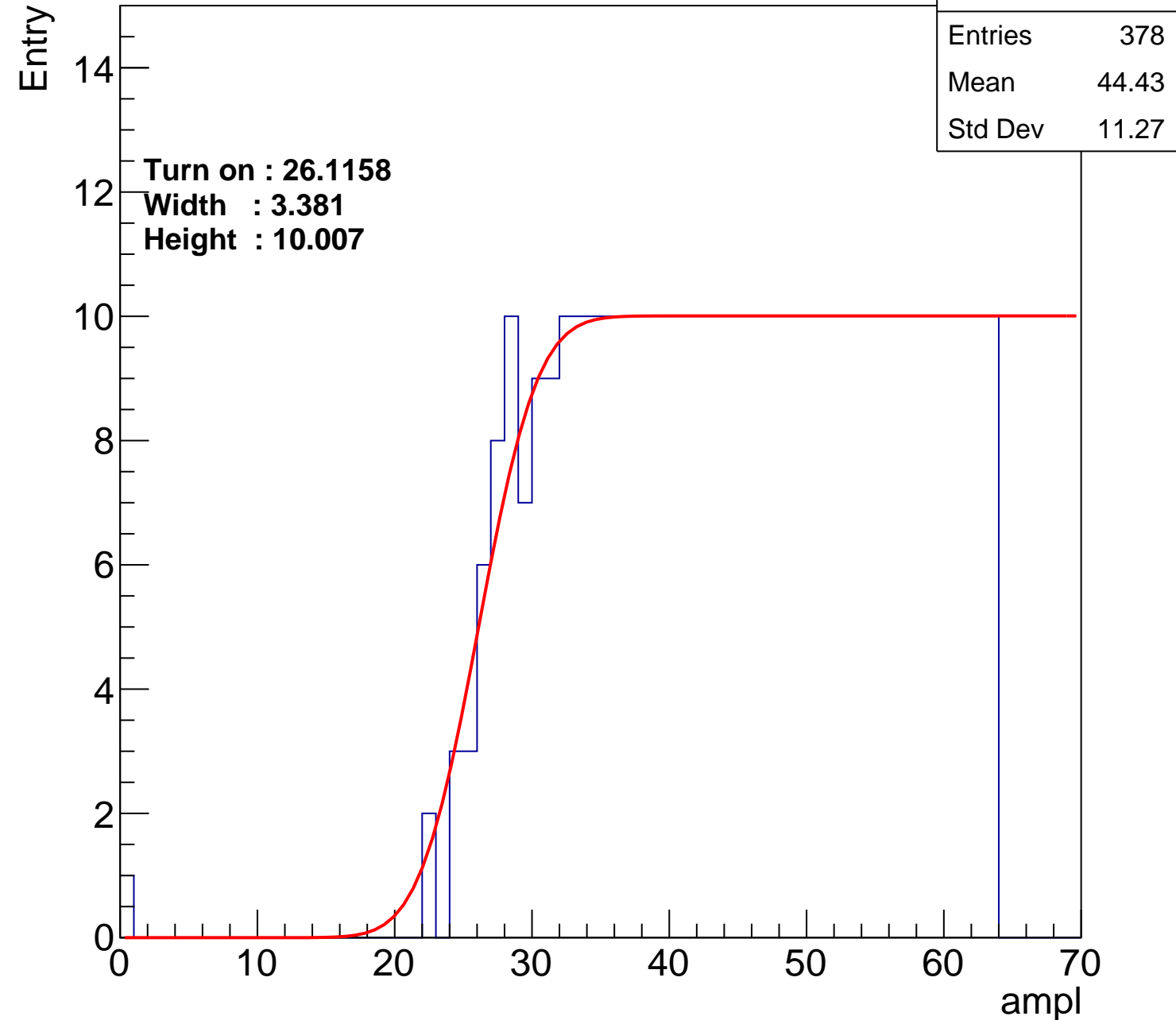
Width : 3.381

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
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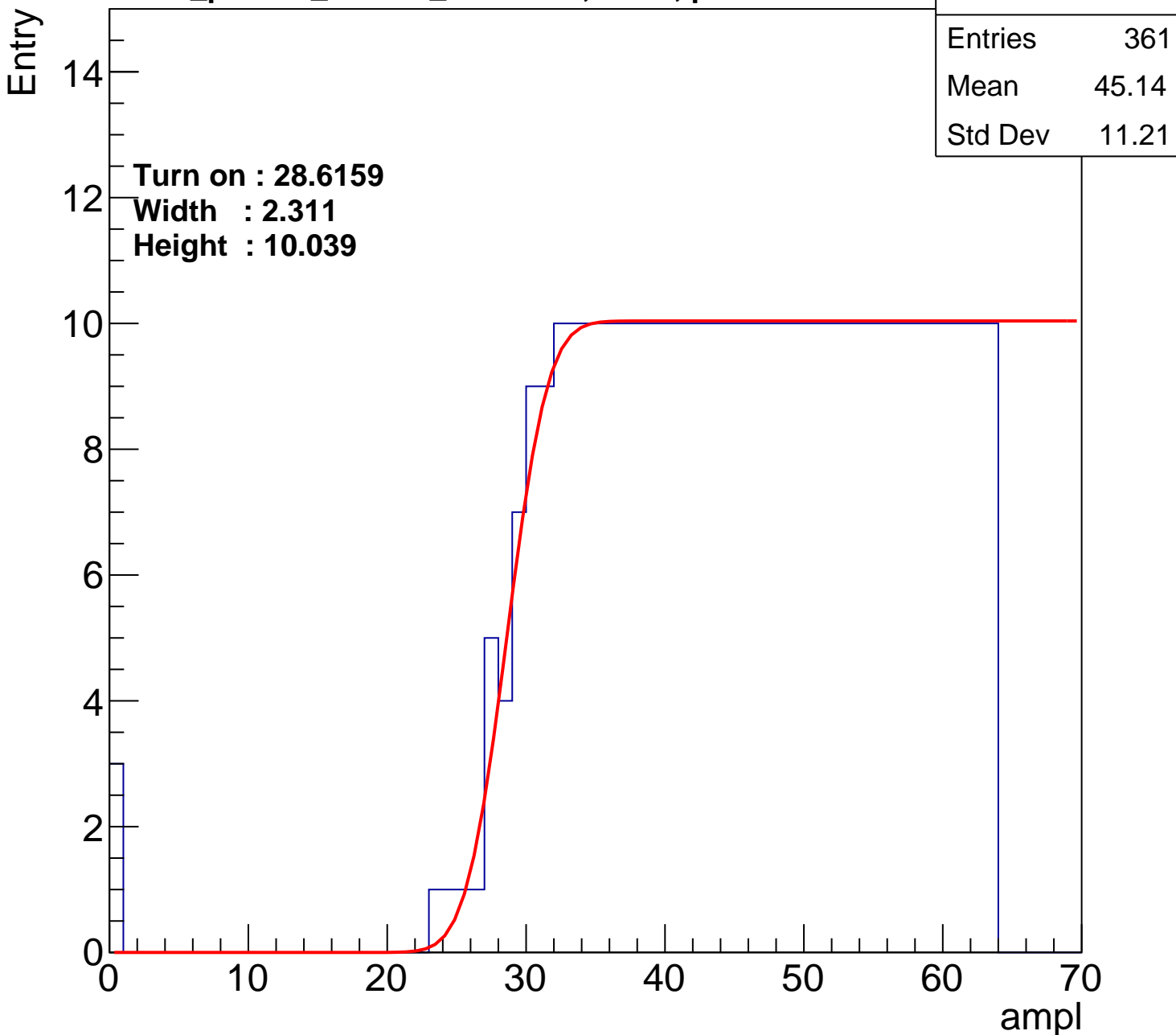
Mean	45.14
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Std Dev	11.21
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Turn on : 28.6159

Width : 2.311

Height : 10.039



B0L000S, U13-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.01
Std Dev	11.45

Turn on : 28.5814

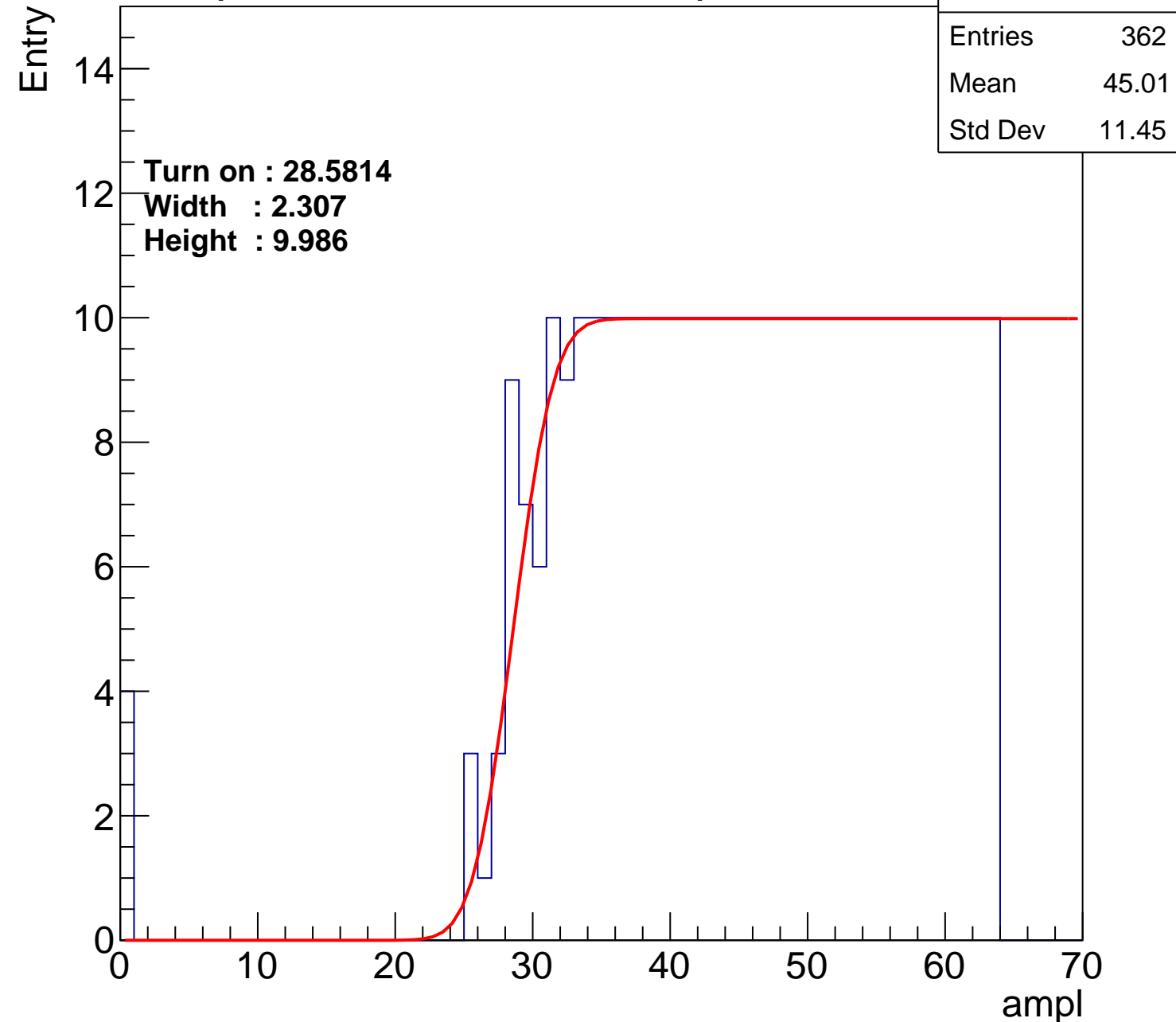
Width : 2.307

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 27.8535

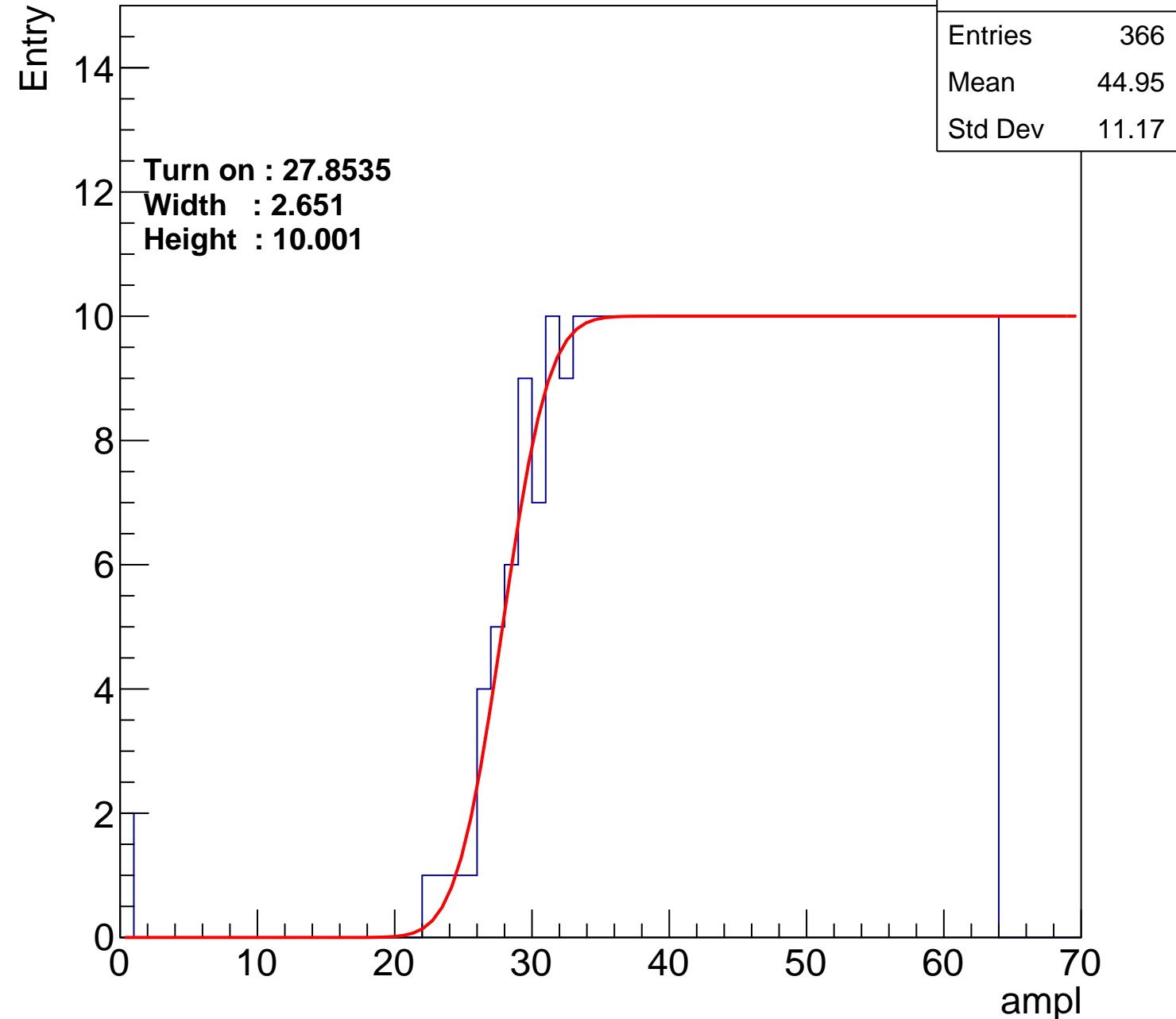
Width : 2.651

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.62
Std Dev	10.79

Turn on : 29.3775

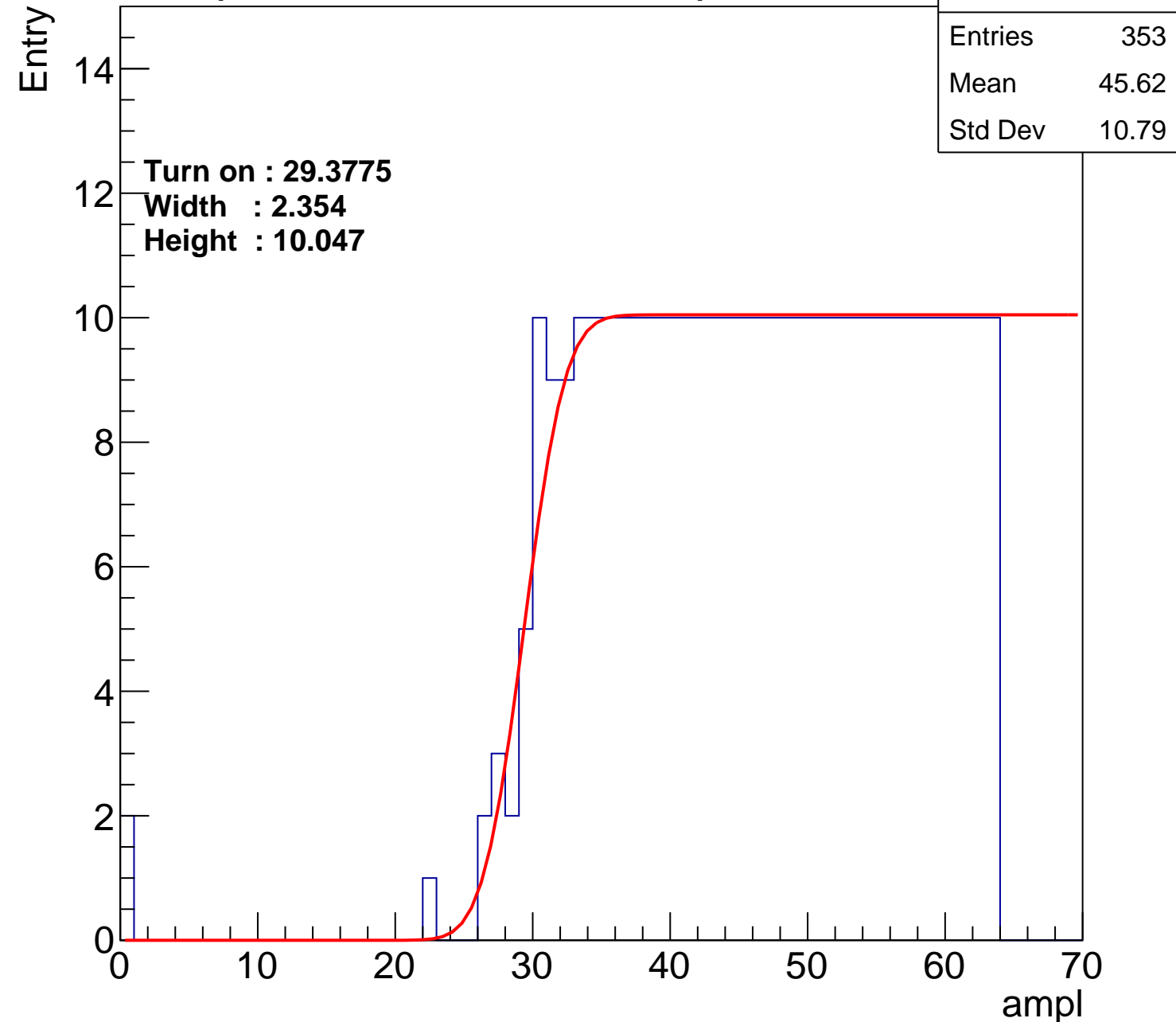
Width : 2.354

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.01
Std Dev	11.46

Turn on : 28.4646

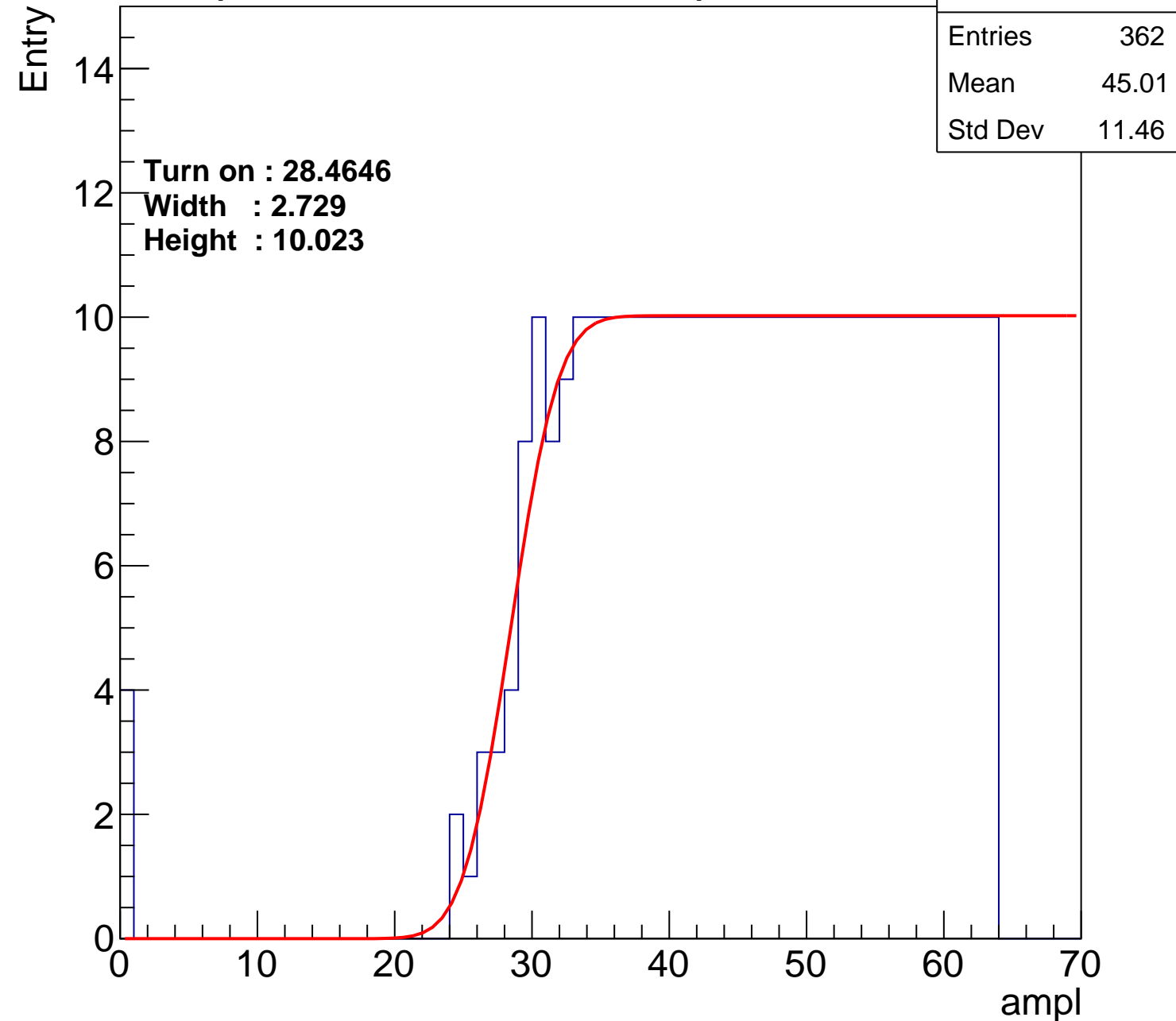
Width : 2.729

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	334
Mean	46.4
Std Dev	10.69

Turn on : 31.1499

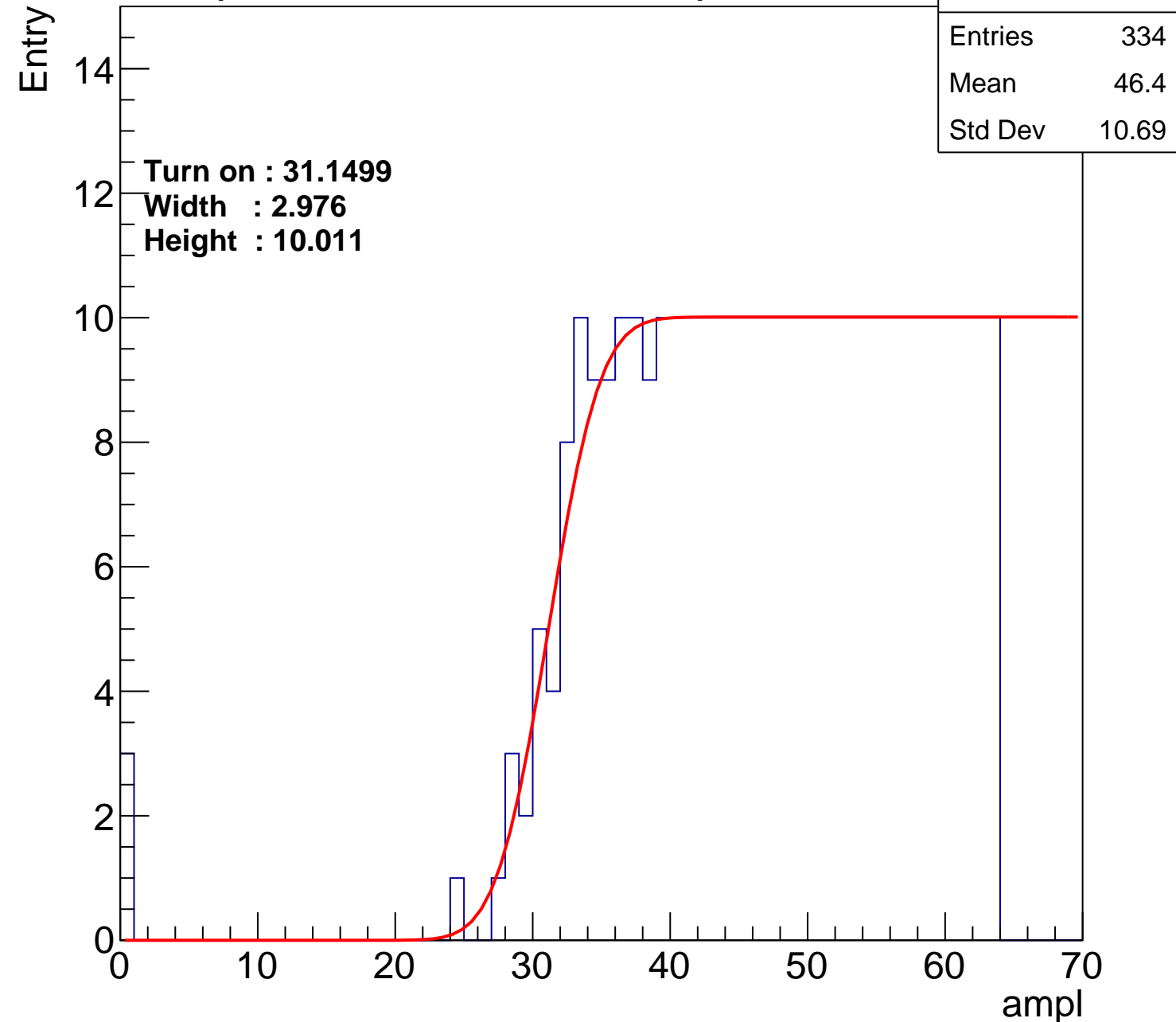
Width : 2.976

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.34
Std Dev	11.35

Turn on : 29.0022

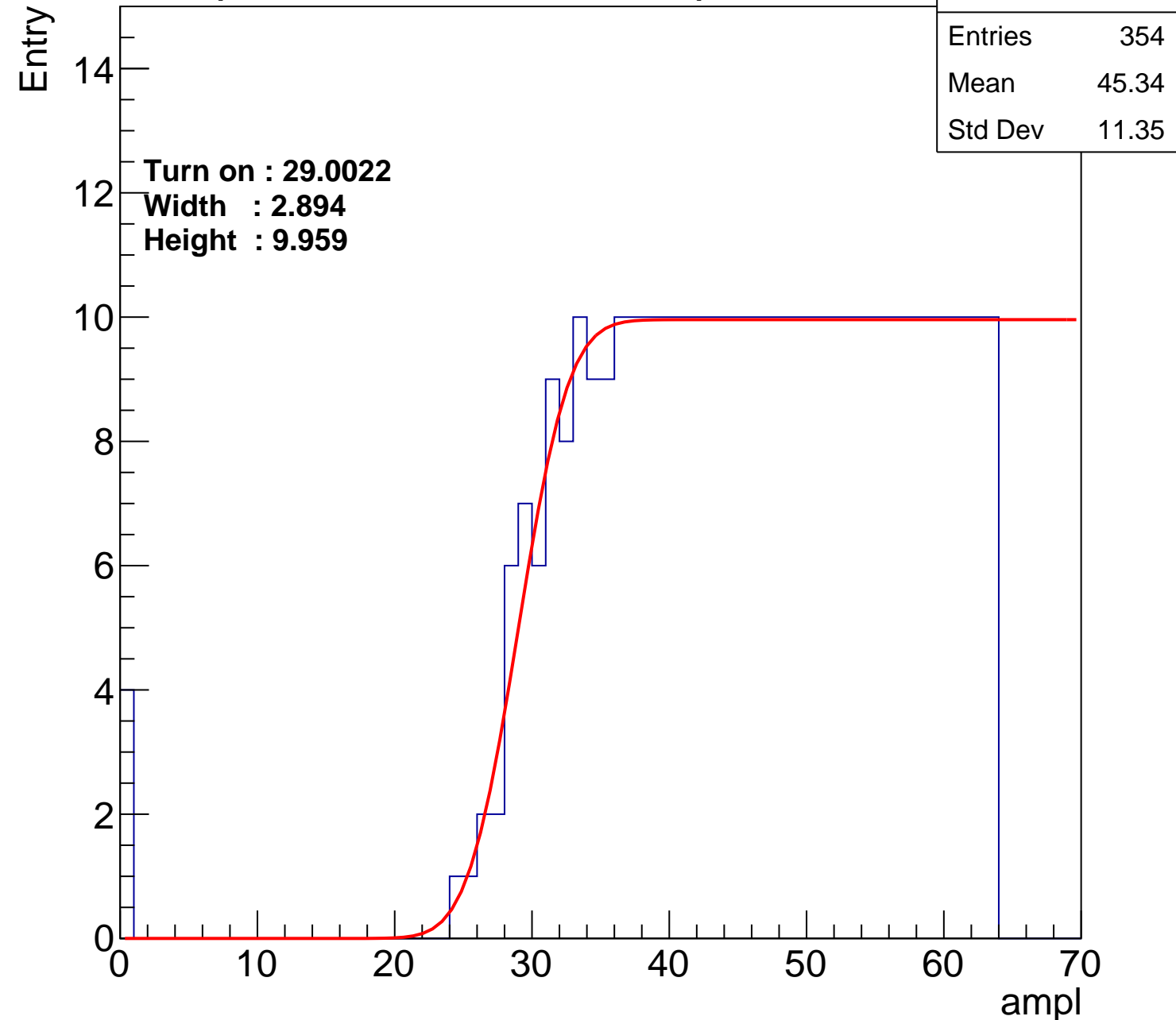
Width : 2.894

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.48
Std Dev	10.91

Turn on : 28.8474

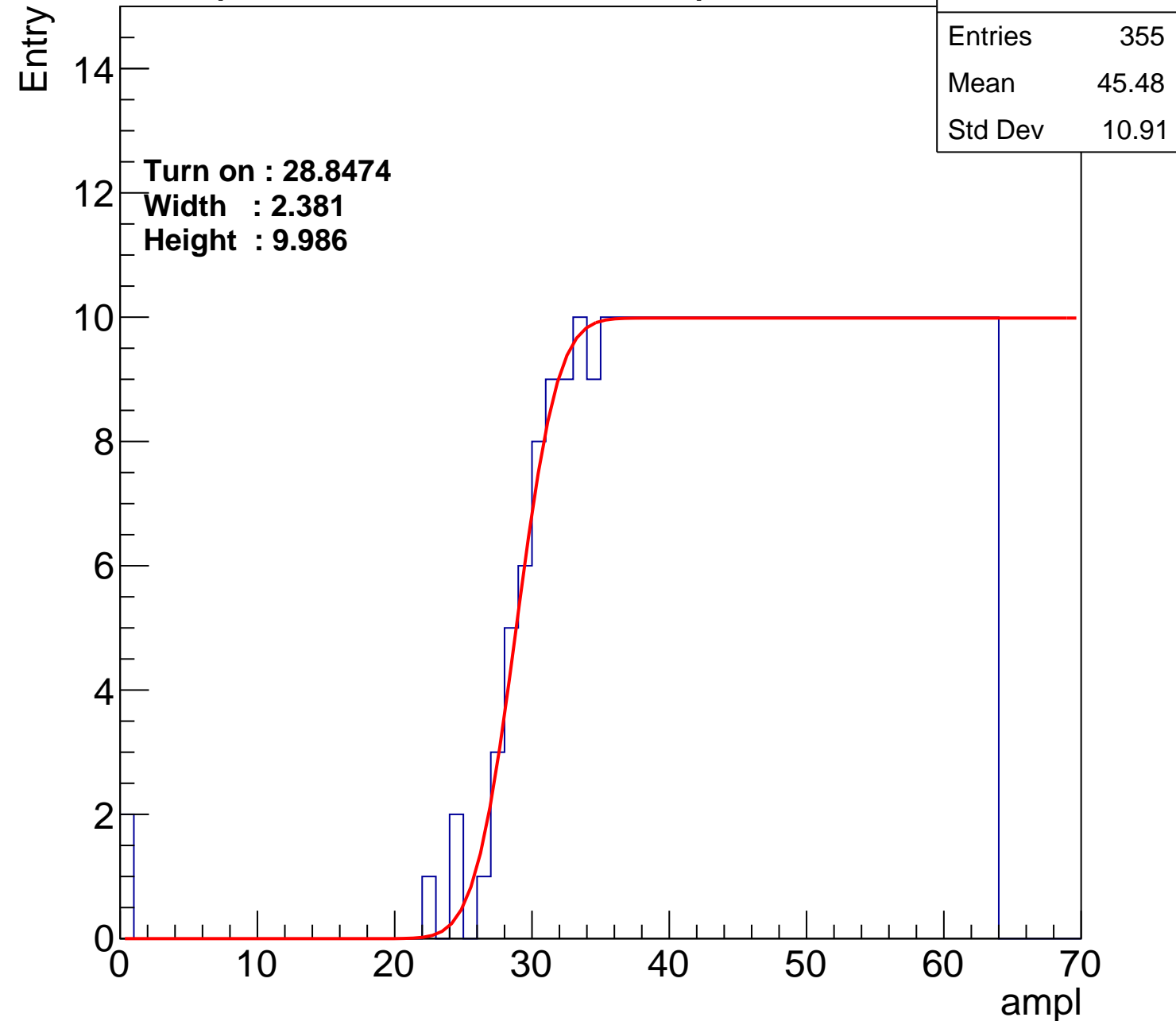
Width : 2.381

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.9
Std Dev	11.04

Turn on : 27.4810

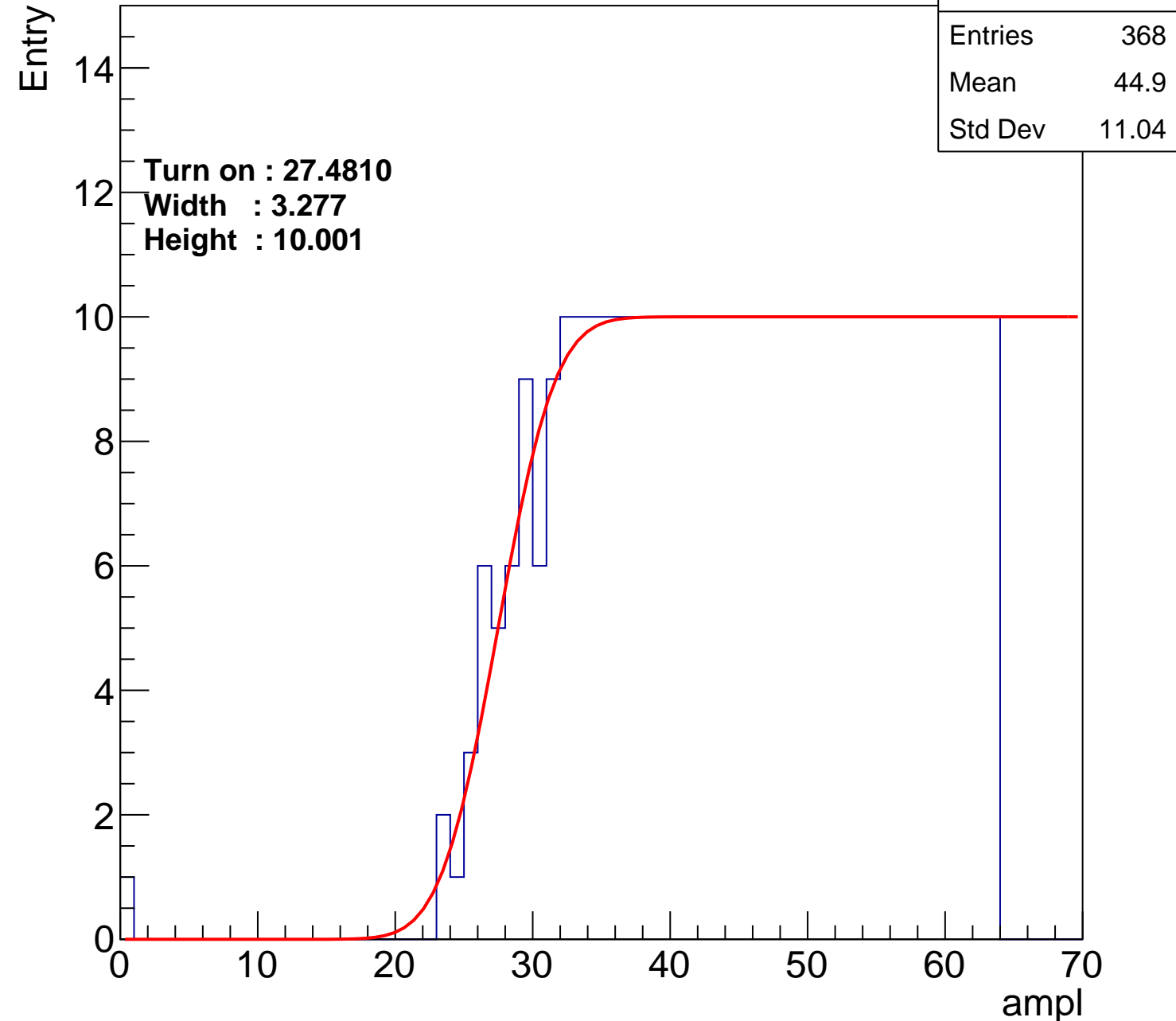
Width : 3.277

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	45.93
Std Dev	10.93

Turn on : 30.2620

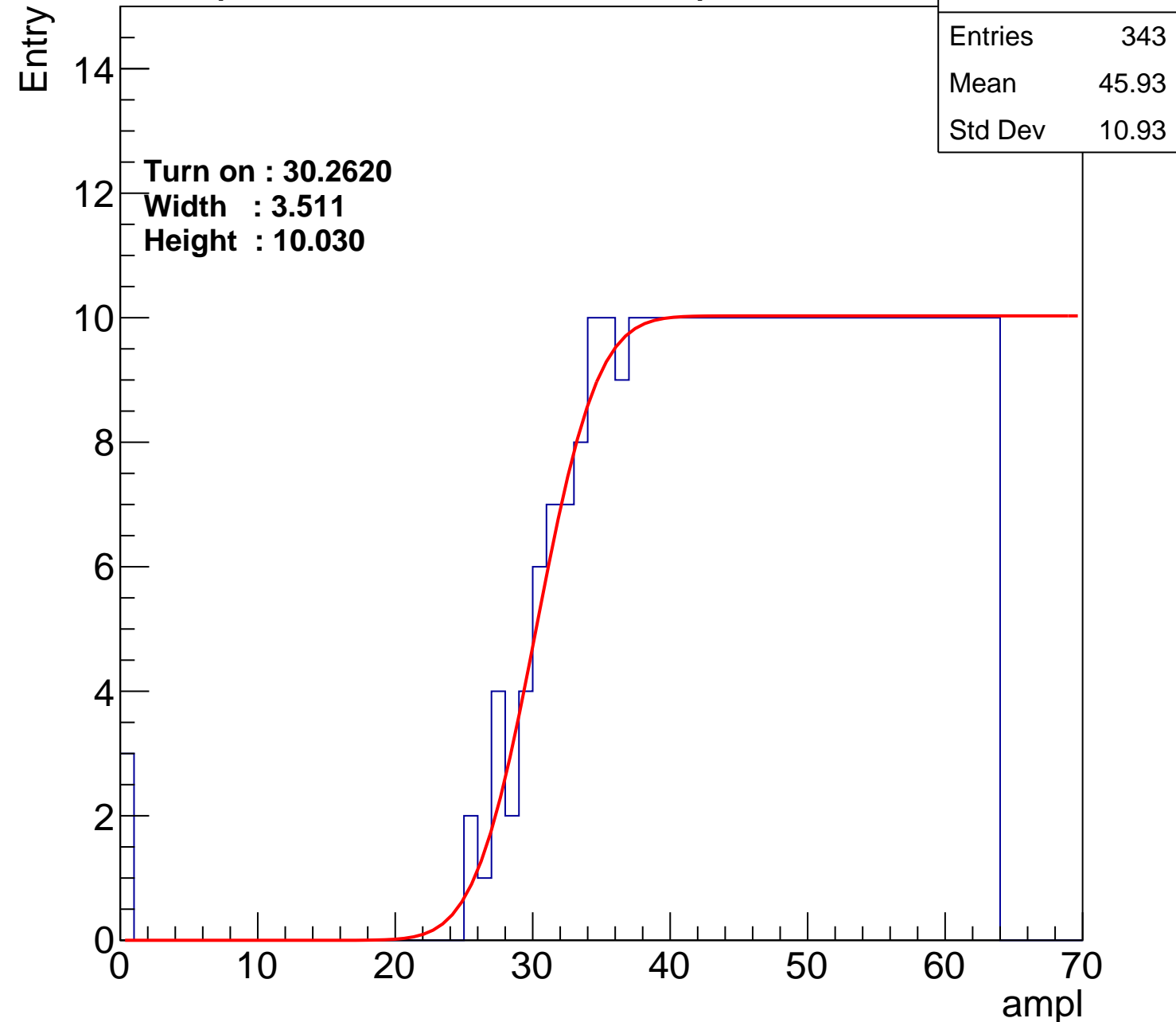
Width : 3.511

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.17
Std Dev	11.43

Turn on : 28.8980

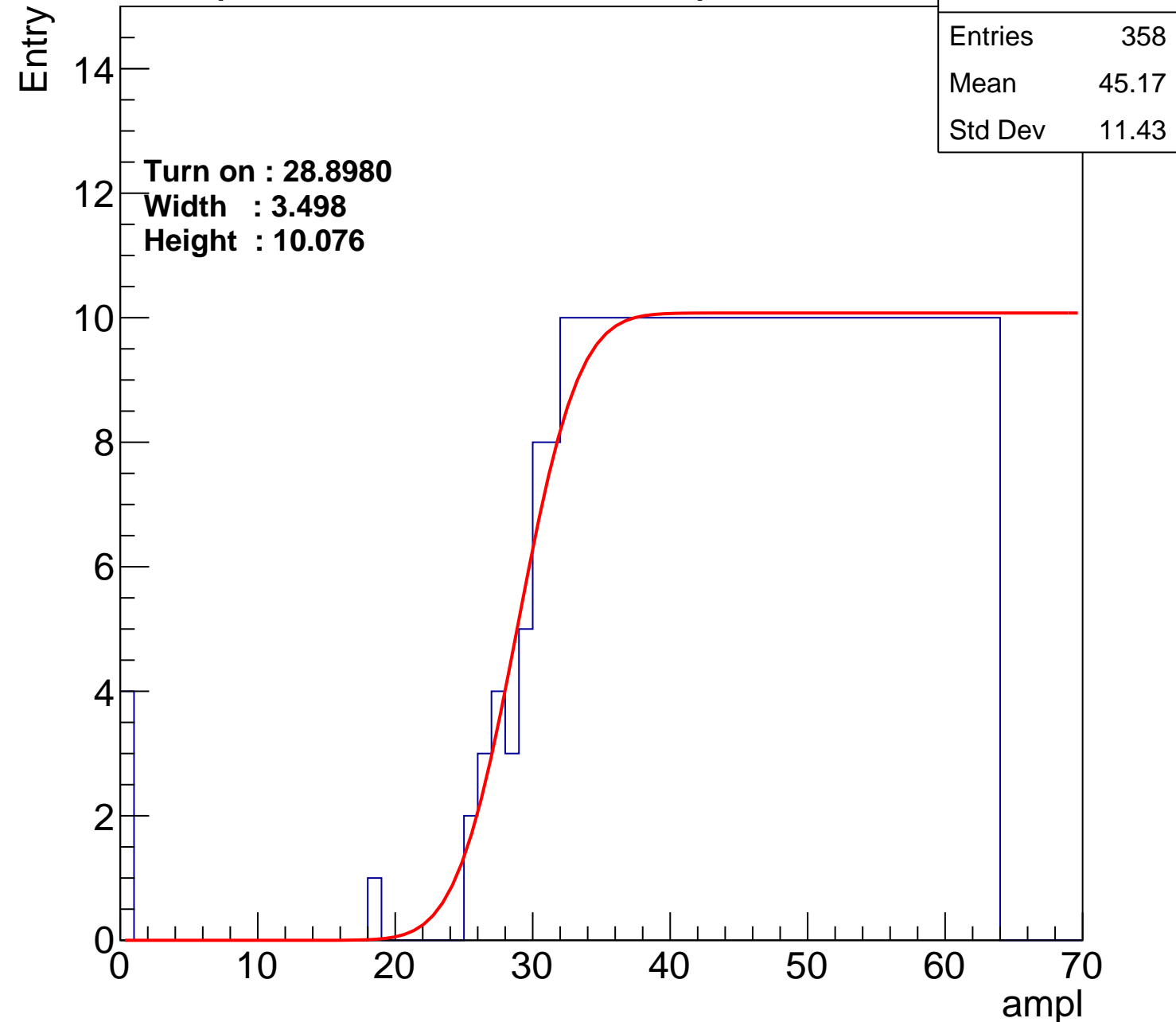
Width : 3.498

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.4
Std Dev	11.59

Turn on : 26.7978

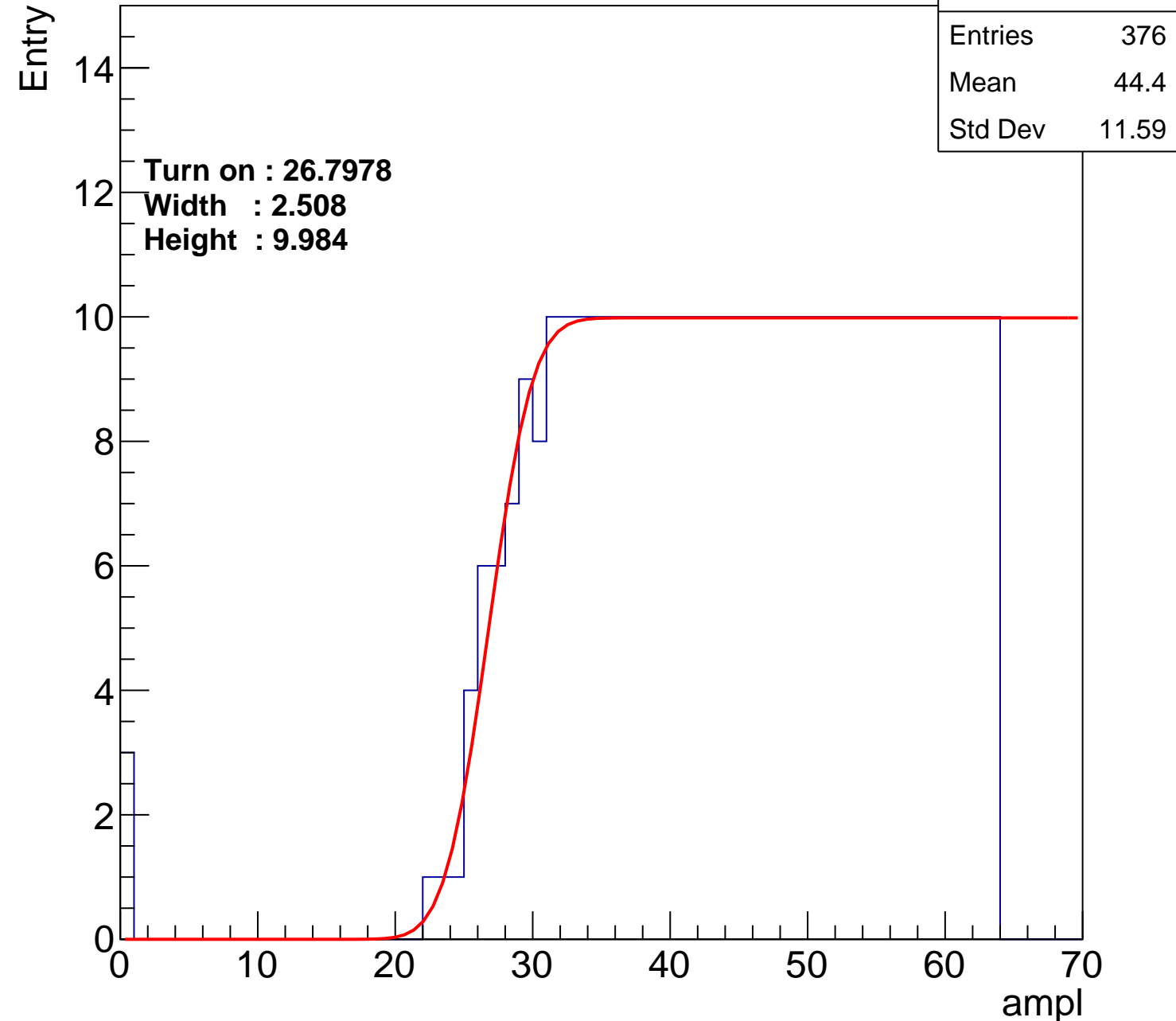
Width : 2.508

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	345
Mean	45.99
Std Dev	10.62

Turn on : 30.0292

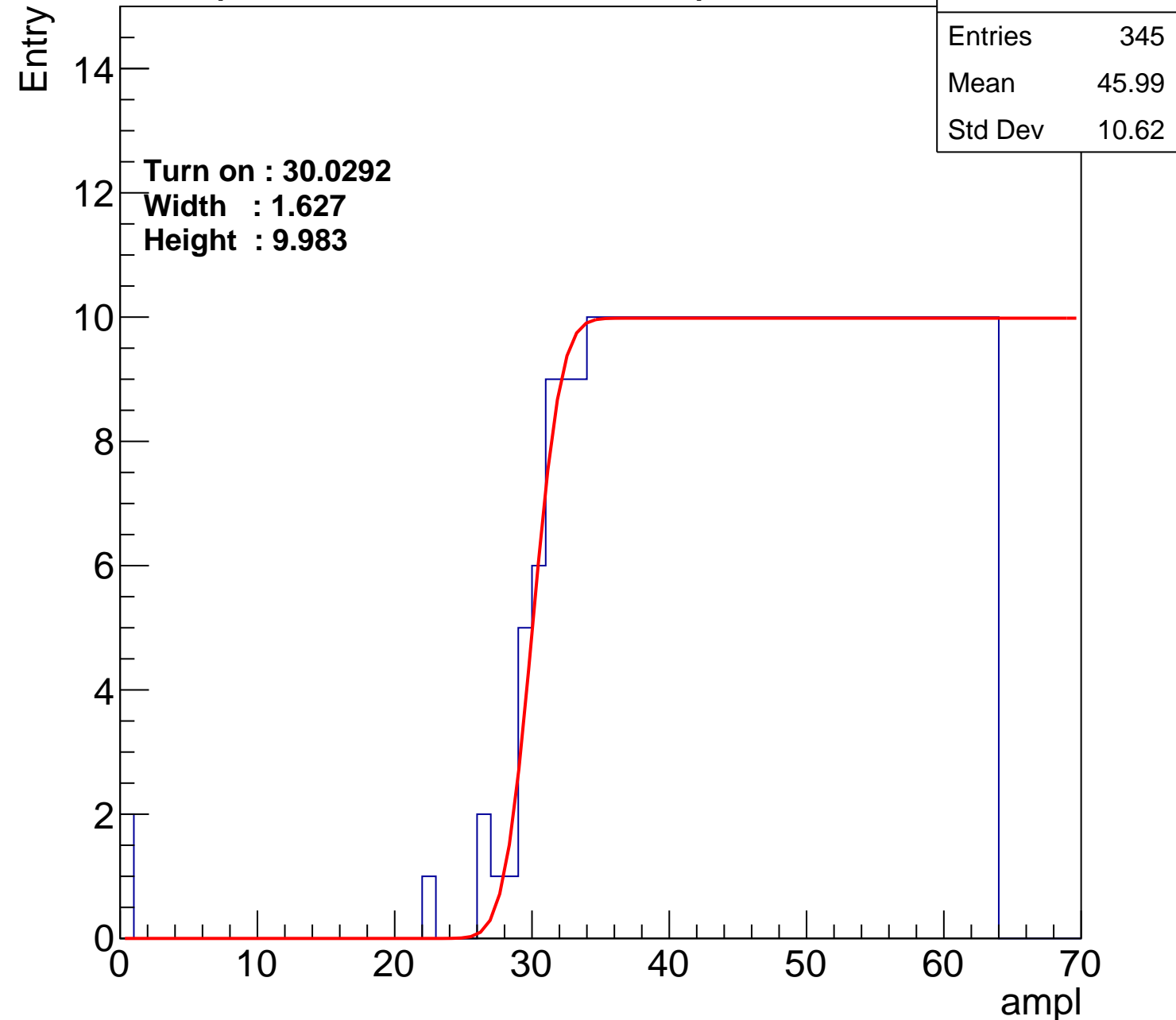
Width : 1.627

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	337
Mean	46.27
Std Dev	10.74

Turn on : 30.9995

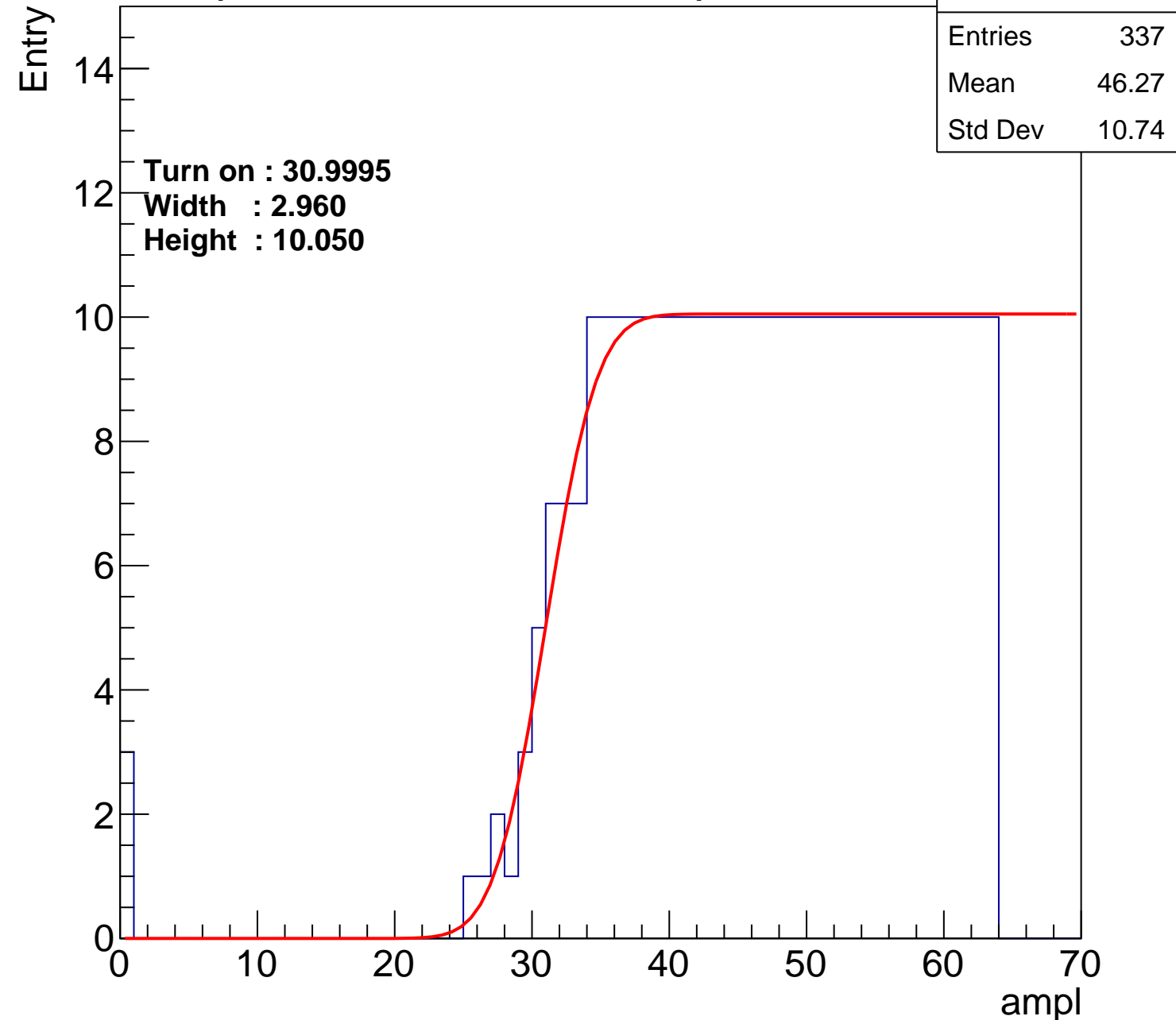
Width : 2.960

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.17
Std Dev	11.08

Turn on : 28.2559

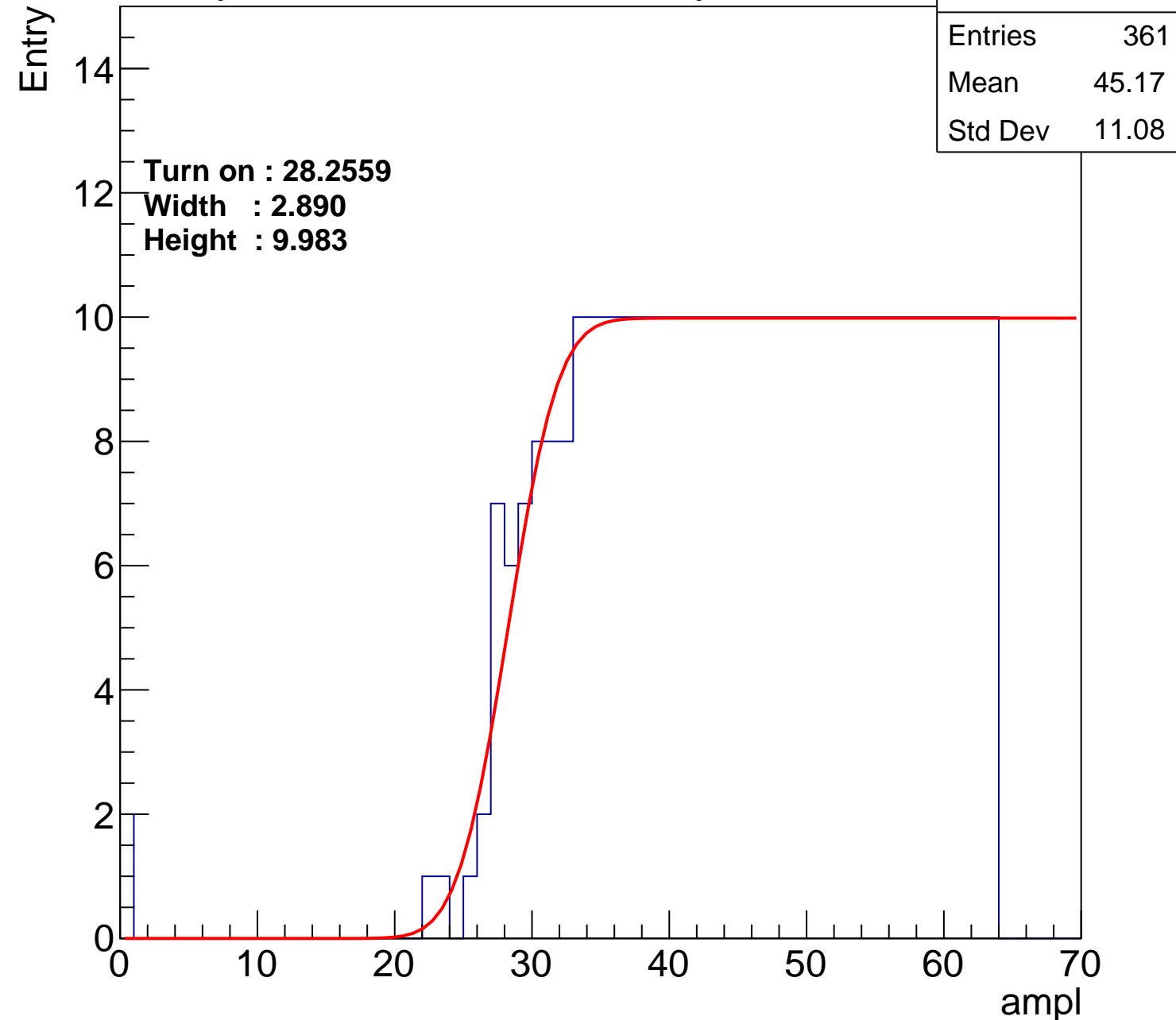
Width : 2.890

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch122

calib_packv5_042523_0143.root, FC#5, port B1

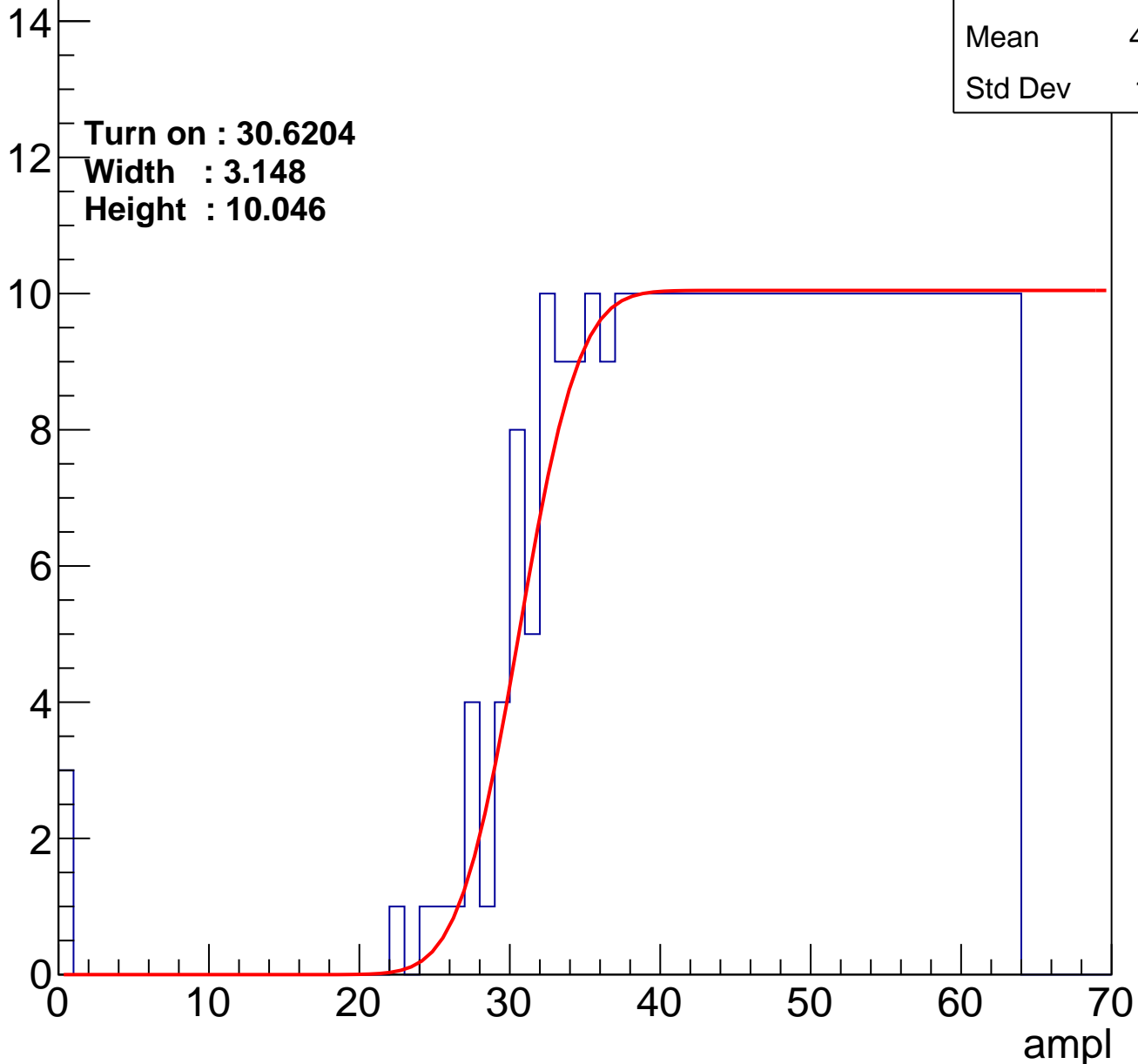
Entries	346
Mean	45.78
Std Dev	11.01

Turn on : 30.6204

Width : 3.148

Height : 10.046

Entry



B0L000S, U13-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.2055

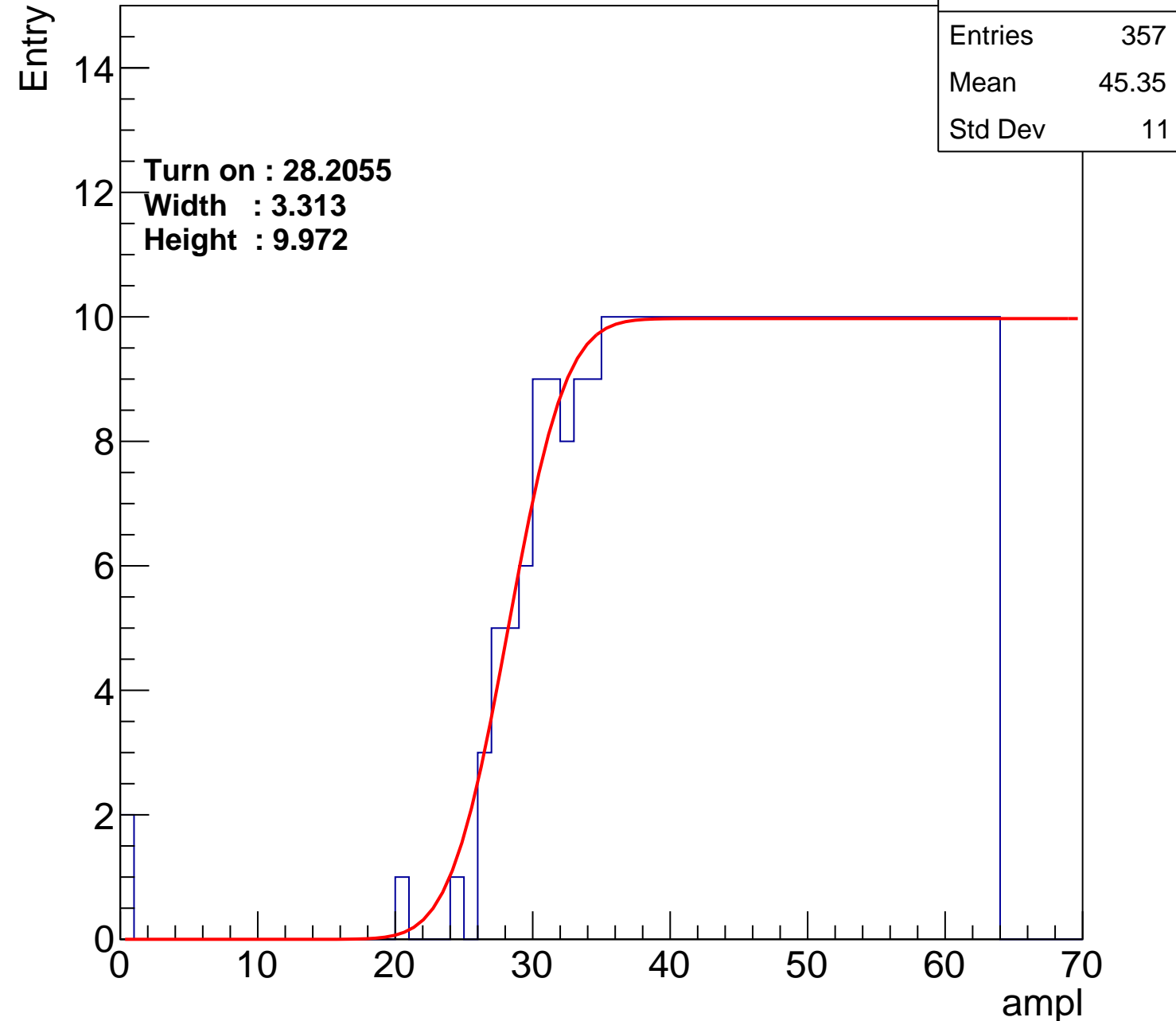
Width : 3.313

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
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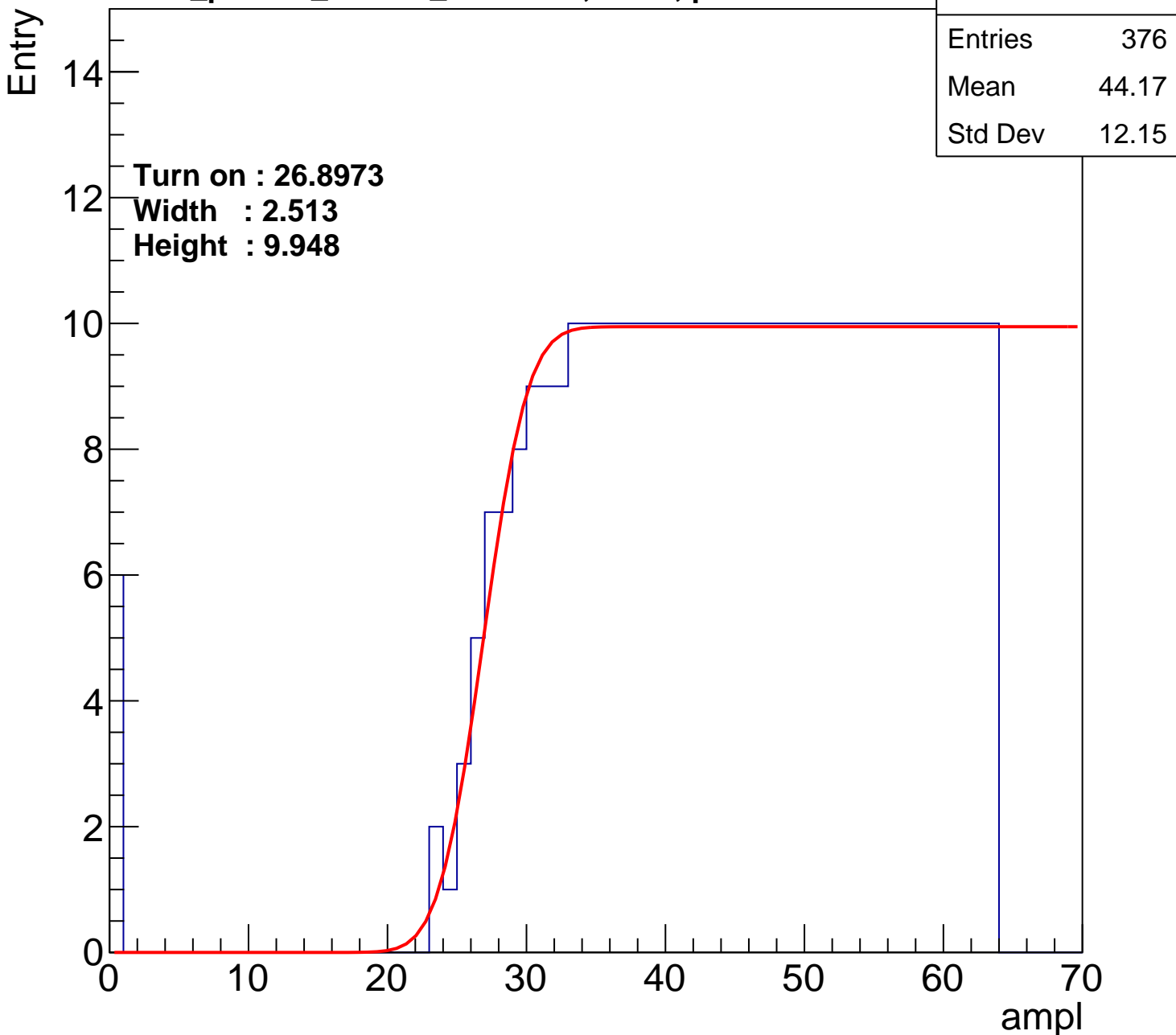
Mean	44.17
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Std Dev	12.15
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Turn on : 26.8973

Width : 2.513

Height : 9.948



B0L000S, U13-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.81
Std Dev	10.7

Turn on : 29.0237

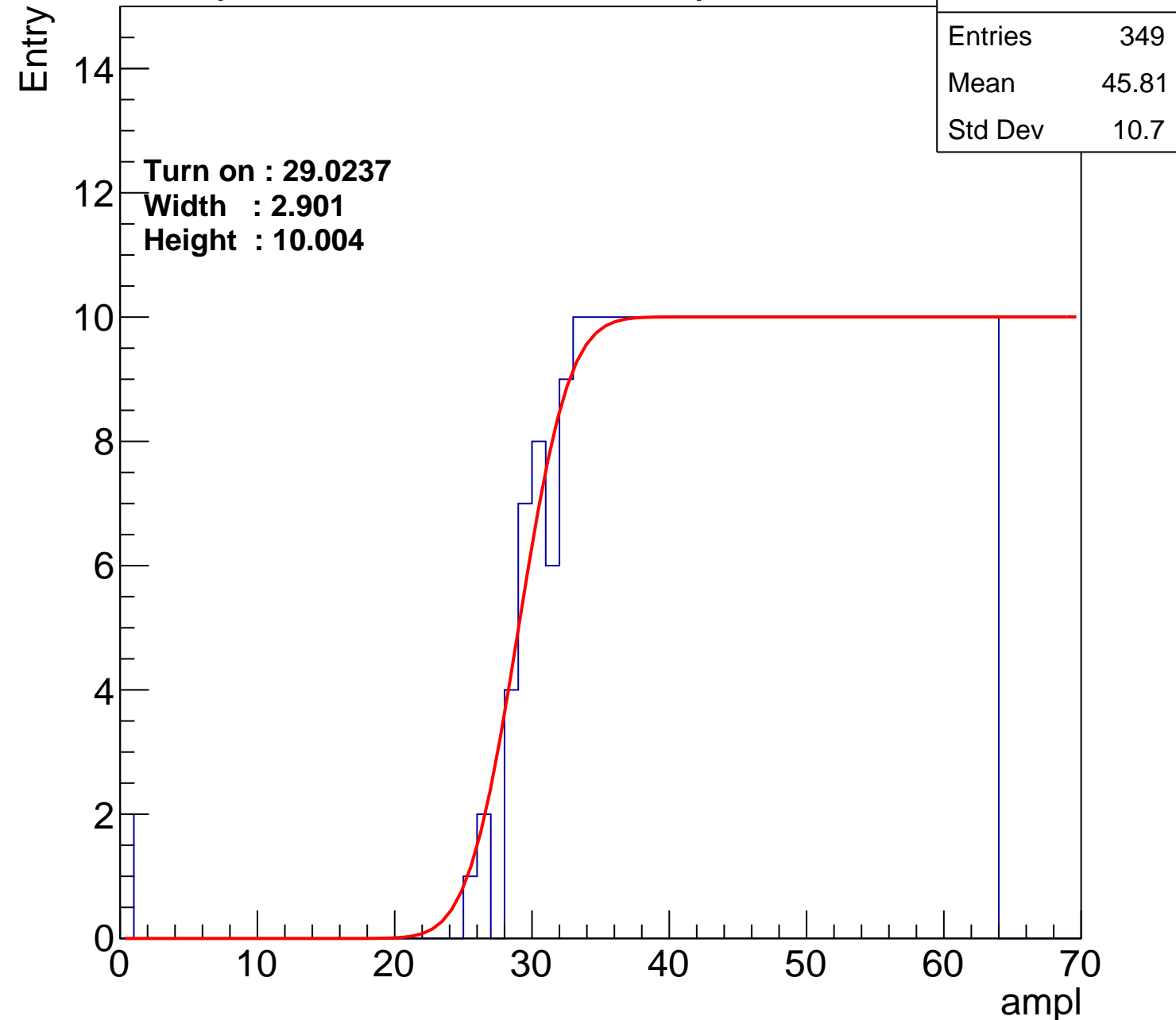
Width : 2.901

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.36
Std Dev	10.72

Turn on : 28.0961

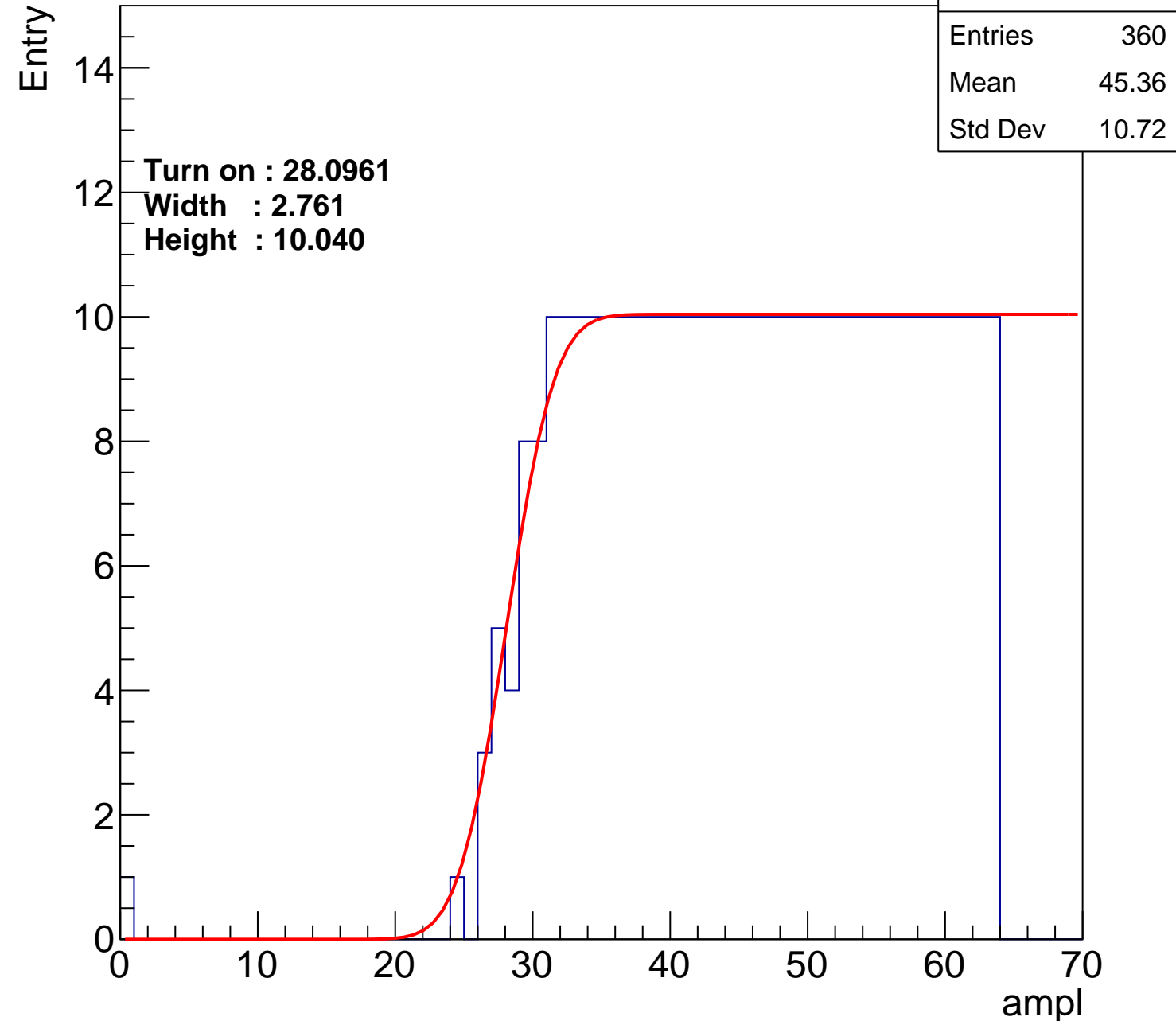
Width : 2.761

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U13-ch127

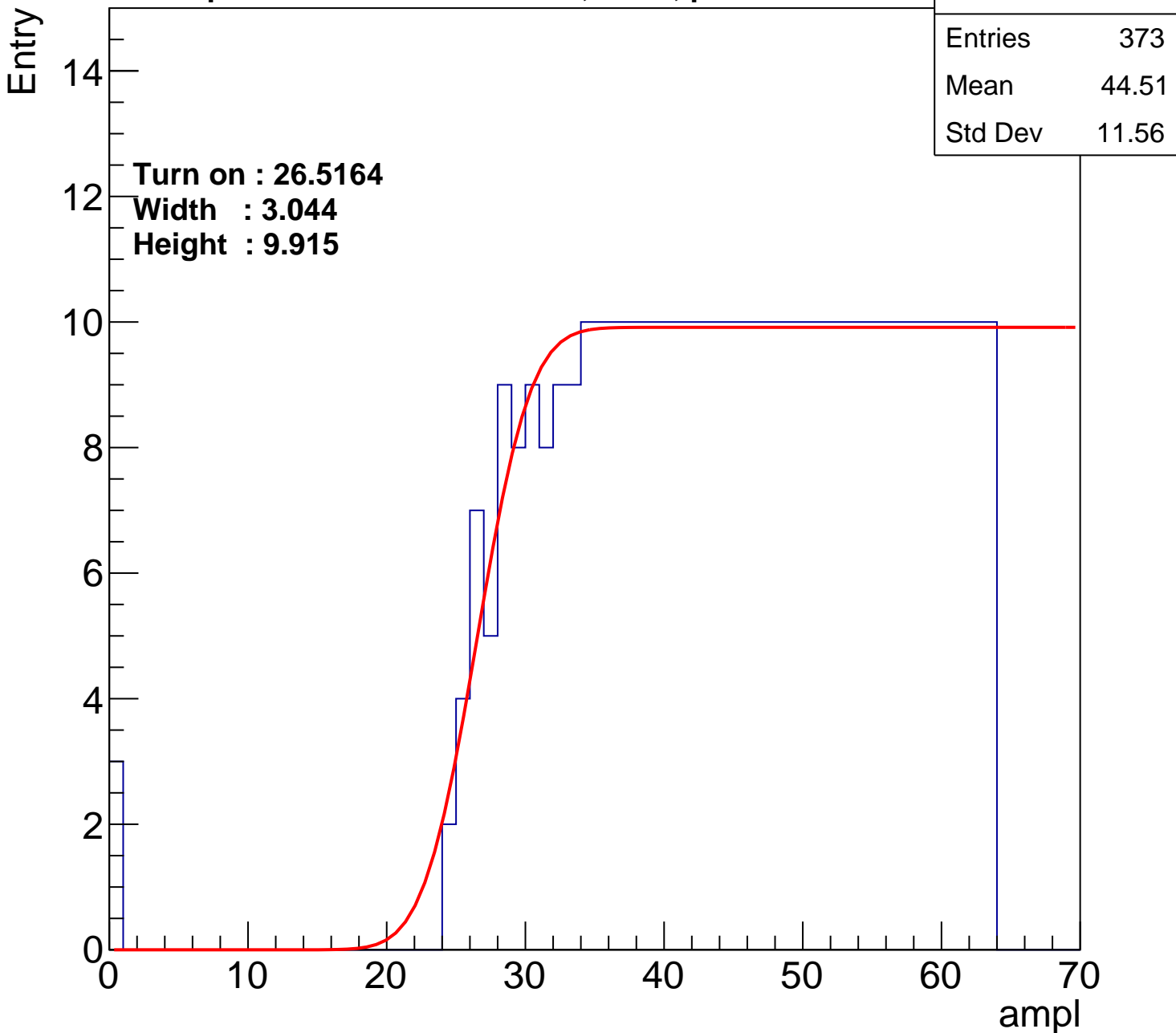
calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.51
Std Dev	11.56

Turn on : 26.5164

Width : 3.044

Height : 9.915



B0L000S, U13-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.51
Std Dev	11.56

Turn on : 26.5164

Width : 3.044

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl

