

B0L102S, U16-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.04
Std Dev	12.53

Turn on : 26.7207

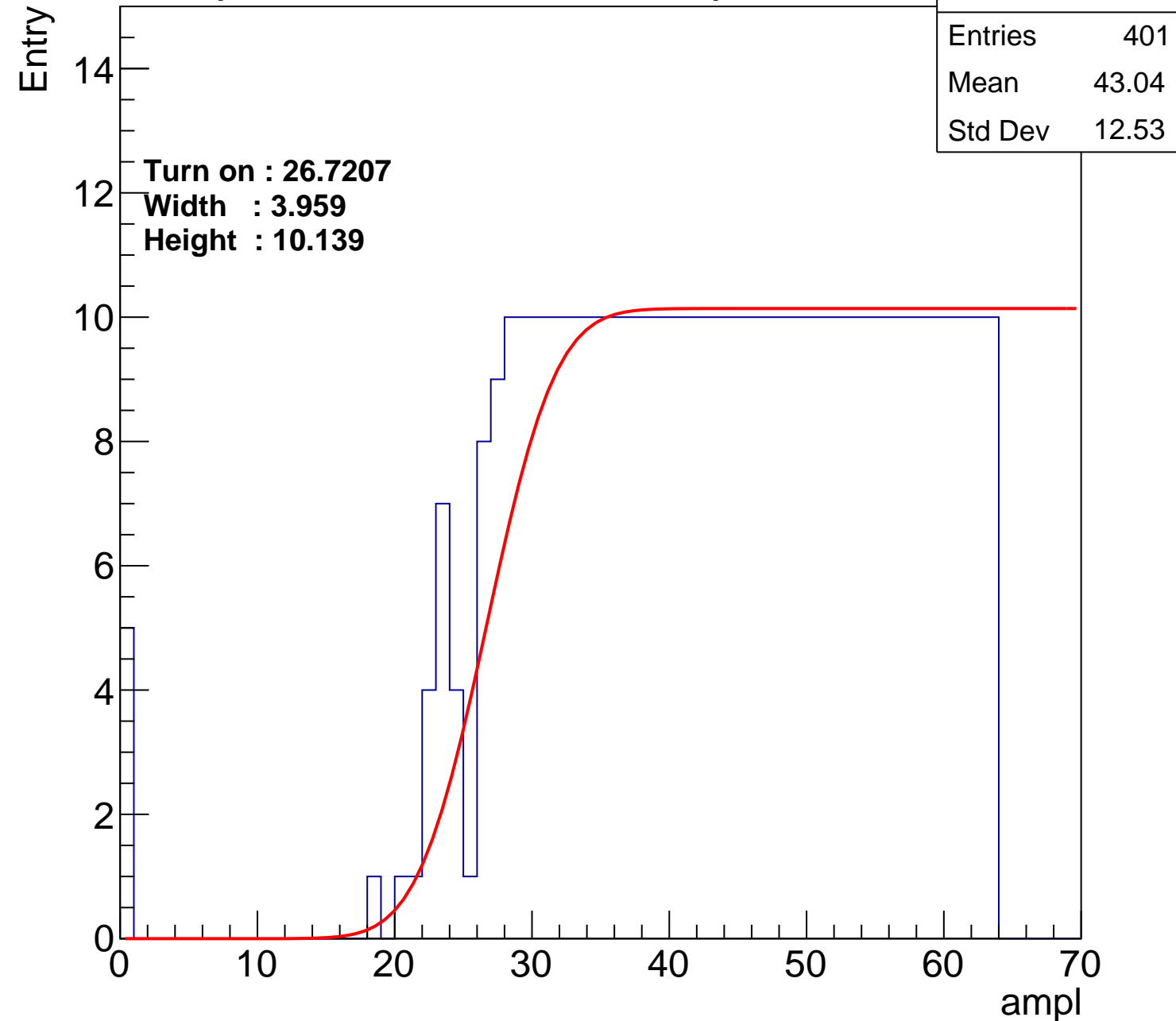
Width : 3.959

Height : 10.139

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.1
Std Dev	12

Turn on : 23.5880

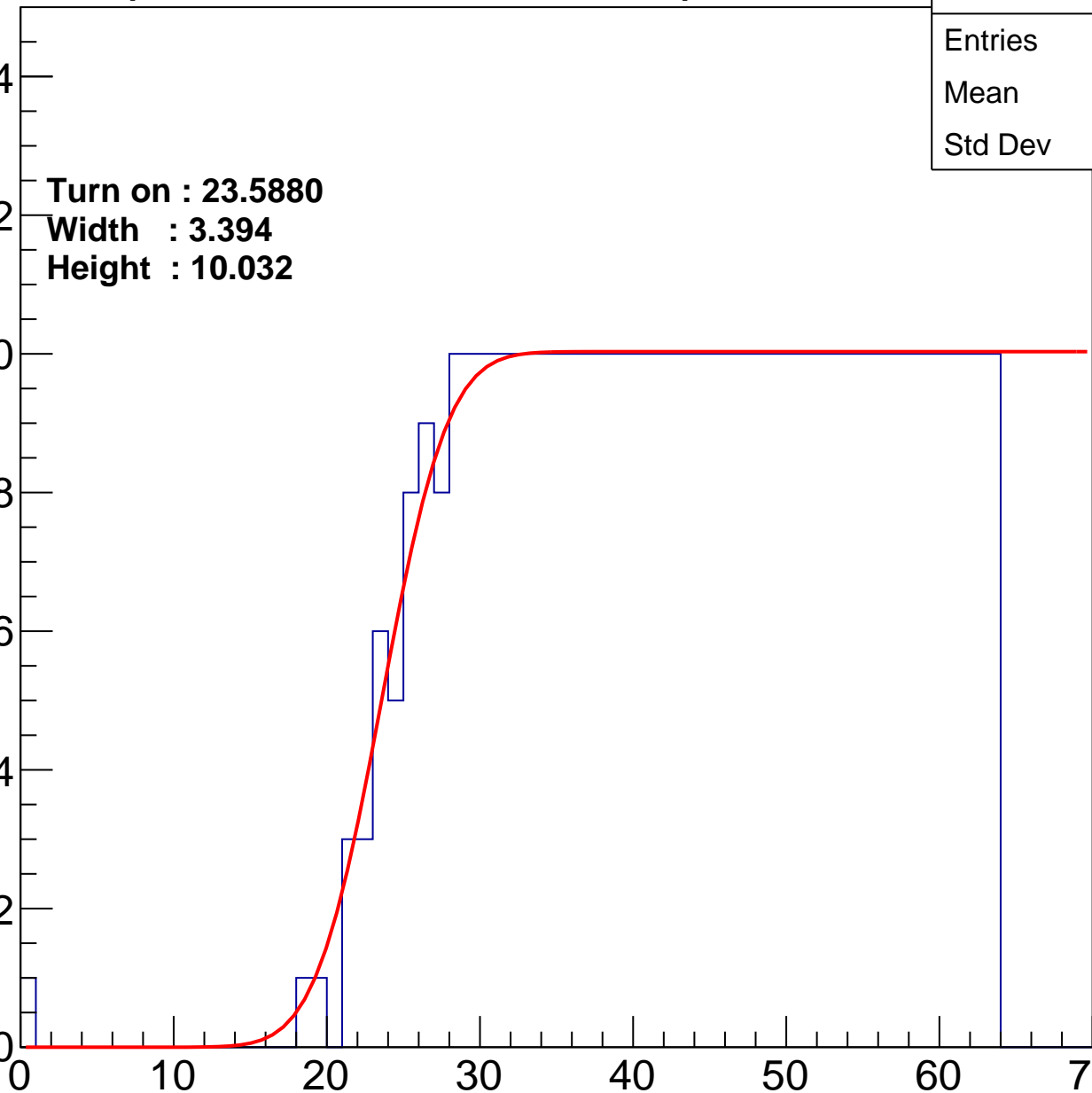
Width : 3.394

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 26.0191

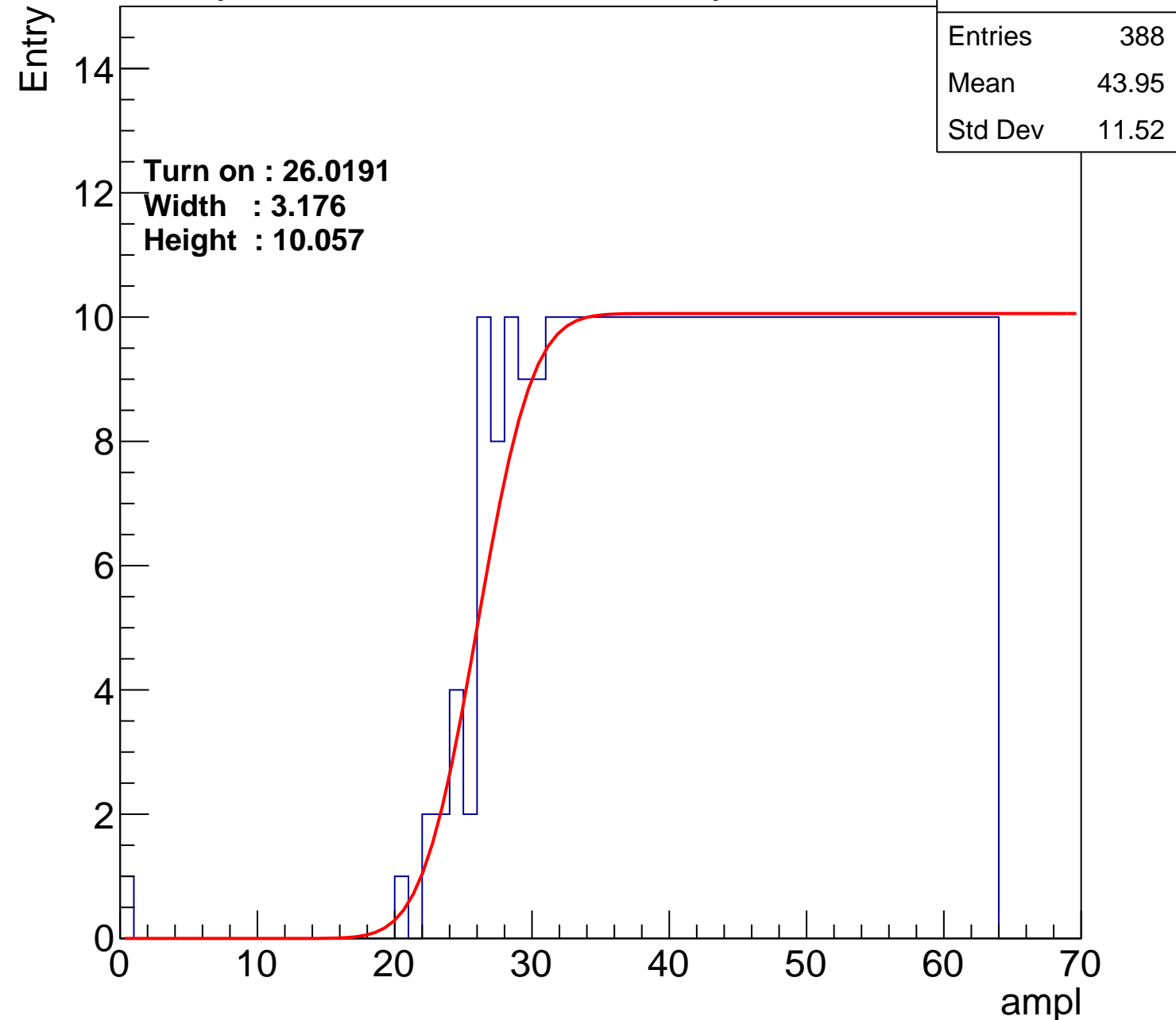
Width : 3.176

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.64
Std Dev	11.65

Turn on : 27.3036

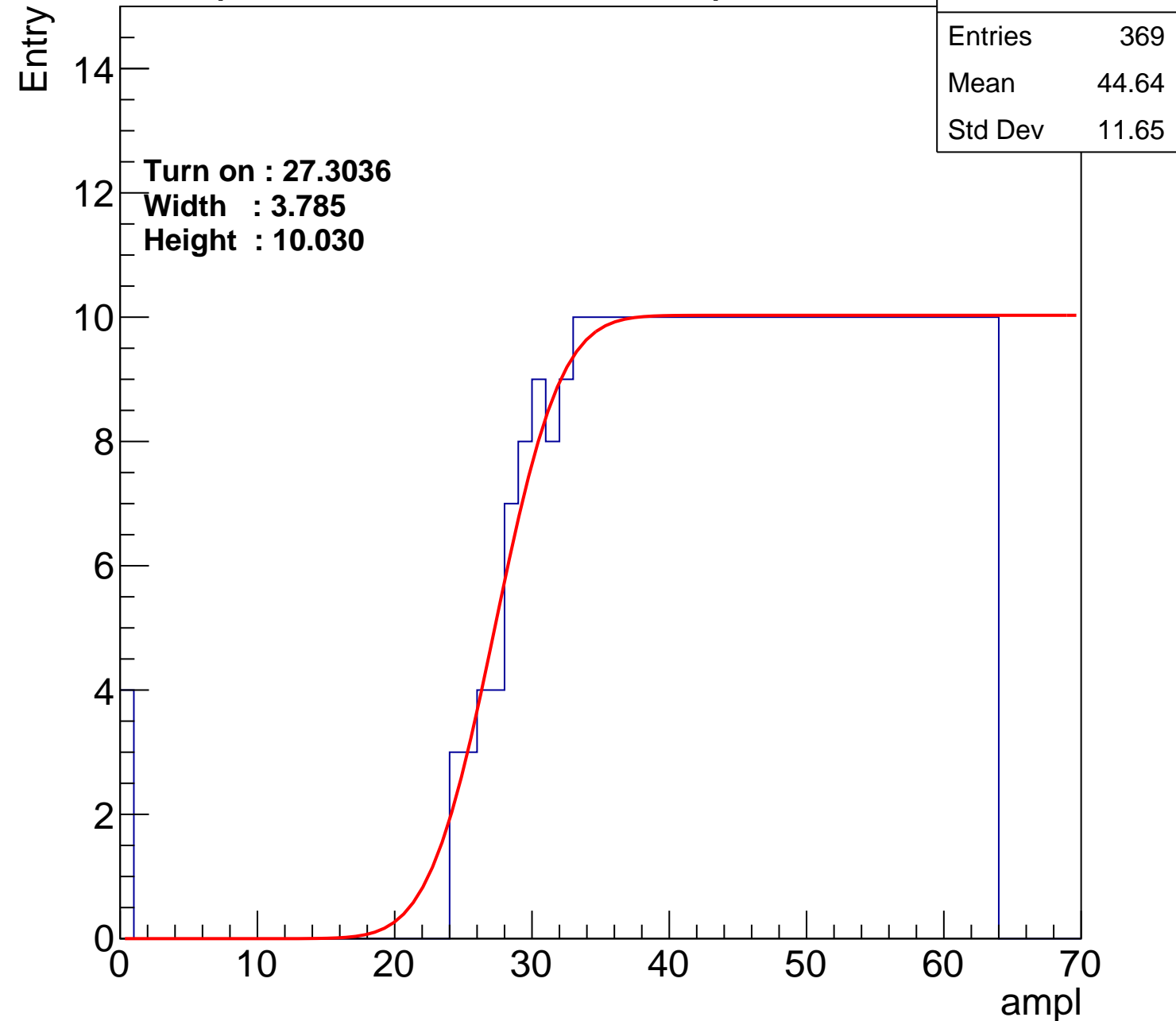
Width : 3.785

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entry

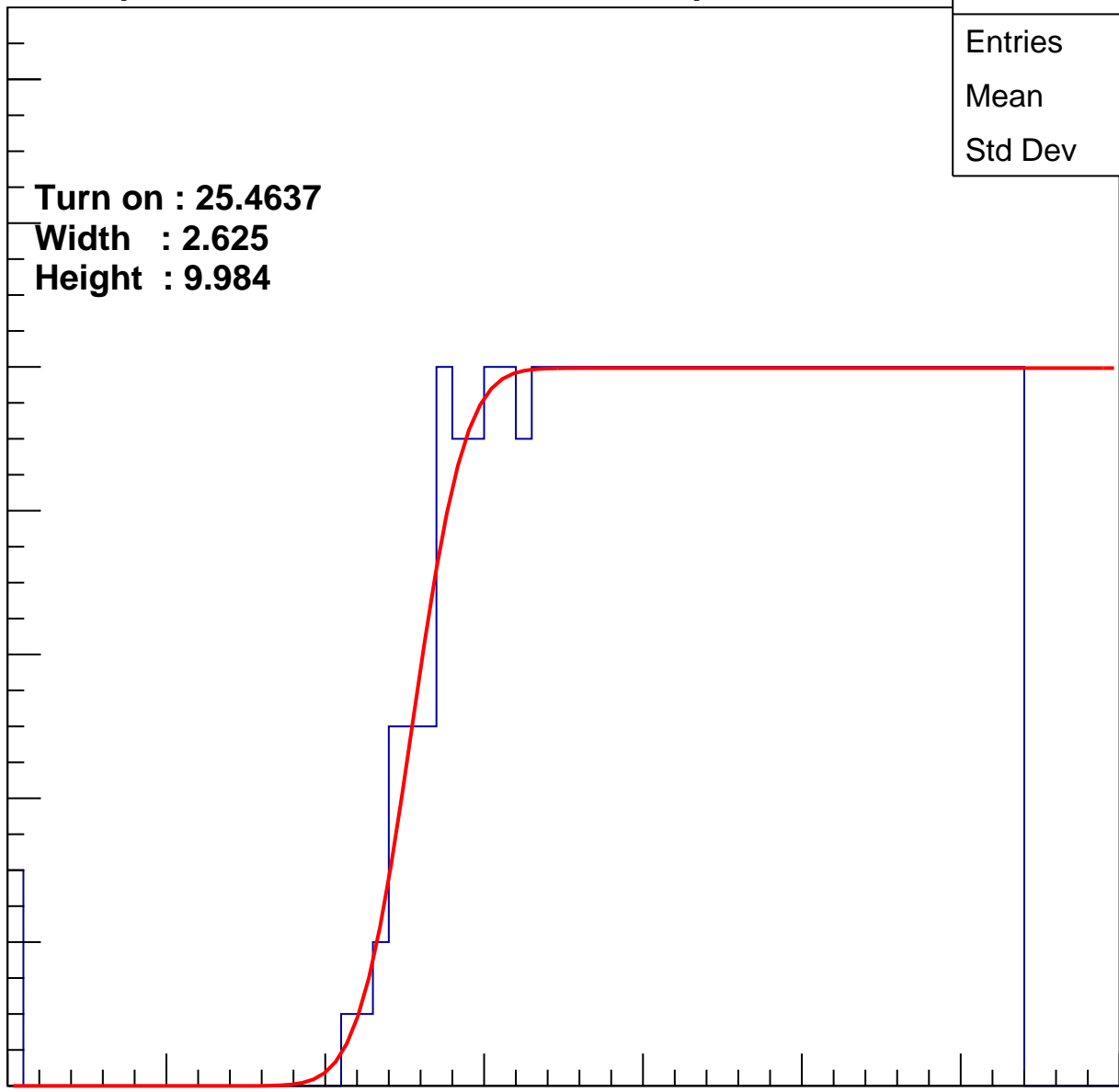
Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.4637
Width : 2.625
Height : 9.984

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U16-ch5

calib_packv5_042523_0143.root, FC#12, port B1

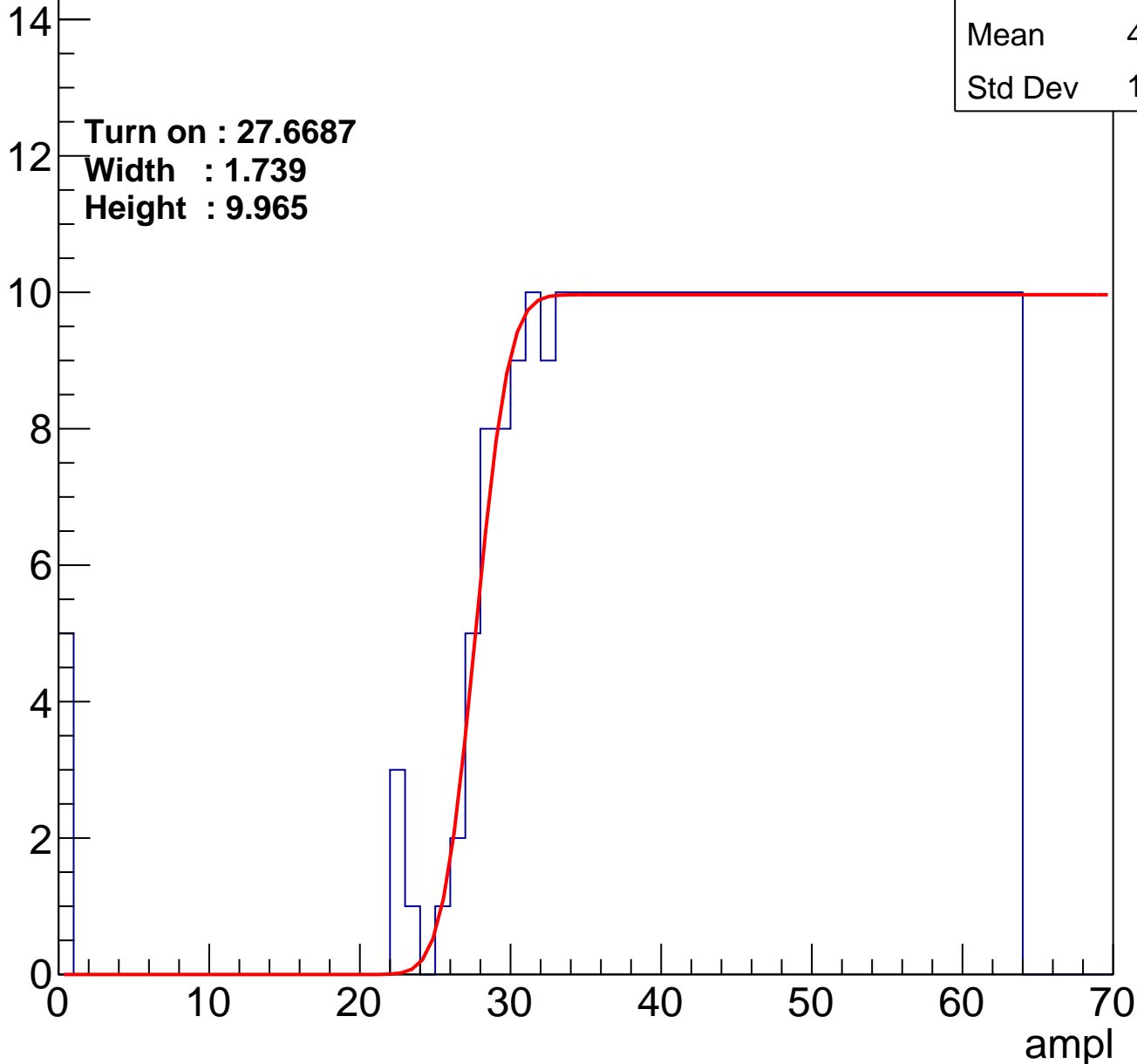
Entry

Entries	371
Mean	44.49
Std Dev	11.87

Turn on : 27.6687

Width : 1.739

Height : 9.965



B0L102S, U16-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9449

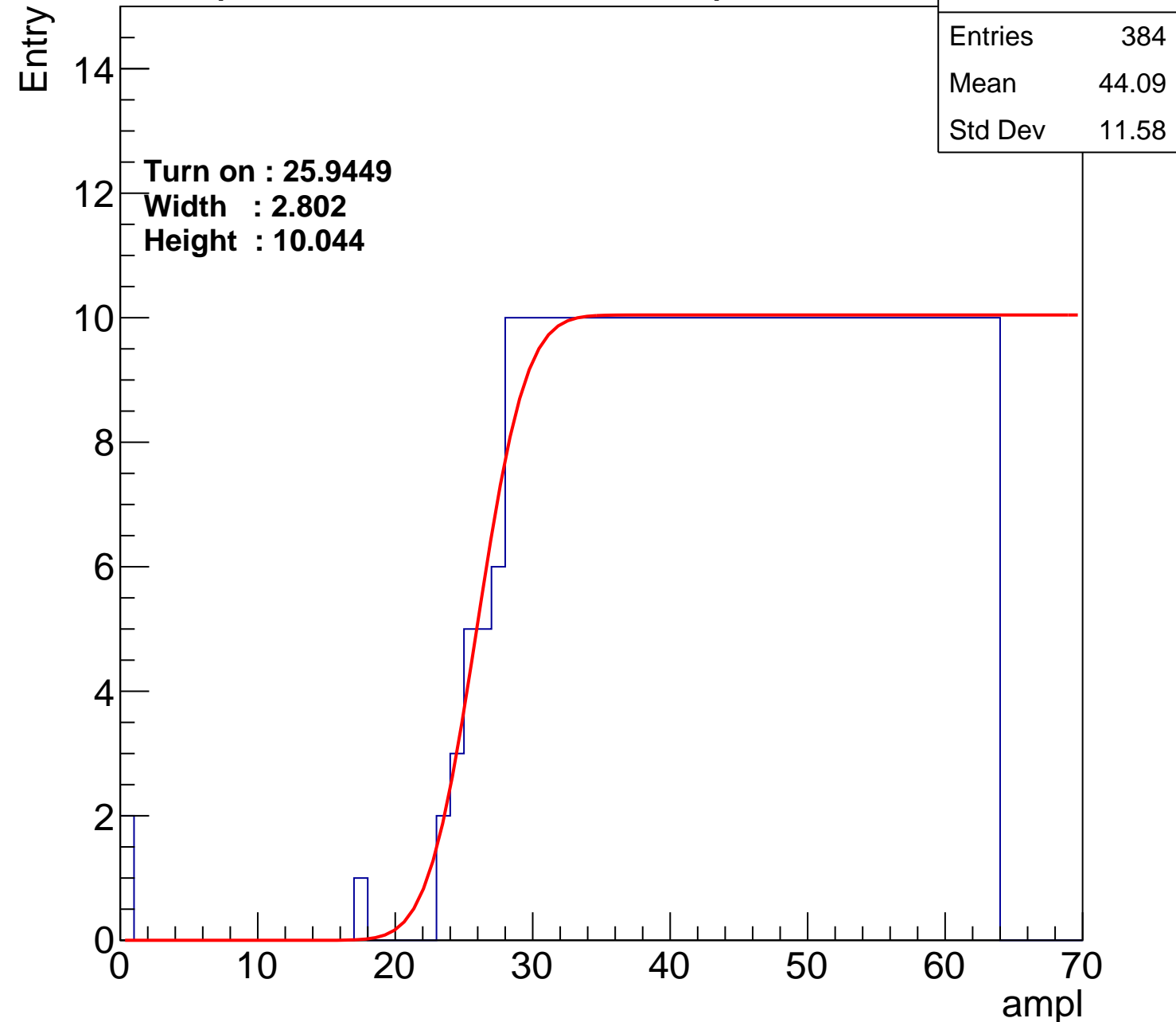
Width : 2.802

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.2970

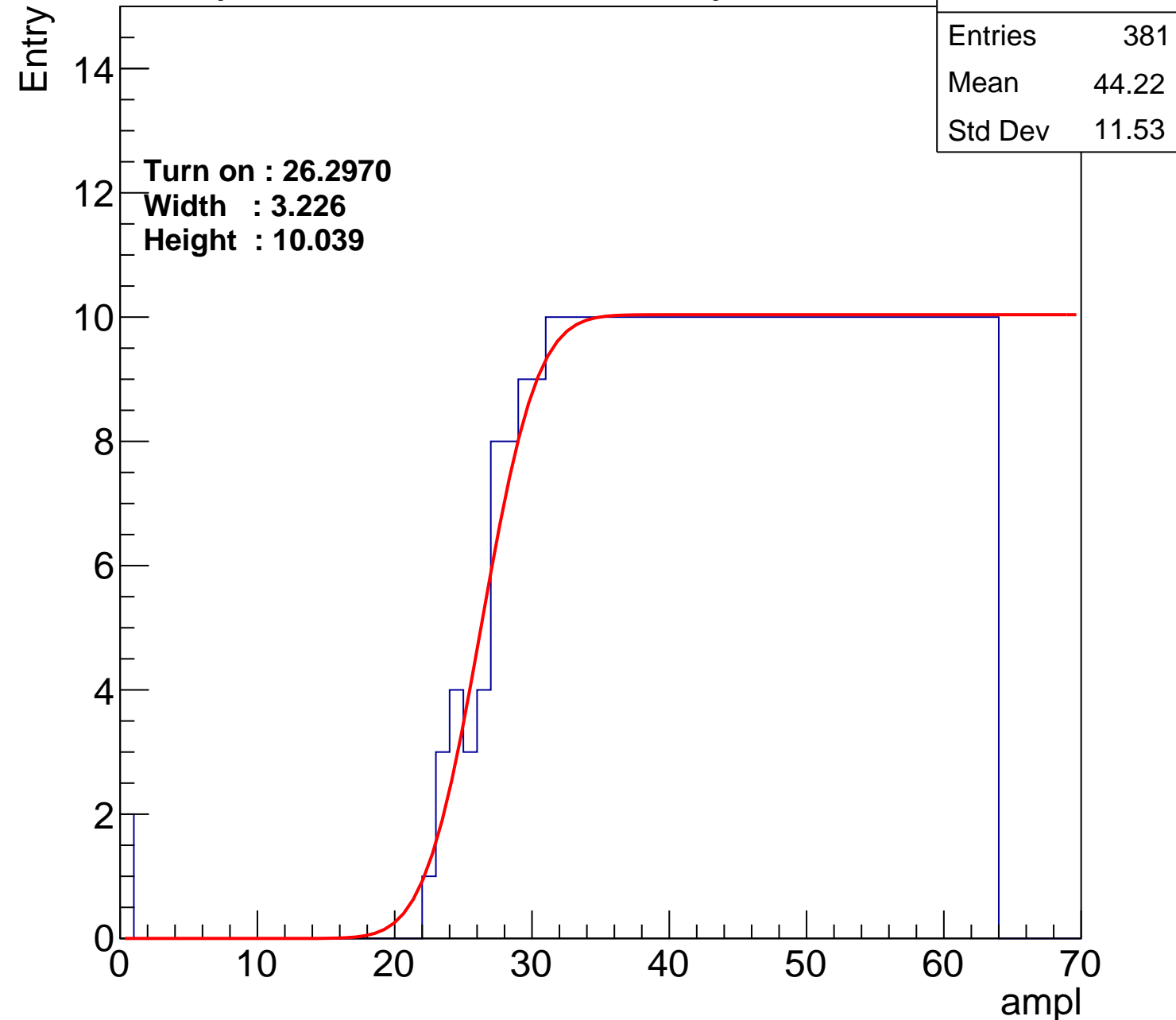
Width : 3.226

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
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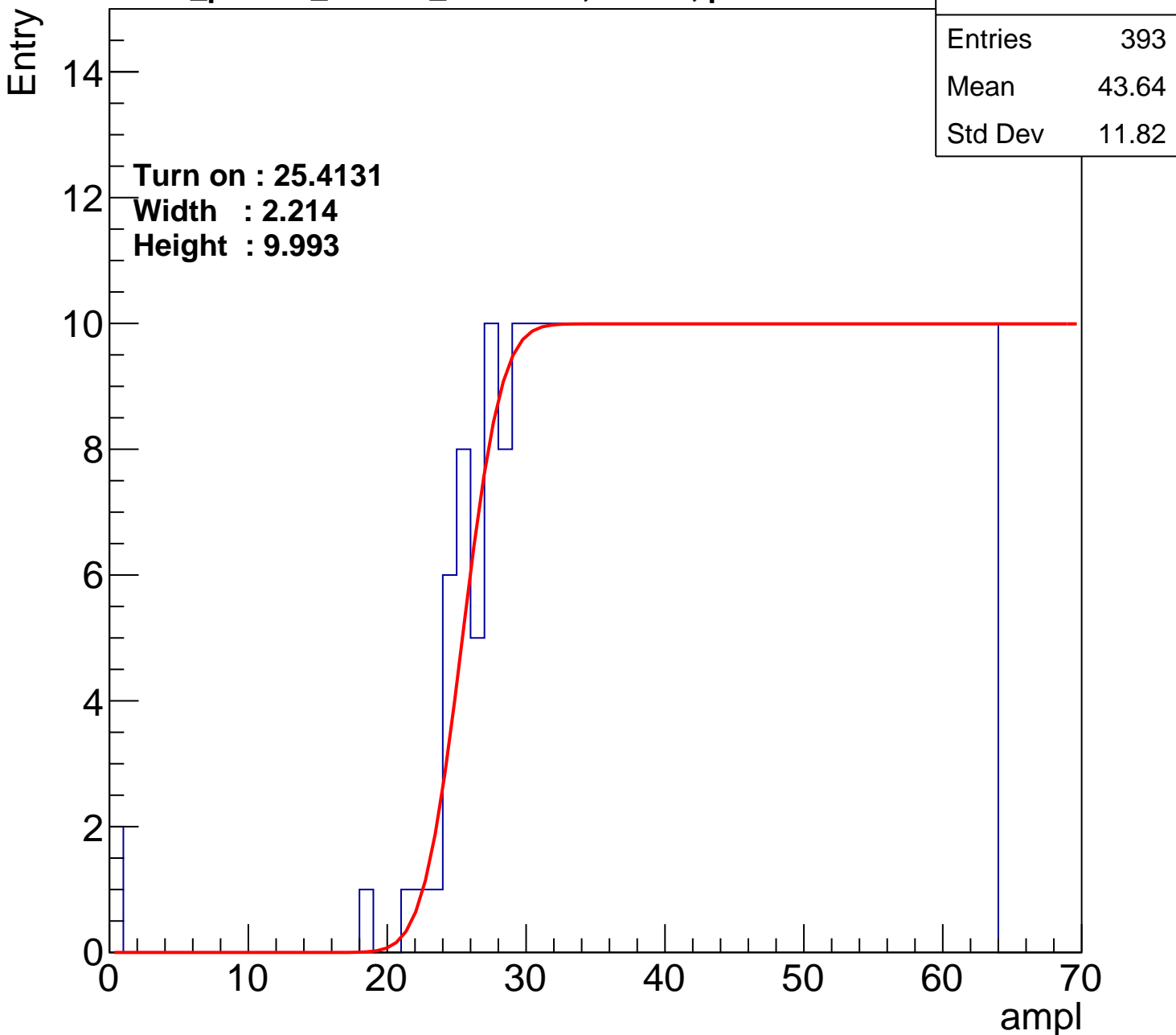
Mean	43.64
------	-------

Std Dev	11.82
---------	-------

Turn on : 25.4131

Width : 2.214

Height : 9.993



B0L102S, U16-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.04
Std Dev	11.13

Turn on : 28.0286

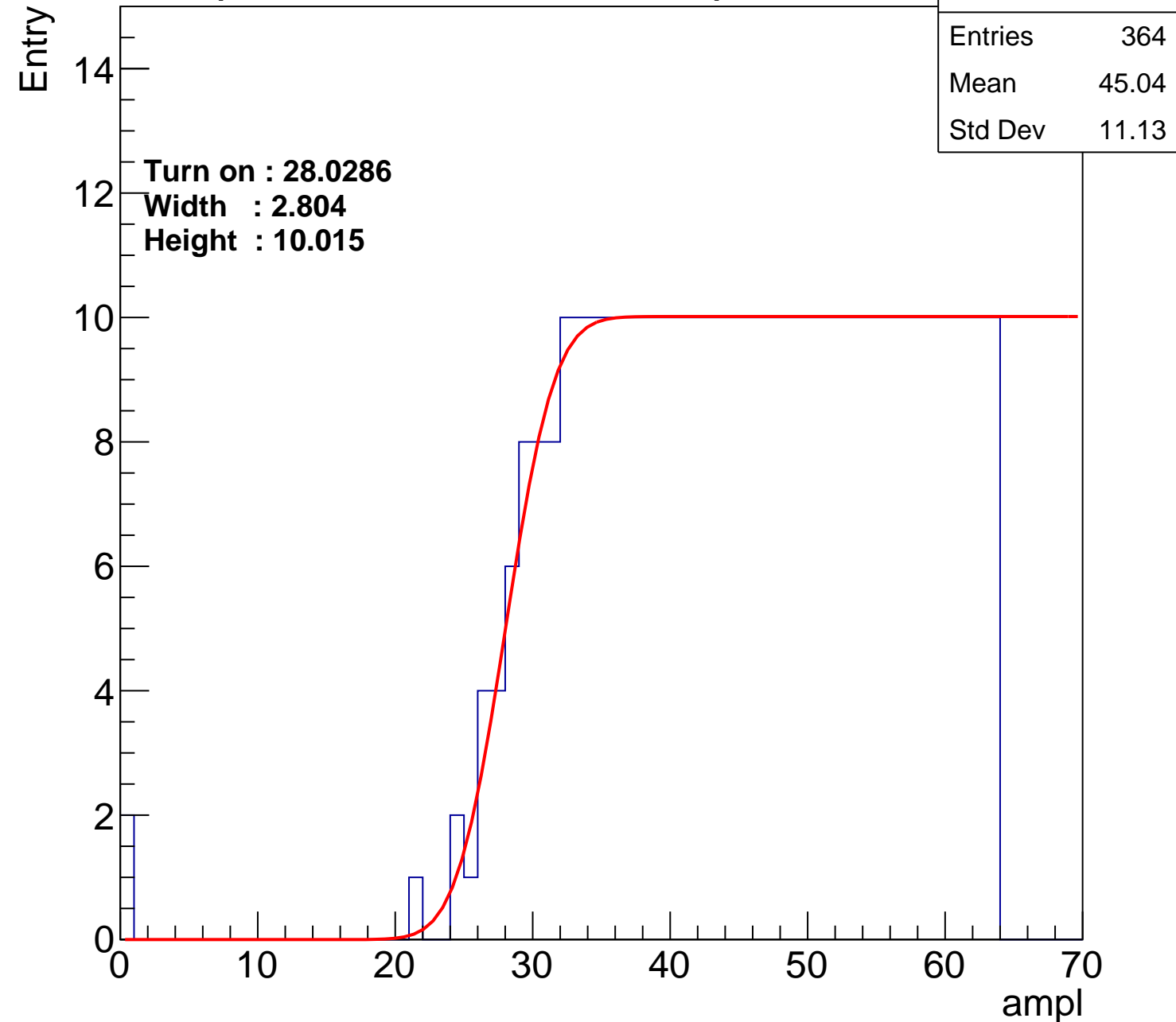
Width : 2.804

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.96
Std Dev	12.32

Turn on : 23.4634

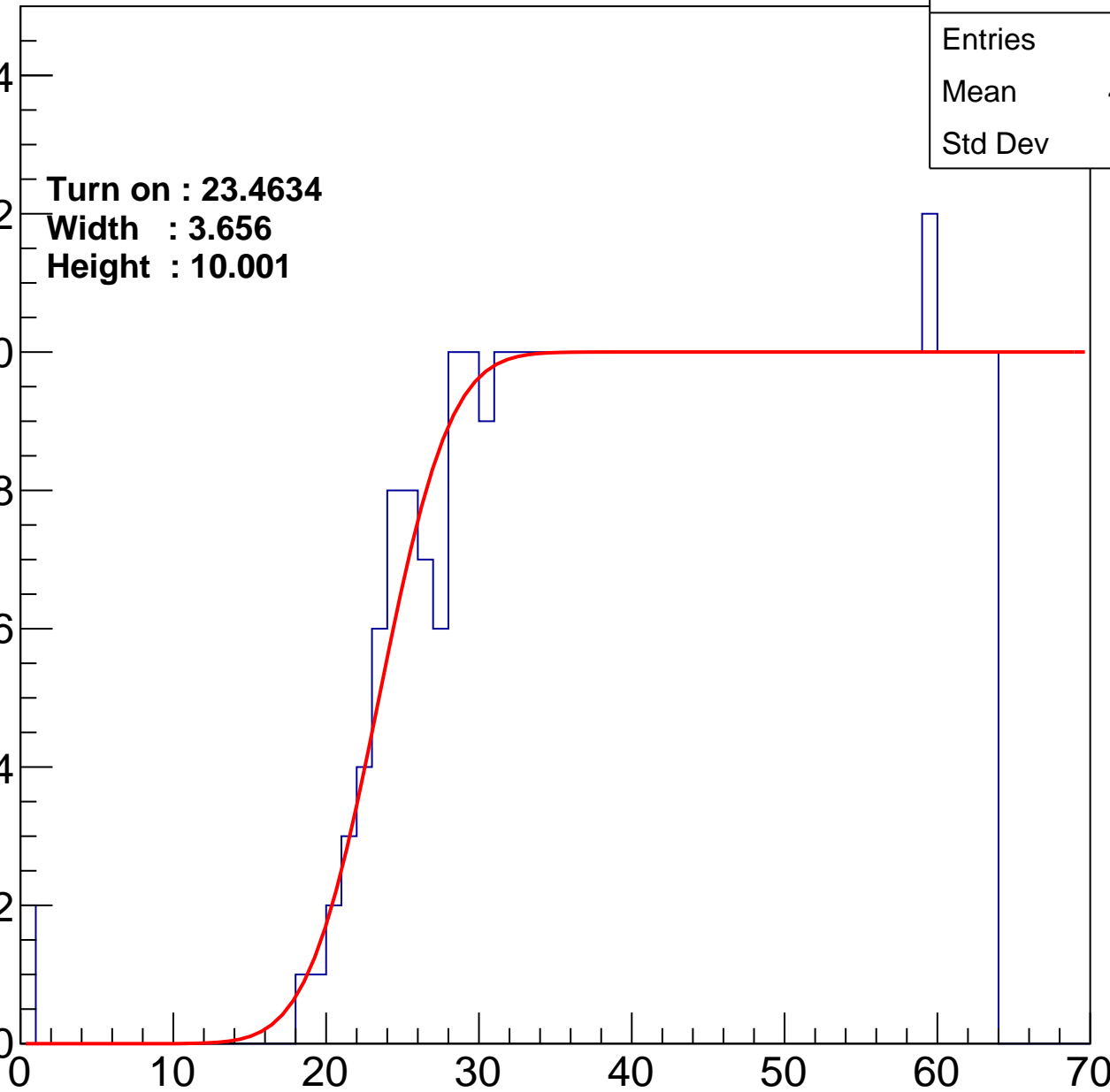
Width : 3.656

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.6657

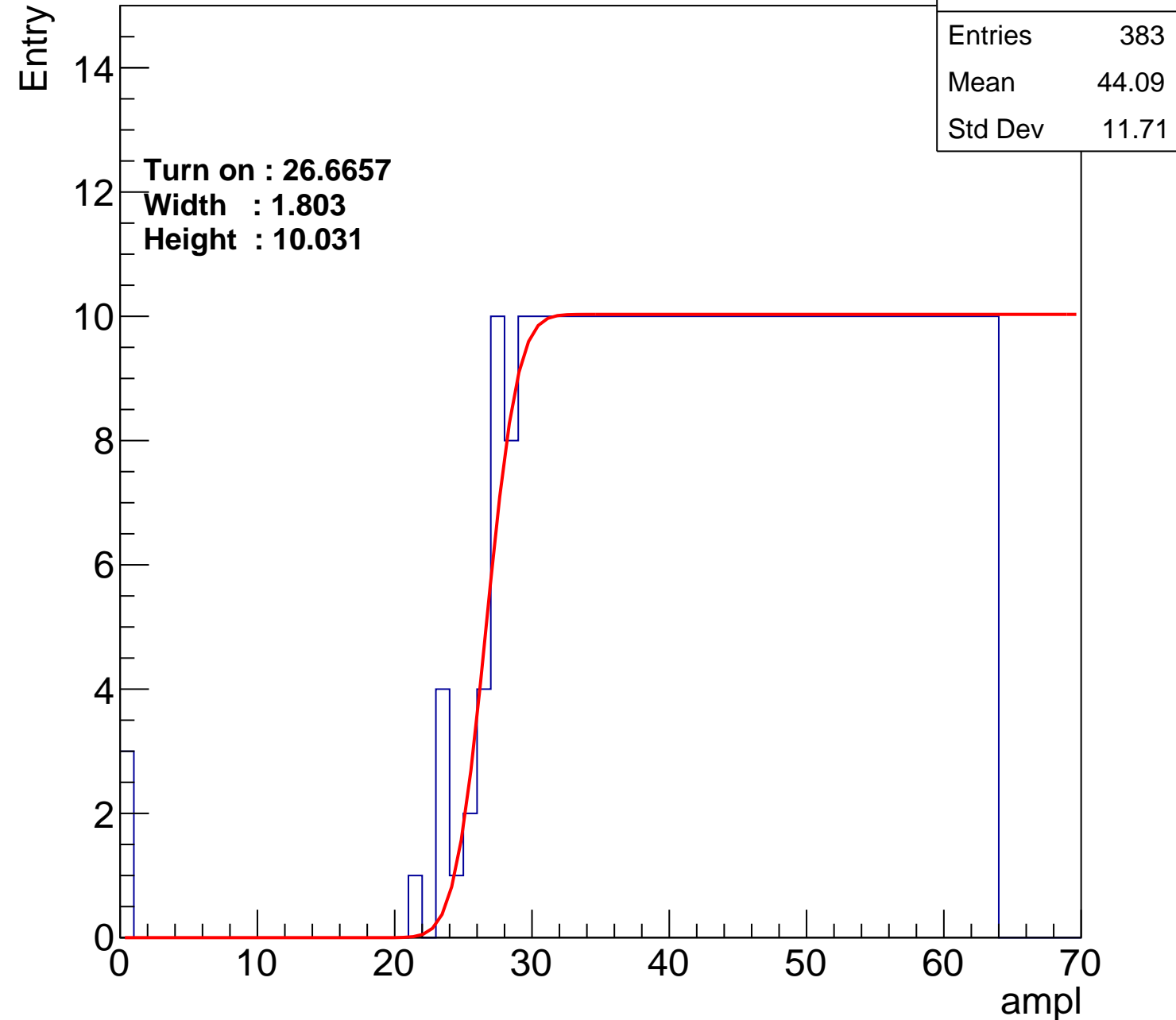
Width : 1.803

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 26.1118

Width : 2.955

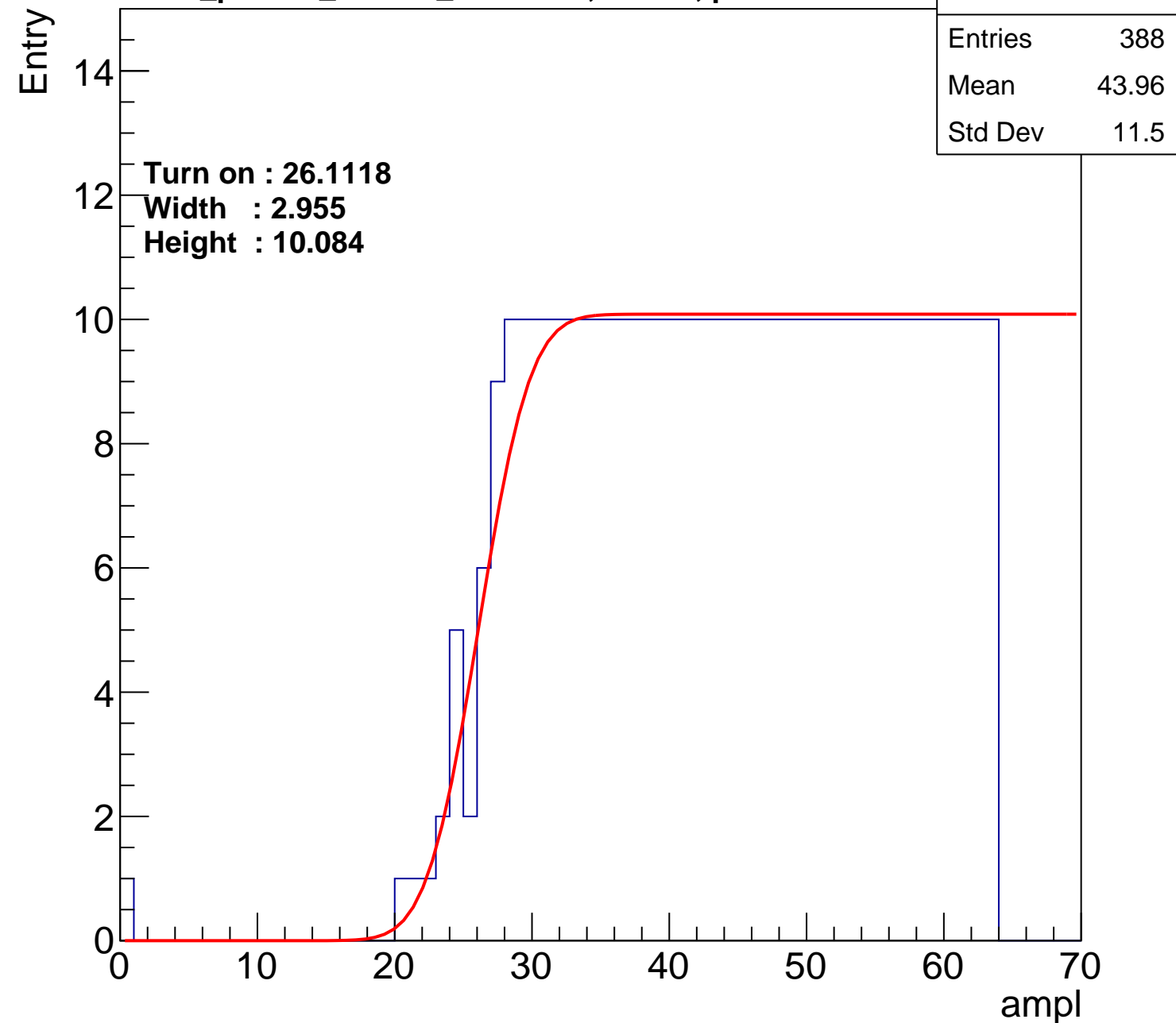
Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U16-ch13

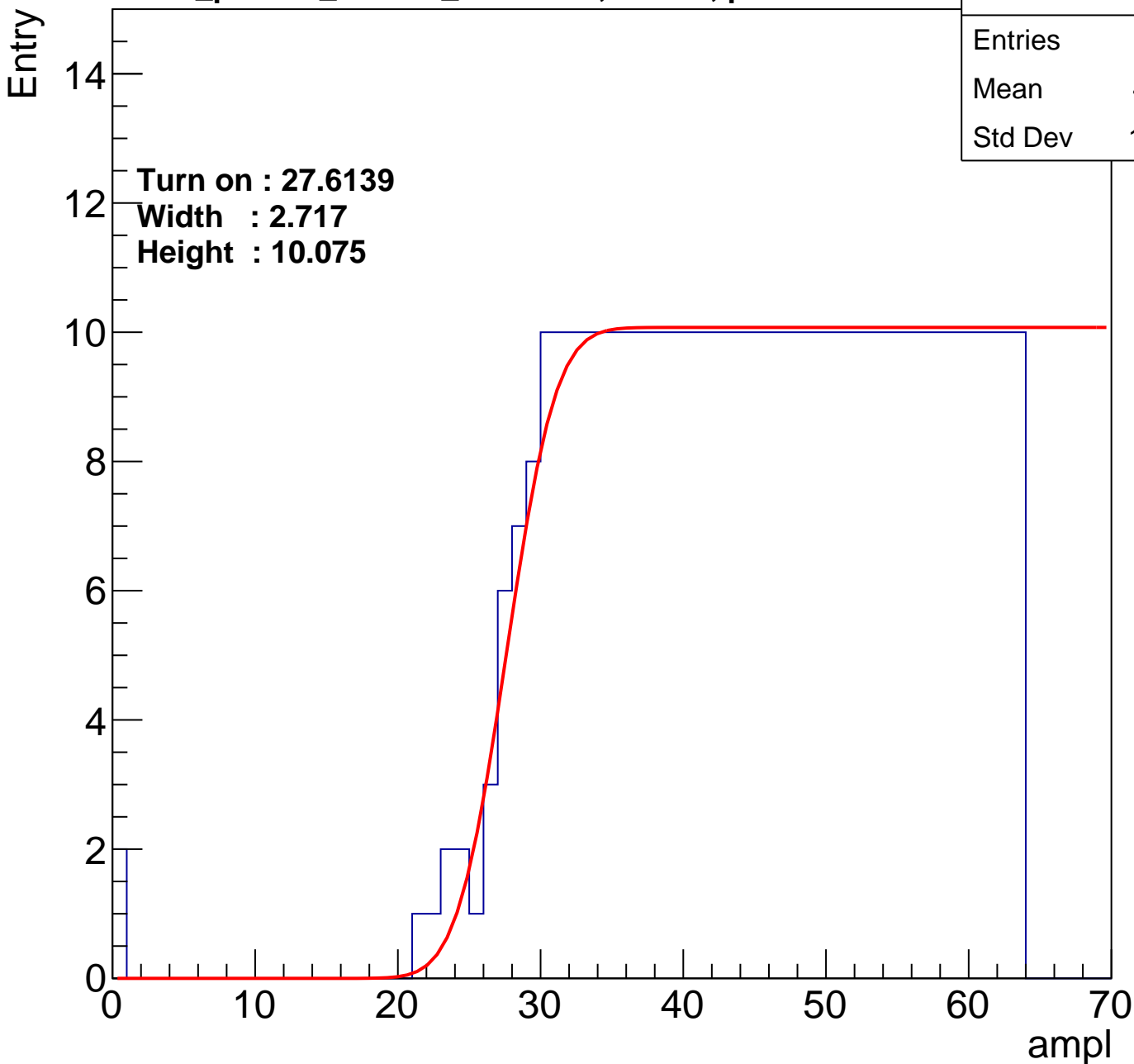
calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.6139

Width : 2.717

Height : 10.075



B0L102S, U16-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.89
Std Dev	11.99

Turn on : 26.2441

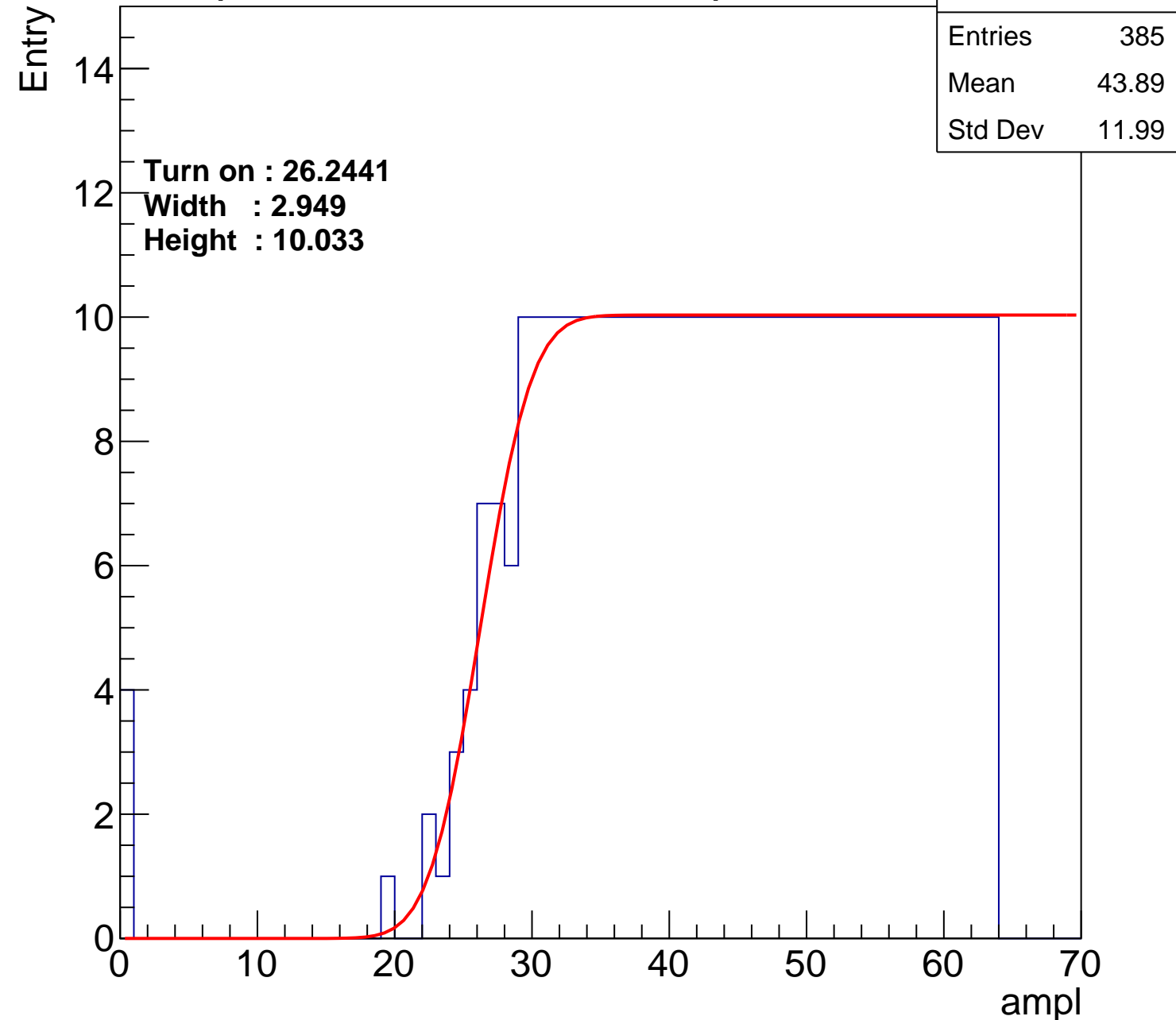
Width : 2.949

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.6
Std Dev	11.99

Turn on : 25.0722

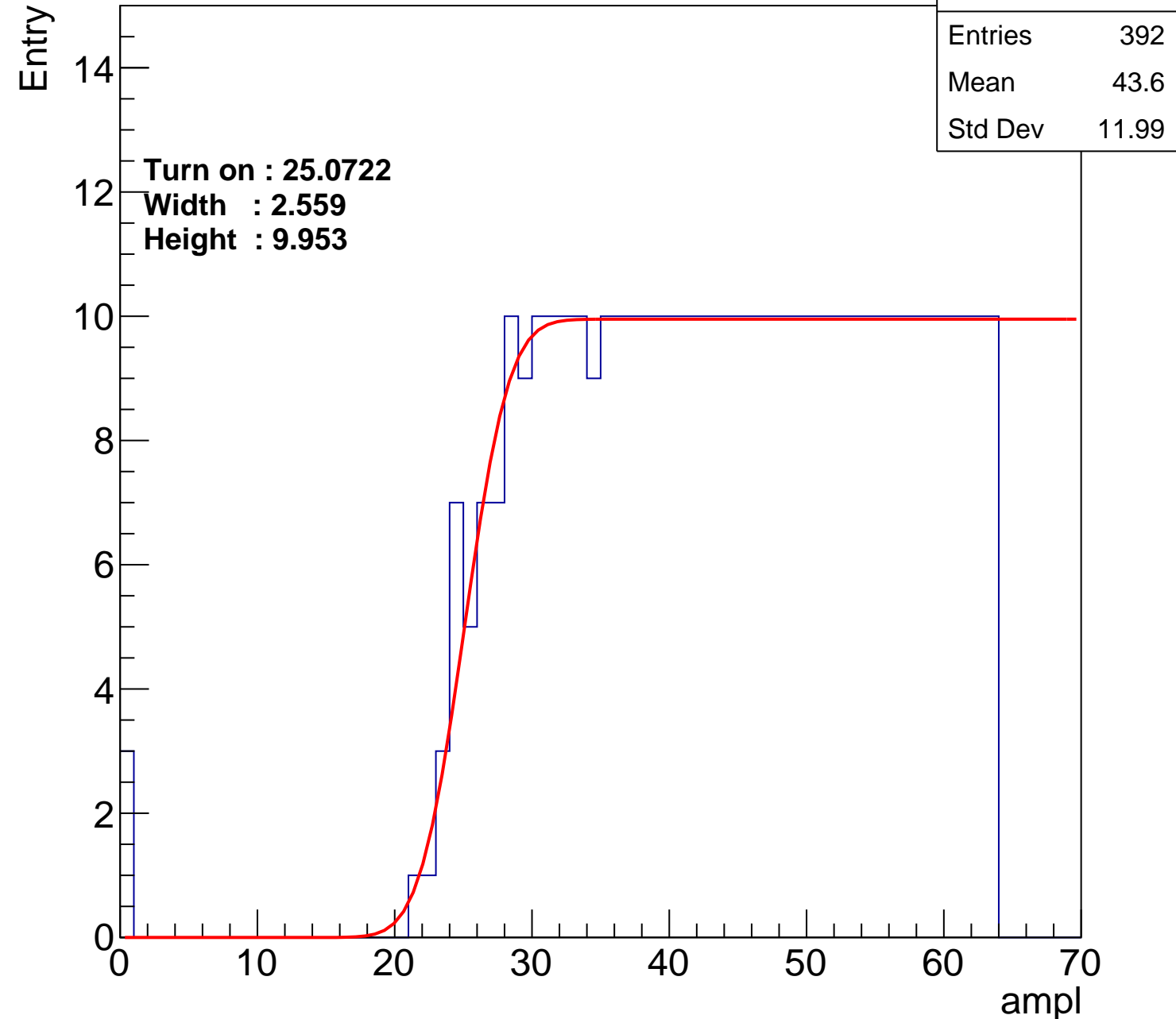
Width : 2.559

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.99
Std Dev	12.19

Turn on : 24.1004

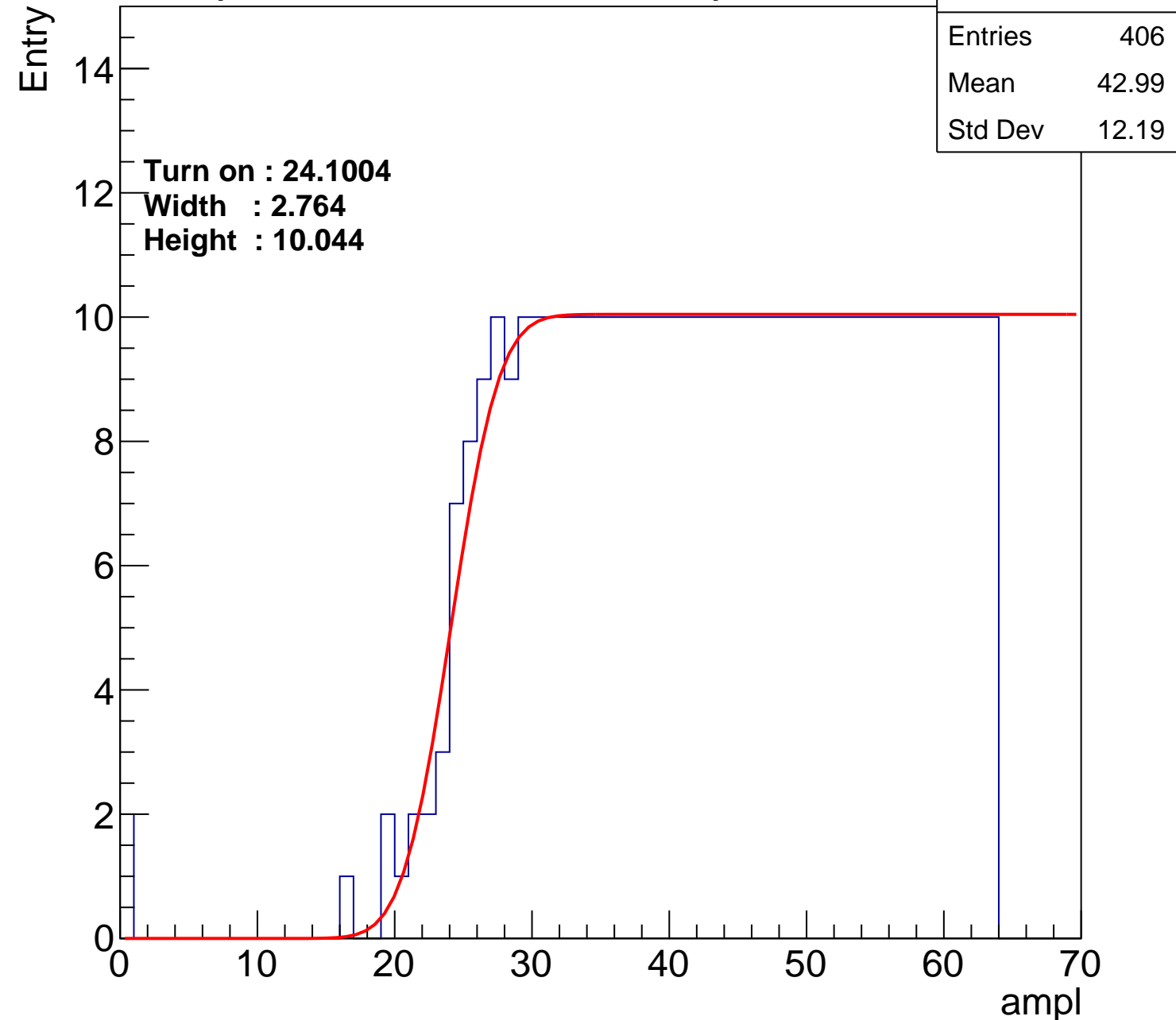
Width : 2.764

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.16
Std Dev	12.34

Turn on : 24.4328

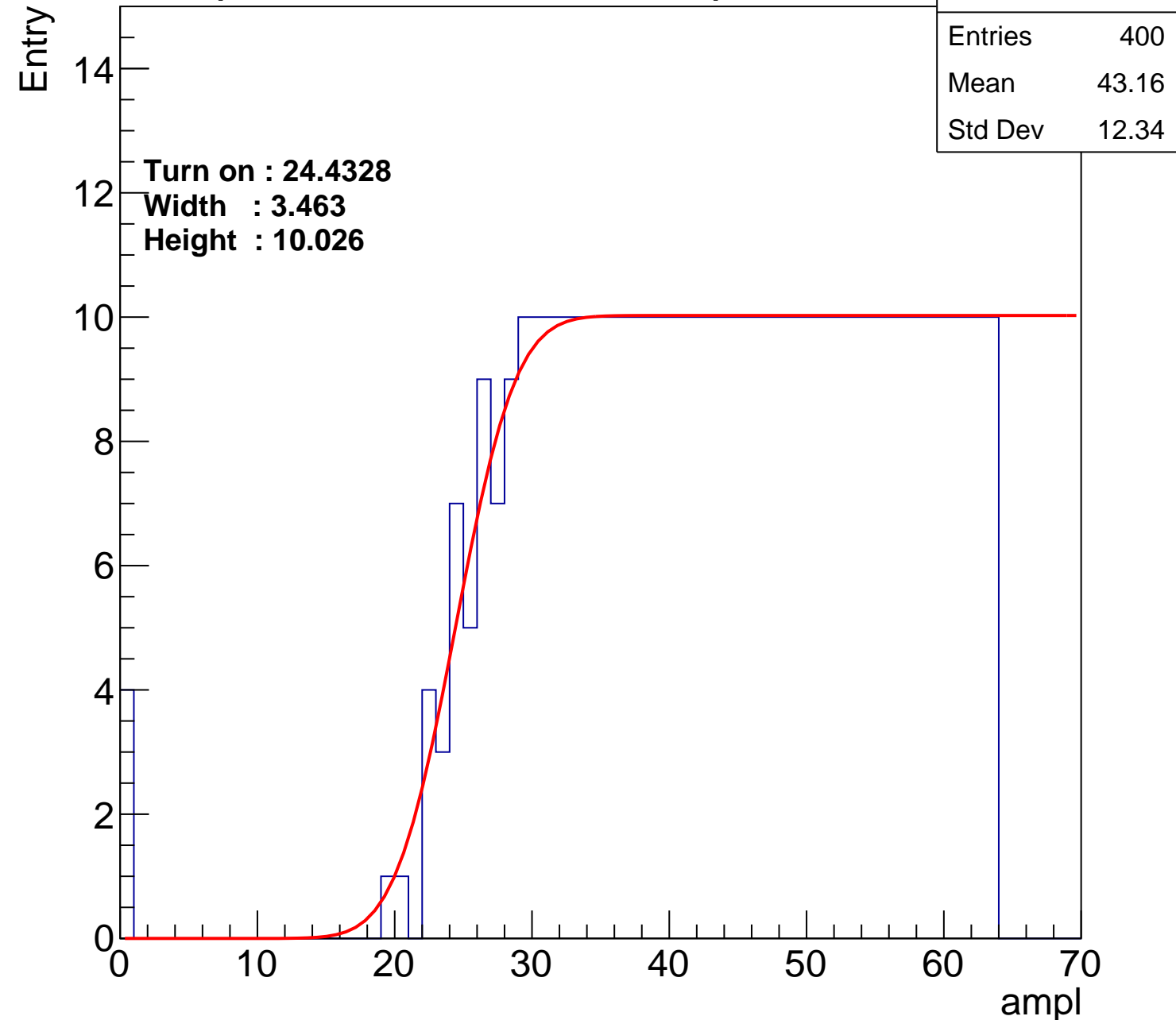
Width : 3.463

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.62
Std Dev	11.57

Turn on : 27.7501

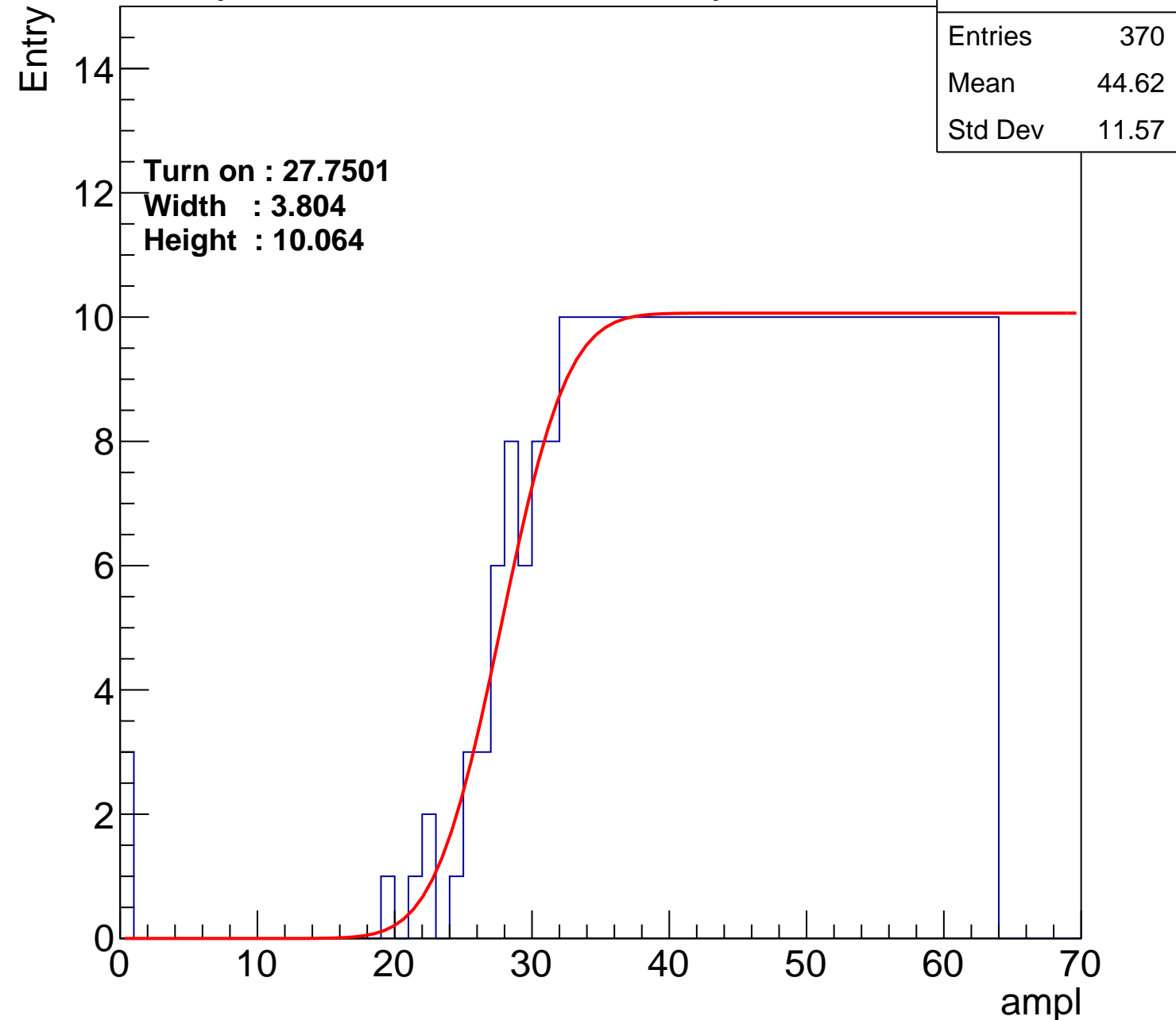
Width : 3.804

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.87
Std Dev	12.12

Turn on : 26.6314

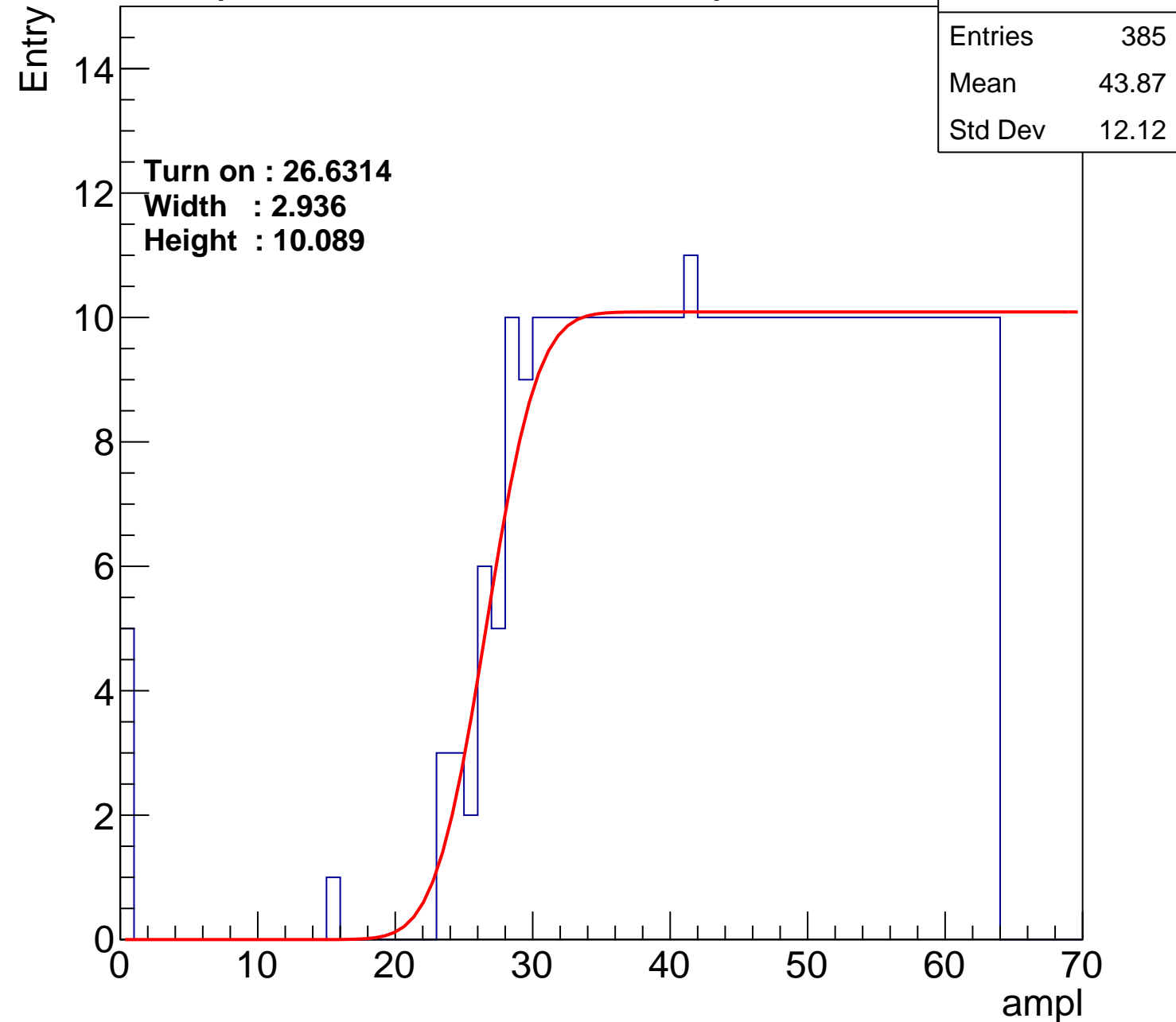
Width : 2.936

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.9719

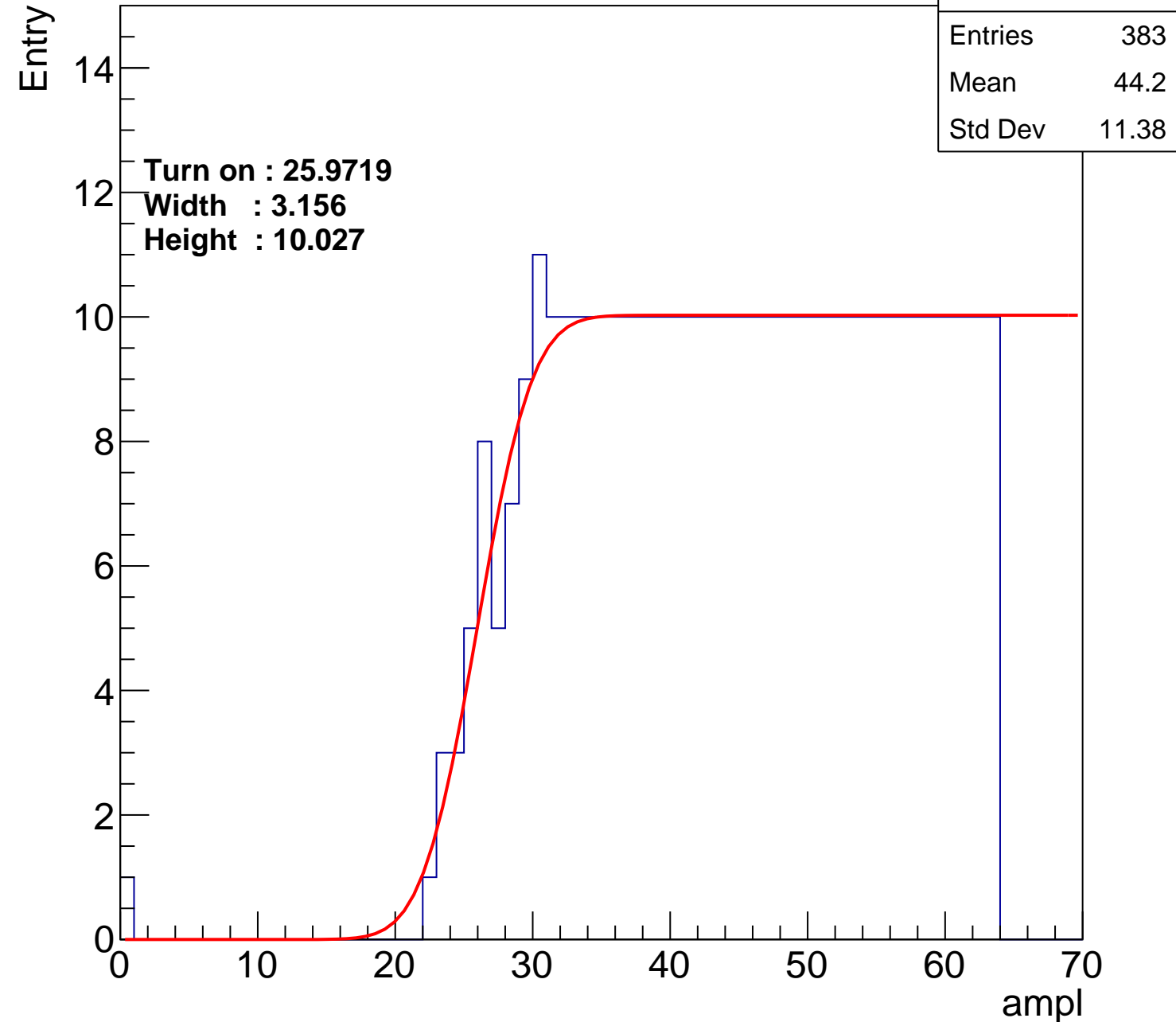
Width : 3.156

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.75
Std Dev	12.16

Turn on : 25.5022

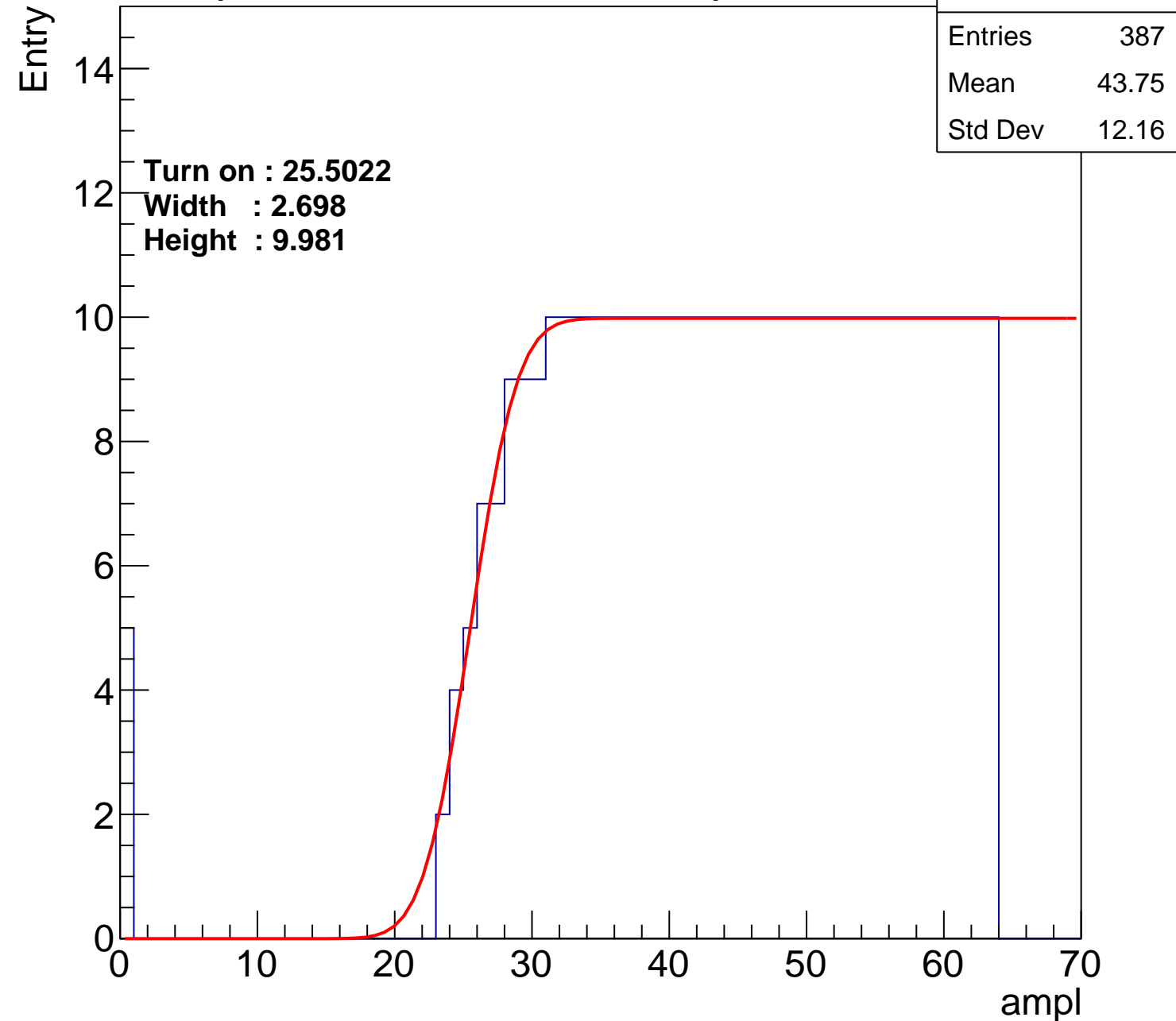
Width : 2.698

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch22

calib_packv5_042523_0143.root, FC#12, port B1

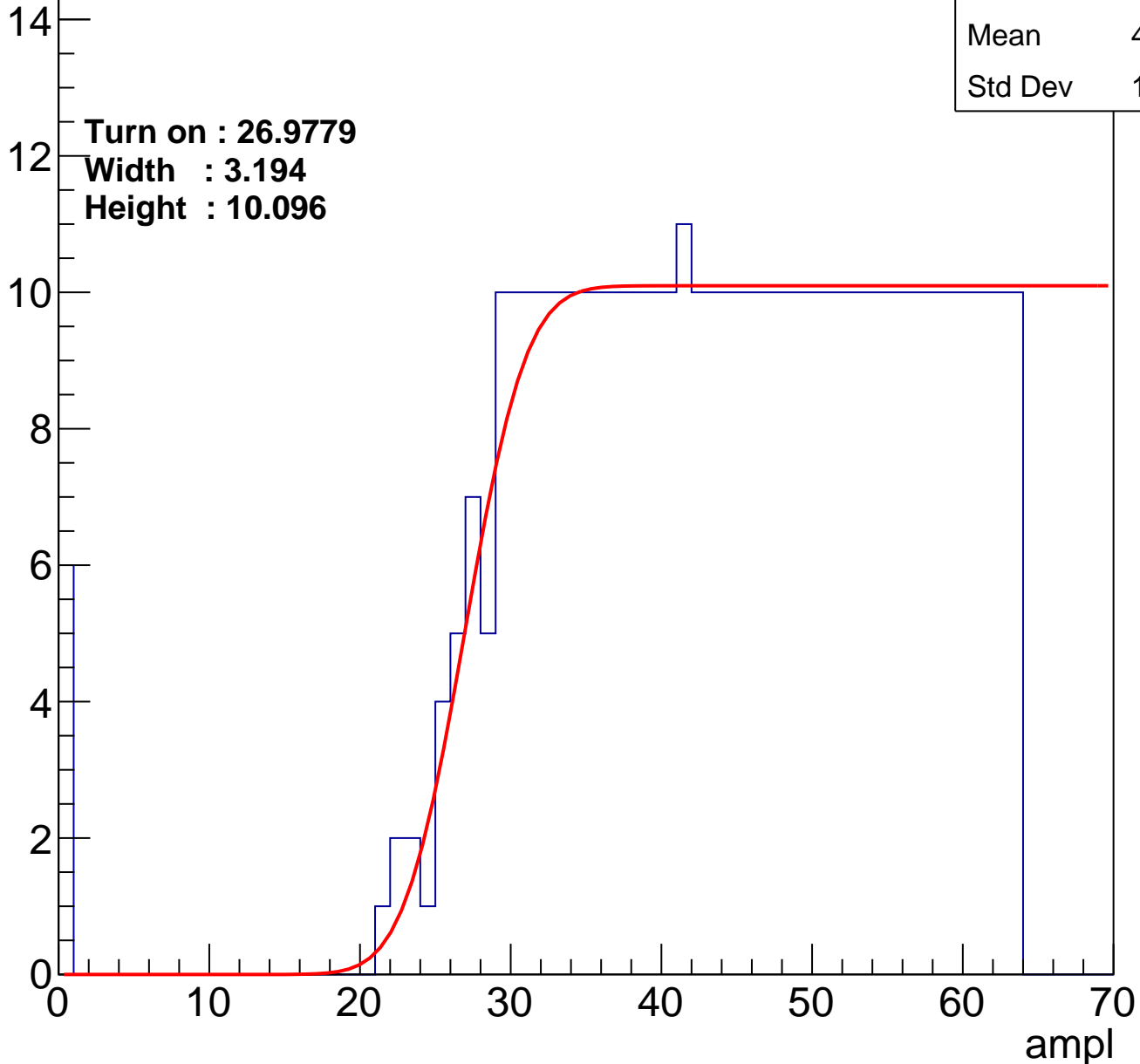
Entries	384
Mean	43.84
Std Dev	12.28

Turn on : 26.9779

Width : 3.194

Height : 10.096

Entry



B0L102S, U16-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.7
Std Dev	11.67

Turn on : 27.7867

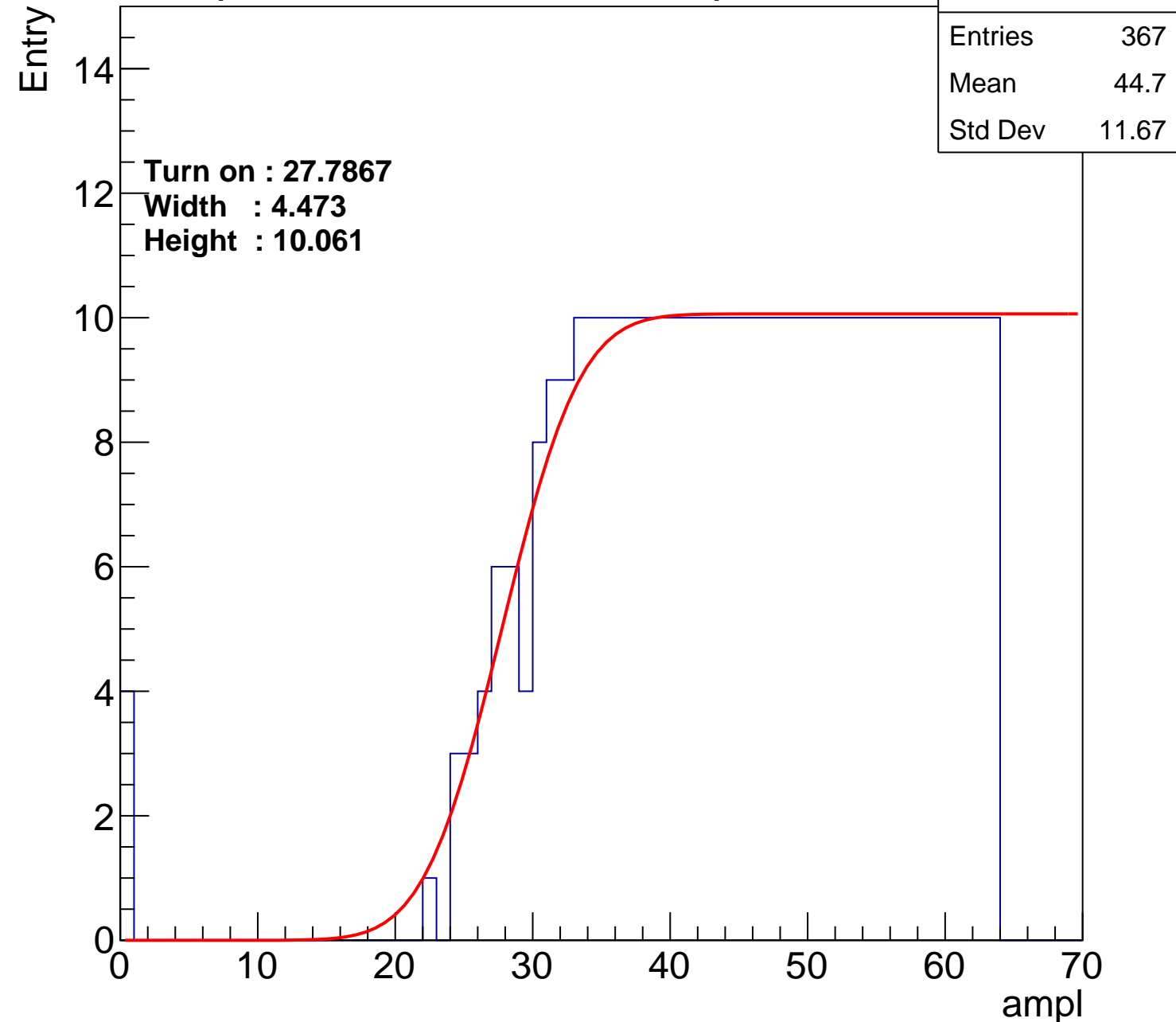
Width : 4.473

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.64
Std Dev	11.81

Turn on : 24.5112

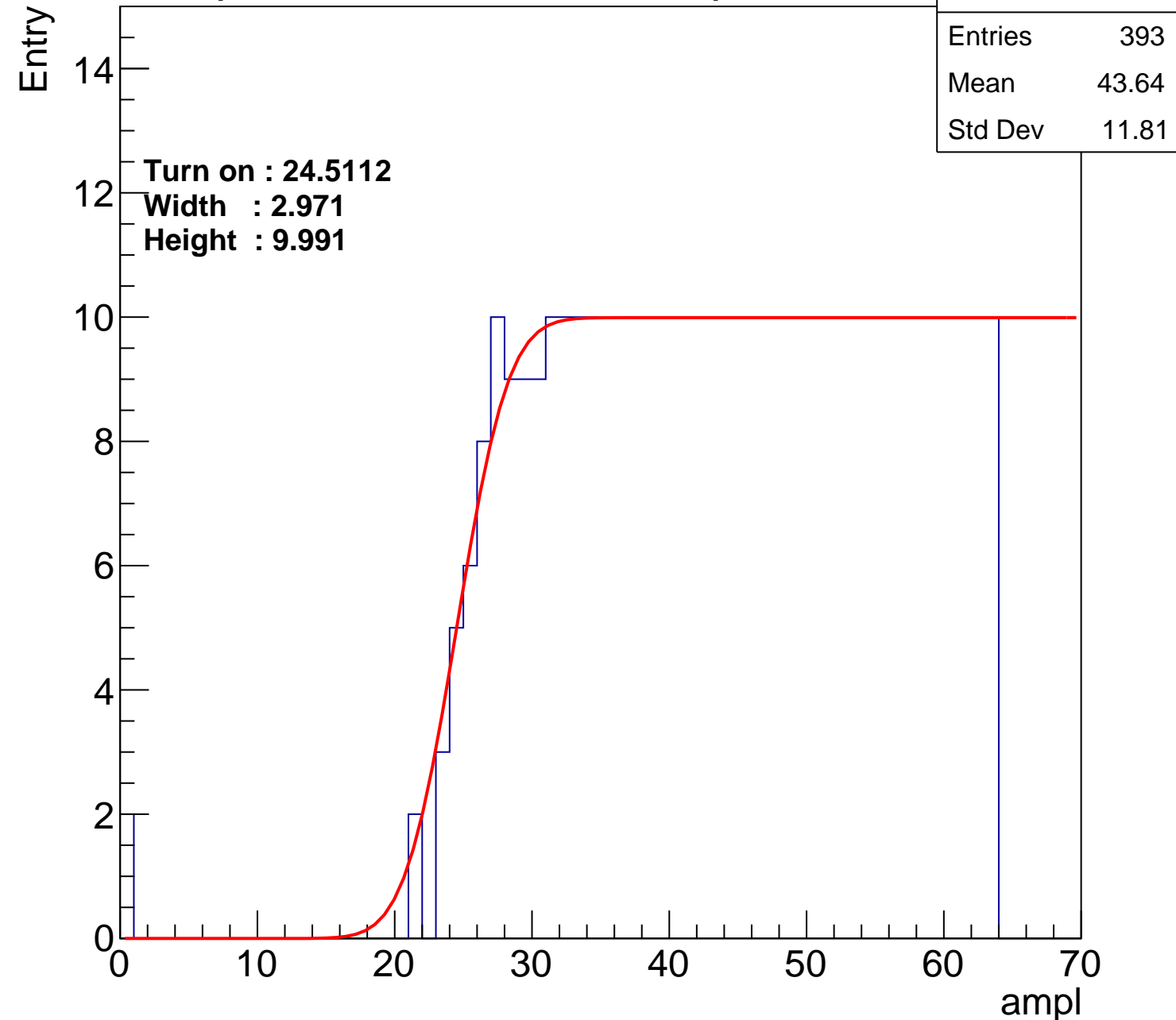
Width : 2.971

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch25

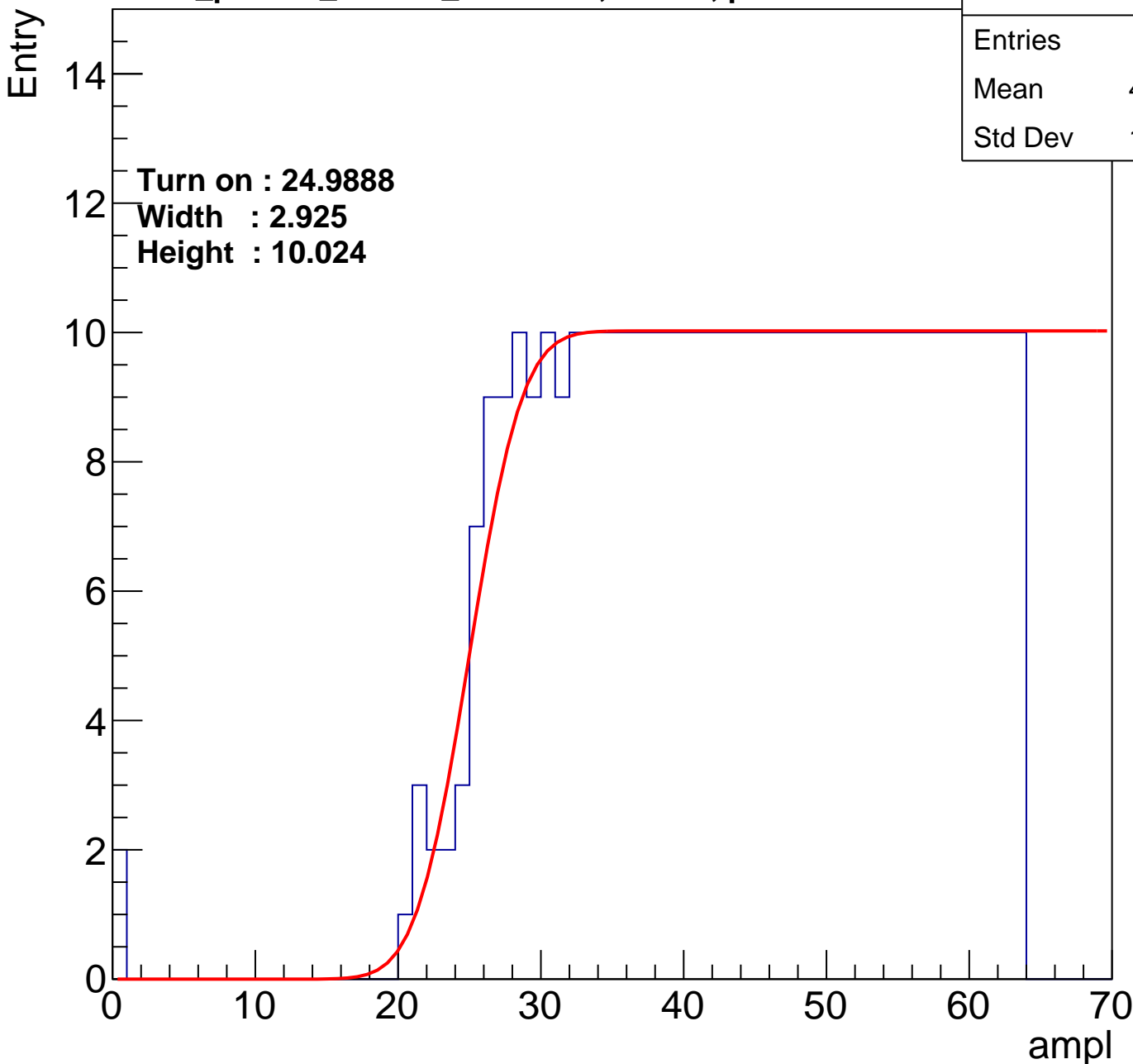
calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.48
Std Dev	11.92

Turn on : 24.9888

Width : 2.925

Height : 10.024



B0L102S, U16-ch26

calib_packv5_042523_0143.root, FC#12, port B1

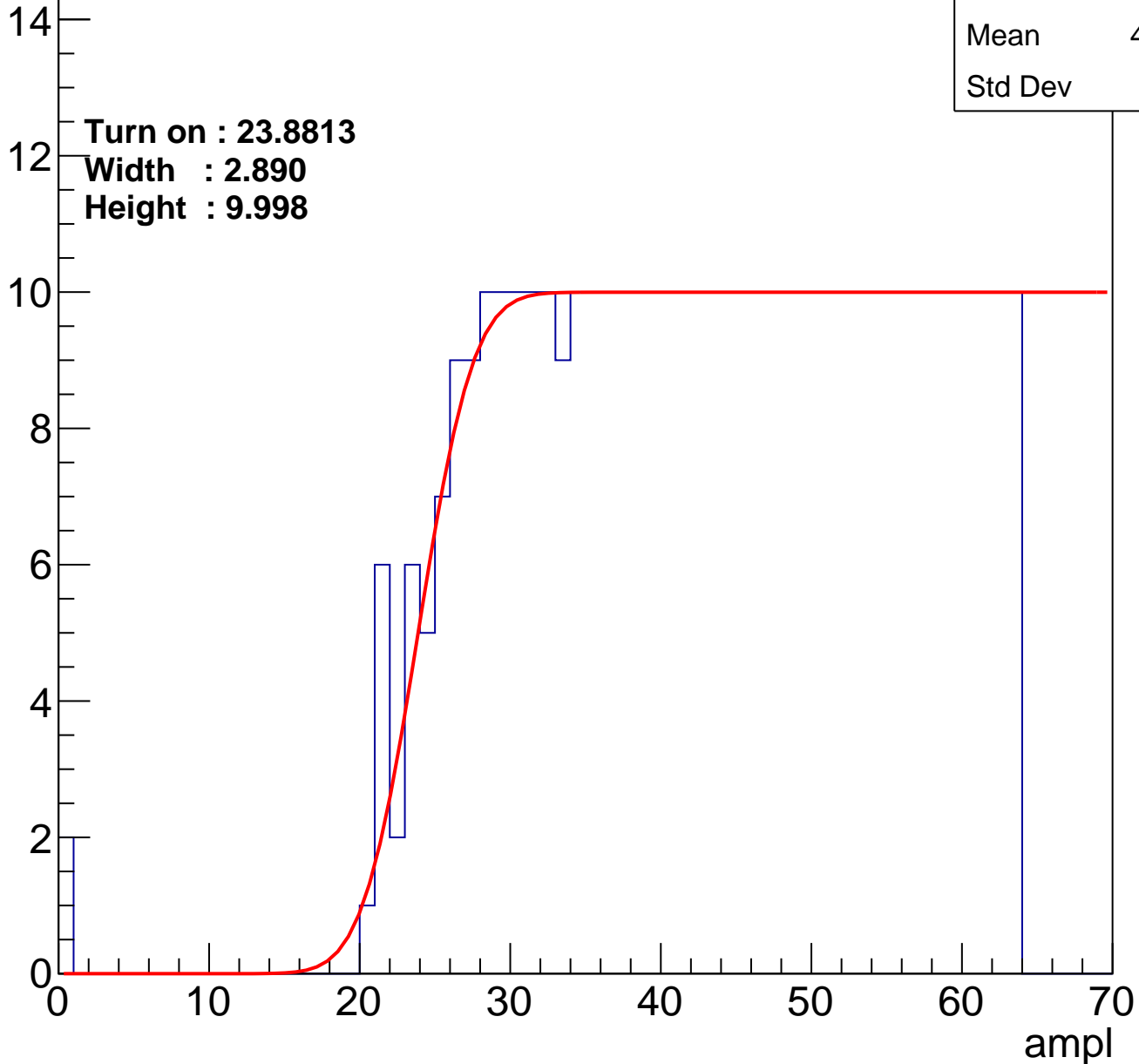
Entries	406
Mean	42.97
Std Dev	12.2

Turn on : 23.8813

Width : 2.890

Height : 9.998

Entry



B0L102S, U16-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.7336

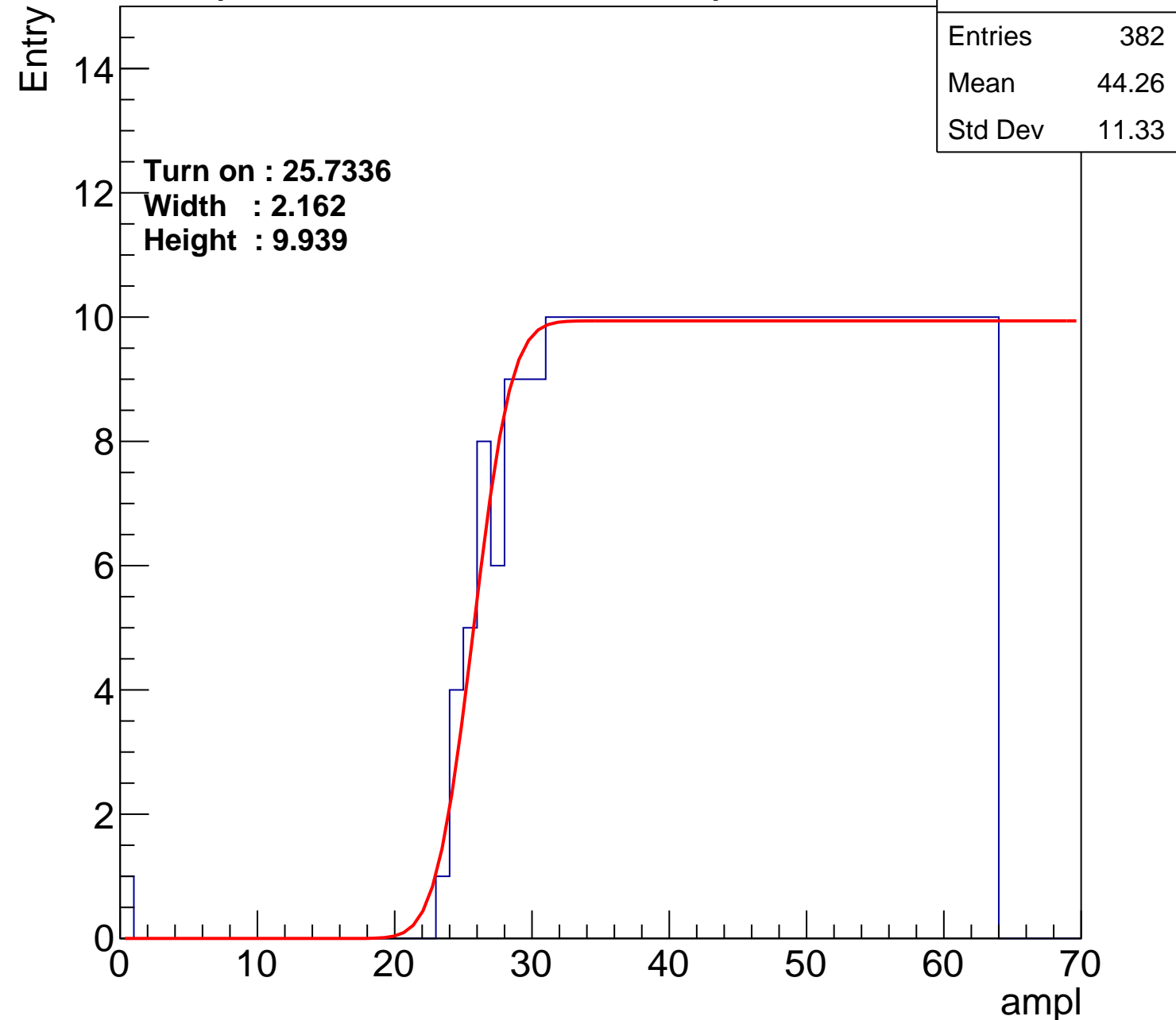
Width : 2.162

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.96
Std Dev	12.45

Turn on : 24.3861

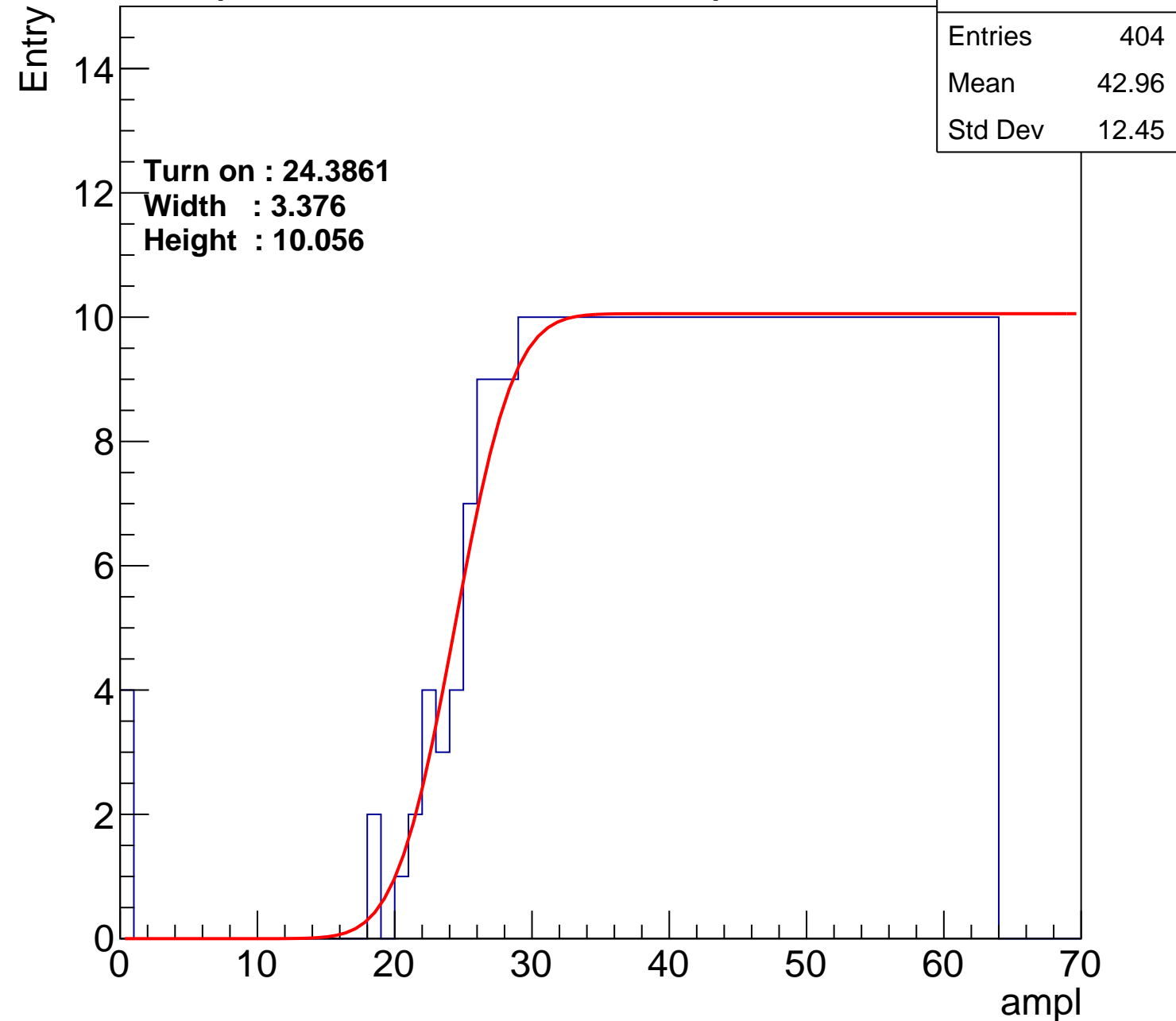
Width : 3.376

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.82
Std Dev	11.05

Turn on : 26.4822

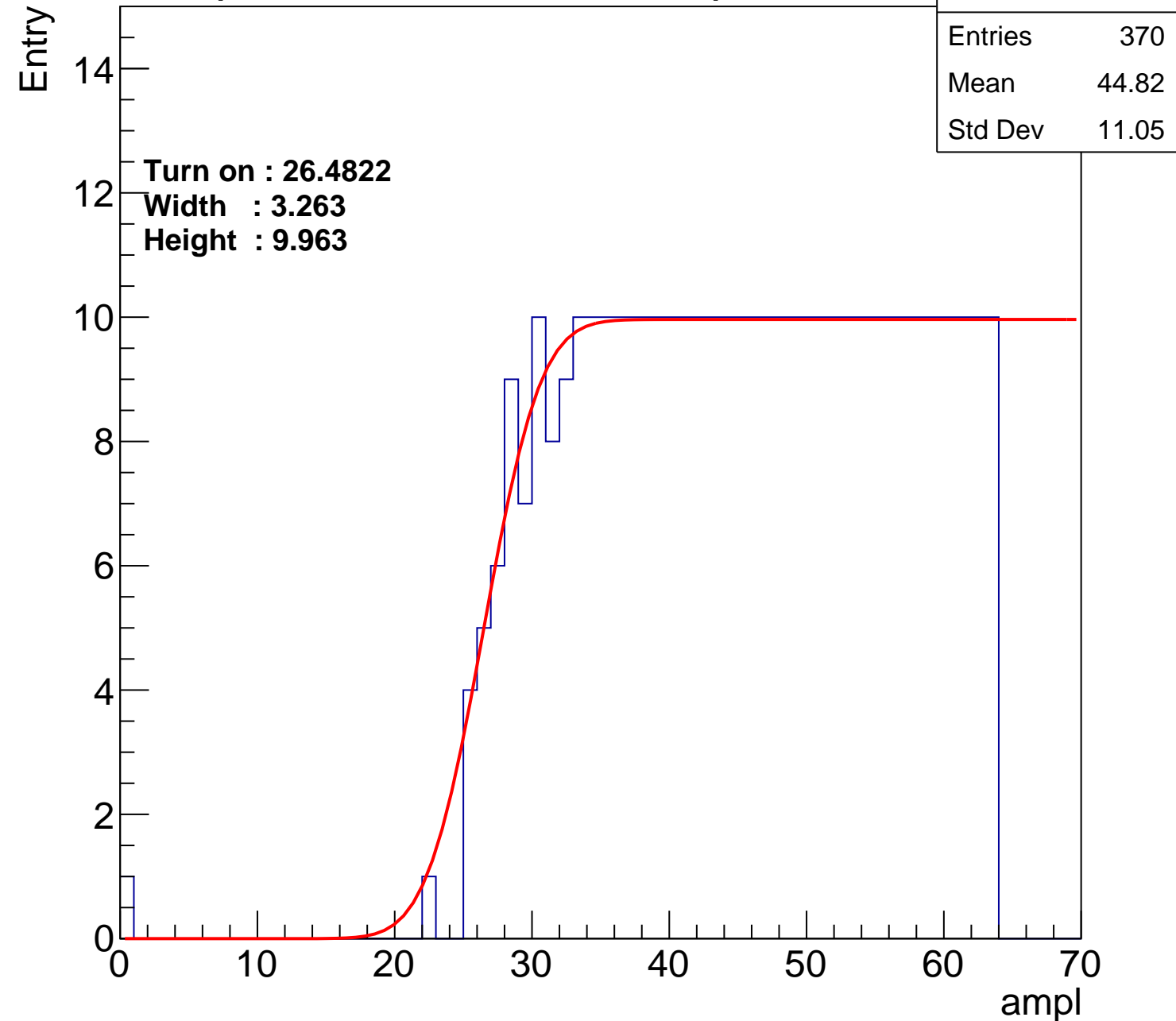
Width : 3.263

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch30

calib_packv5_042523_0143.root, FC#12, port B1

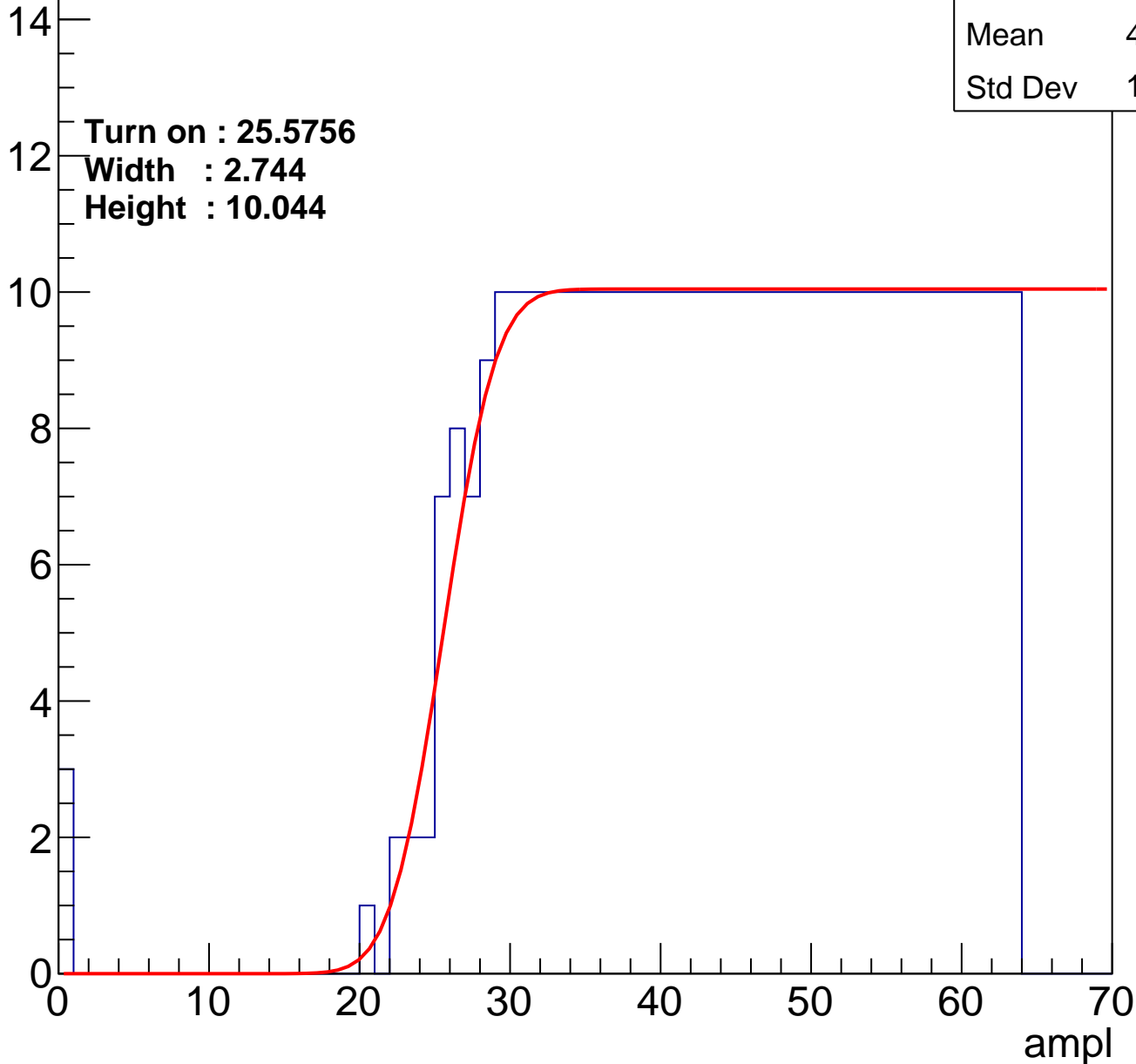
Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.5756

Width : 2.744

Height : 10.044

Entry



B0L102S, U16-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.09
Std Dev	12

Turn on : 26.4002

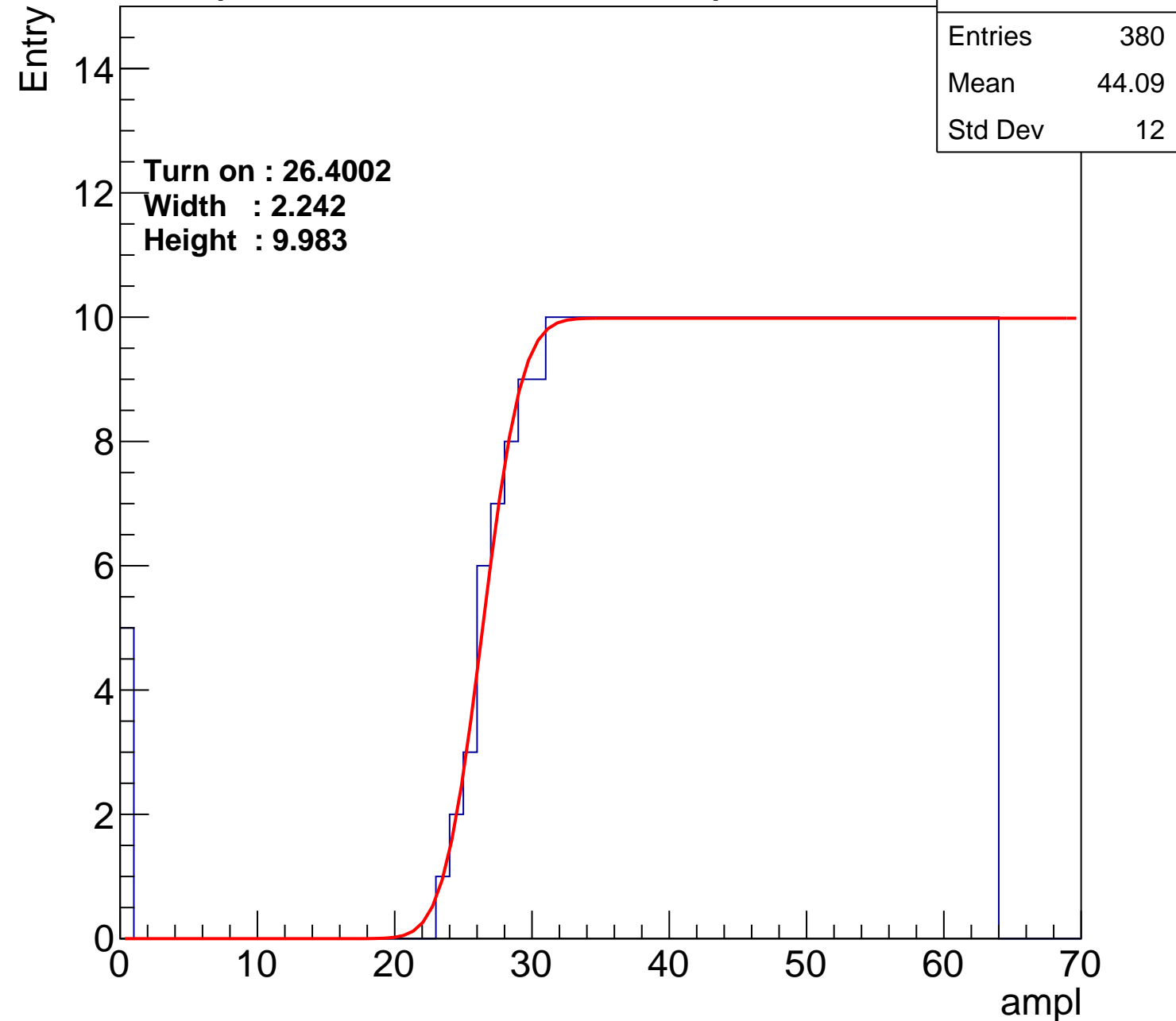
Width : 2.242

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch32

calib_packv5_042523_0143.root, FC#12, port B1

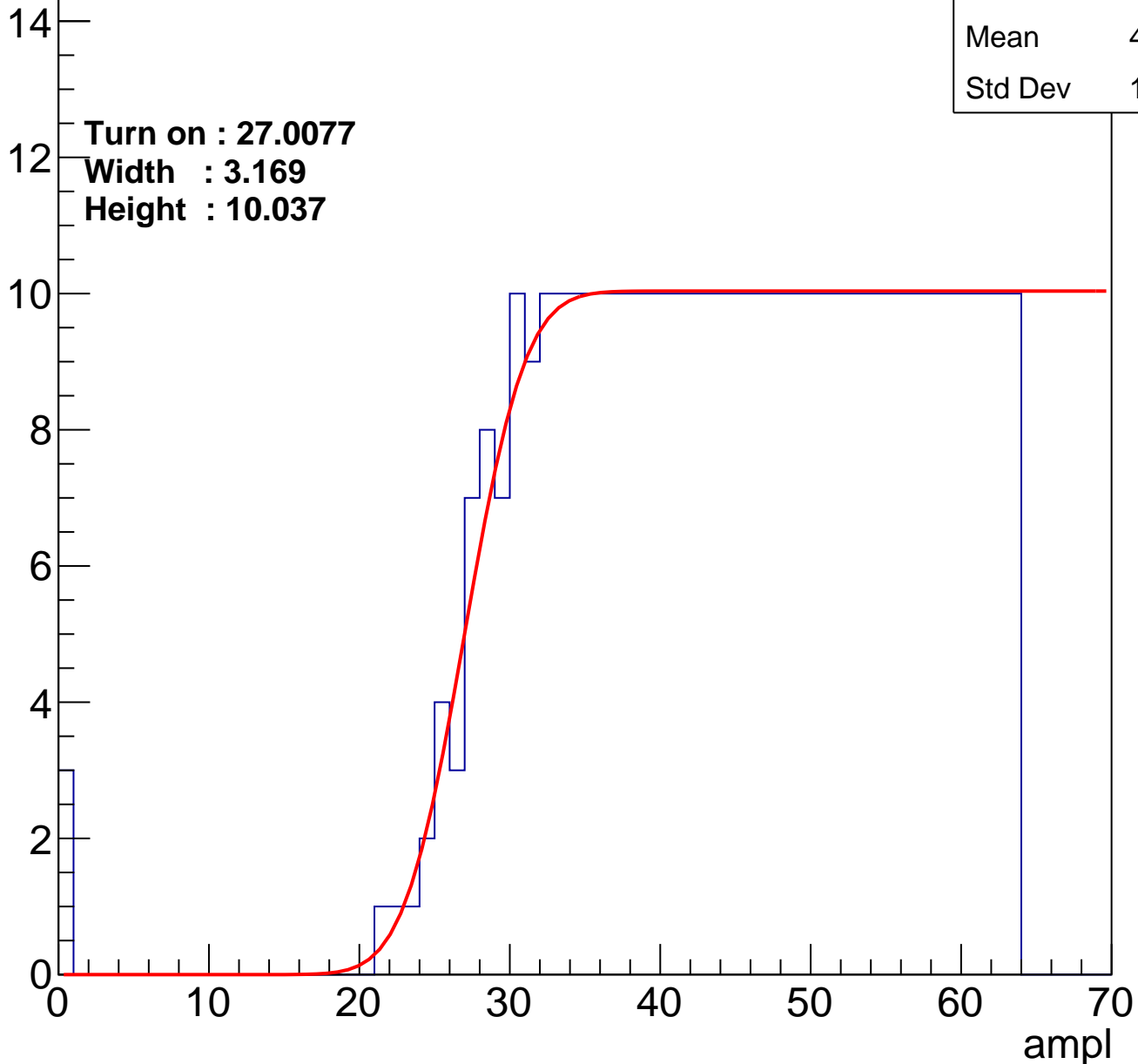
Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 27.0077

Width : 3.169

Height : 10.037

Entry



calib_packv5_042523_0143.root, FC#12, port B1

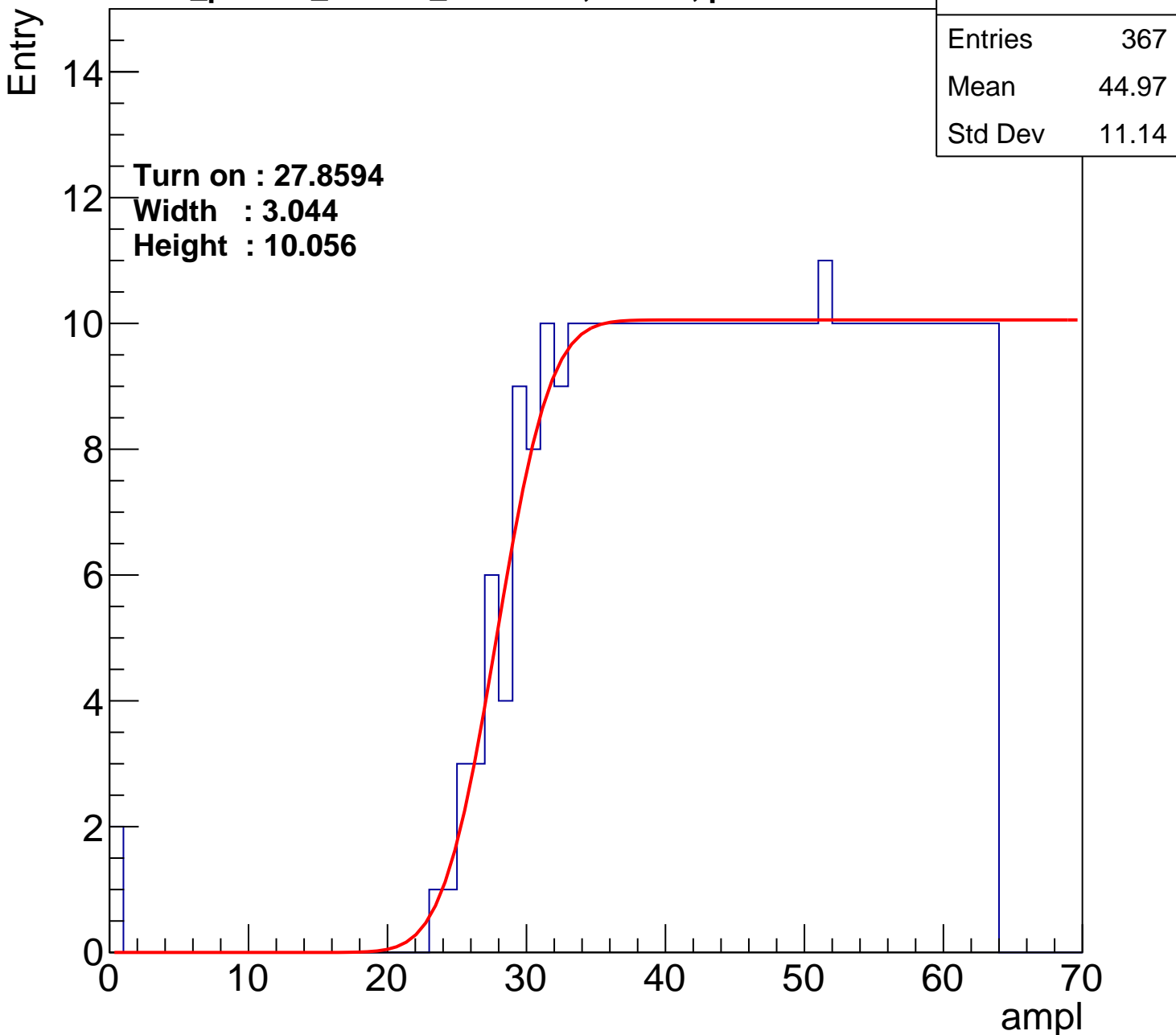
Entries	367
Mean	44.97
Std Dev	11.14

Mean	44.97
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Std Dev	11.14
---------	-------

Width : 3.044

Height : 10.056



B0L102S, U16-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.3135

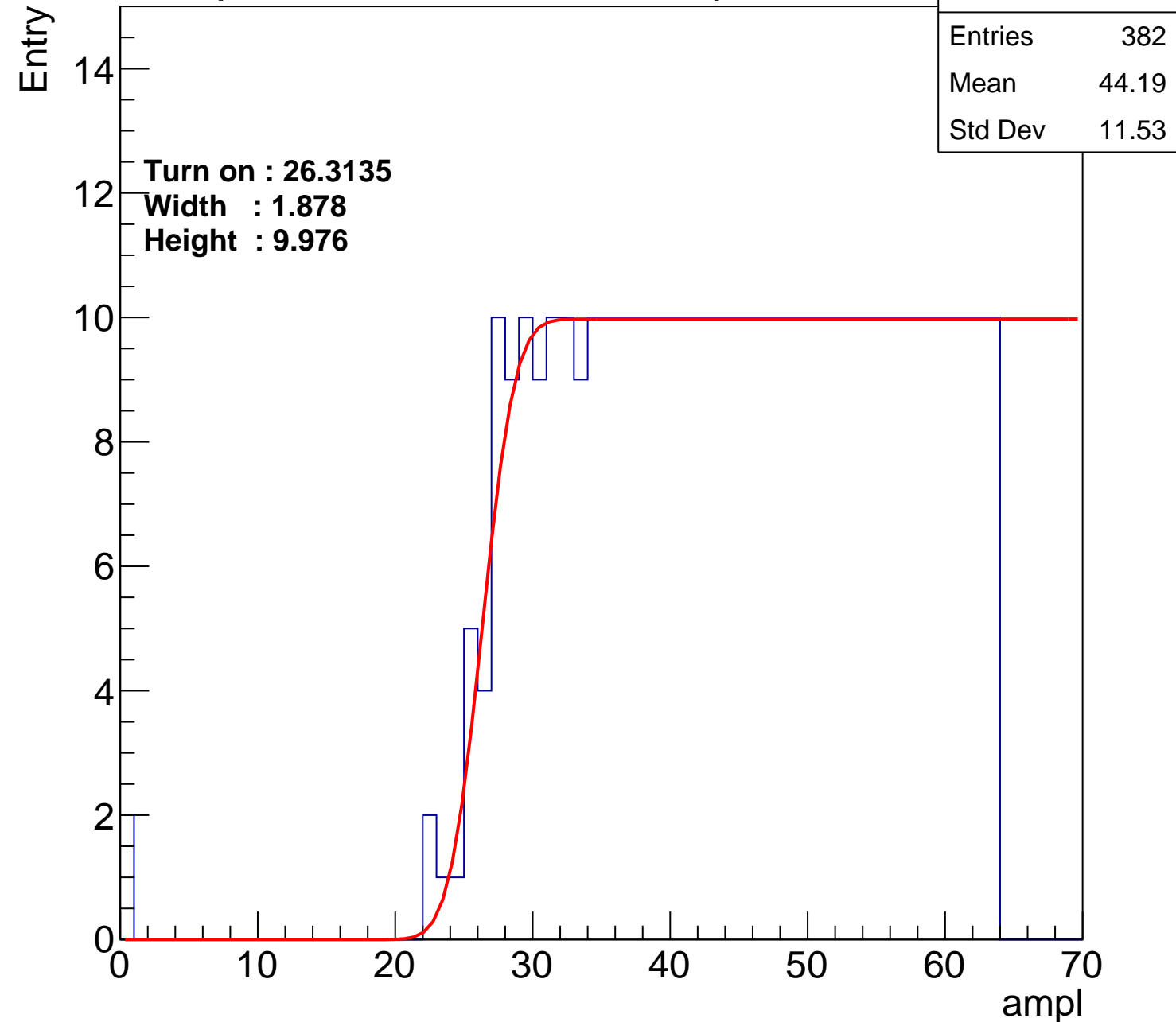
Width : 1.878

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.58
Std Dev	12.22

Turn on : 25.1080

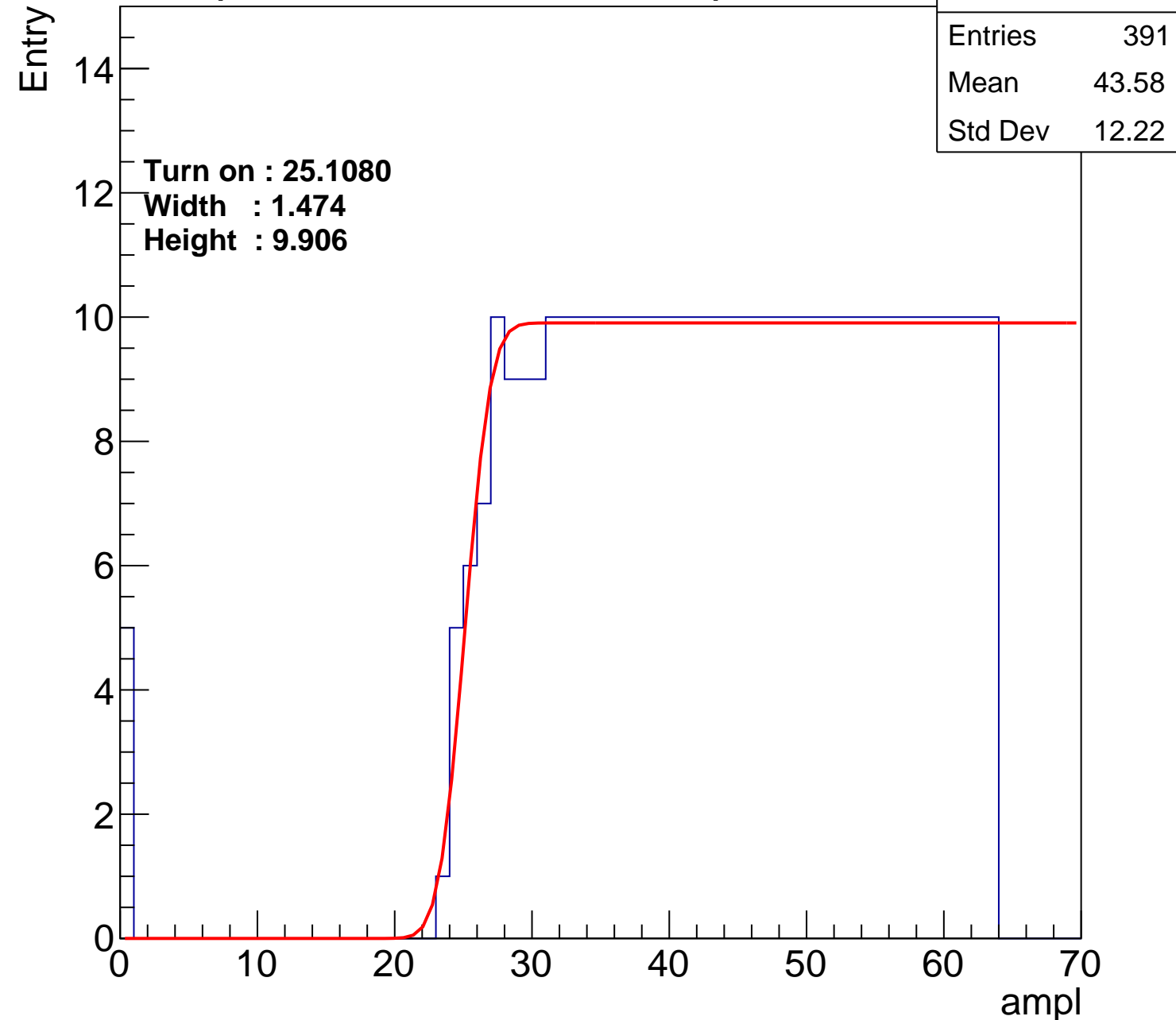
Width : 1.474

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch36

calib_packv5_042523_0143.root, FC#12, port B1

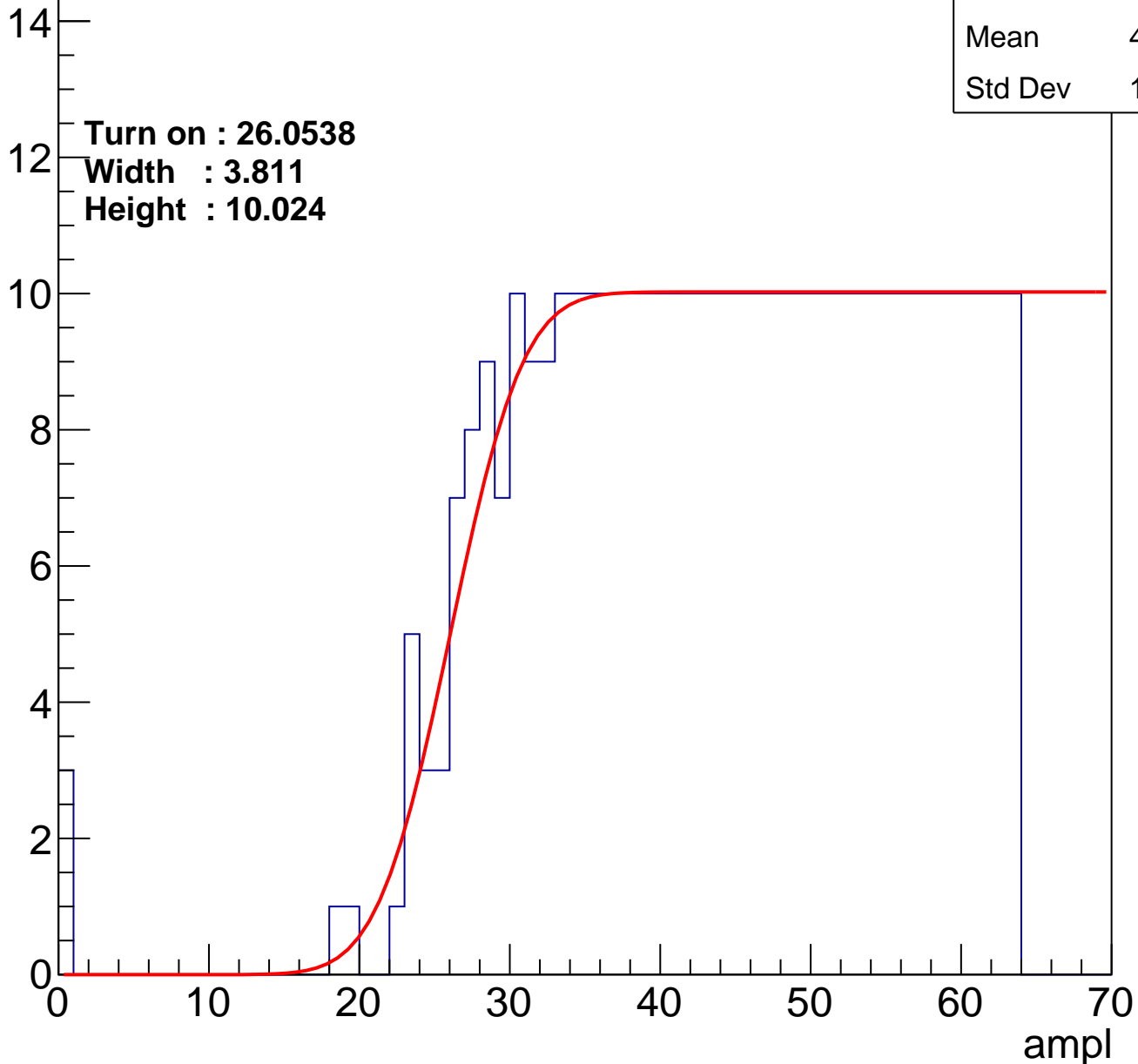
Entries	386
Mean	43.84
Std Dev	11.94

Turn on : 26.0538

Width : 3.811

Height : 10.024

Entry



B0L102S, U16-ch37

calib_packv5_042523_0143.root, FC#12, port B1

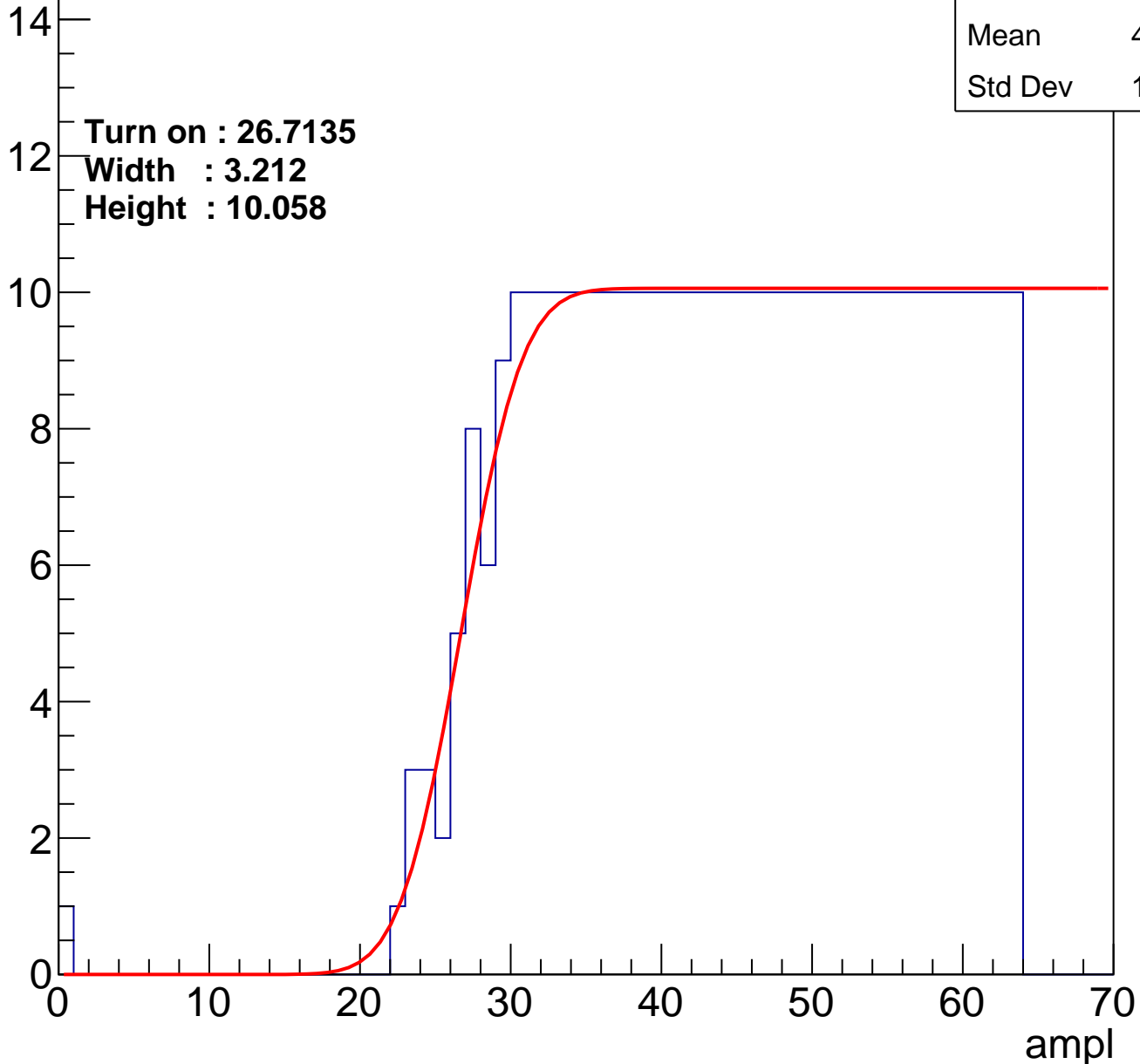
Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.7135

Width : 3.212

Height : 10.058

Entry



B0L102S, U16-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.33
Std Dev	11.79

Turn on : 26.8727

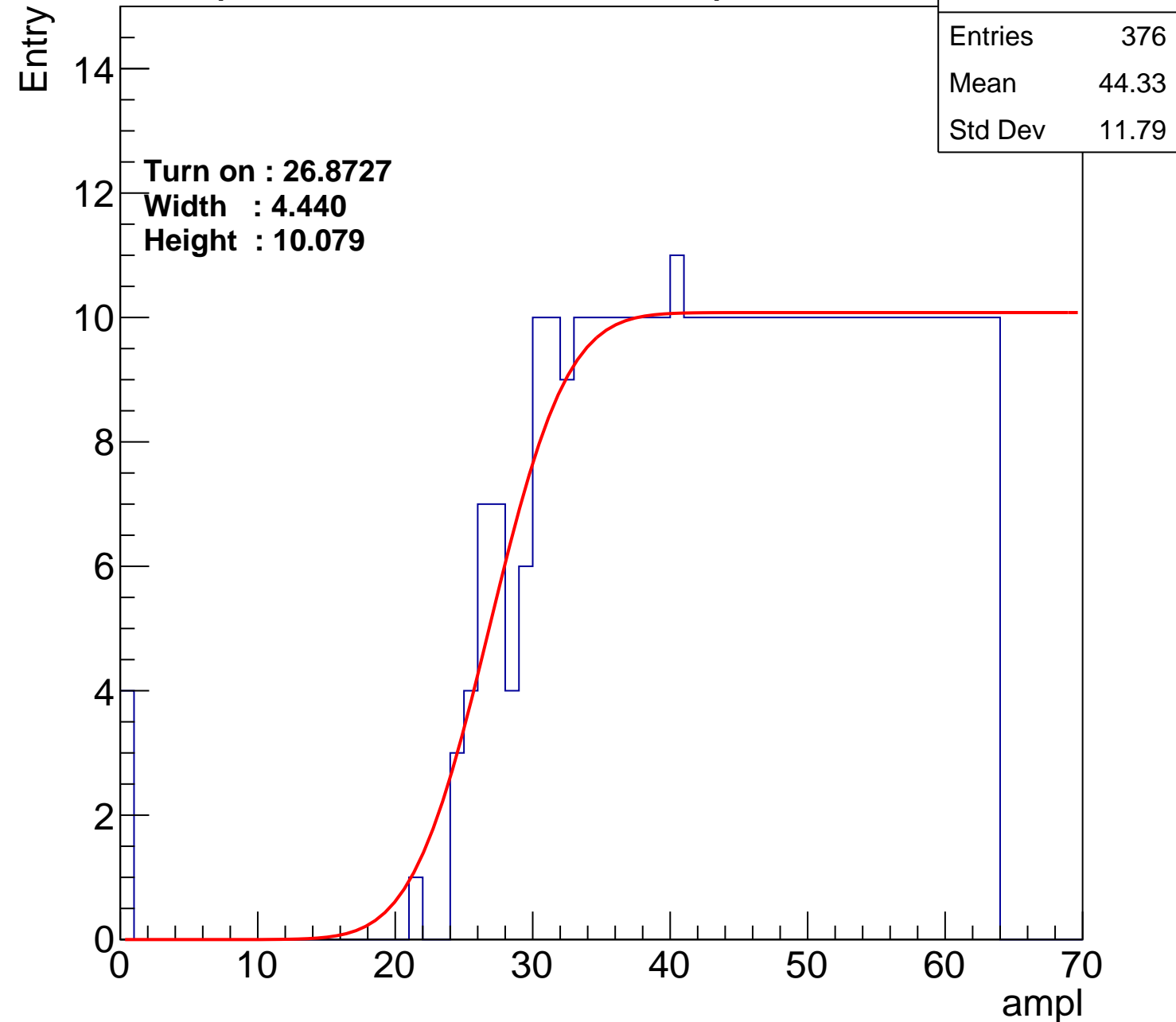
Width : 4.440

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.5781

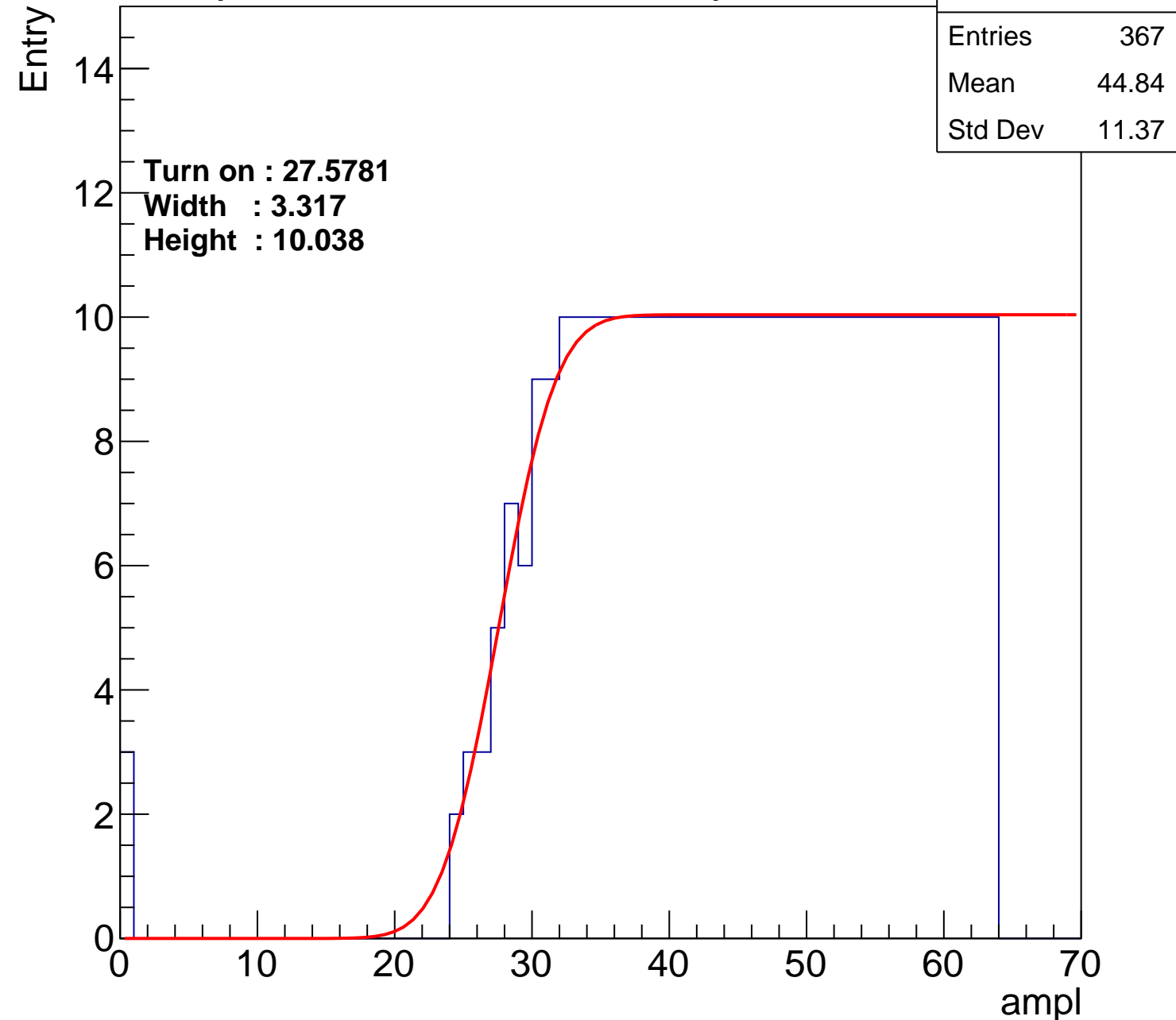
Width : 3.317

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 26.2769

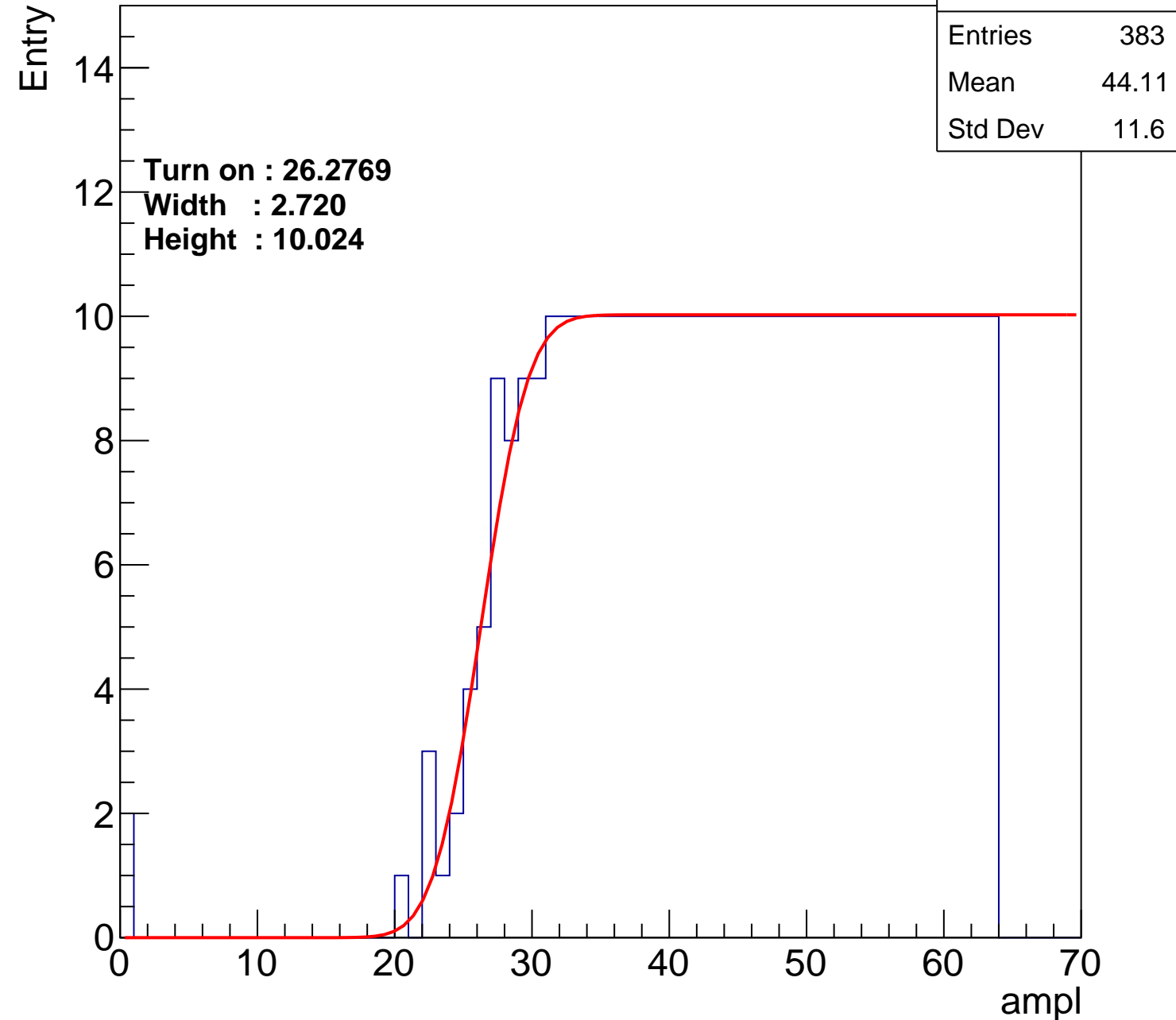
Width : 2.720

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch41

calib_packv5_042523_0143.root, FC#12, port B1

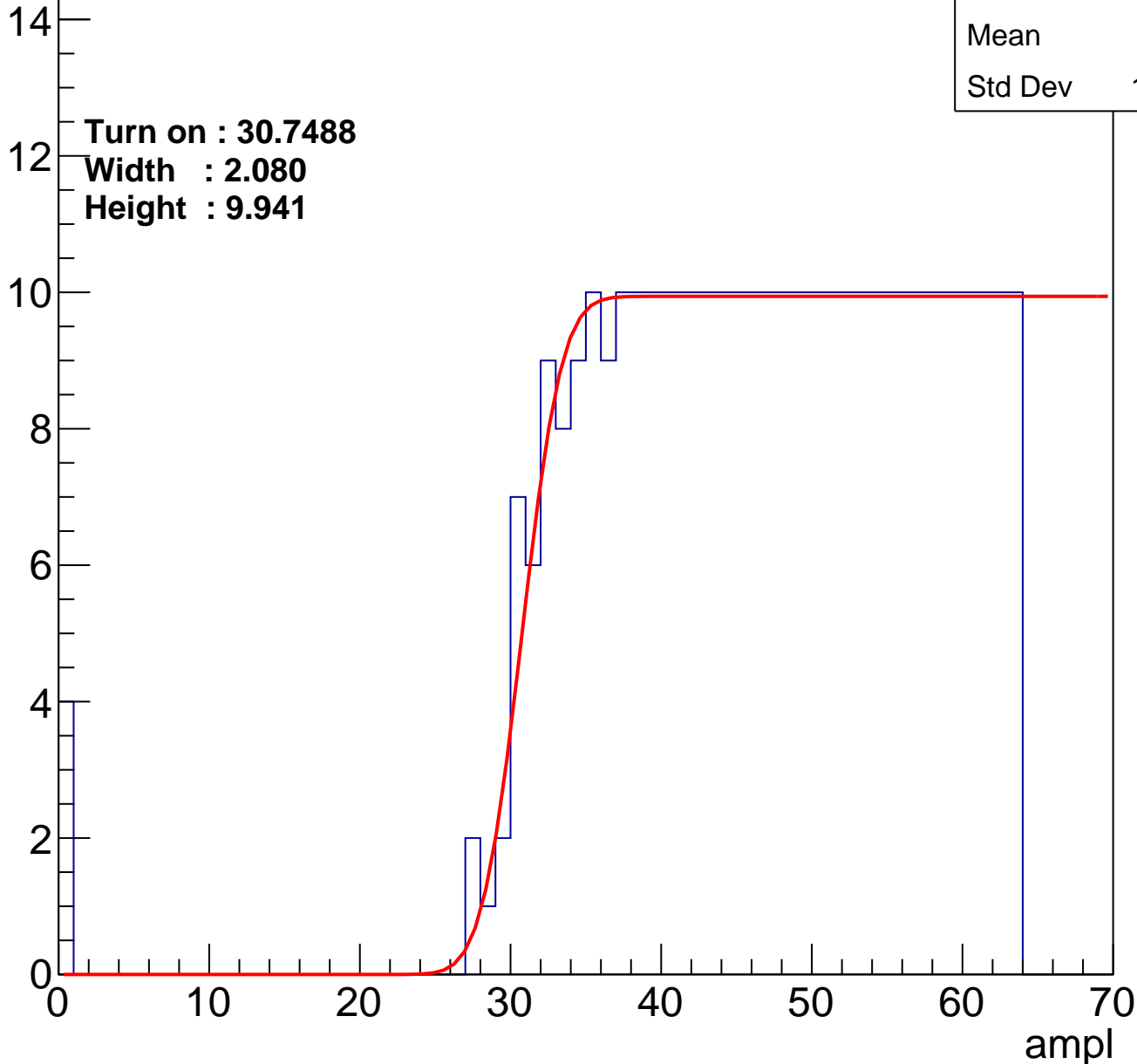
Entries	337
Mean	46.2
Std Dev	10.95

Turn on : 30.7488

Width : 2.080

Height : 9.941

Entry



B0L102S, U16-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.68
Std Dev	12.06

Turn on : 25.5185

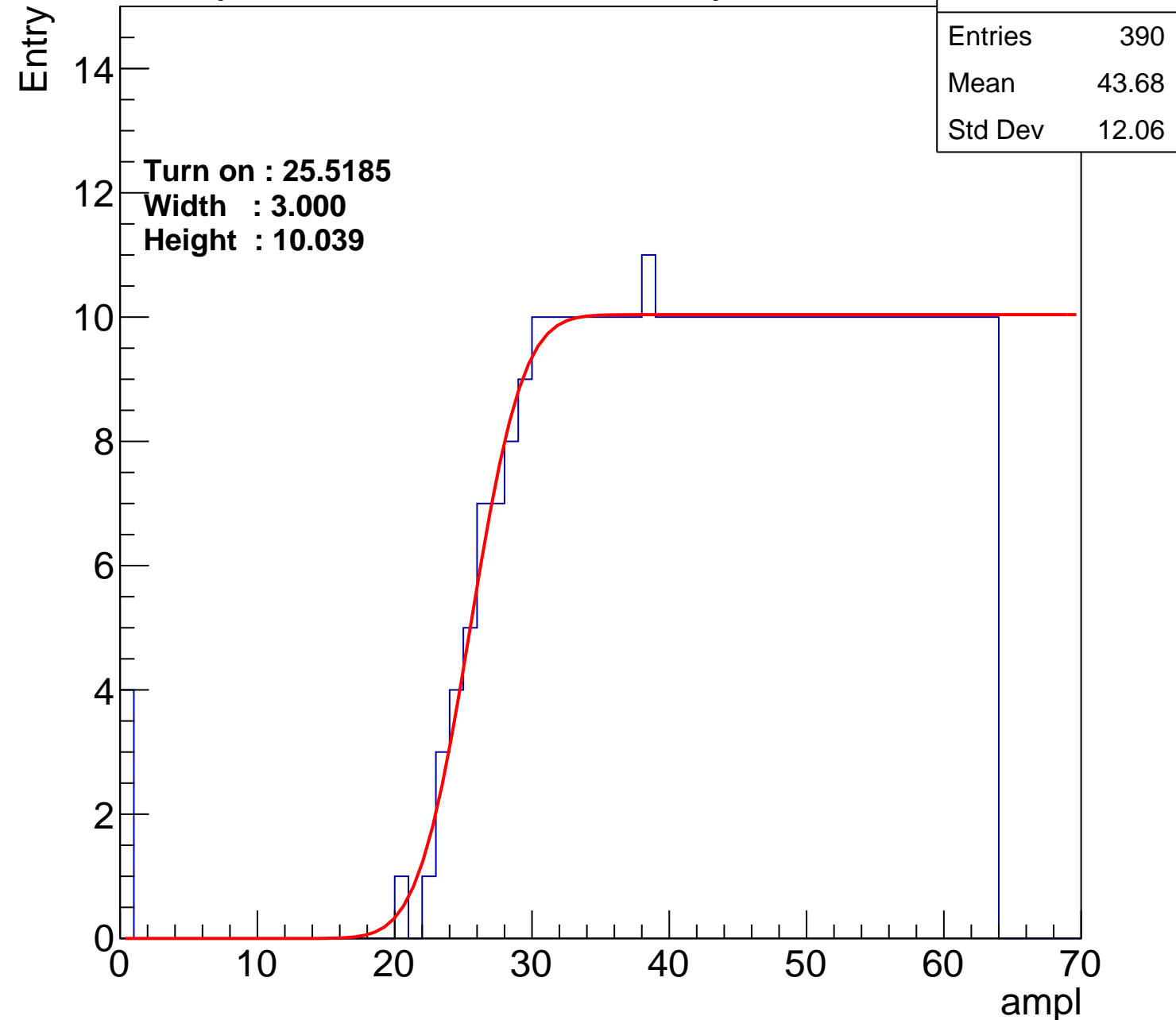
Width : 3.000

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.03
Std Dev	12.1

Turn on : 26.4758

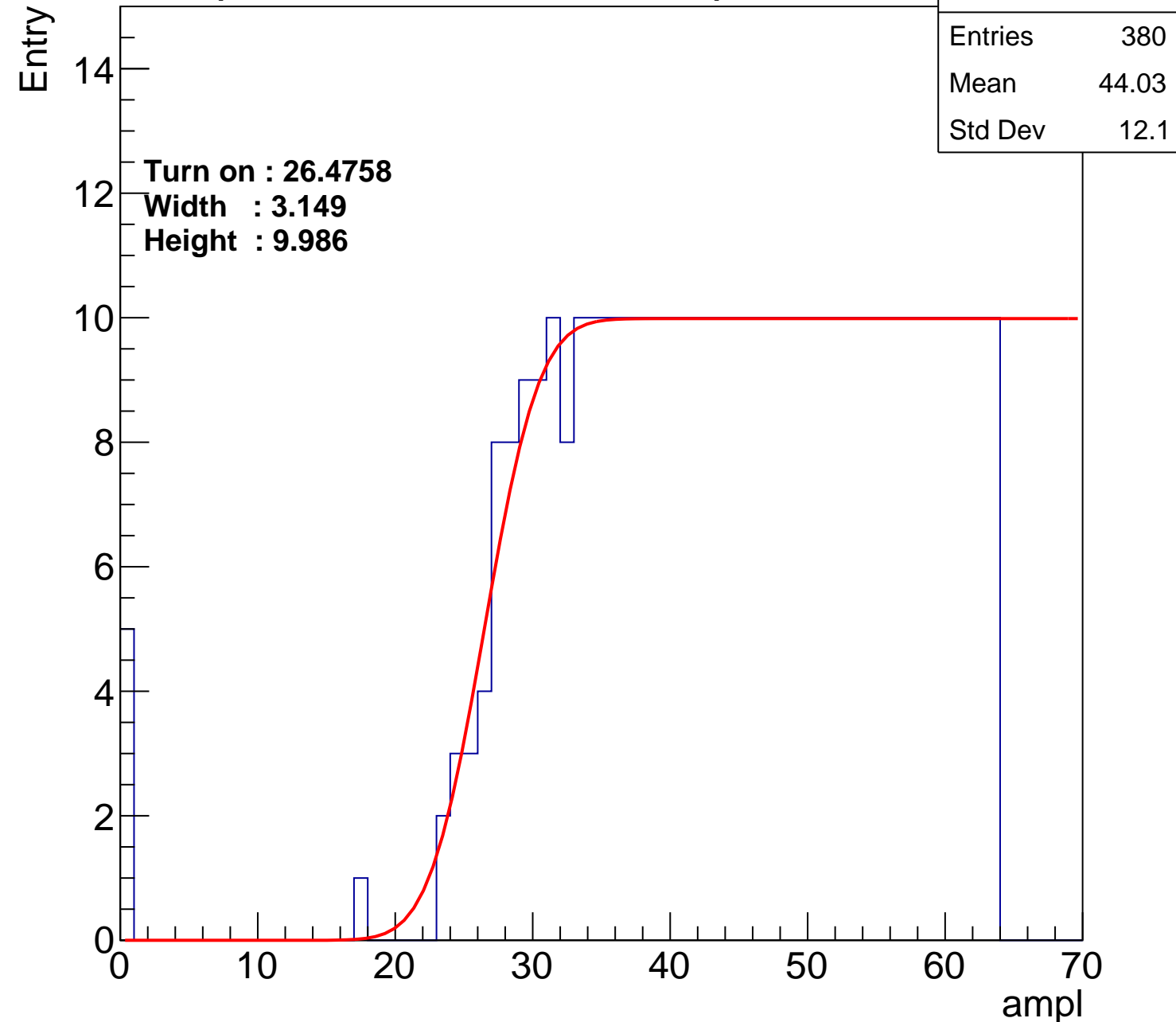
Width : 3.149

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.6051

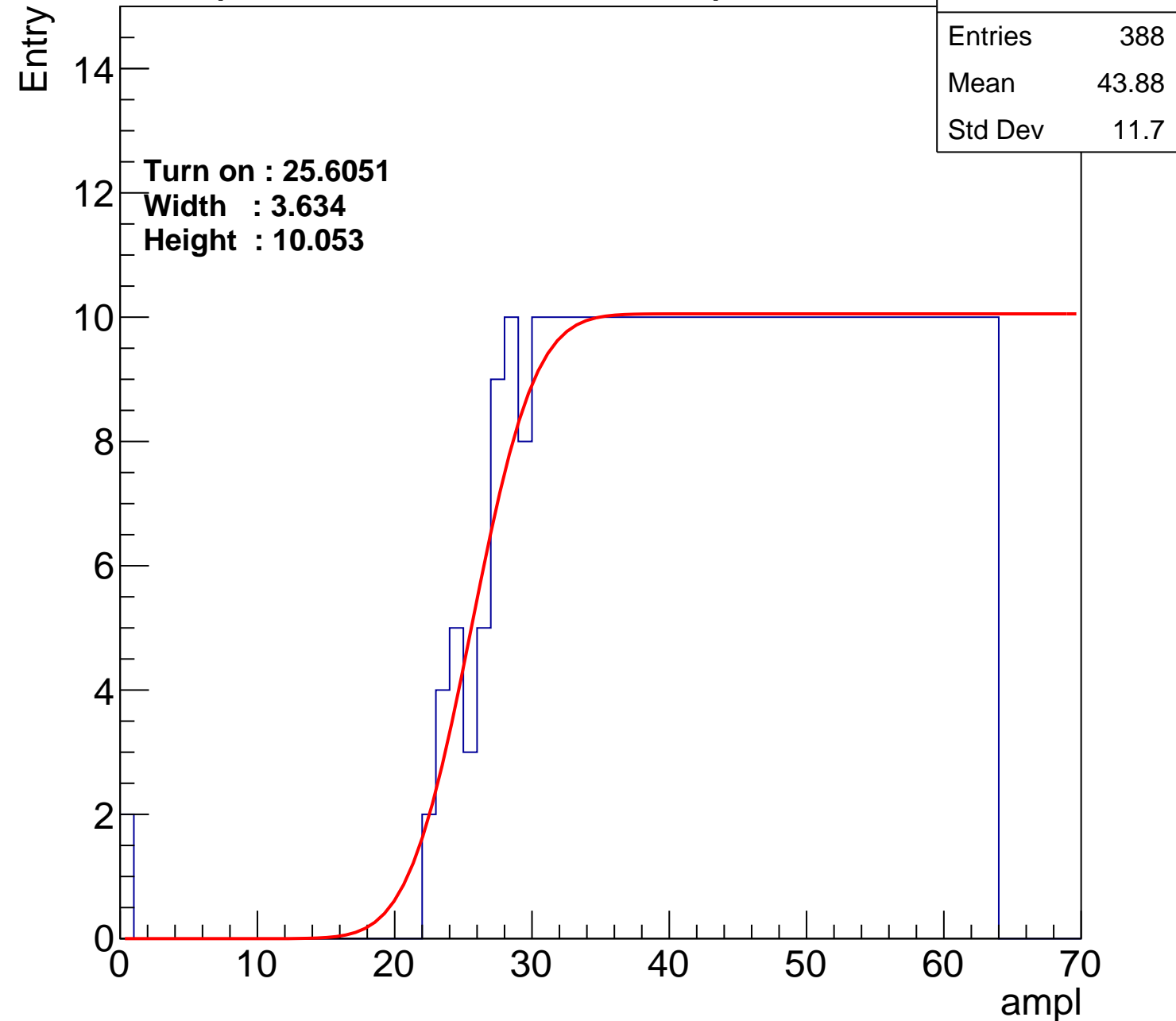
Width : 3.634

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.42
Std Dev	11.6

Turn on : 26.8694

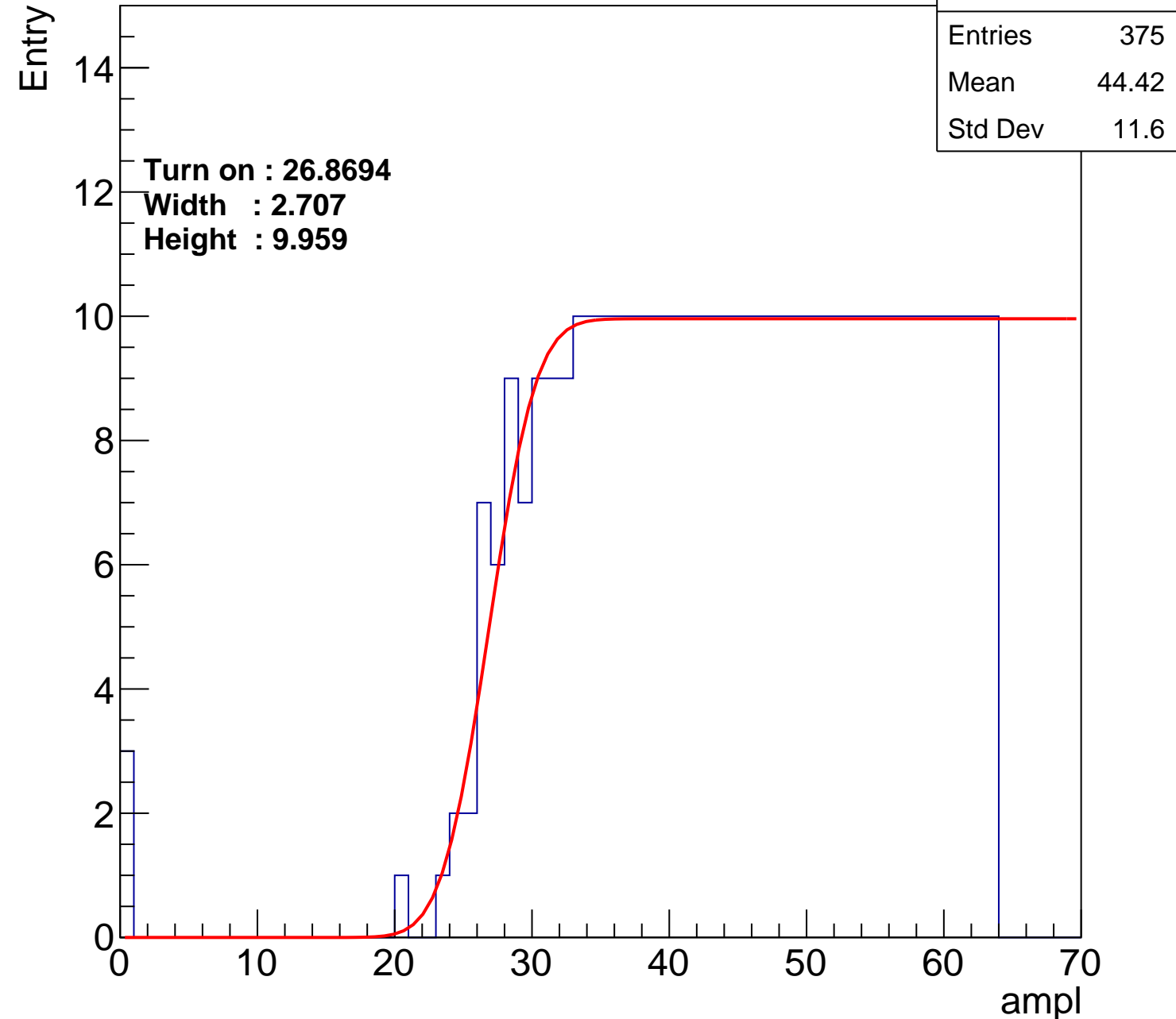
Width : 2.707

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.77
Std Dev	11.47

Turn on : 27.5551

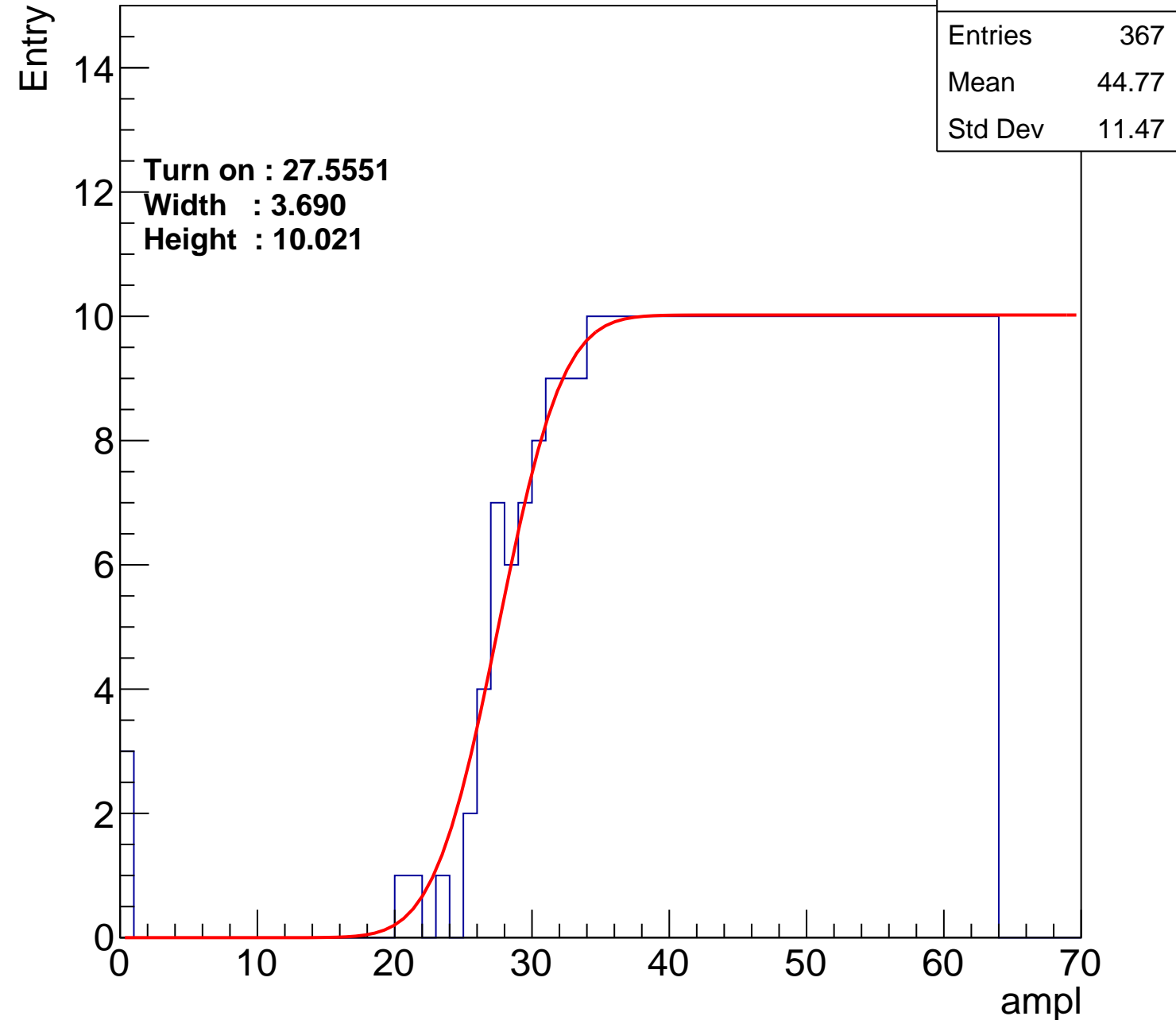
Width : 3.690

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch47

calib_packv5_042523_0143.root, FC#12, port B1

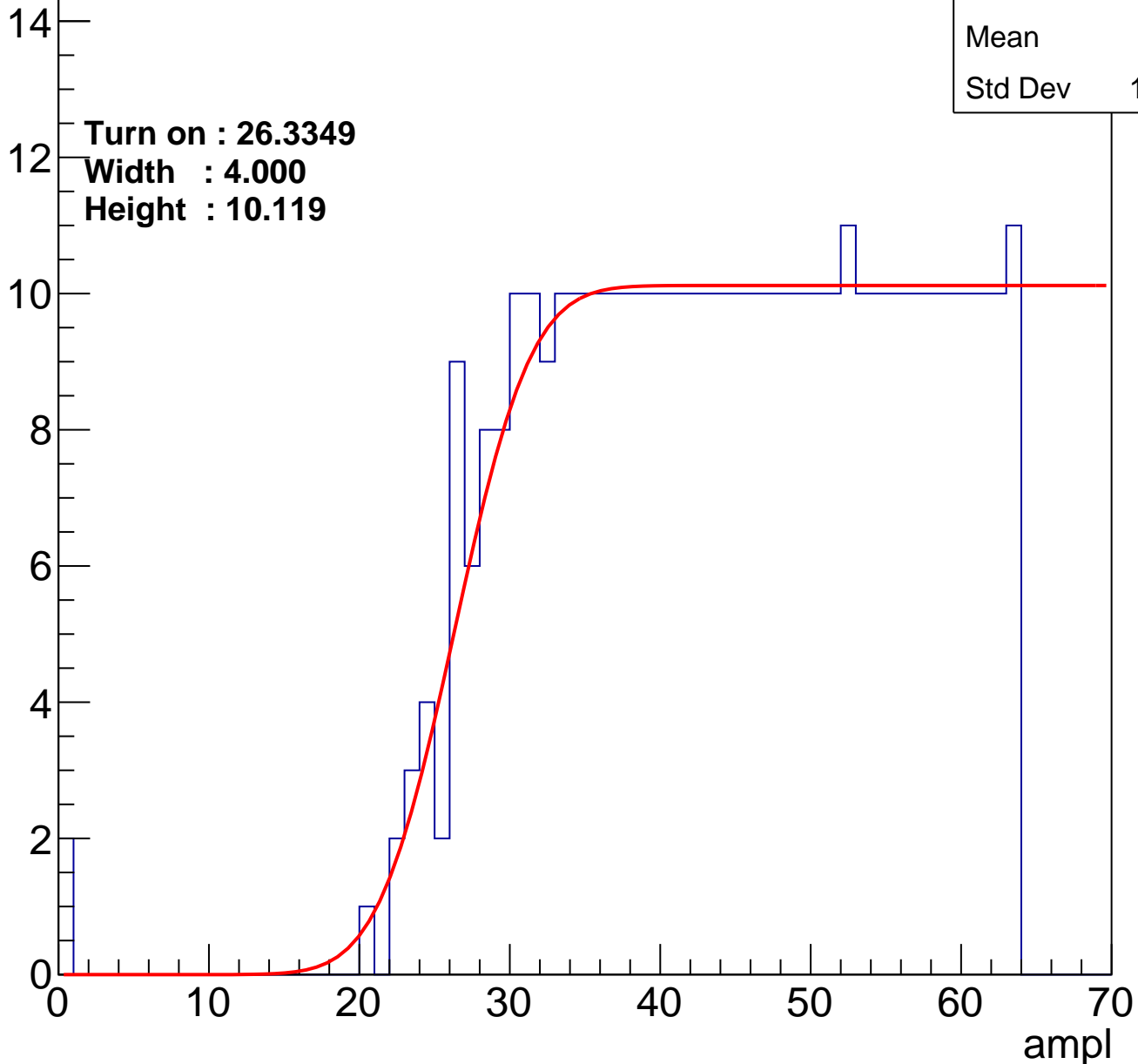
Entries	386
Mean	44.1
Std Dev	11.68

Turn on : 26.3349

Width : 4.000

Height : 10.119

Entry



B0L102S, U16-ch48

calib_packv5_042523_0143.root, FC#12, port B1

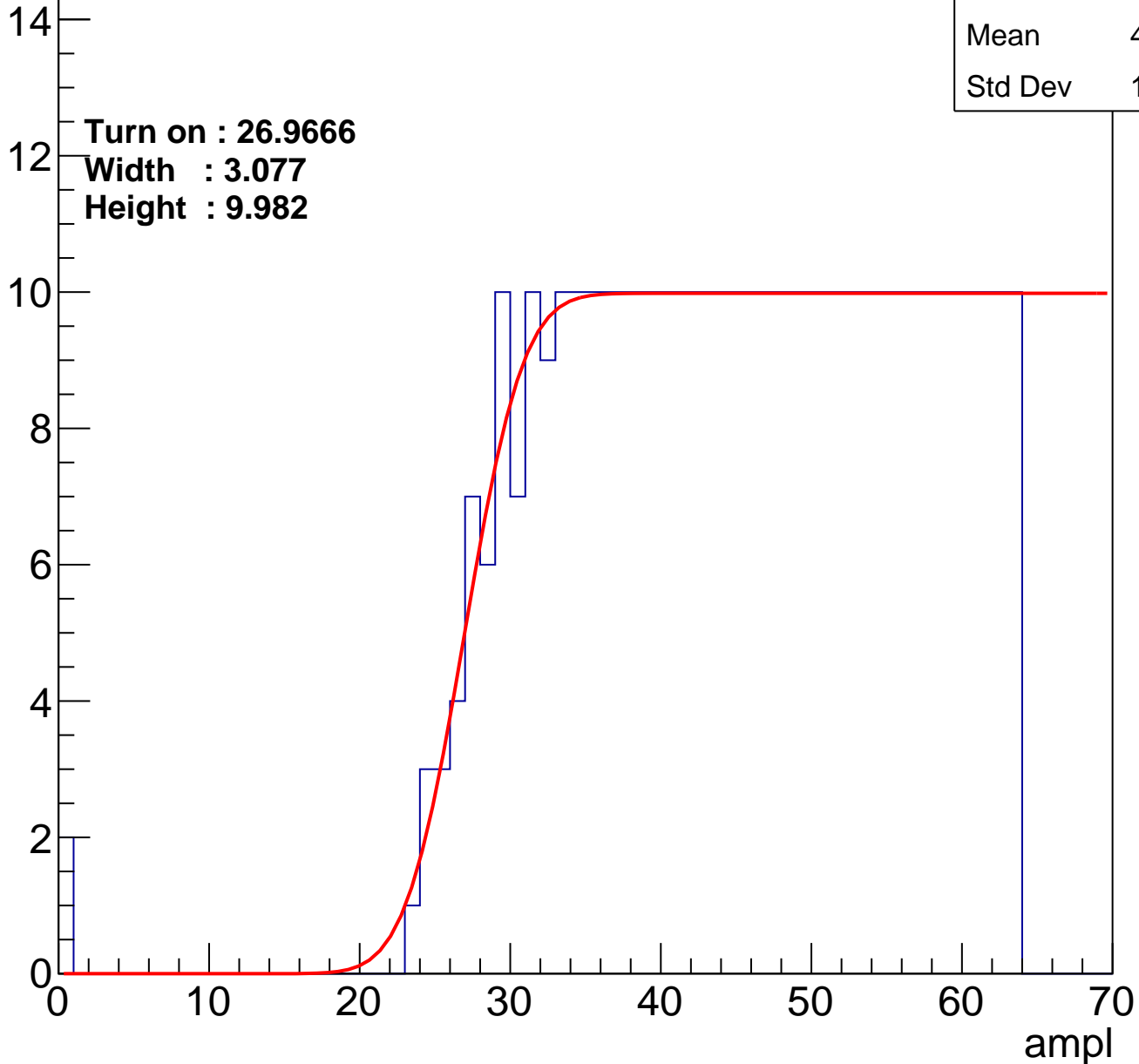
Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 26.9666

Width : 3.077

Height : 9.982

Entry



B0L102S, U16-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.11
Std Dev	10.87

Turn on : 27.3983

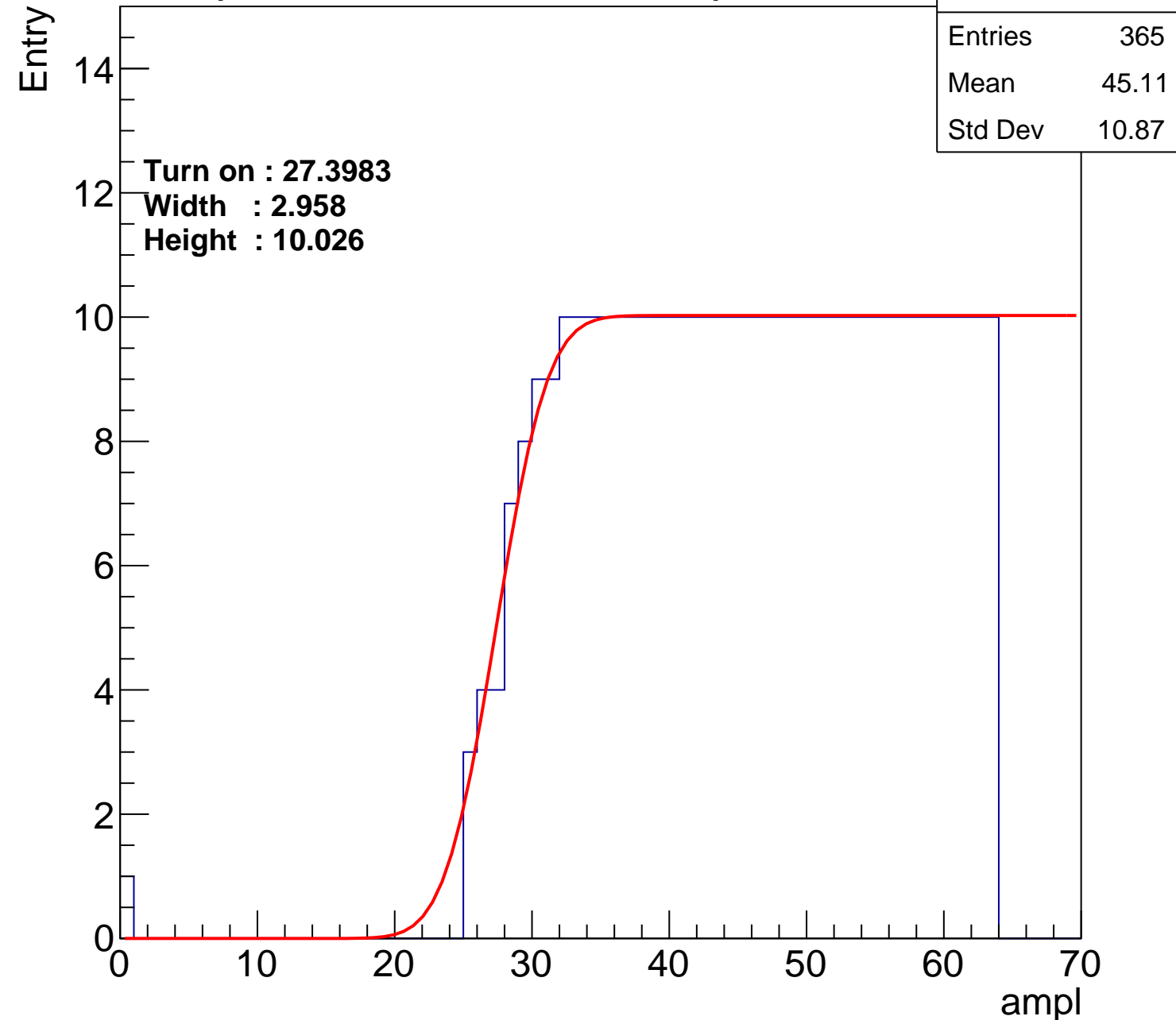
Width : 2.958

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.57
Std Dev	12.05

Turn on : 24.9260

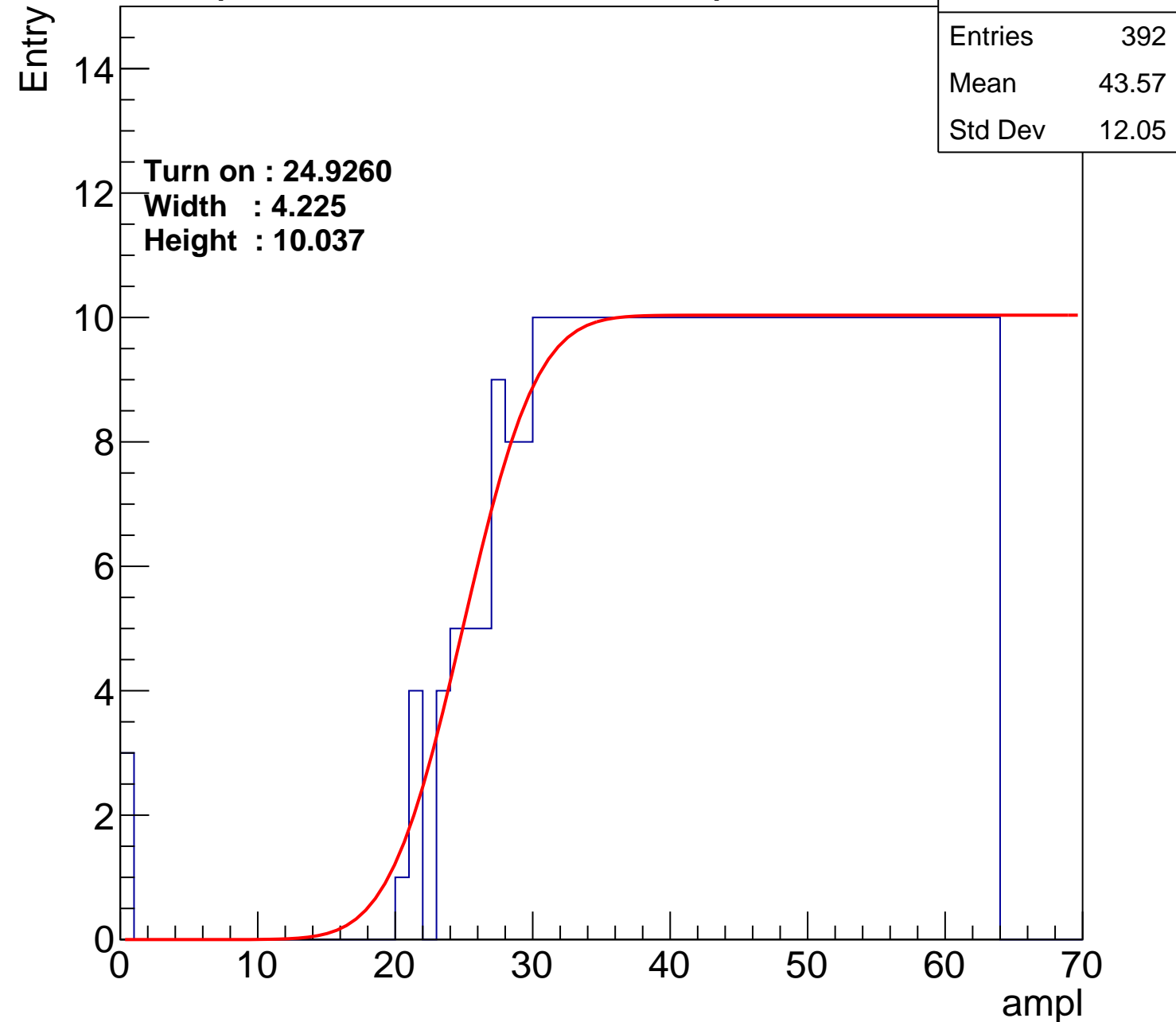
Width : 4.225

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.77
Std Dev	12.36

Turn on : 26.4325

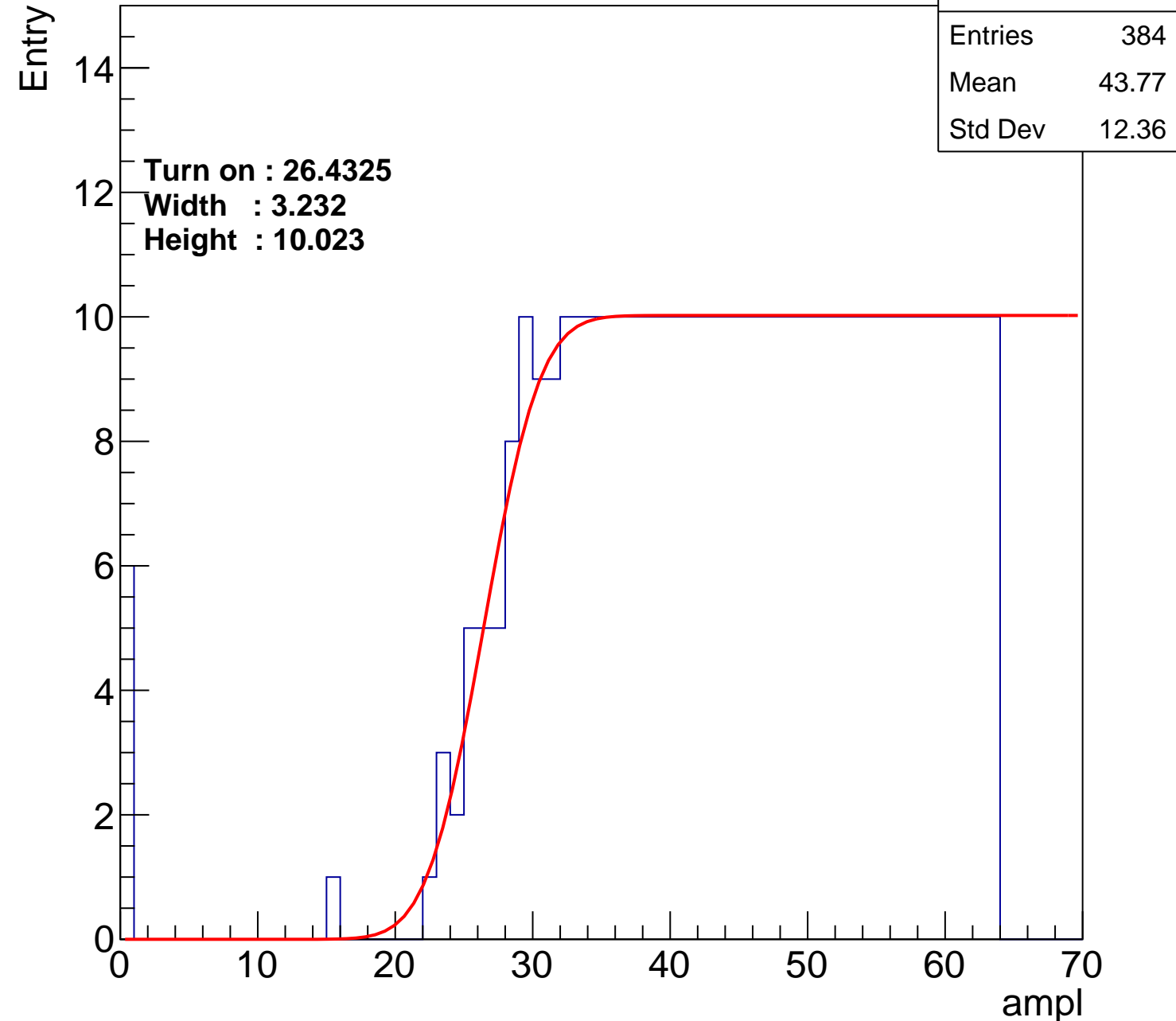
Width : 3.232

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch52

calib_packv5_042523_0143.root, FC#12, port B1

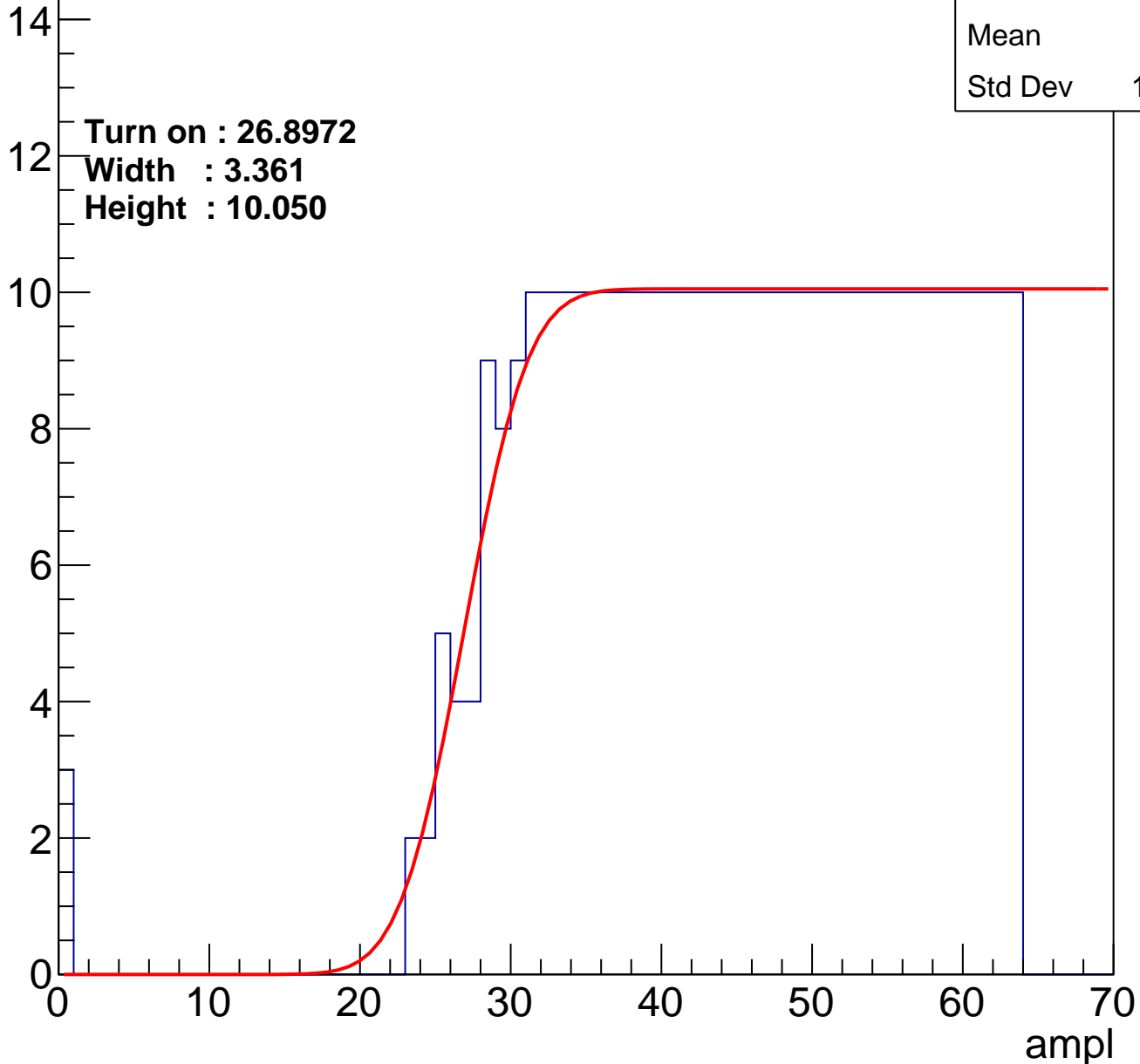
Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.8972

Width : 3.361

Height : 10.050

Entry



B0L102S, U16-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.57
Std Dev	12.35

Turn on : 24.5786

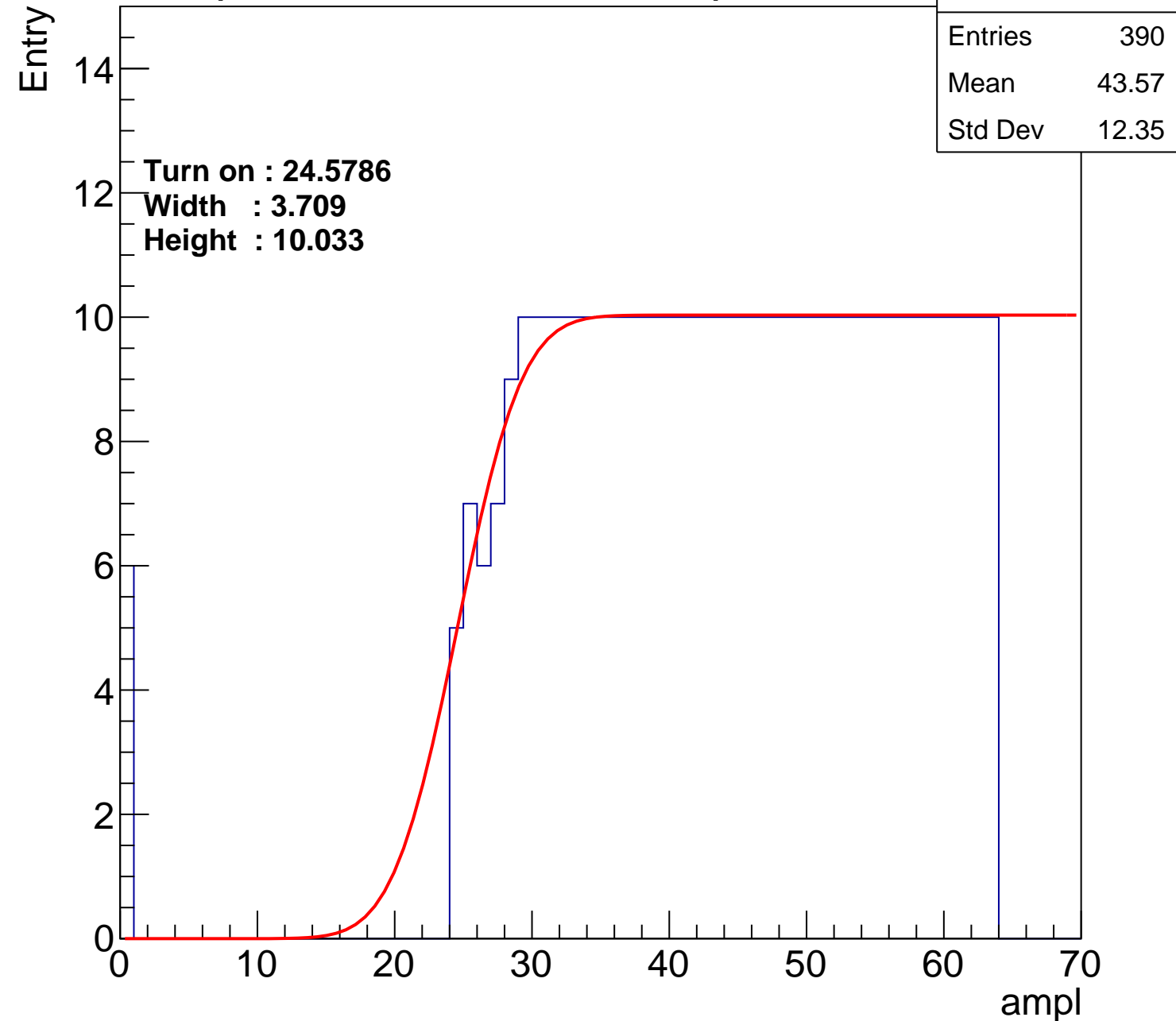
Width : 3.709

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch54

calib_packv5_042523_0143.root, FC#12, port B1

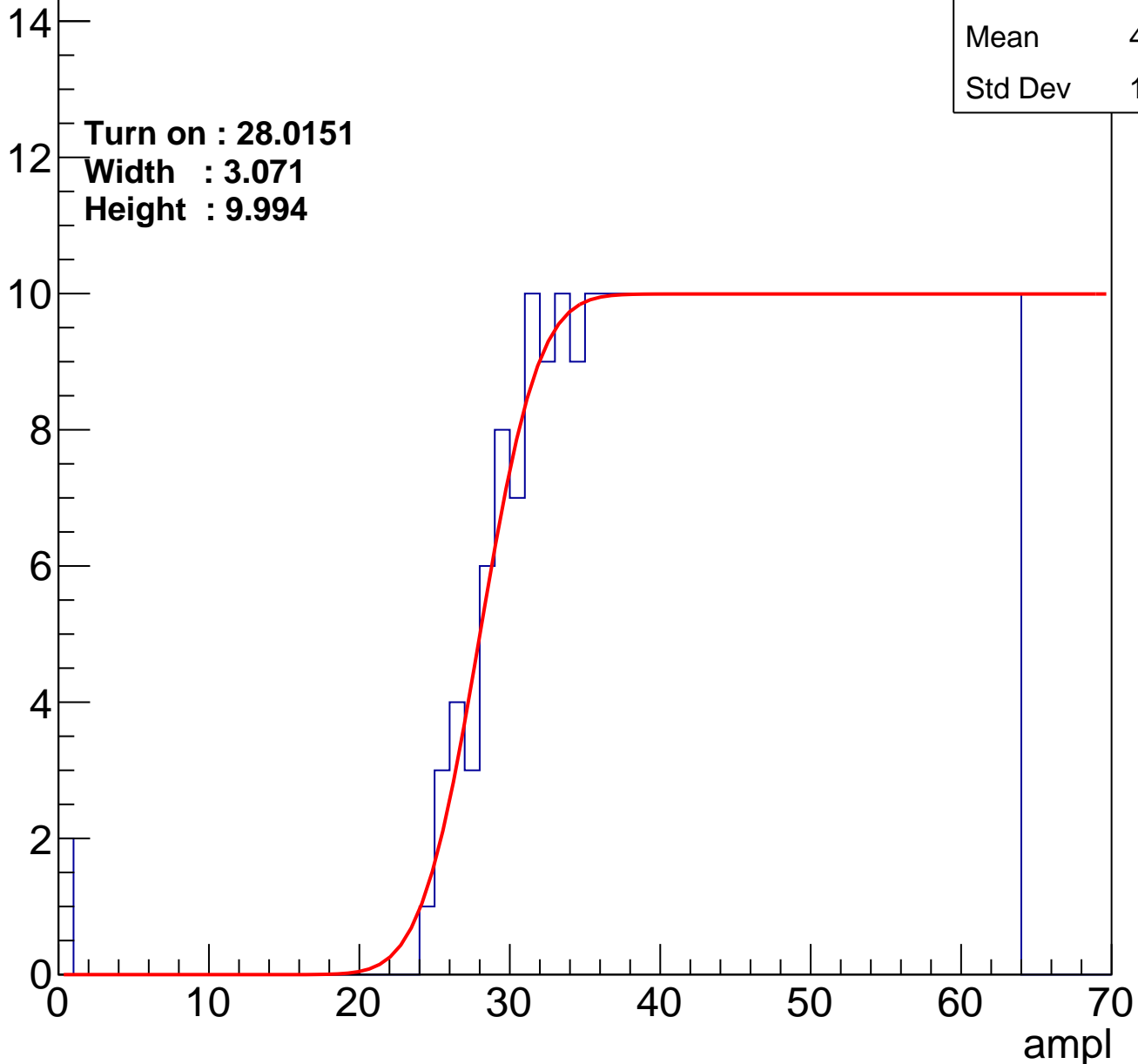
Entries	362
Mean	45.13
Std Dev	11.08

Turn on : 28.0151

Width : 3.071

Height : 9.994

Entry



B0L102S, U16-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.65
Std Dev	11.33

Turn on : 27.3368

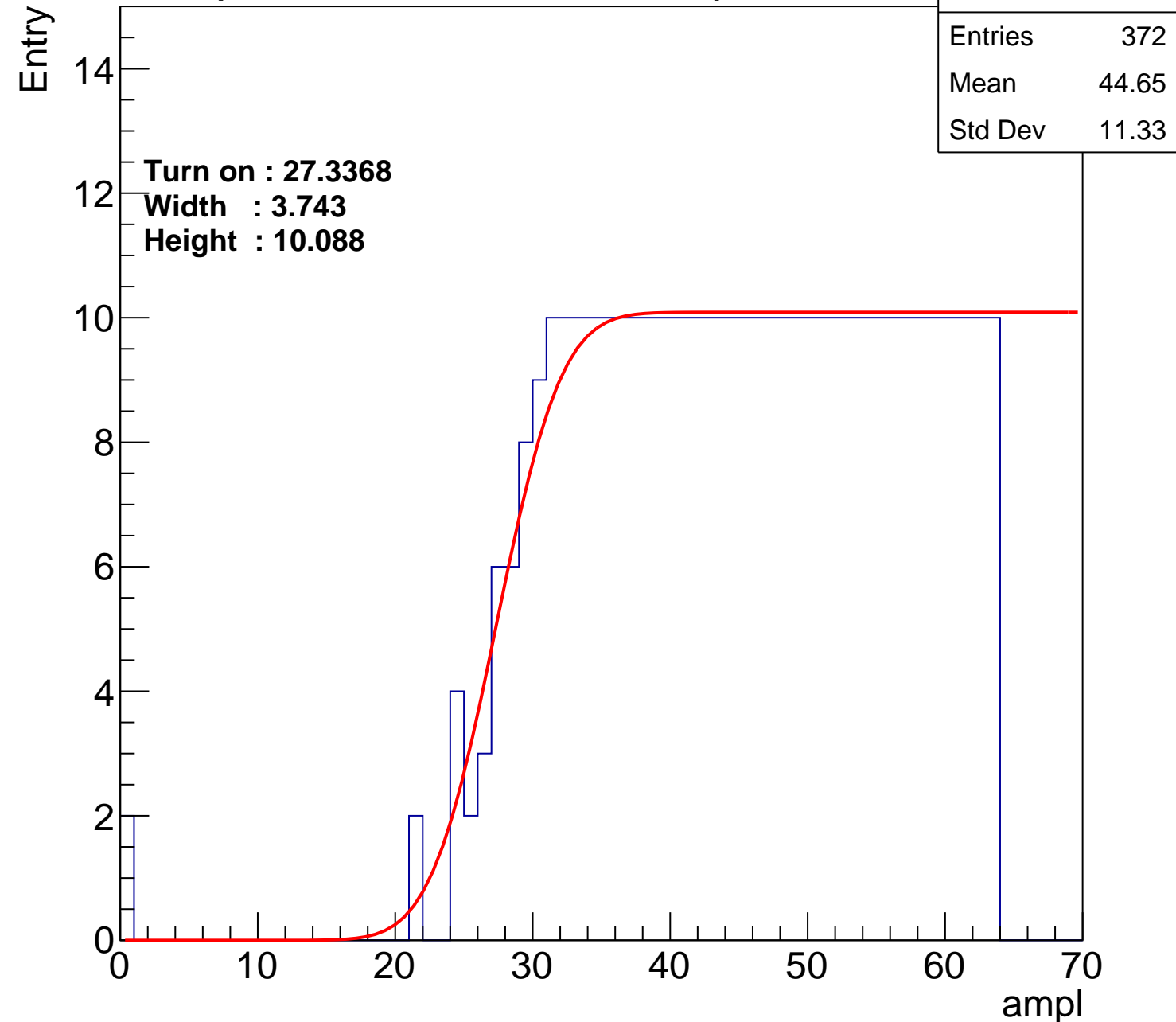
Width : 3.743

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch56

calib_packv5_042523_0143.root, FC#12, port B1

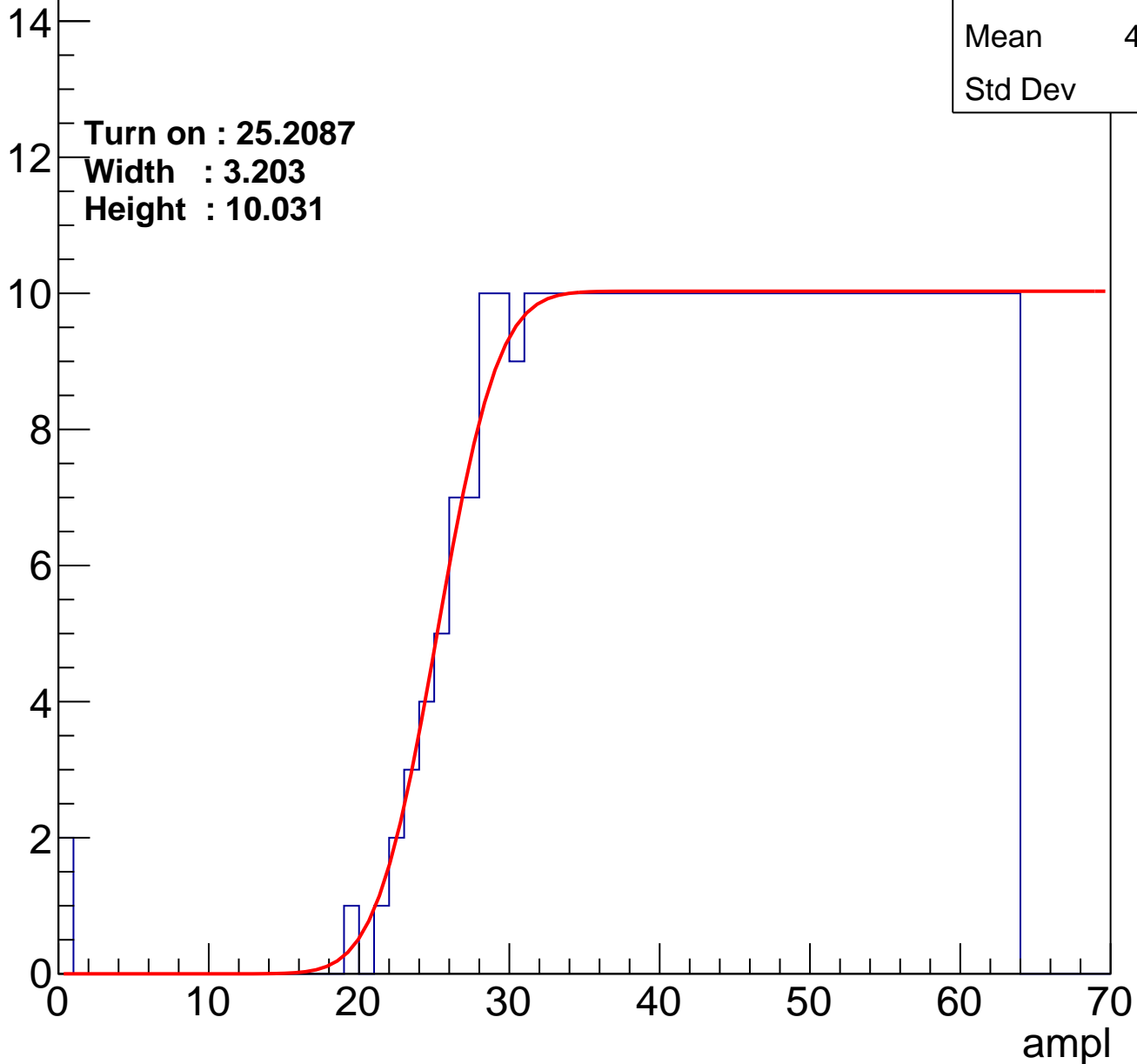
Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.2087

Width : 3.203

Height : 10.031

Entry



B0L102S, U16-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.81
Std Dev	11.59

Turn on : 25.5188

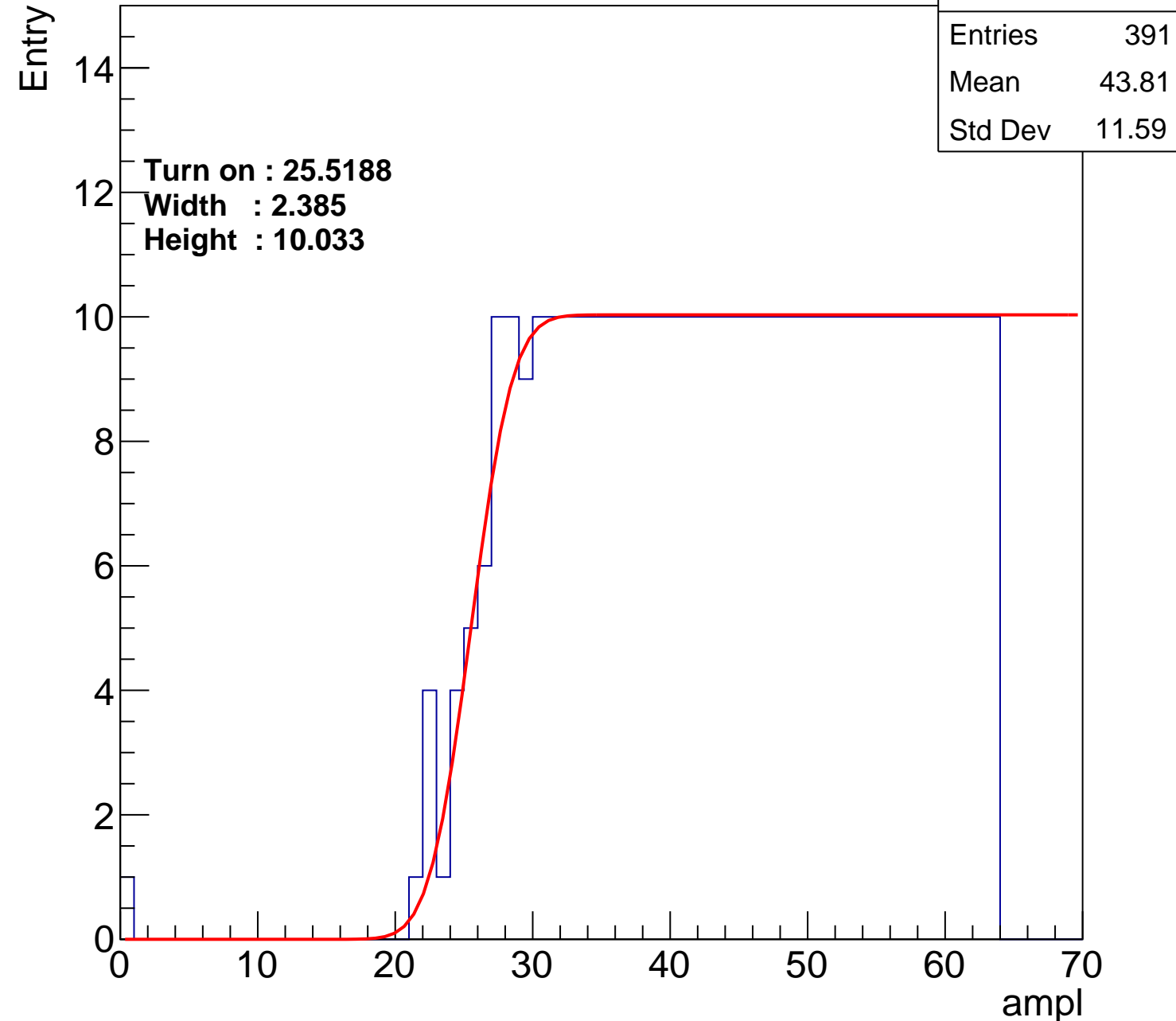
Width : 2.385

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 26.4610

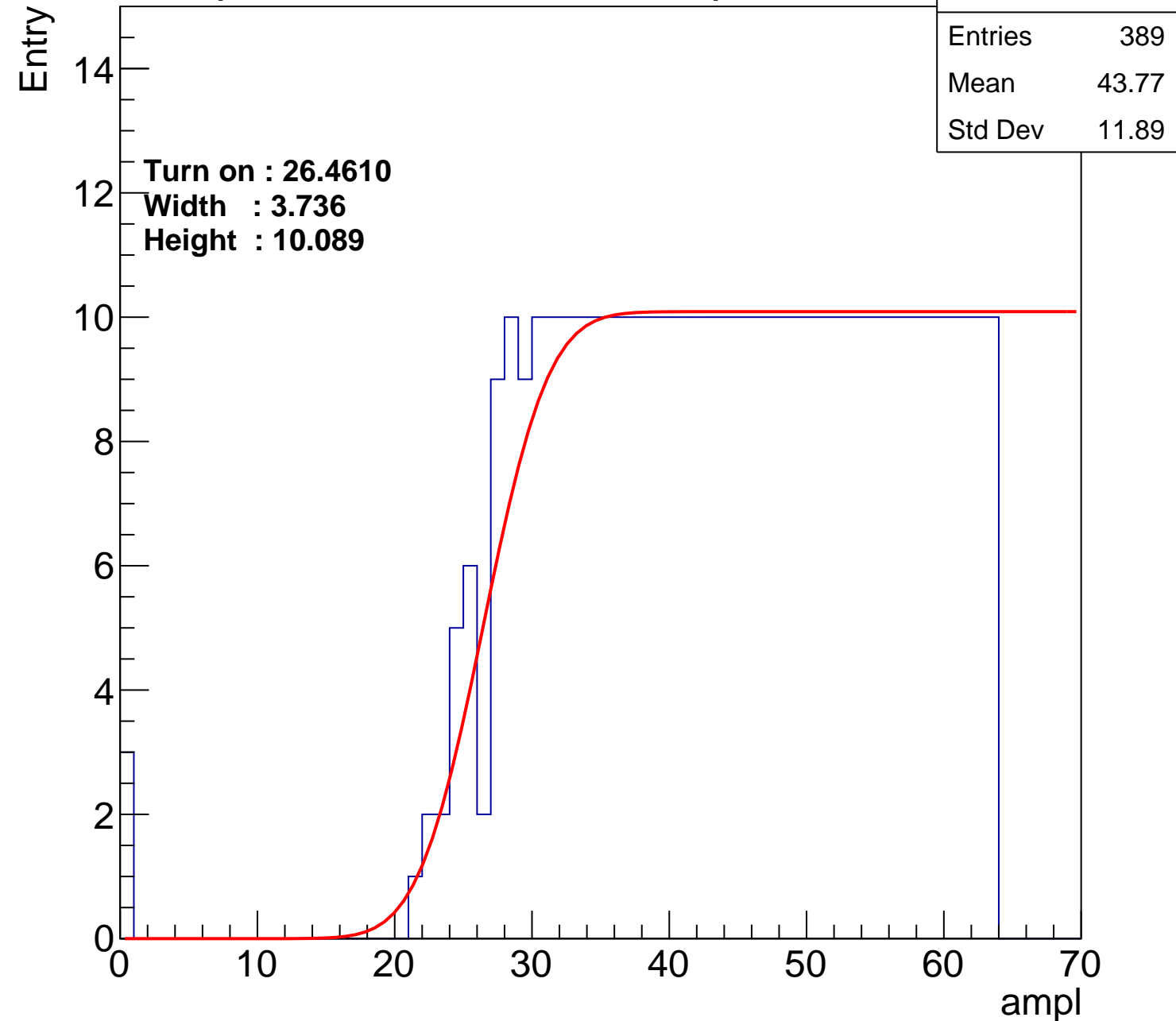
Width : 3.736

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.8186

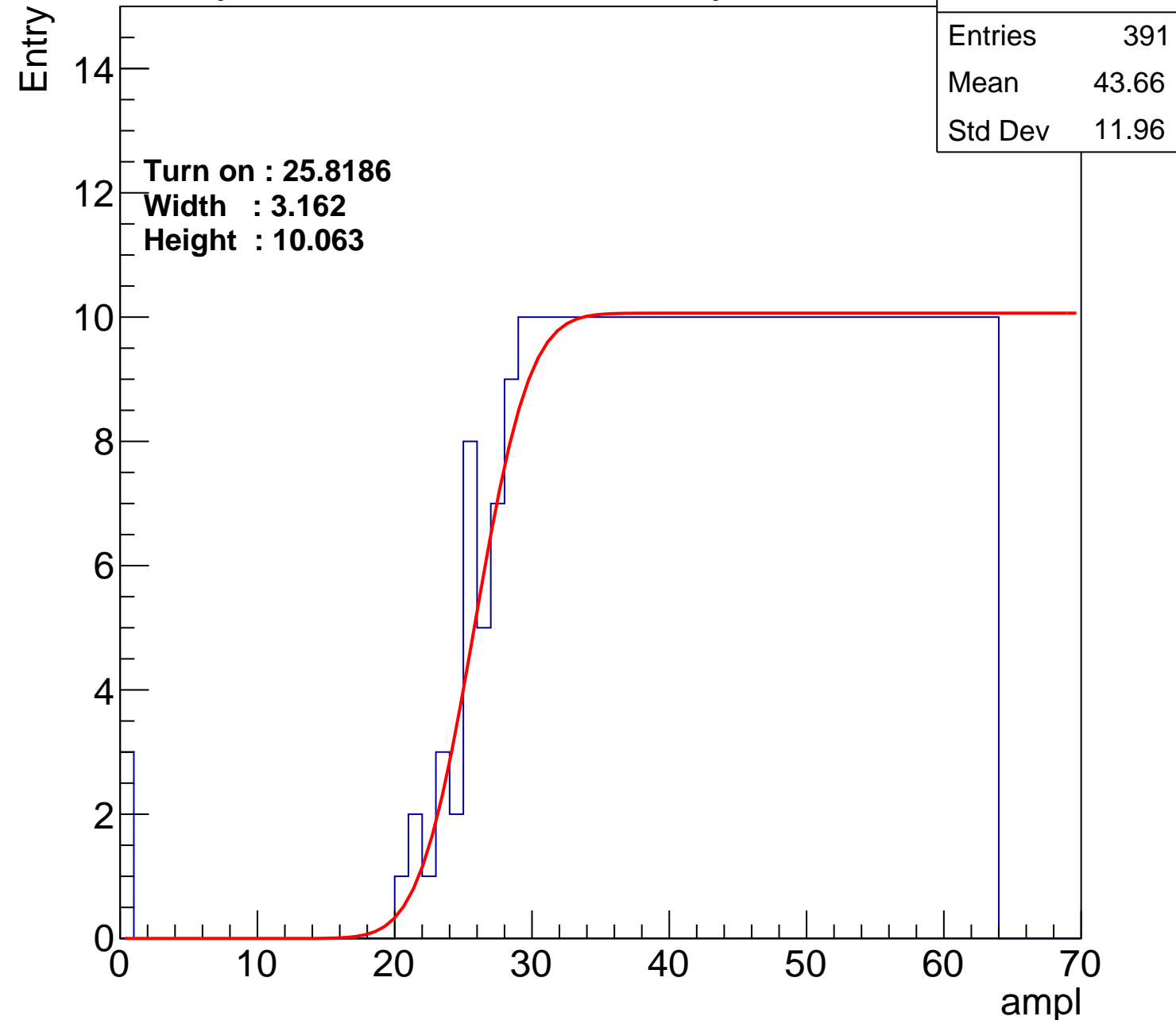
Width : 3.162

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.61
Std Dev	12.14

Turn on : 25.2724

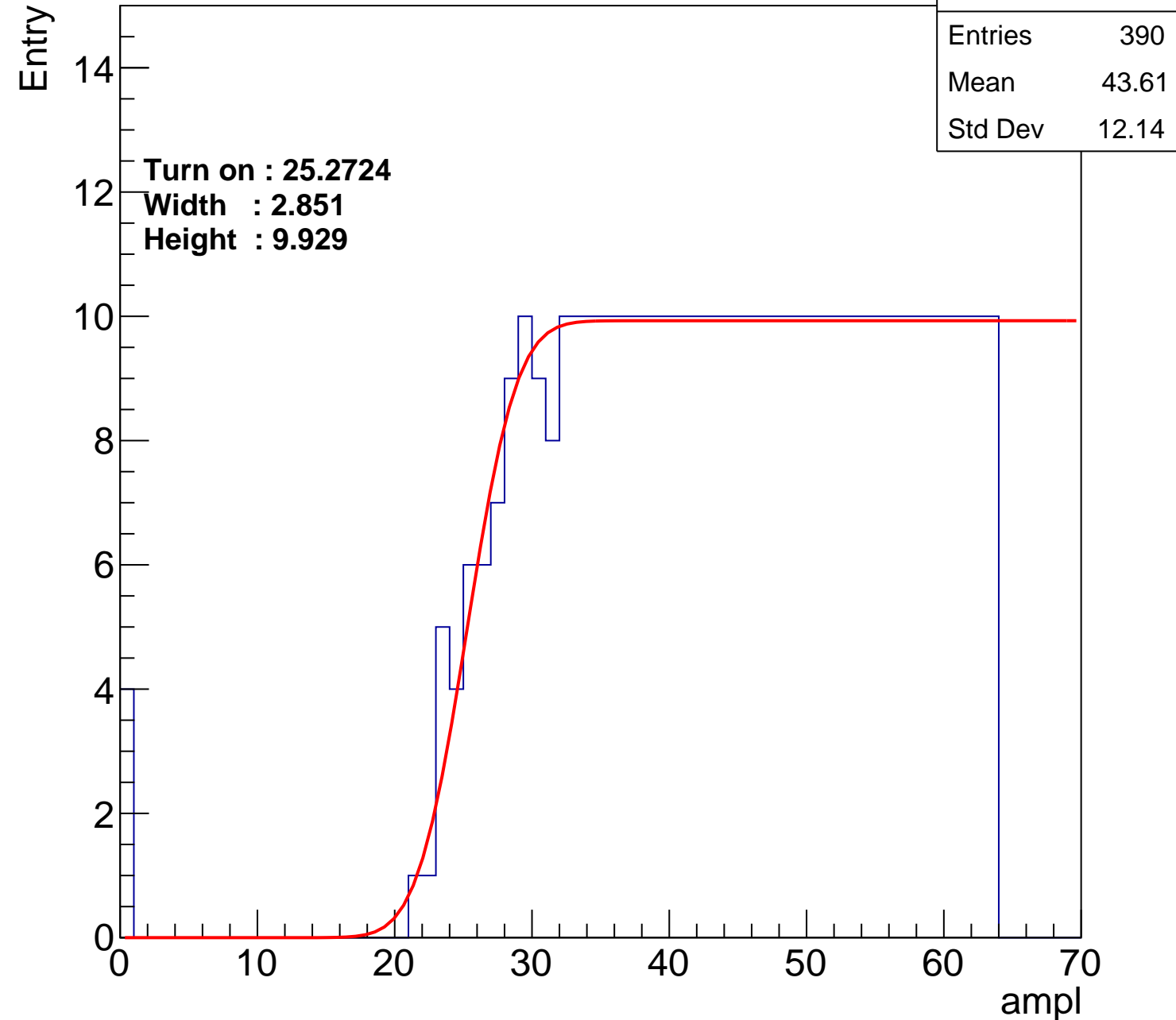
Width : 2.851

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch61

calib_packv5_042523_0143.root, FC#12, port B1

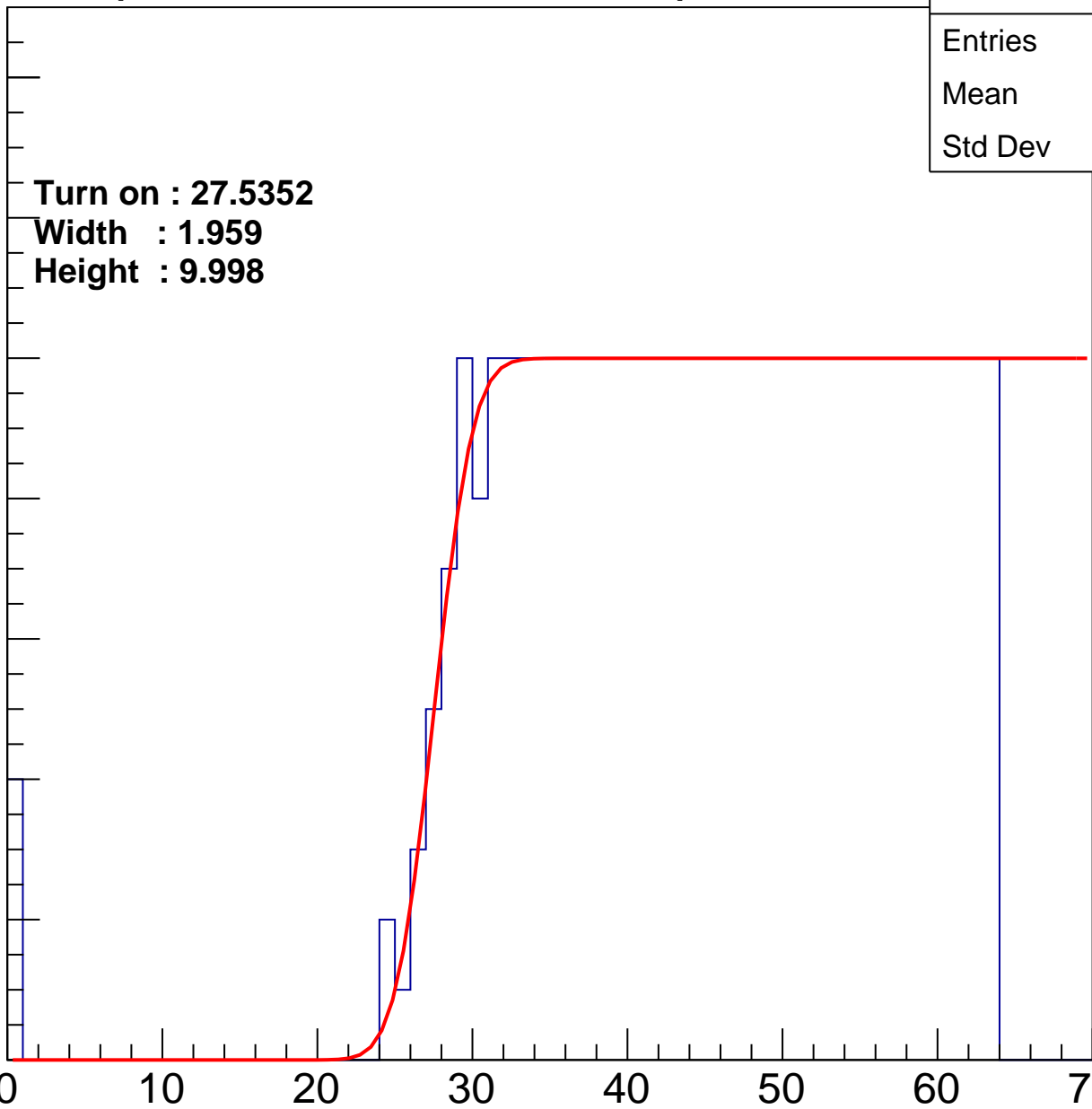
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5352
Width : 1.959
Height : 9.998

Entries	370
Mean	44.65
Std Dev	11.59

ampl



B0L102S, U16-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.78
Std Dev	12.52

Turn on : 23.4894

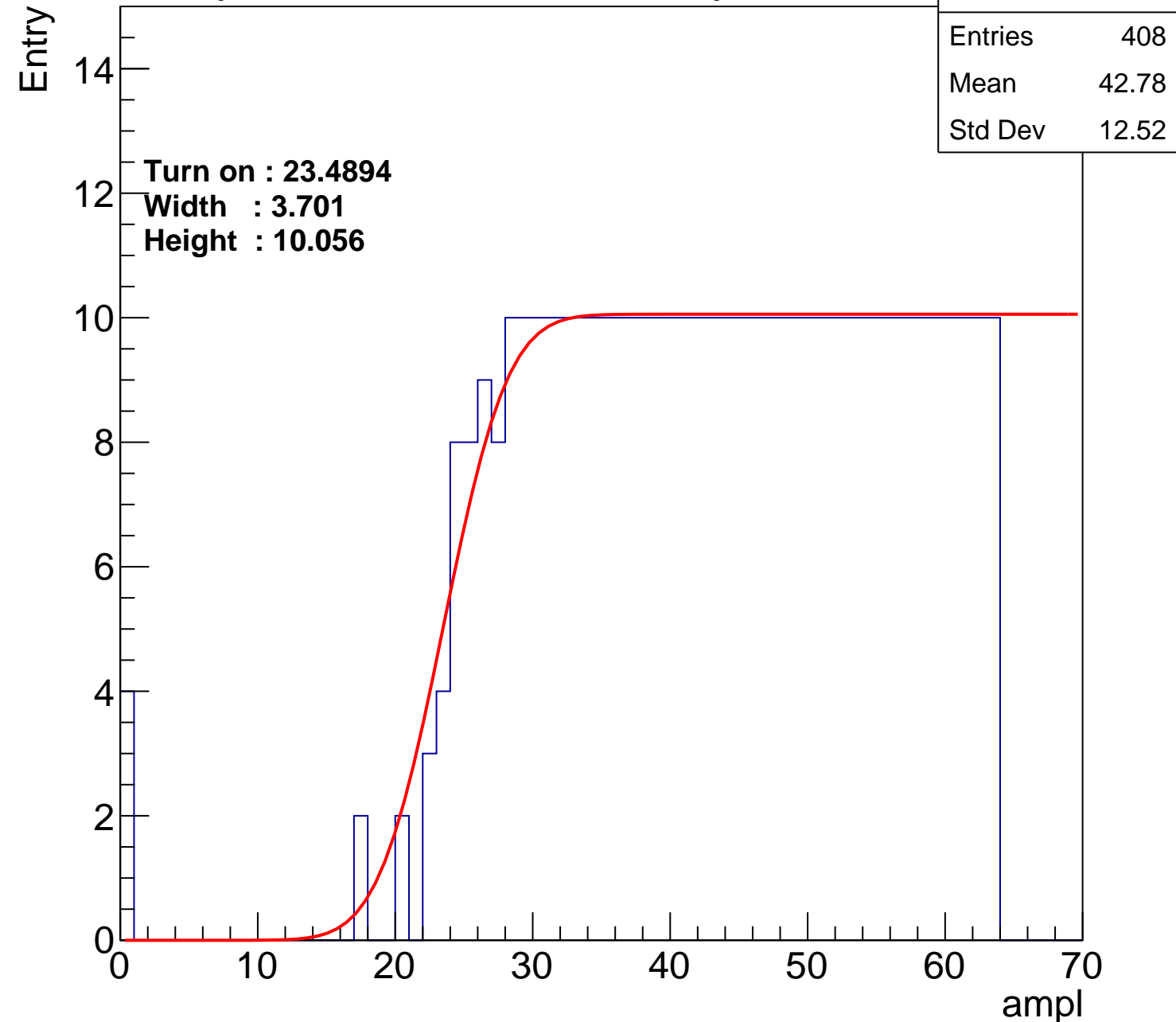
Width : 3.701

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.17
Std Dev	11.07

Turn on : 27.8168

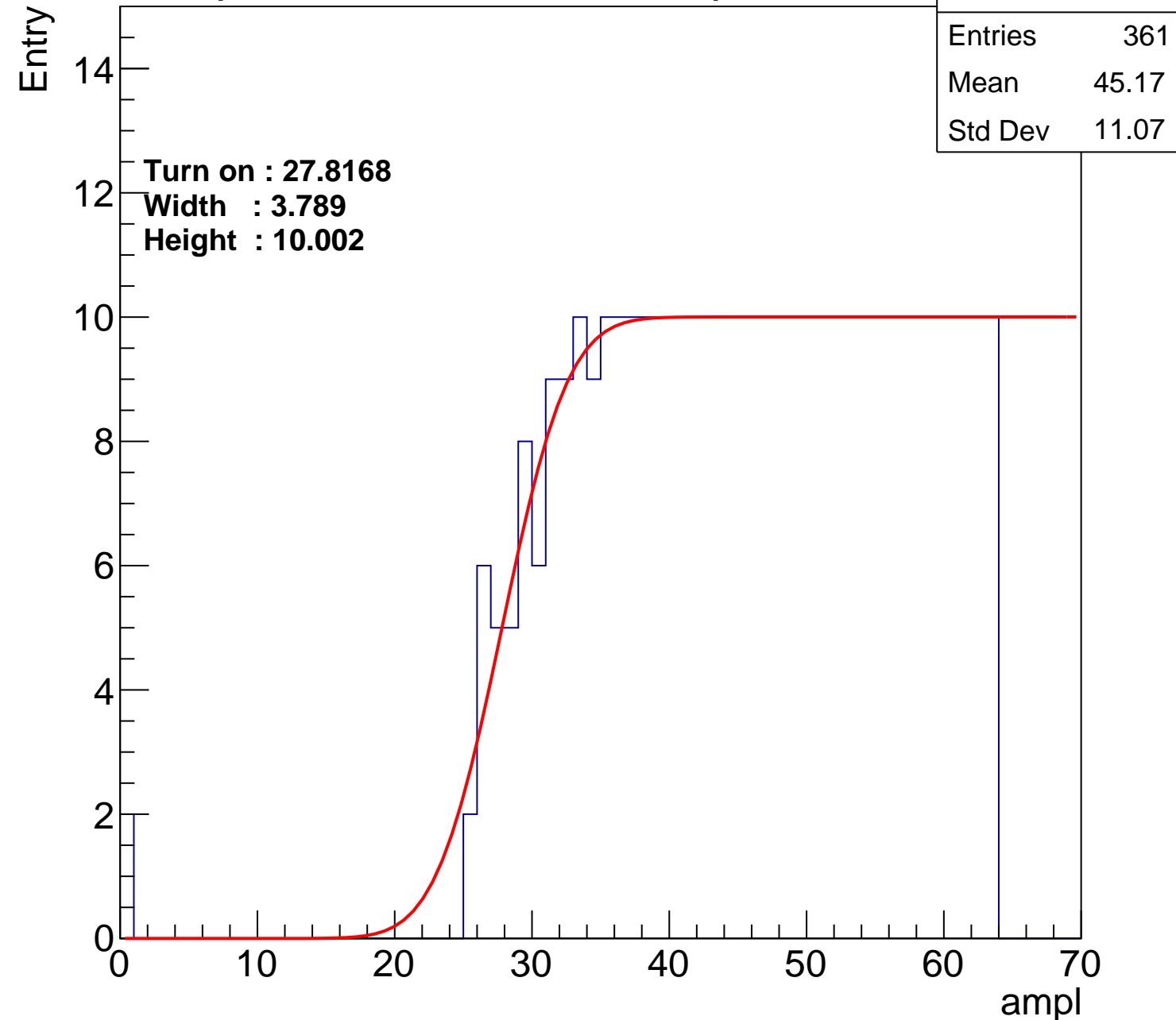
Width : 3.789

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch64

calib_packv5_042523_0143.root, FC#12, port B1

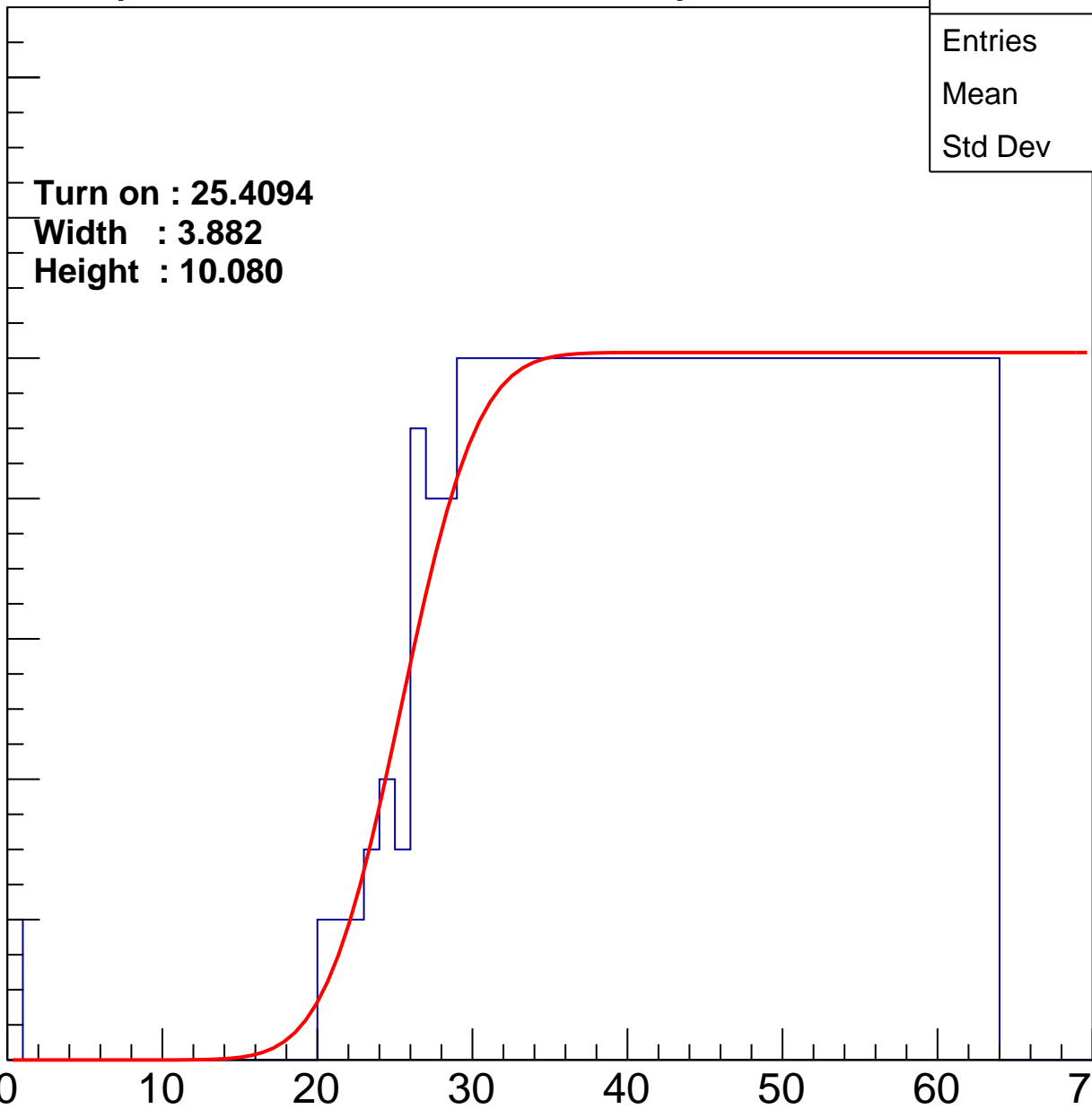
Entry

14
12
10
8
6
4
2
0

Turn on : 25.4094
Width : 3.882
Height : 10.080

Entries	393
Mean	43.61
Std Dev	11.87

ampl



B0L102S, U16-ch65

calib_packv5_042523_0143.root, FC#12, port B1

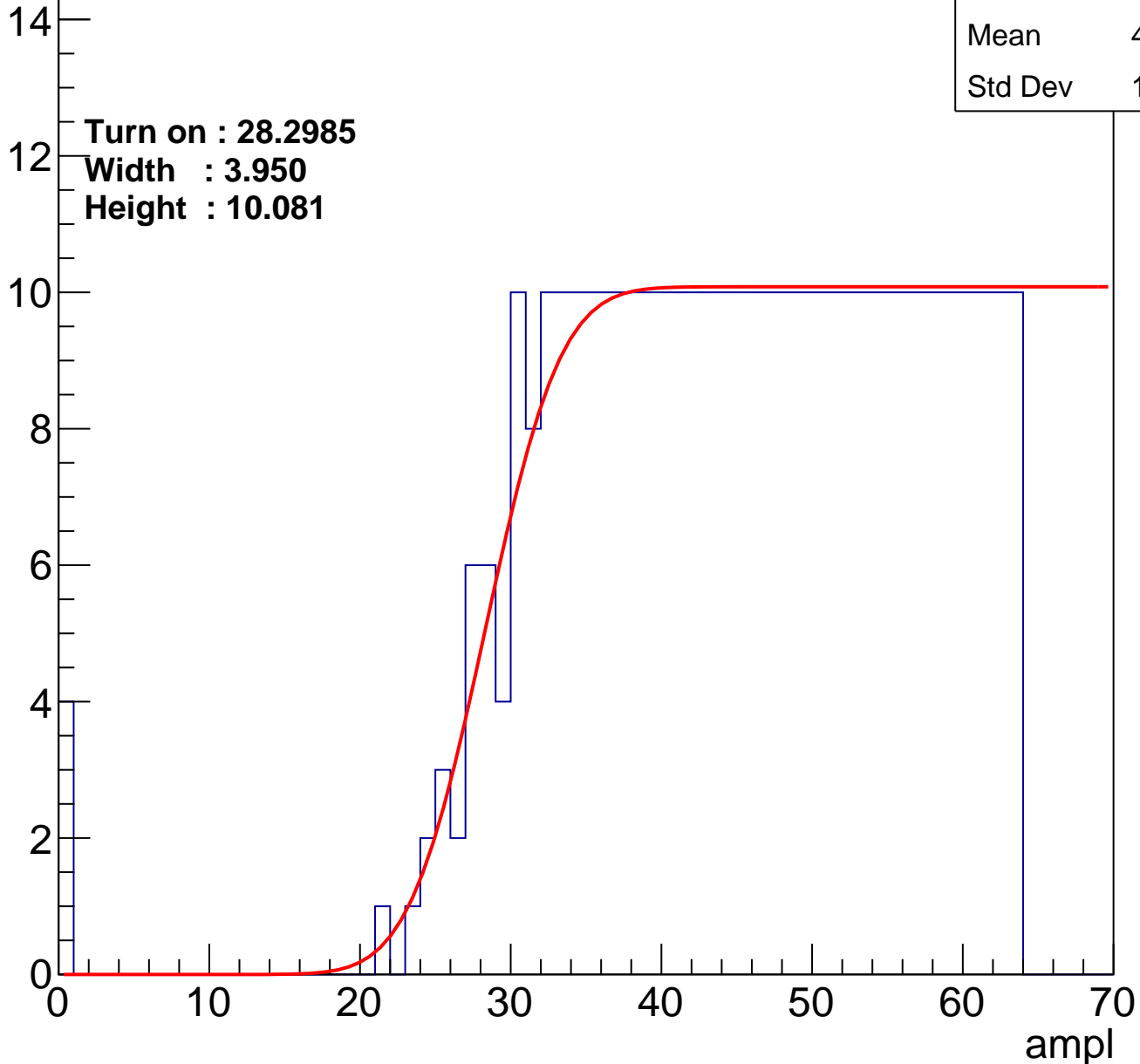
Entries	367
Mean	44.72
Std Dev	11.64

Turn on : 28.2985

Width : 3.950

Height : 10.081

Entry



B0L102S, U16-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.46
Std Dev	12.23

Turn on : 25.5699

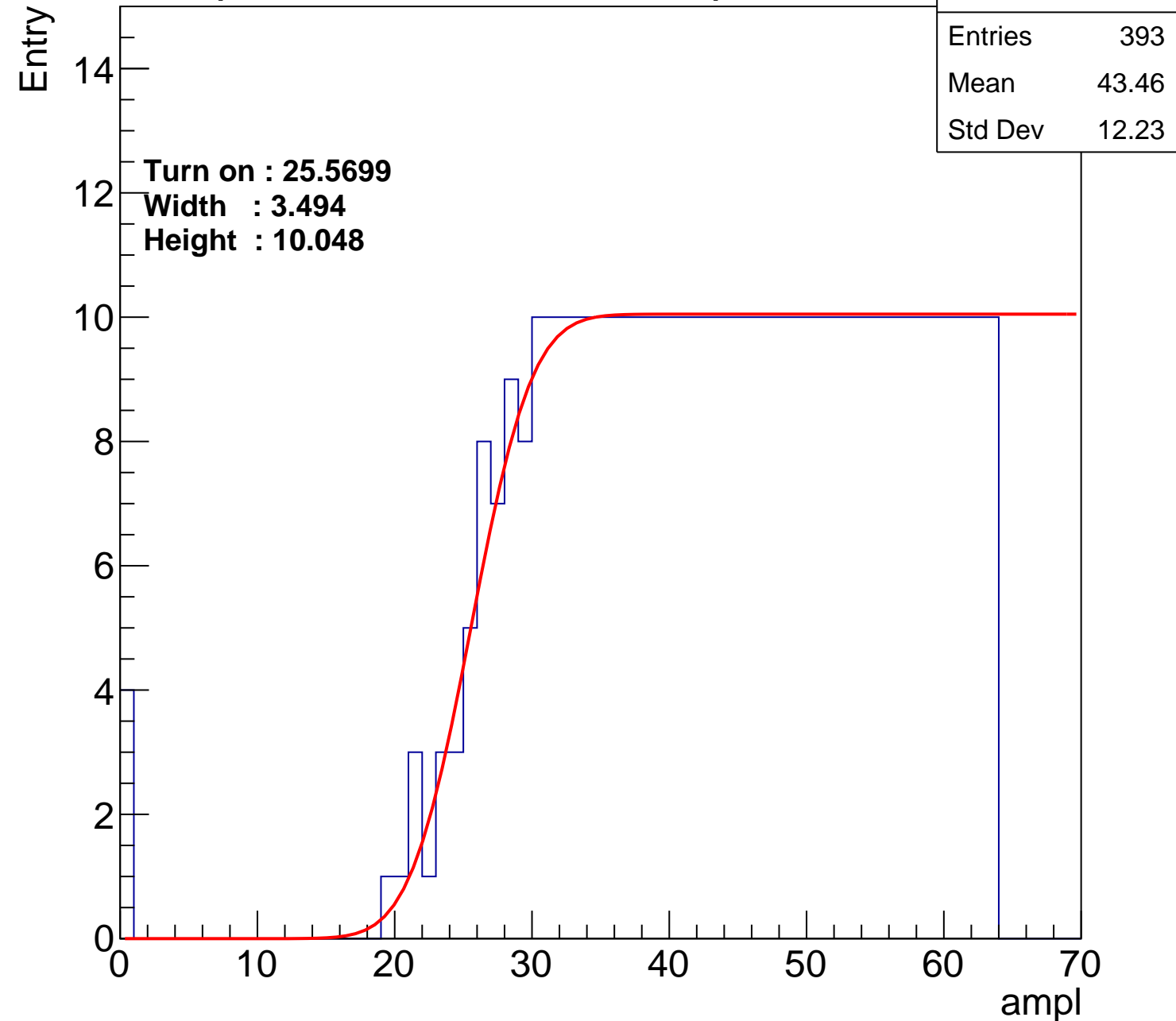
Width : 3.494

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.51
Std Dev	11.88

Turn on : 27.9647

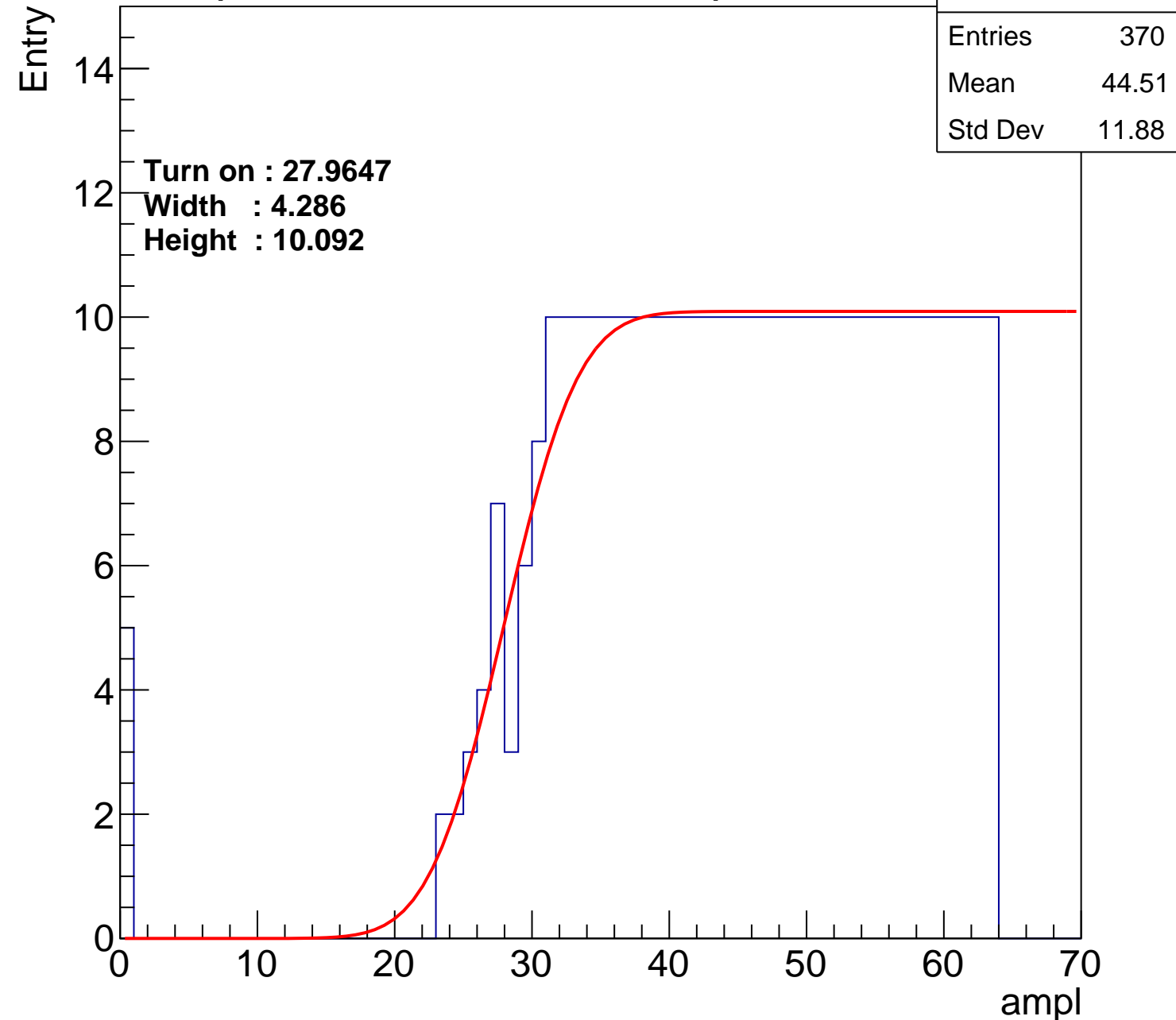
Width : 4.286

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.16
Std Dev	11.82

Turn on : 26.6492

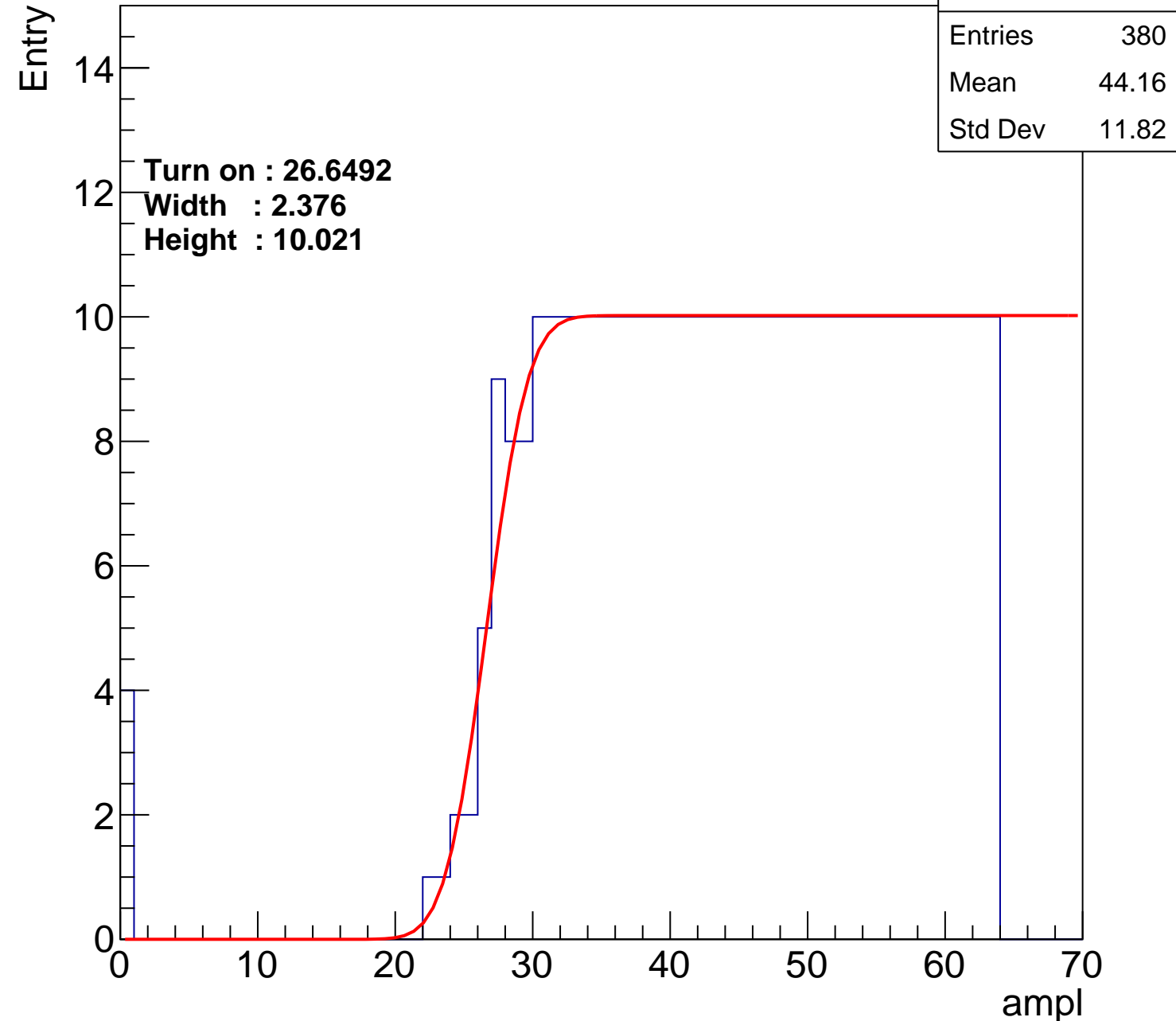
Width : 2.376

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.32
Std Dev	12.02

Turn on : 24.1917

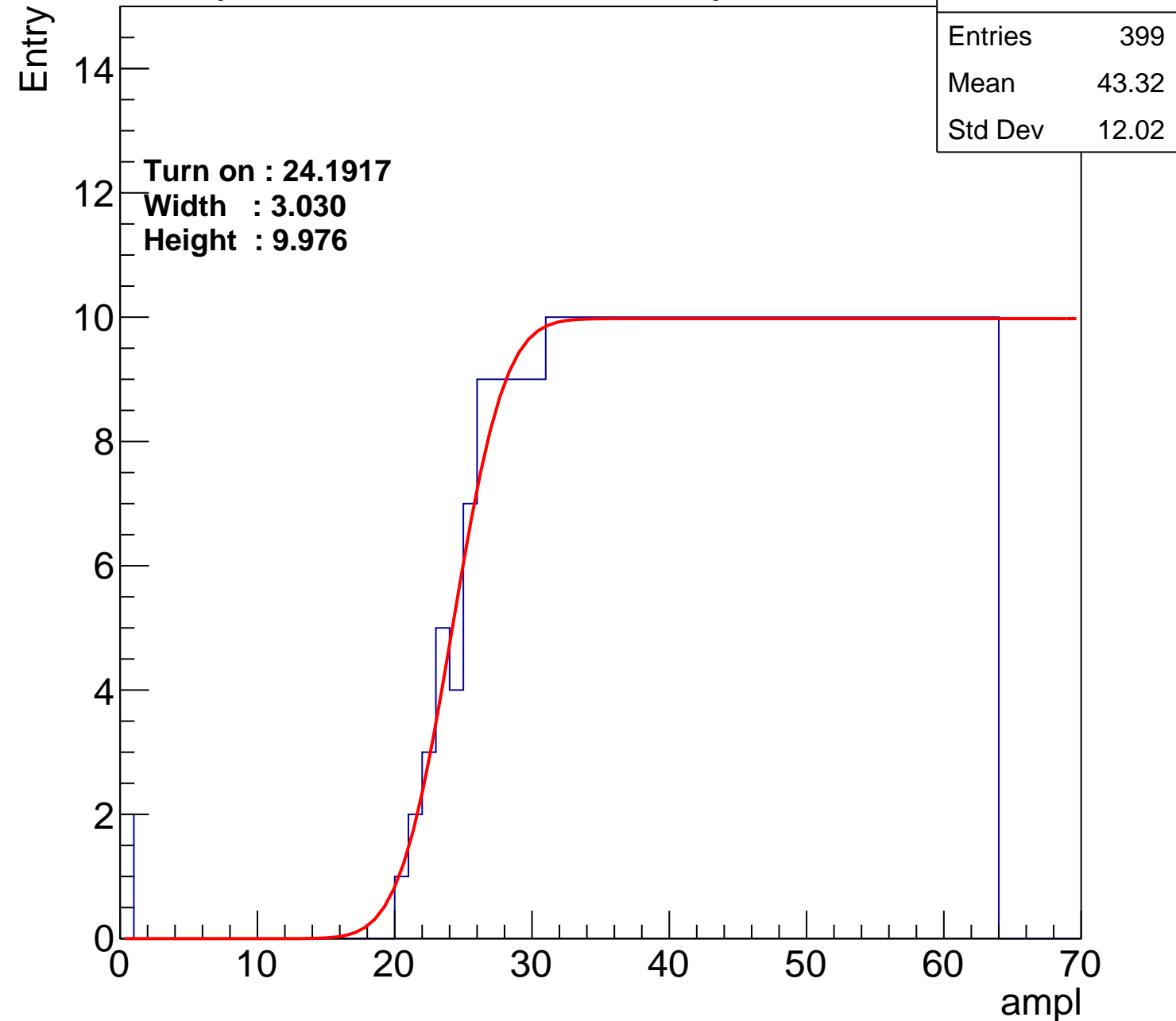
Width : 3.030

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.28
Std Dev	12.31

Turn on : 25.0263

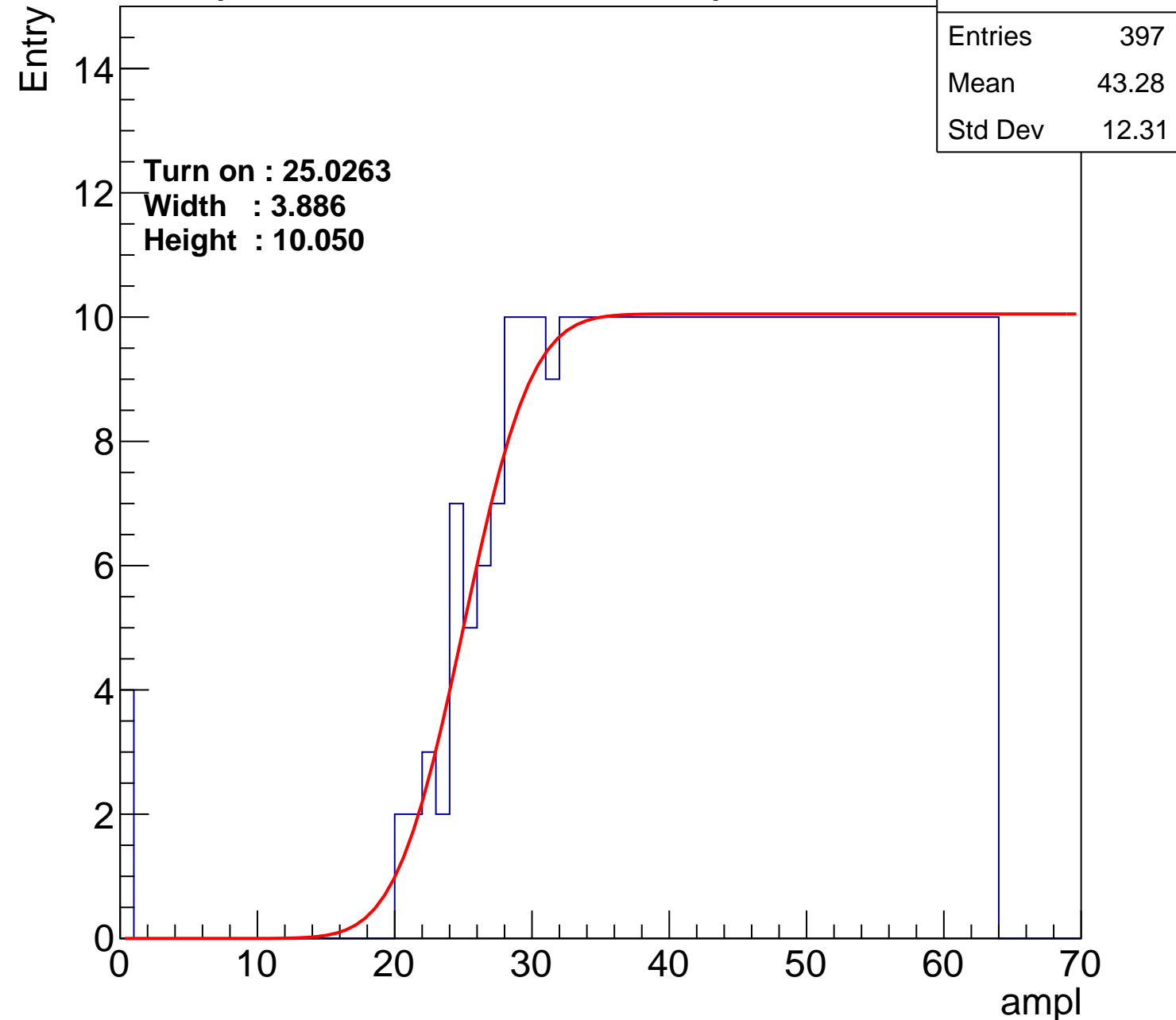
Width : 3.886

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.37
Std Dev	12.21

Turn on : 28.1252

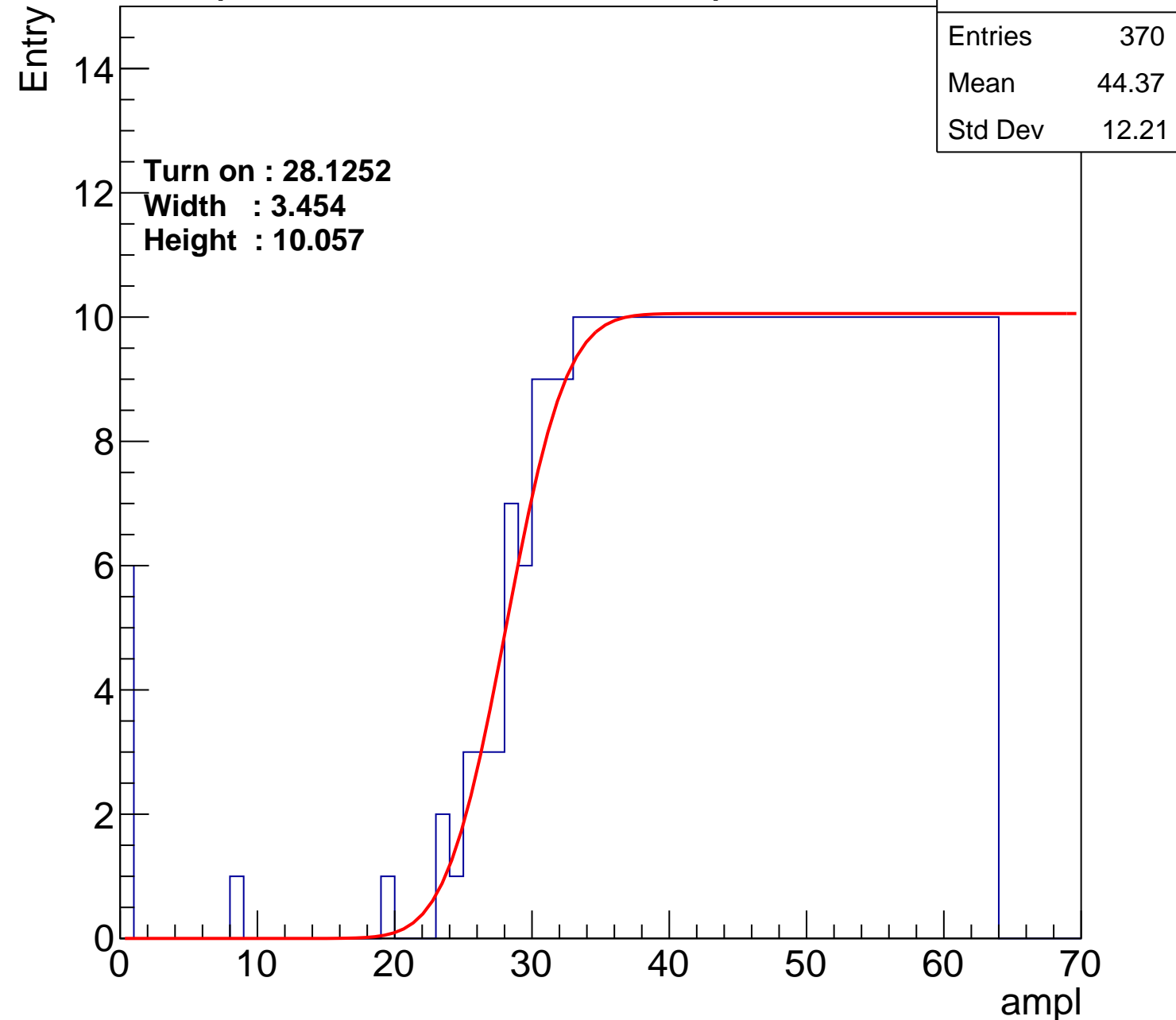
Width : 3.454

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.81
Std Dev	11.74

Turn on : 25.6366

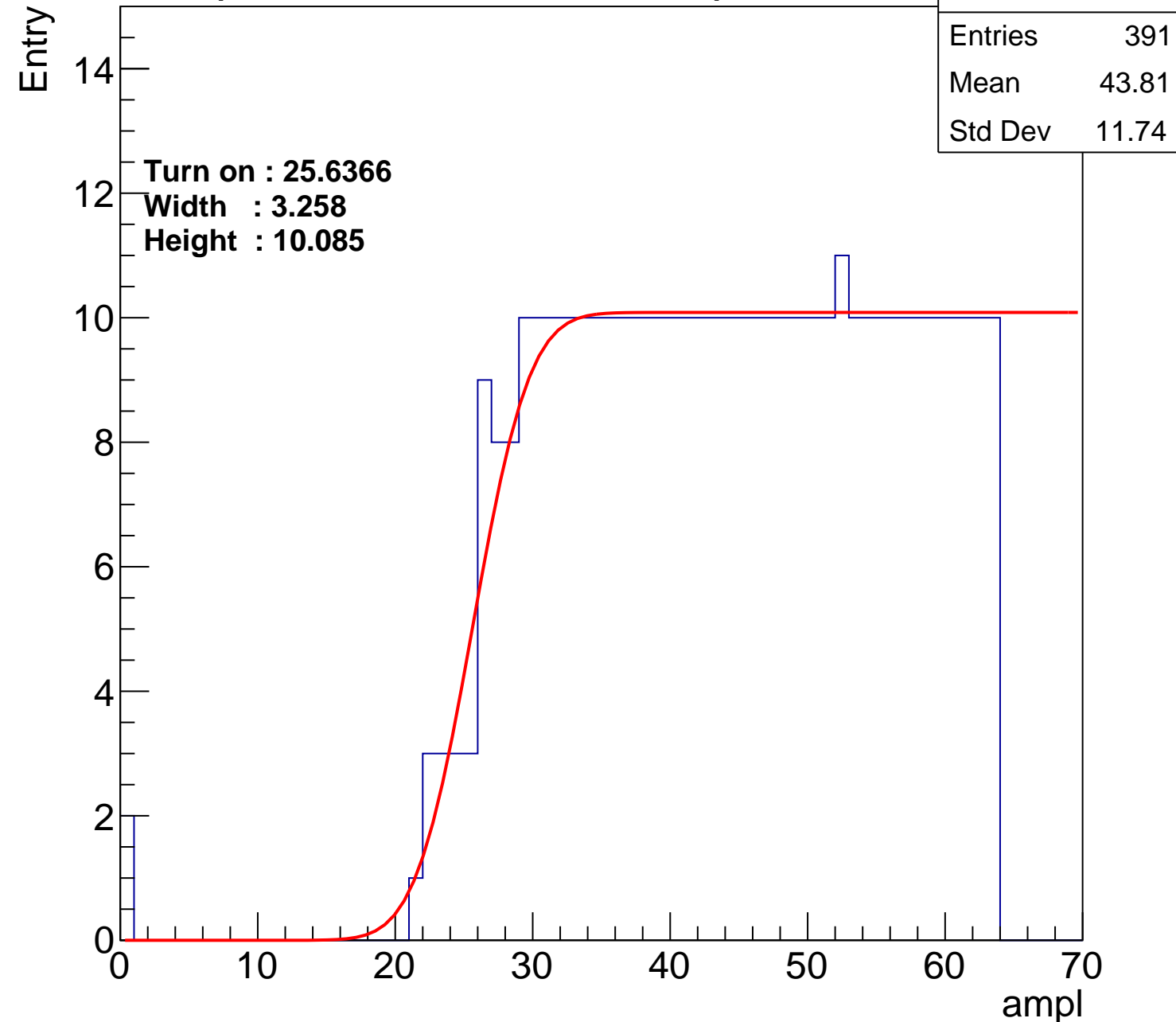
Width : 3.258

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.79
Std Dev	11.48

Turn on : 27.8694

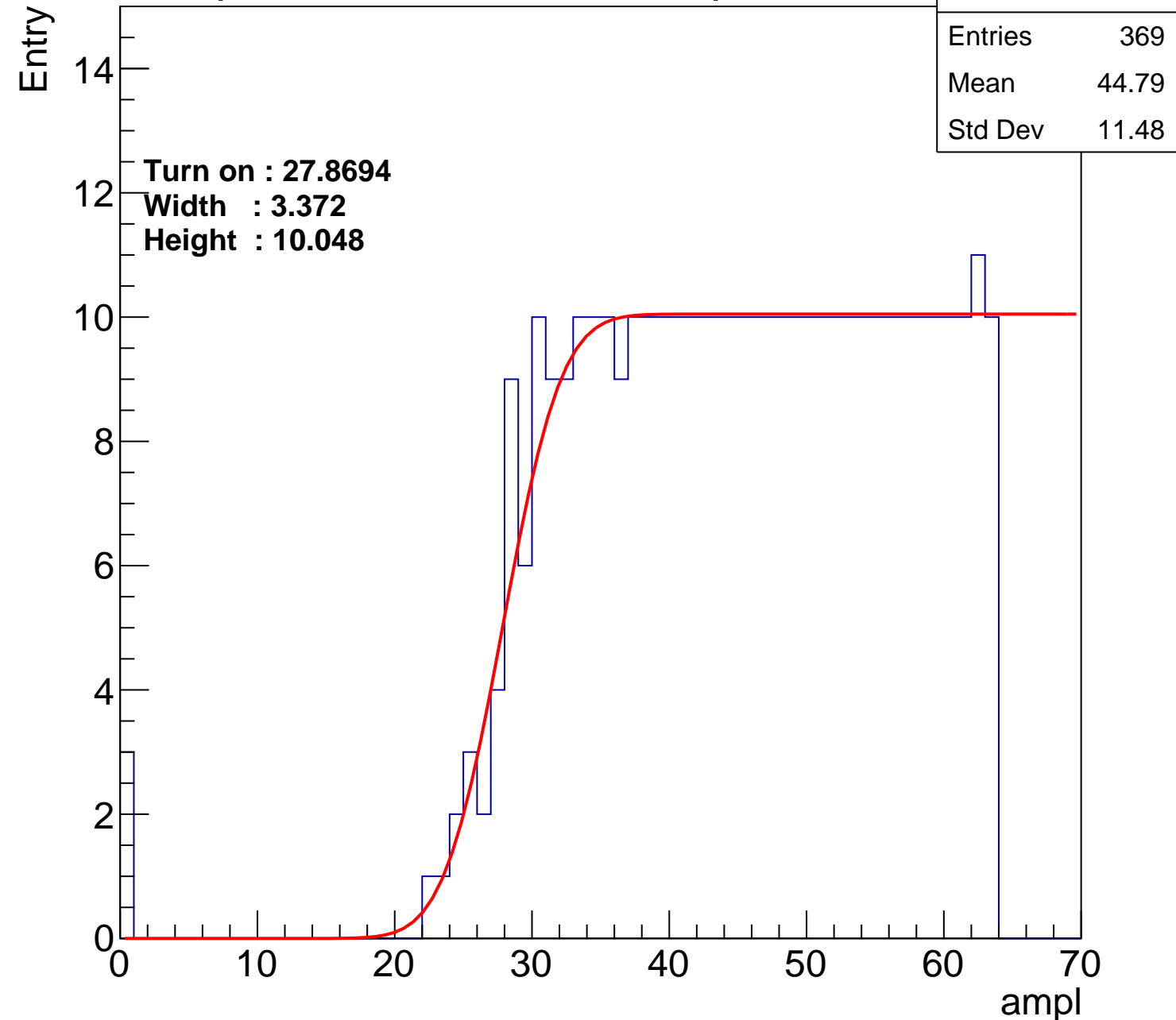
Width : 3.372

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.67
Std Dev	12.11

Turn on : 25.5712

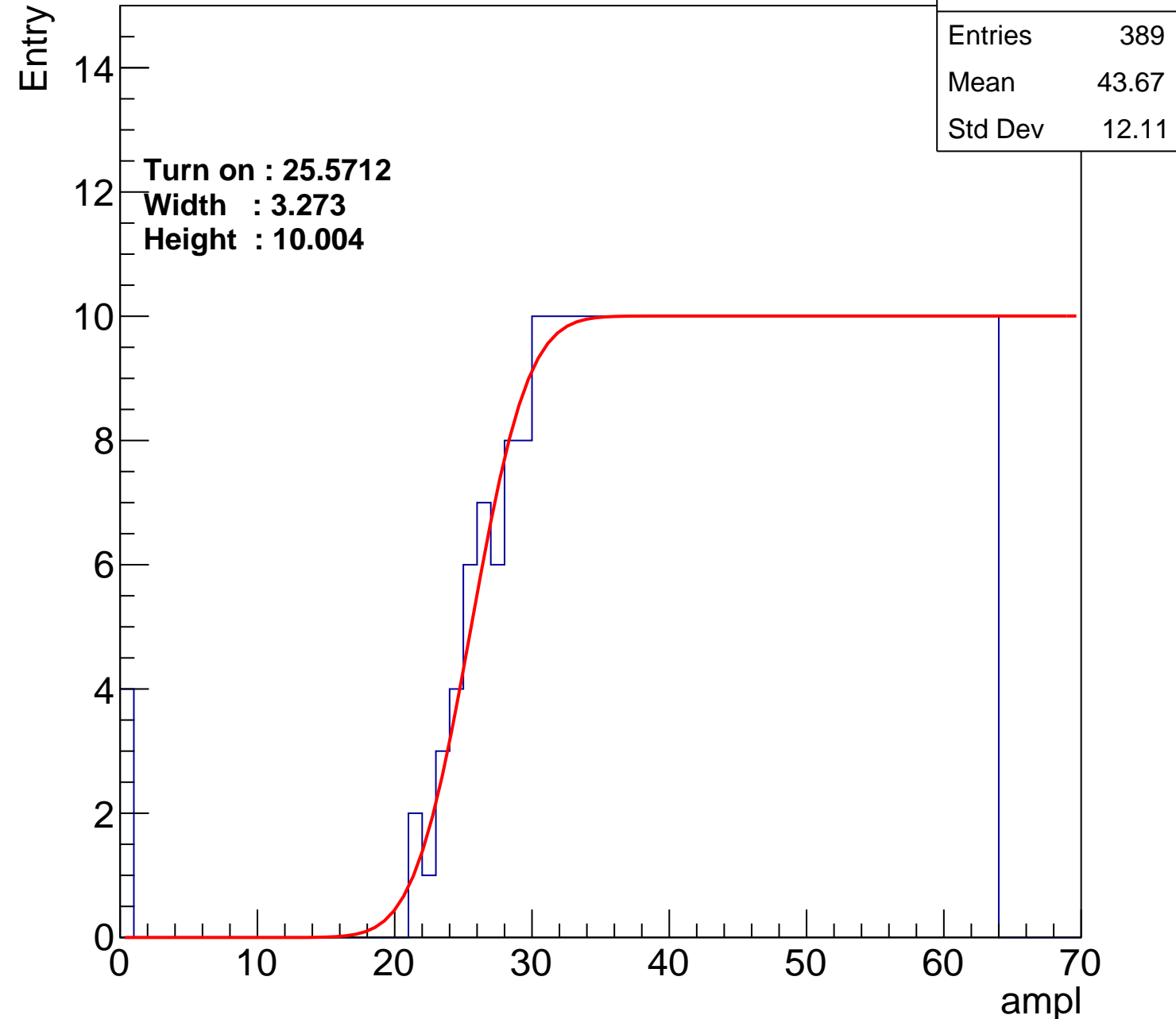
Width : 3.273

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.19
Std Dev	11.19

Turn on : 28.2998

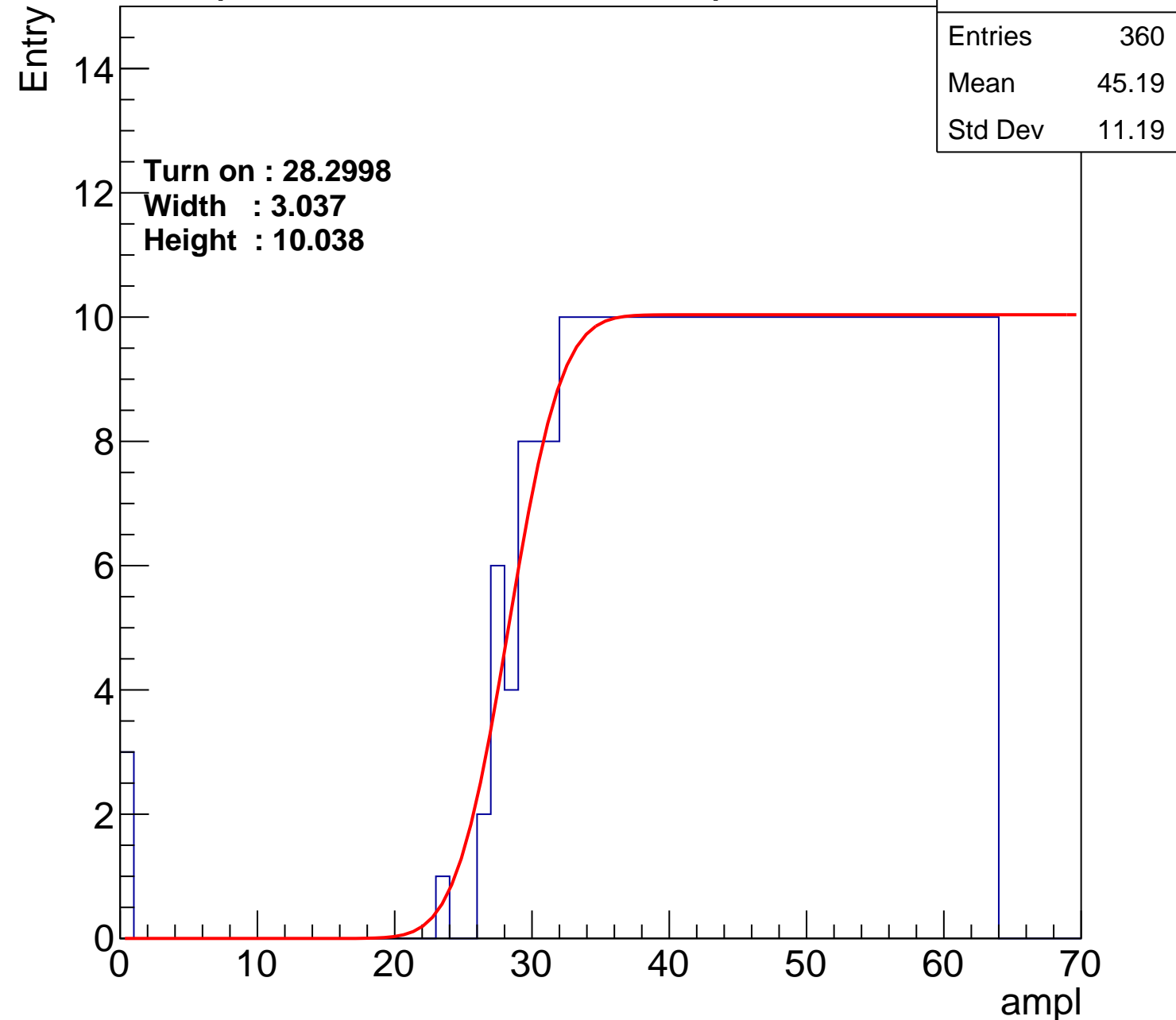
Width : 3.037

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.48
Std Dev	11.7

Turn on : 27.6301

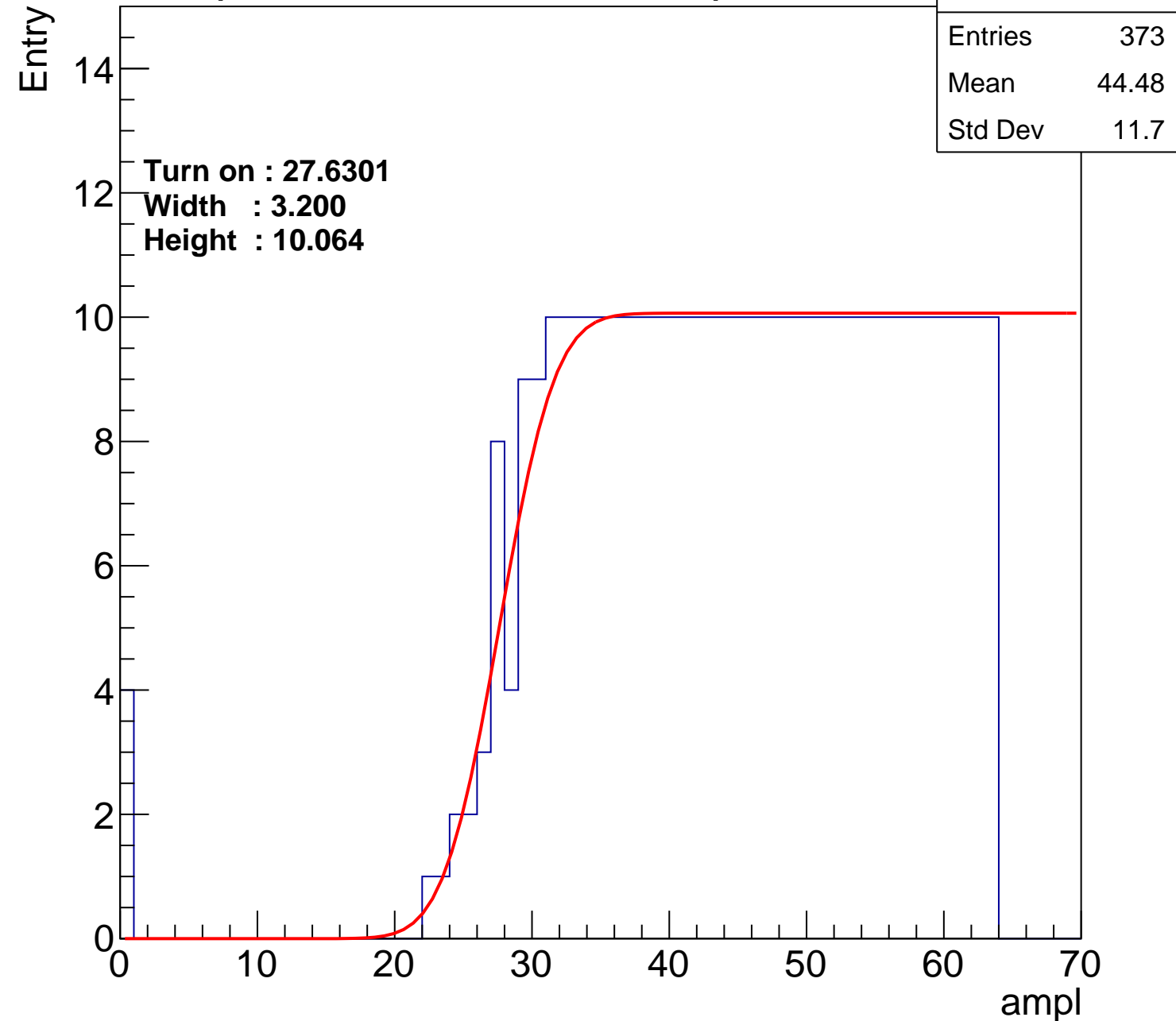
Width : 3.200

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.46
Std Dev	11.94

Turn on : 24.5641

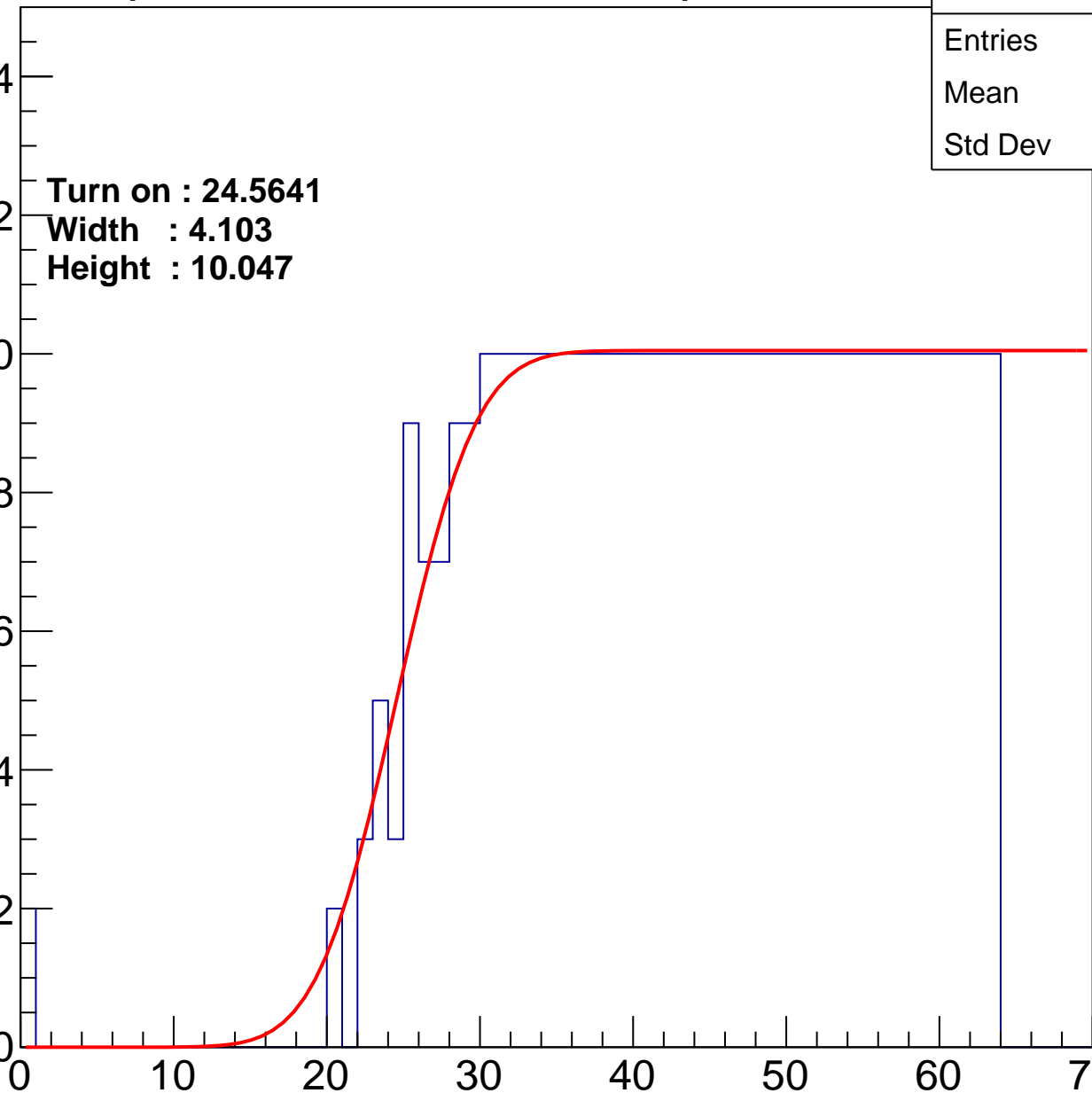
Width : 4.103

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.33
Std Dev	12.28

Turn on : 25.2272

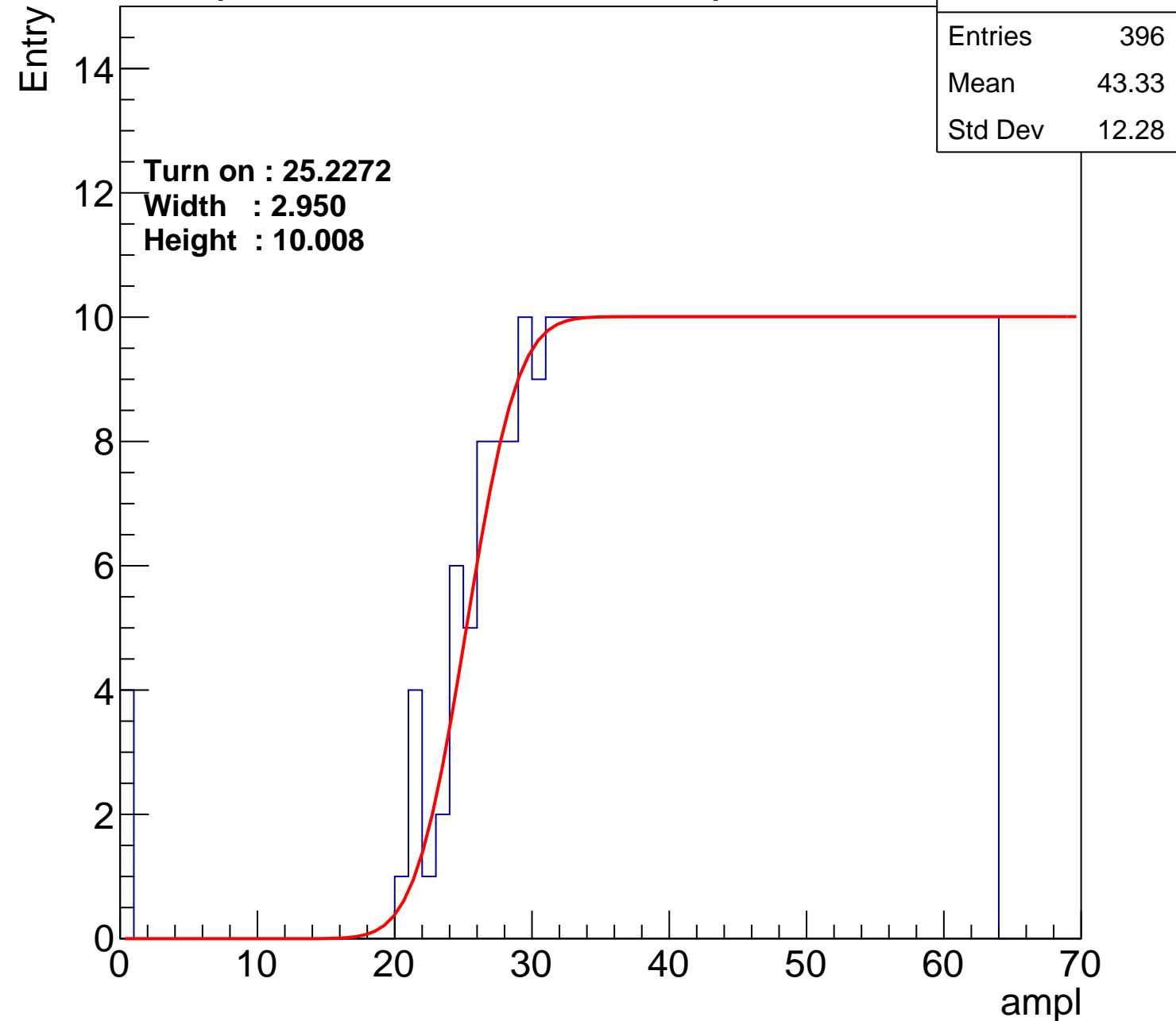
Width : 2.950

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch79

calib_packv5_042523_0143.root, FC#12, port B1

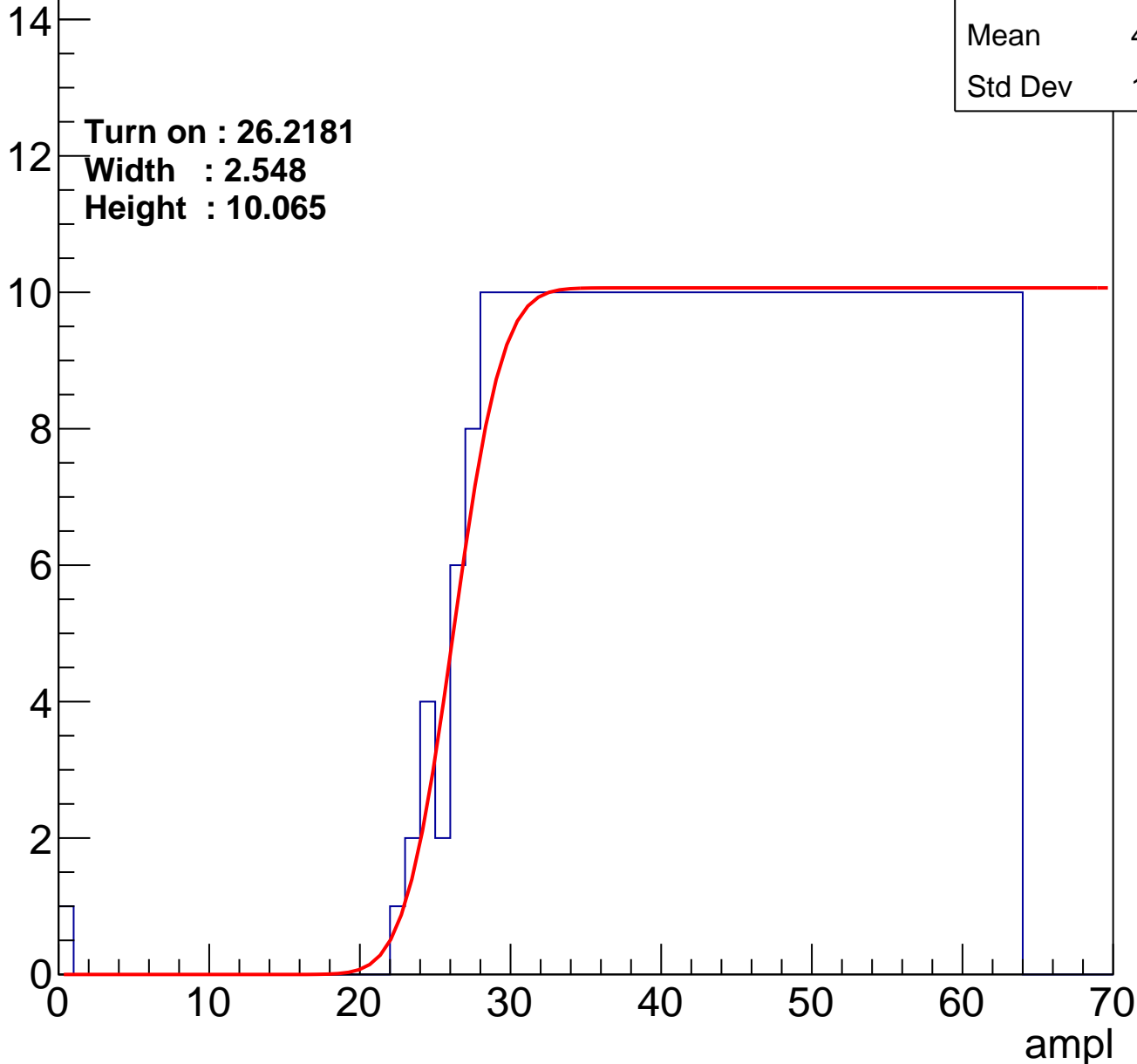
Entries	384
Mean	44.18
Std Dev	11.36

Turn on : 26.2181

Width : 2.548

Height : 10.065

Entry



B0L102S, U16-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.22
Std Dev	12.52

Turn on : 25.2013

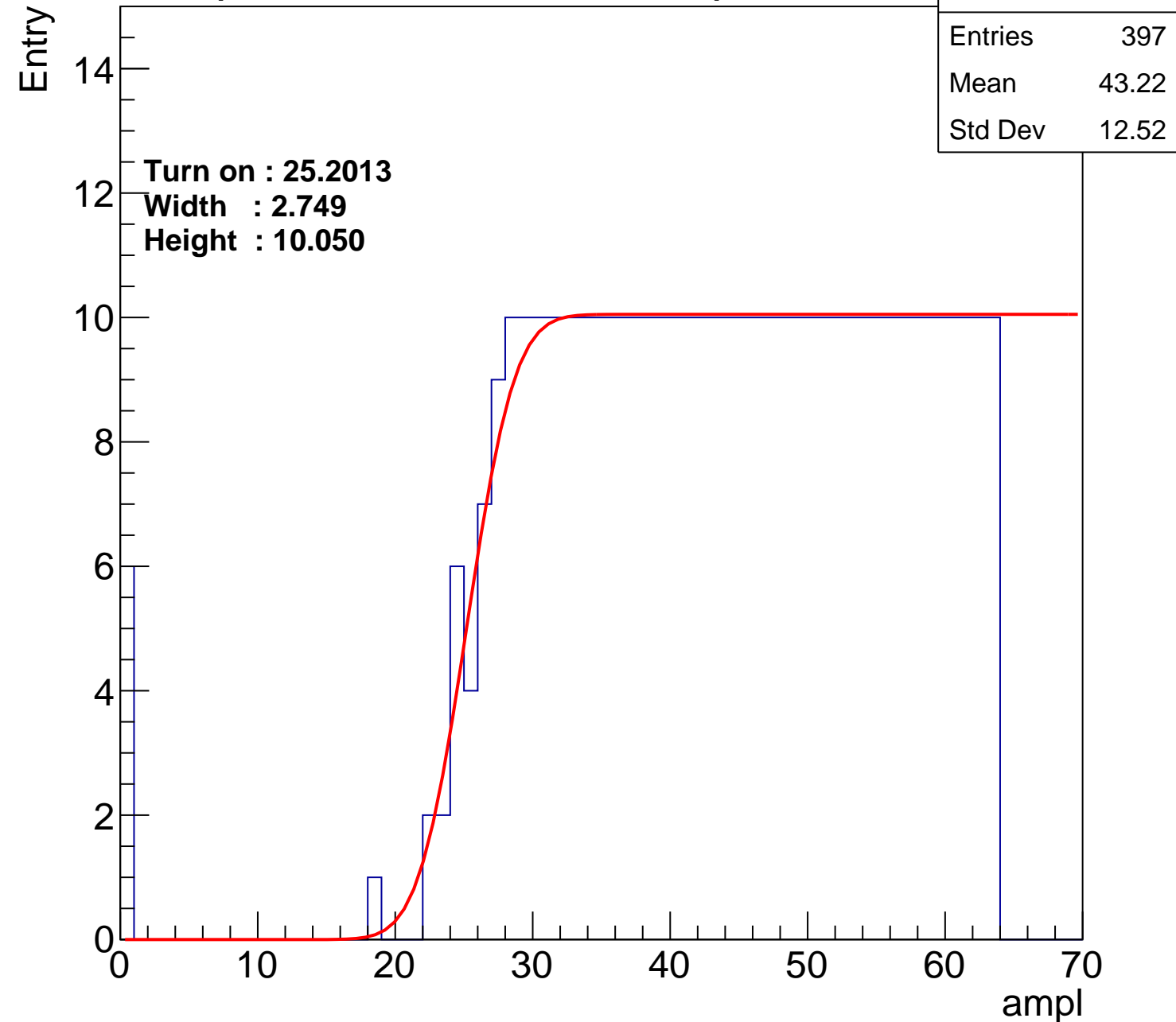
Width : 2.749

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.81
Std Dev	12

Turn on : 26.0163

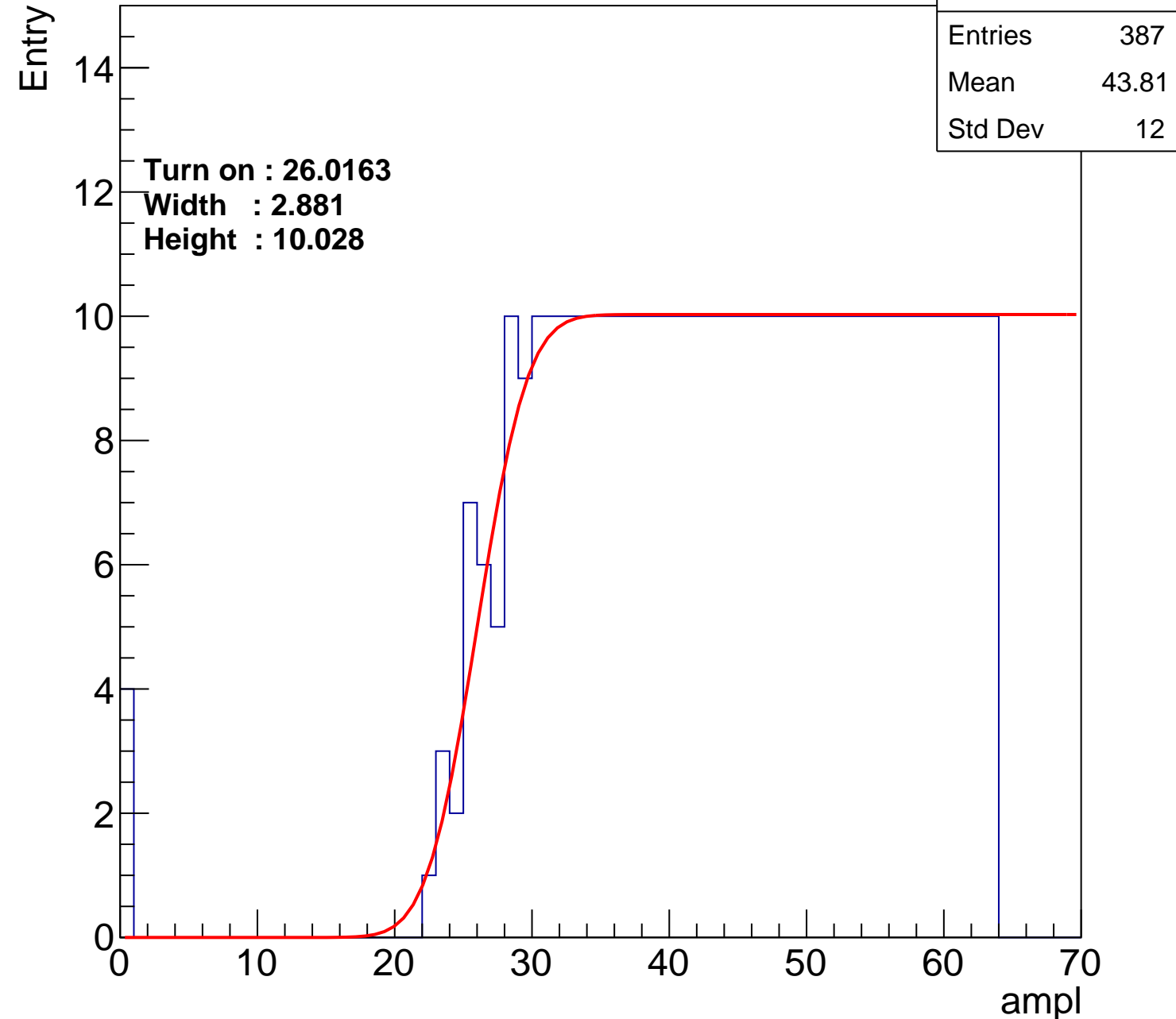
Width : 2.881

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.67
Std Dev	11.73

Turn on : 25.7258

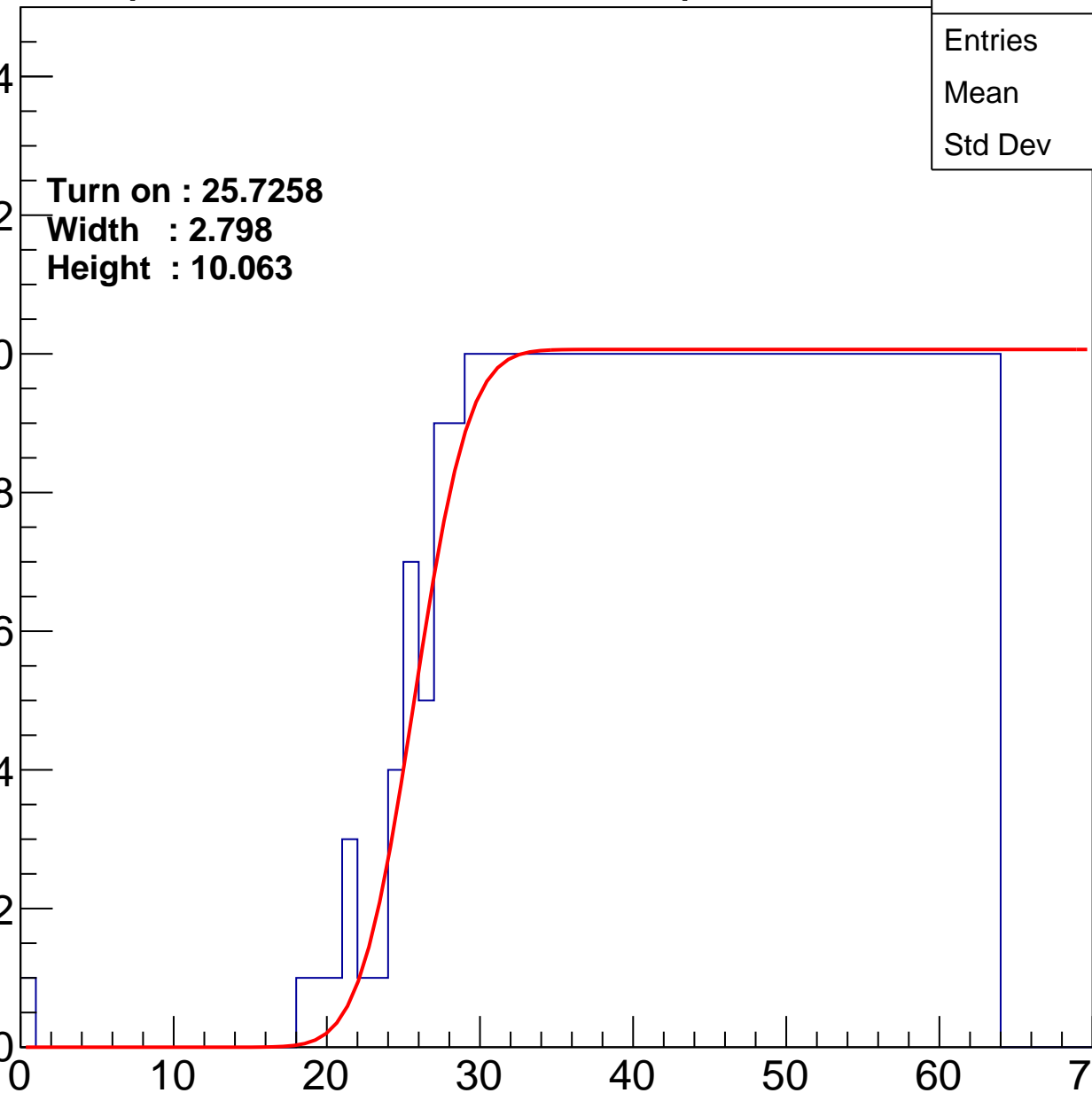
Width : 2.798

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch83

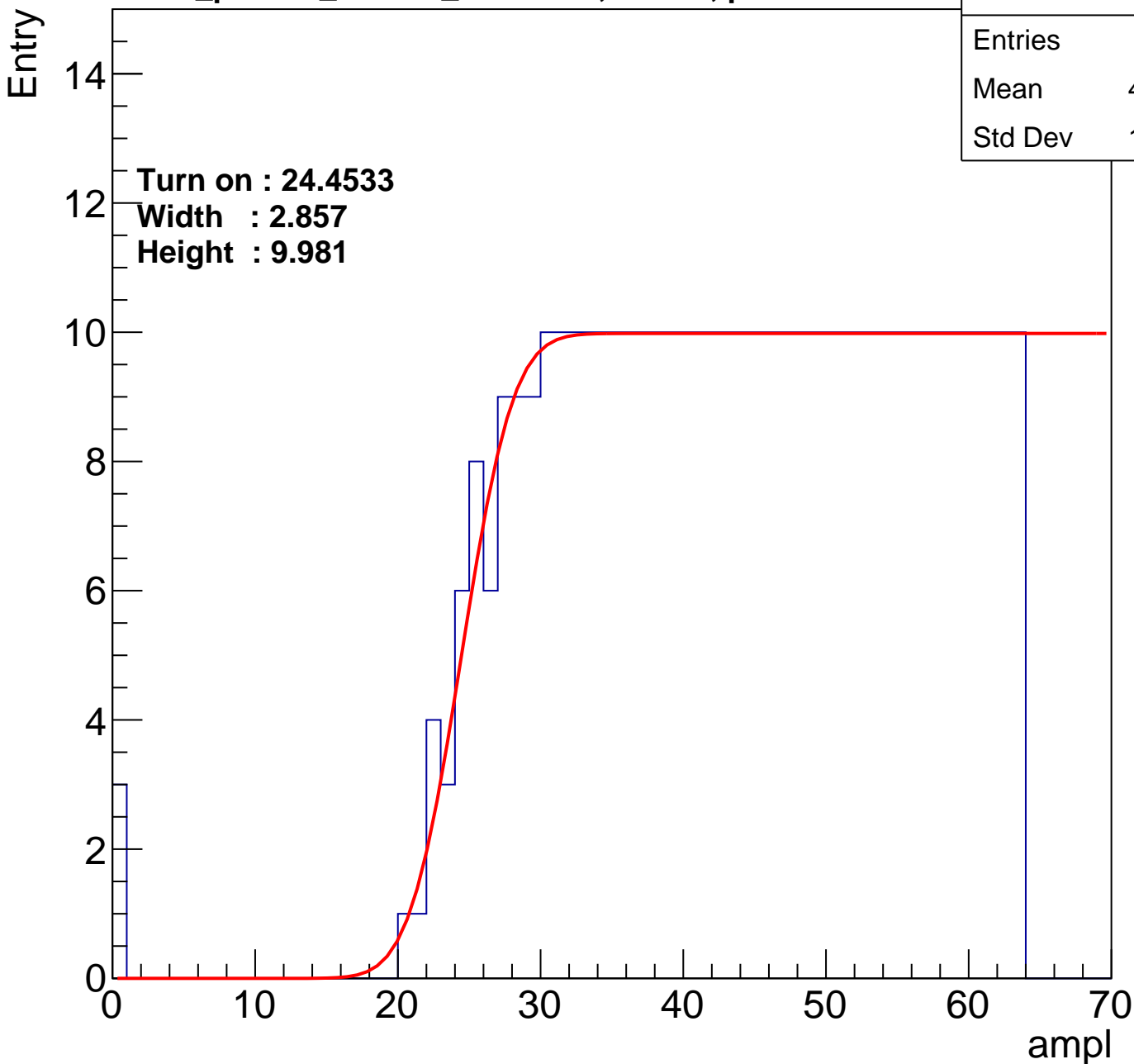
calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.27
Std Dev	12.16

Turn on : 24.4533

Width : 2.857

Height : 9.981



B0L102S, U16-ch84

calib_packv5_042523_0143.root, FC#12, port B1

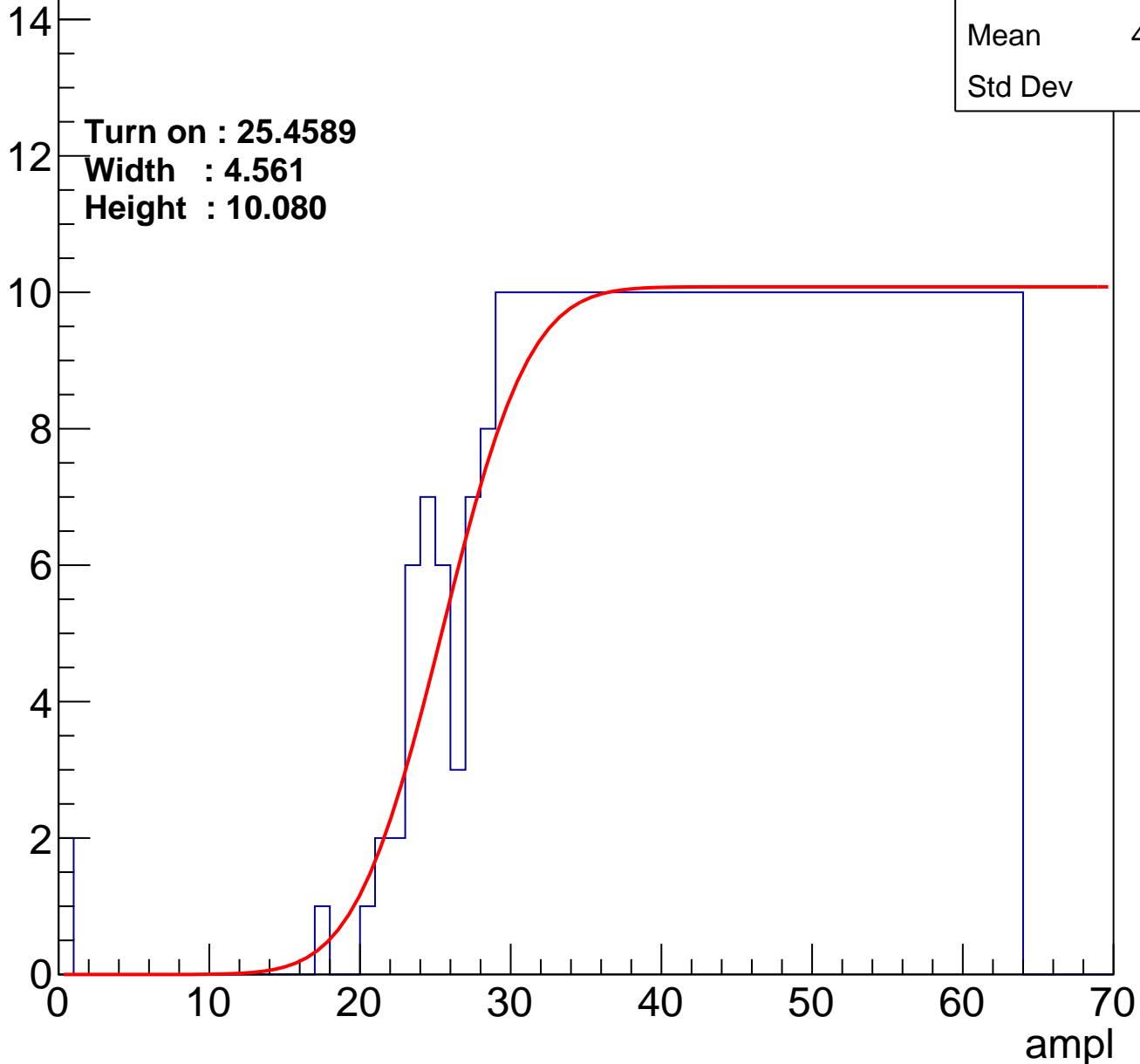
Entries	395
Mean	43.47
Std Dev	12

Turn on : 25.4589

Width : 4.561

Height : 10.080

Entry



B0L102S, U16-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.77
Std Dev	12.39

Turn on : 23.9533

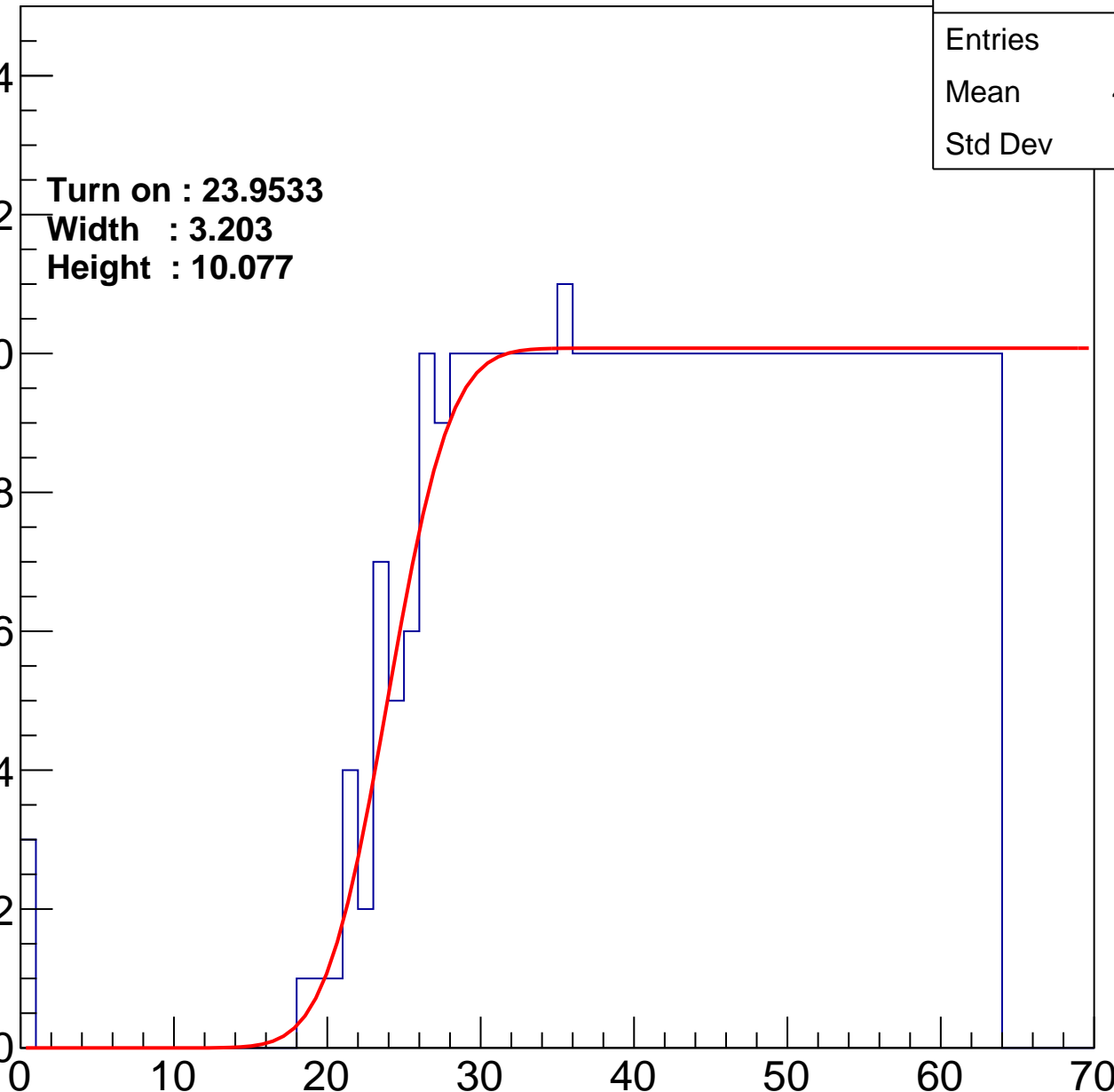
Width : 3.203

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.53
Std Dev	11.38

Turn on : 27.0183

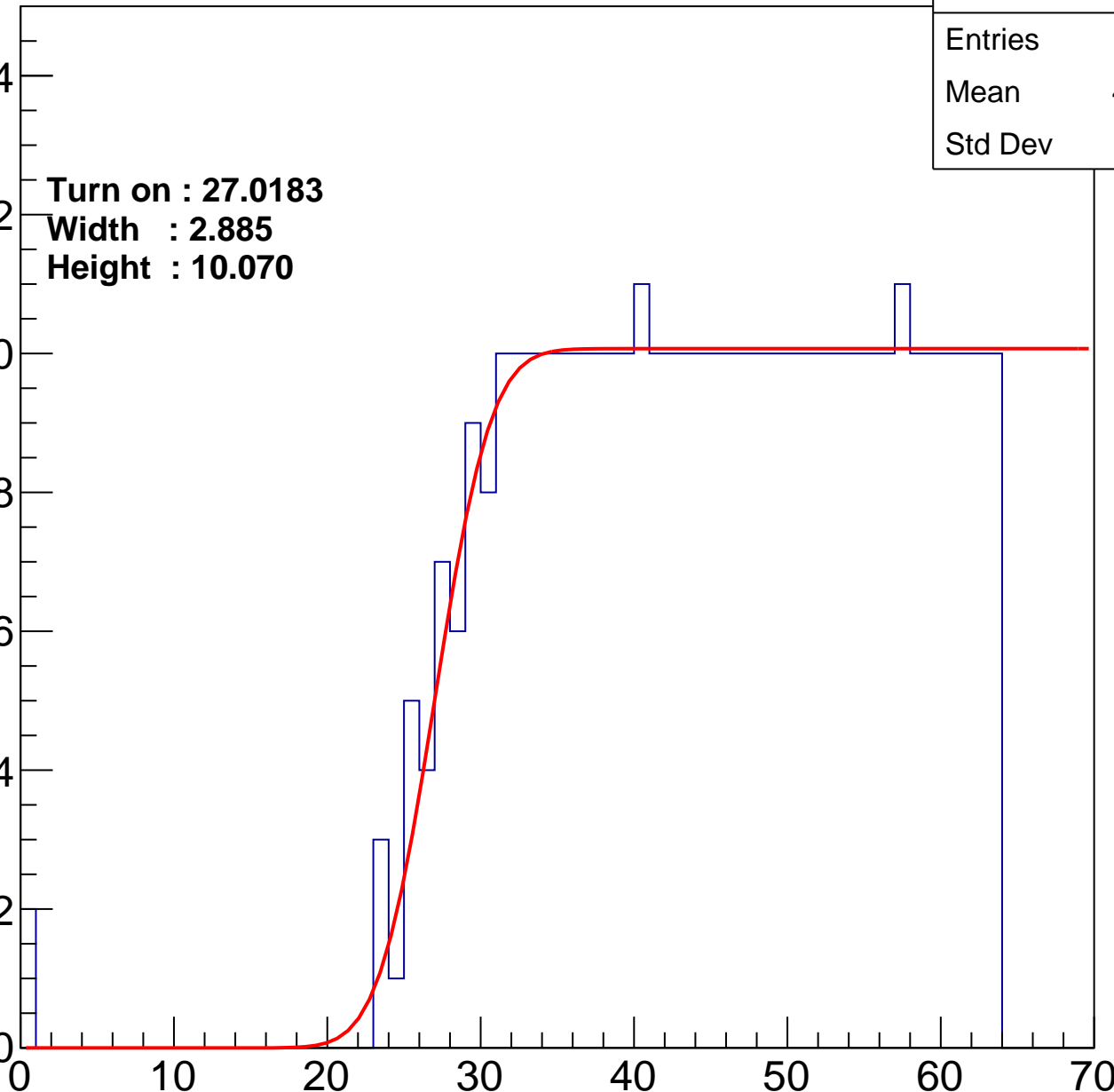
Width : 2.885

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch87

calib_packv5_042523_0143.root, FC#12, port B1

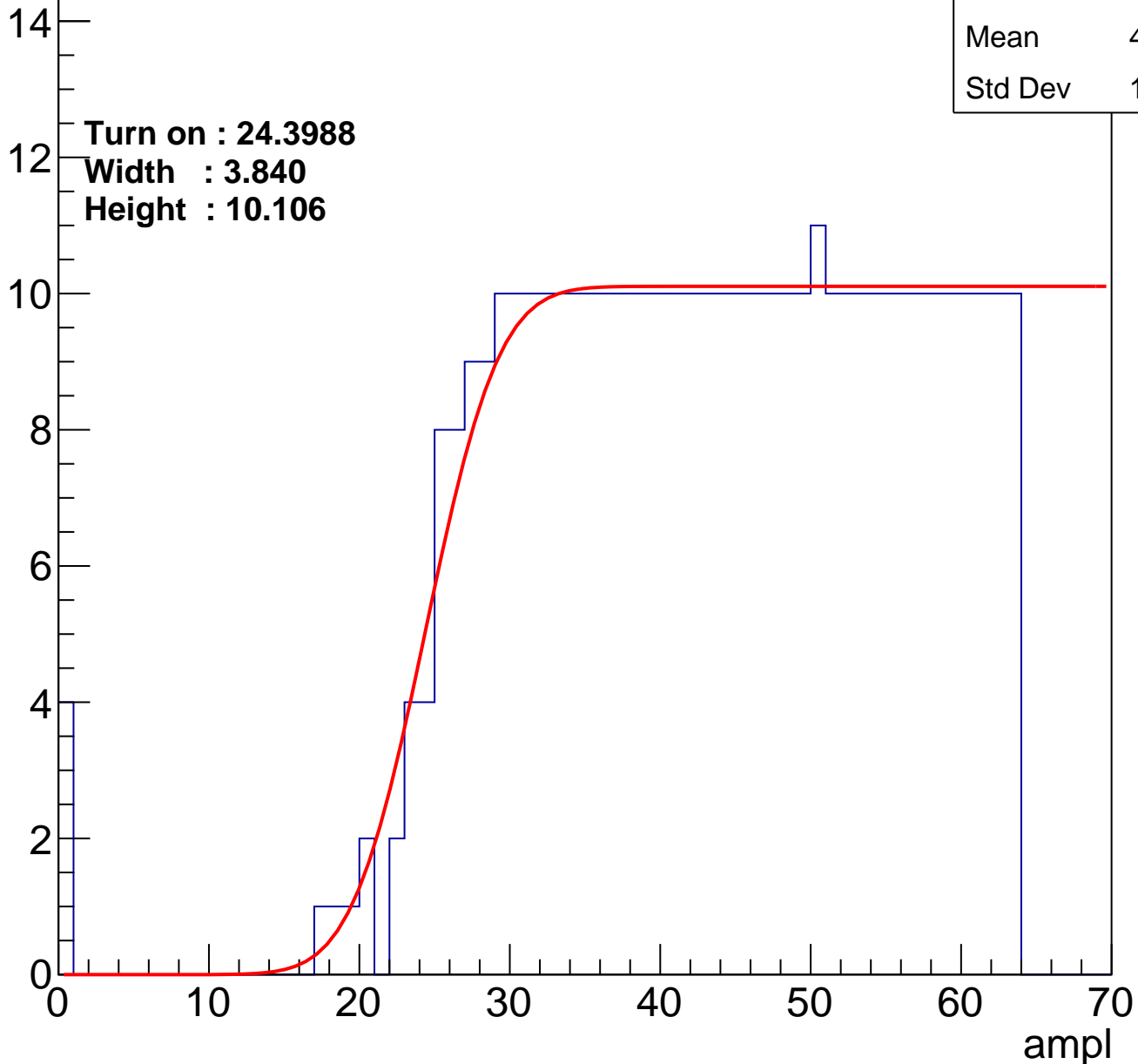
Entries	404
Mean	43.02
Std Dev	12.43

Turn on : 24.3988

Width : 3.840

Height : 10.106

Entry



B0L102S, U16-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.25
Std Dev	12.03

Turn on : 24.3949

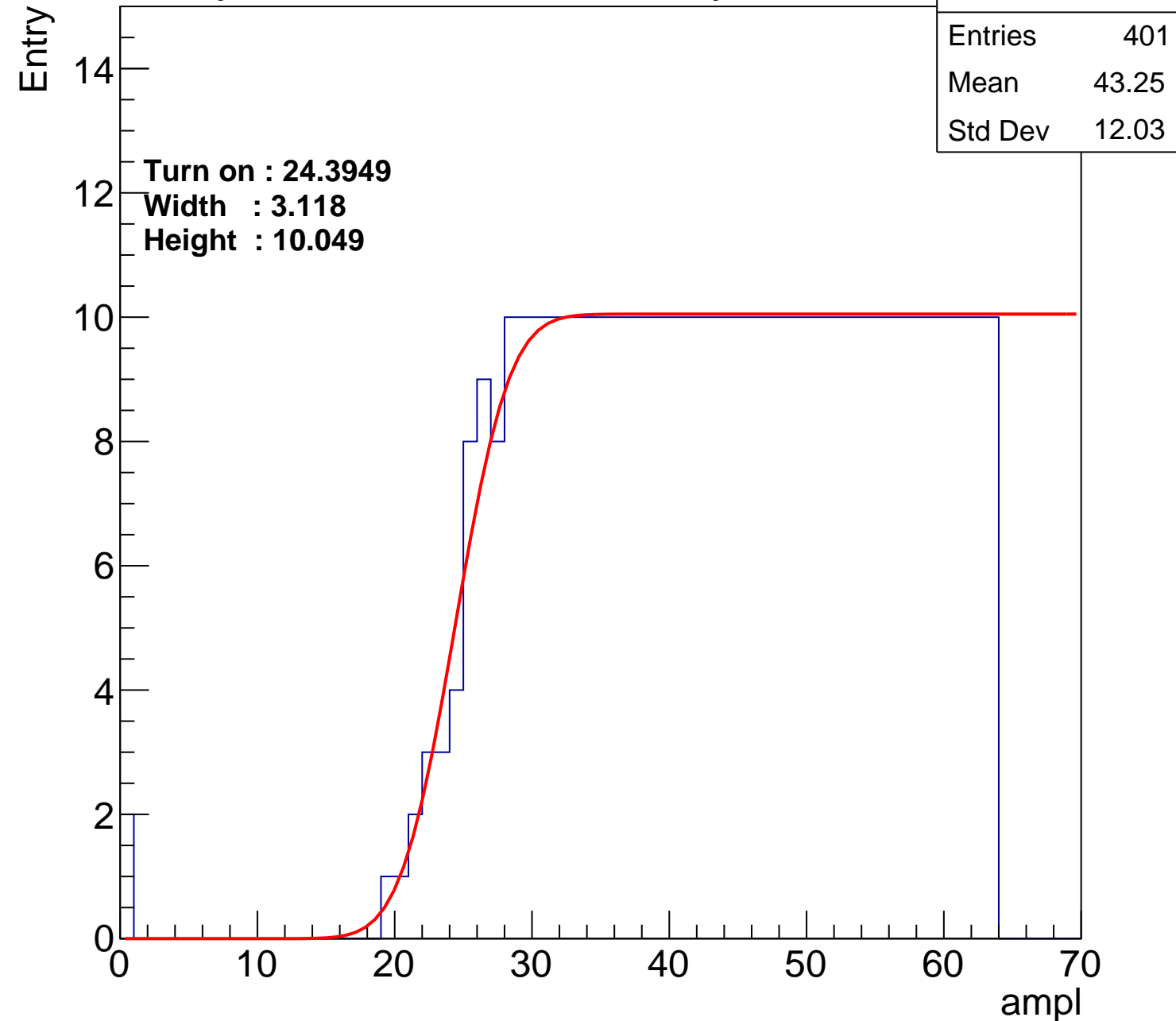
Width : 3.118

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.81
Std Dev	12.42

Turn on : 23.8468

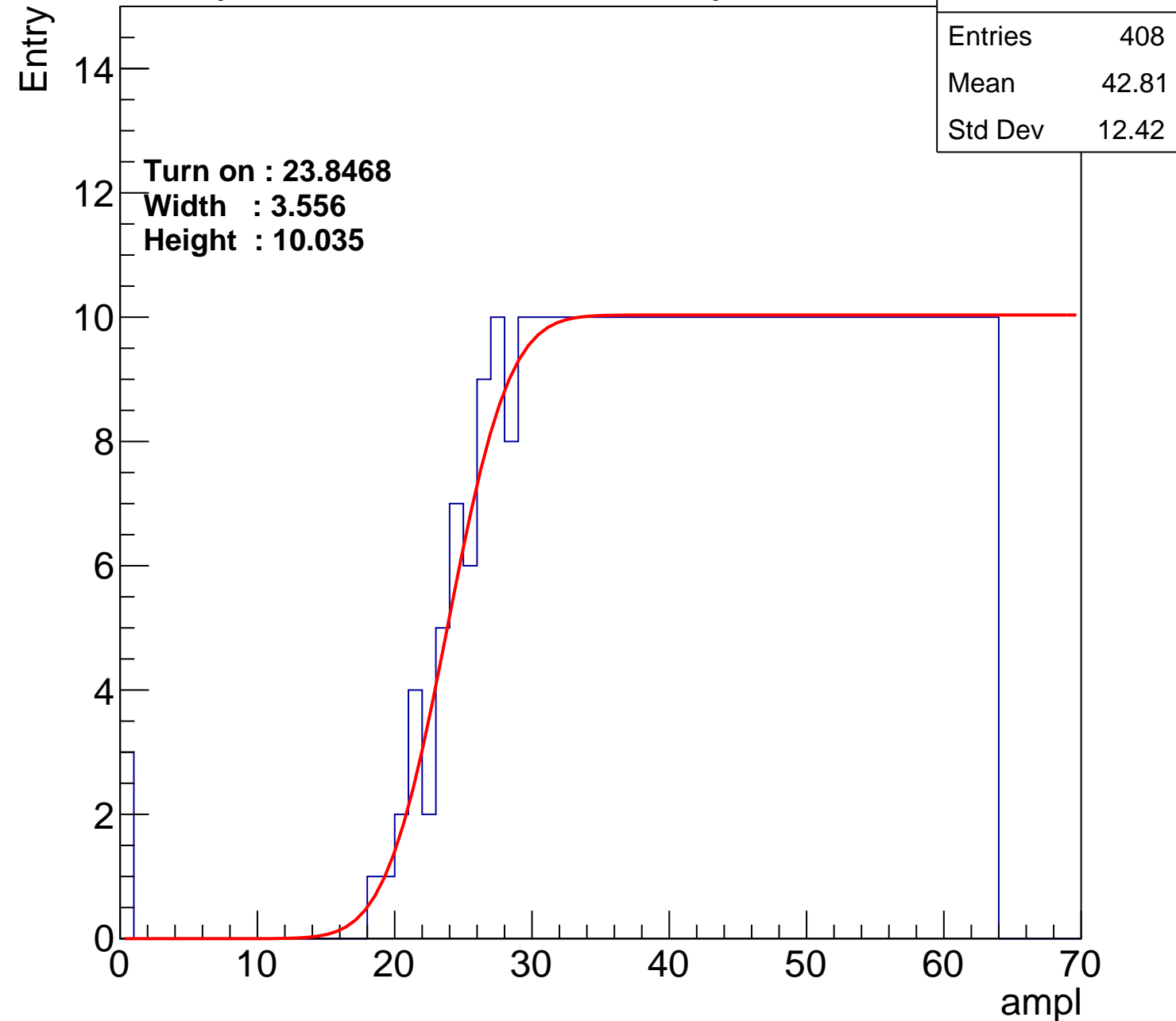
Width : 3.556

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch90

calib_packv5_042523_0143.root, FC#12, port B1

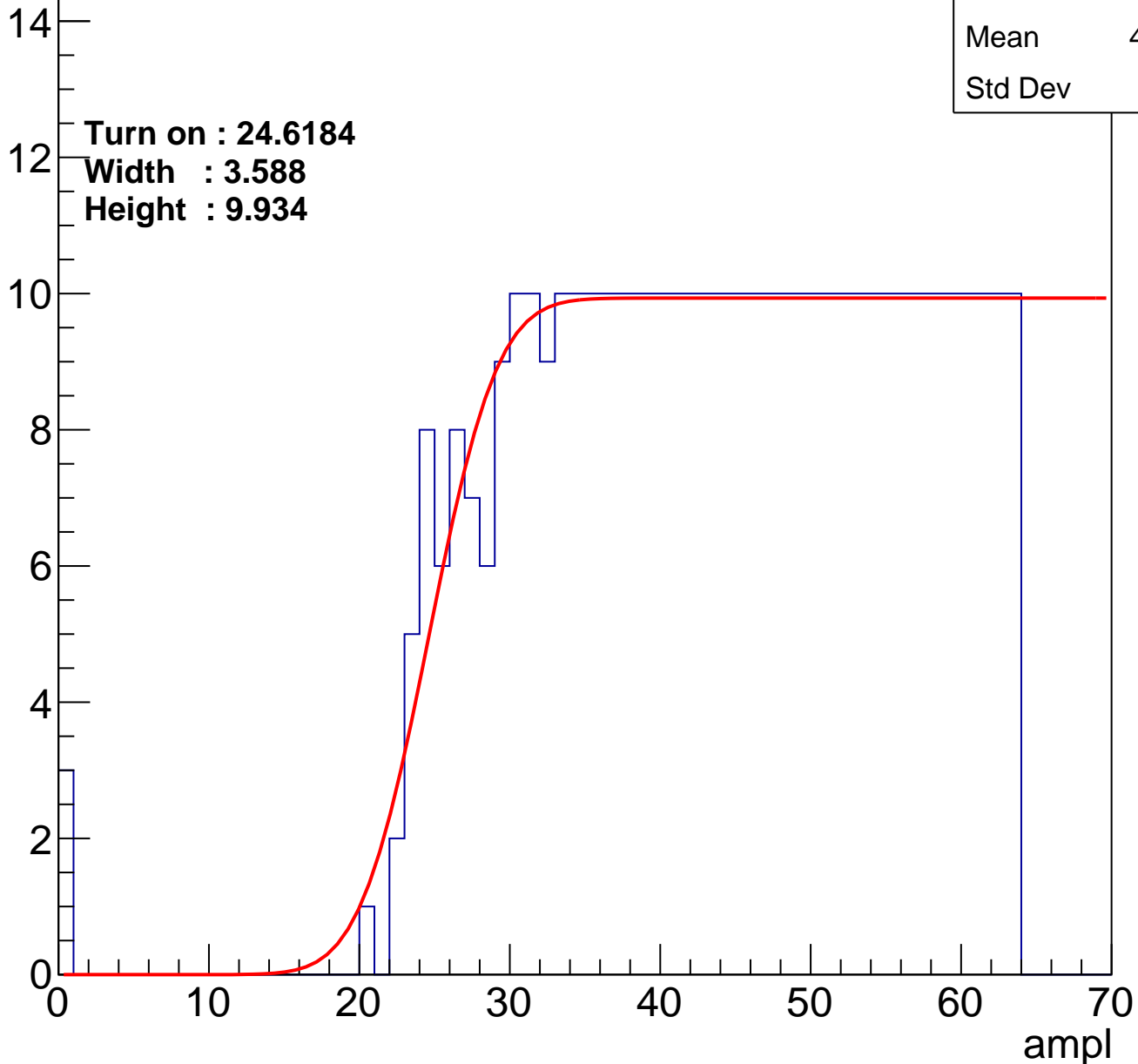
Entries	394
Mean	43.46
Std Dev	12.1

Turn on : 24.6184

Width : 3.588

Height : 9.934

Entry



B0L102S, U16-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.46
Std Dev	11.79

Turn on : 24.6739

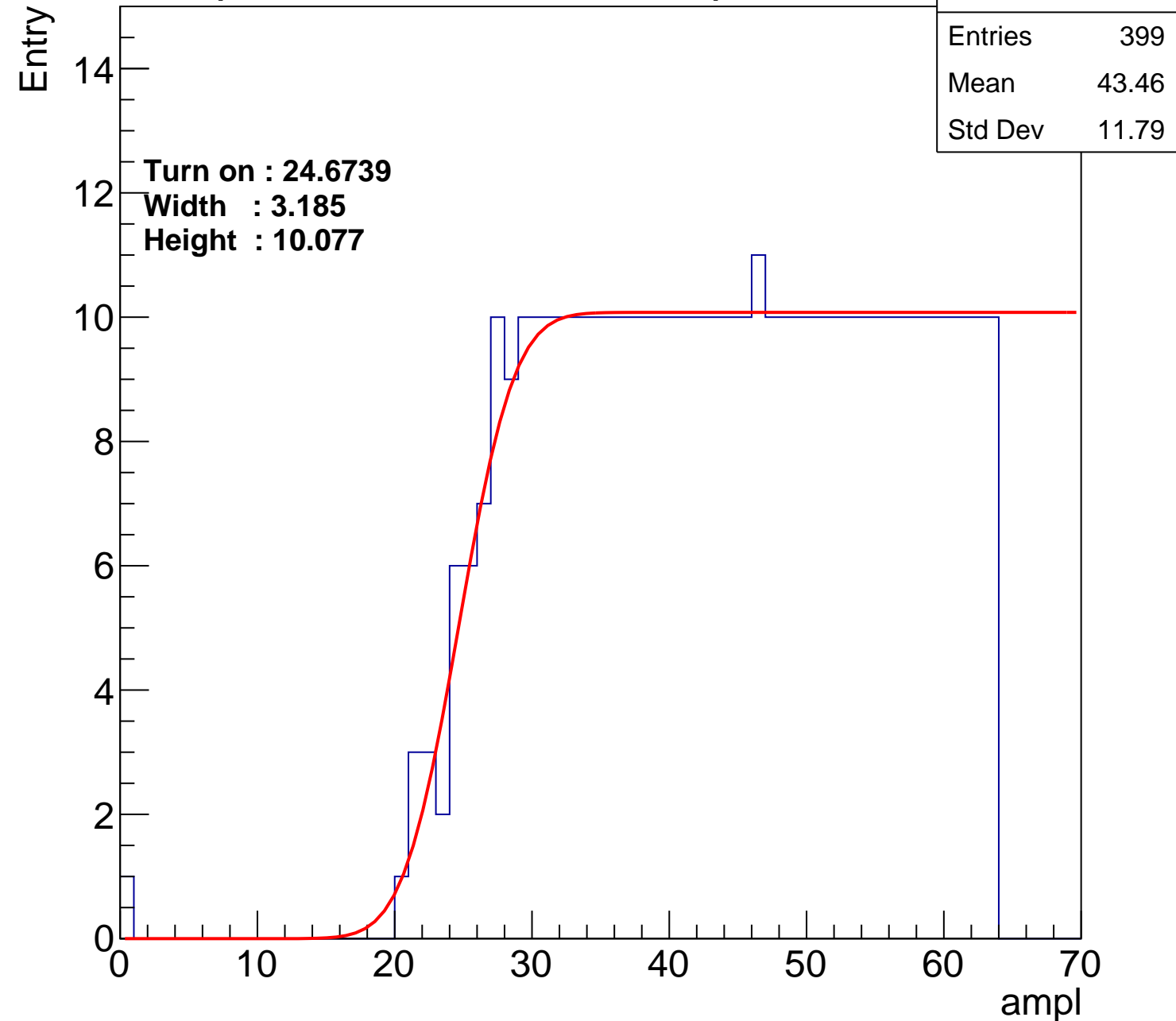
Width : 3.185

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.51
Std Dev	12.21

Turn on : 25.3507

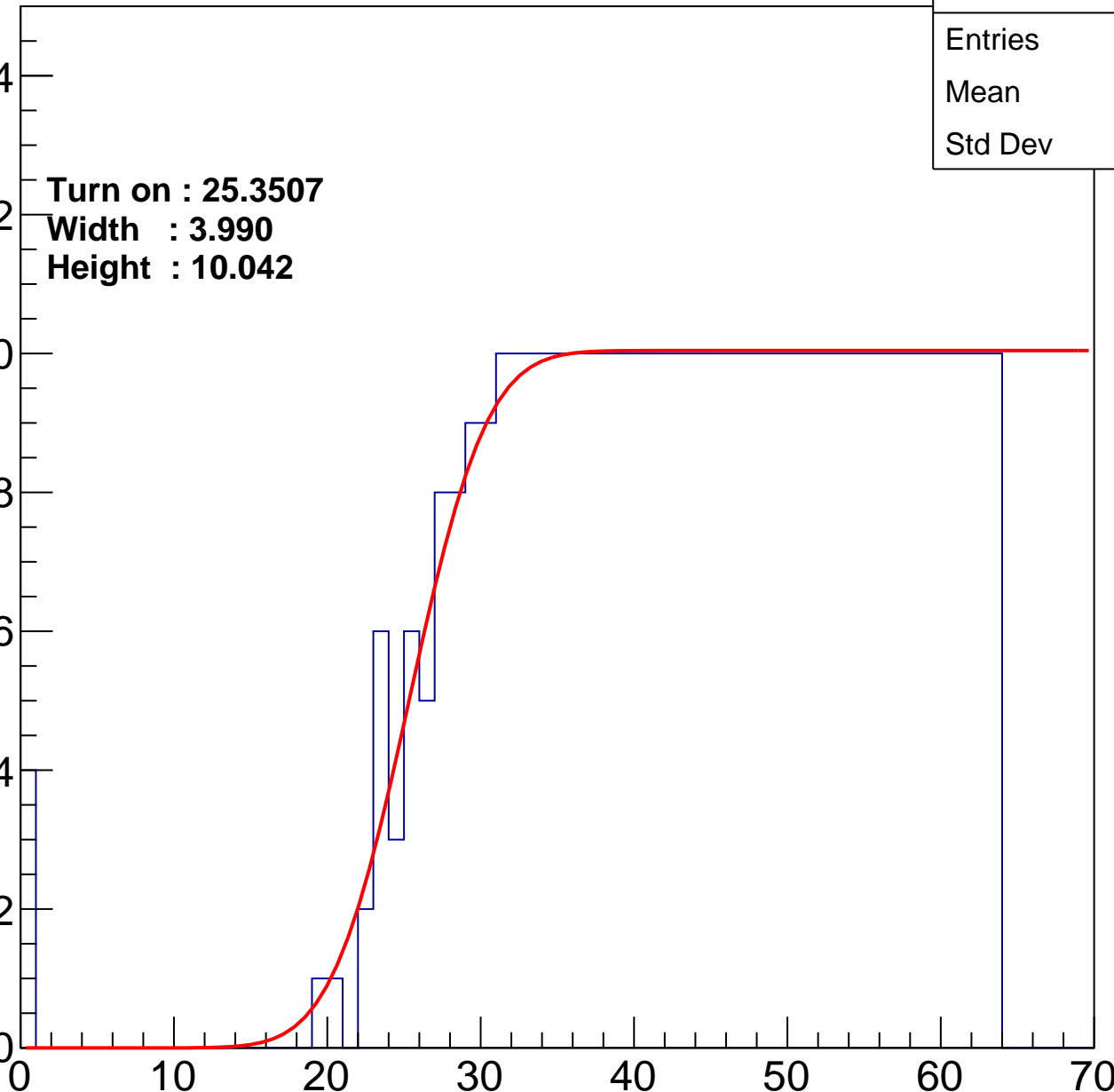
Width : 3.990

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.61
Std Dev	12.51

Turn on : 26.3110

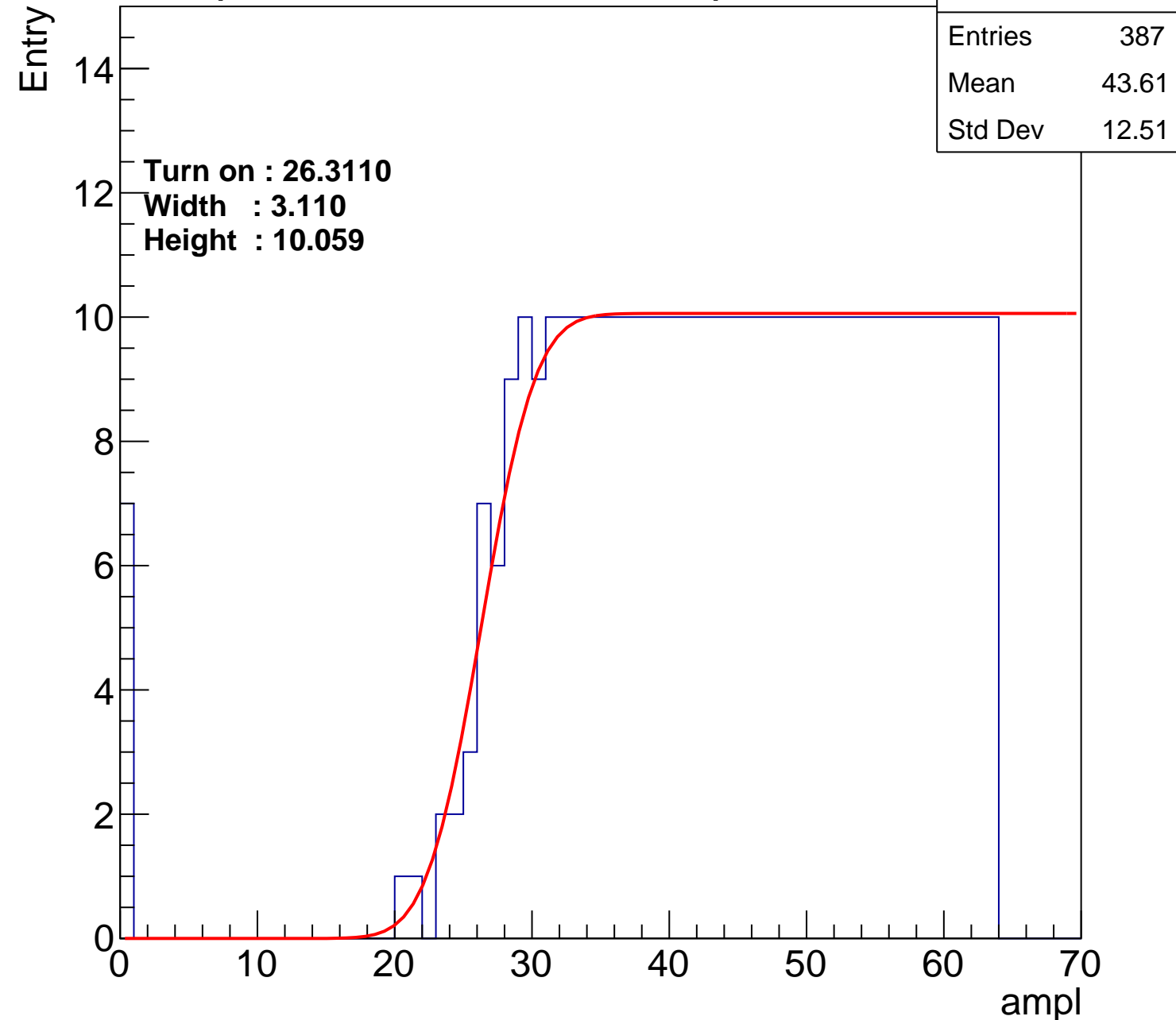
Width : 3.110

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.29
Std Dev	11.39

Turn on : 26.5859

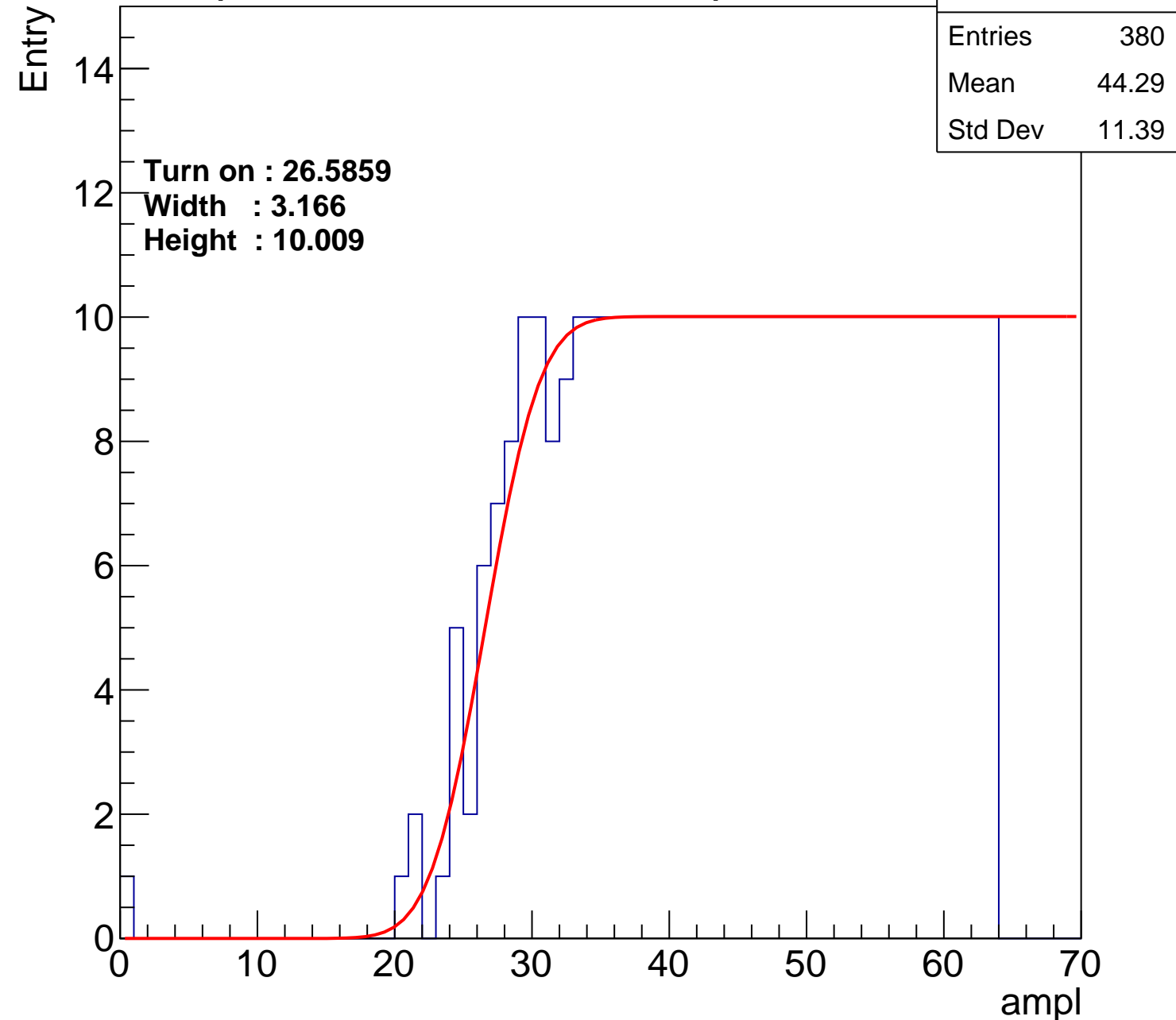
Width : 3.166

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.22
Std Dev	11.63

Turn on : 26.1881

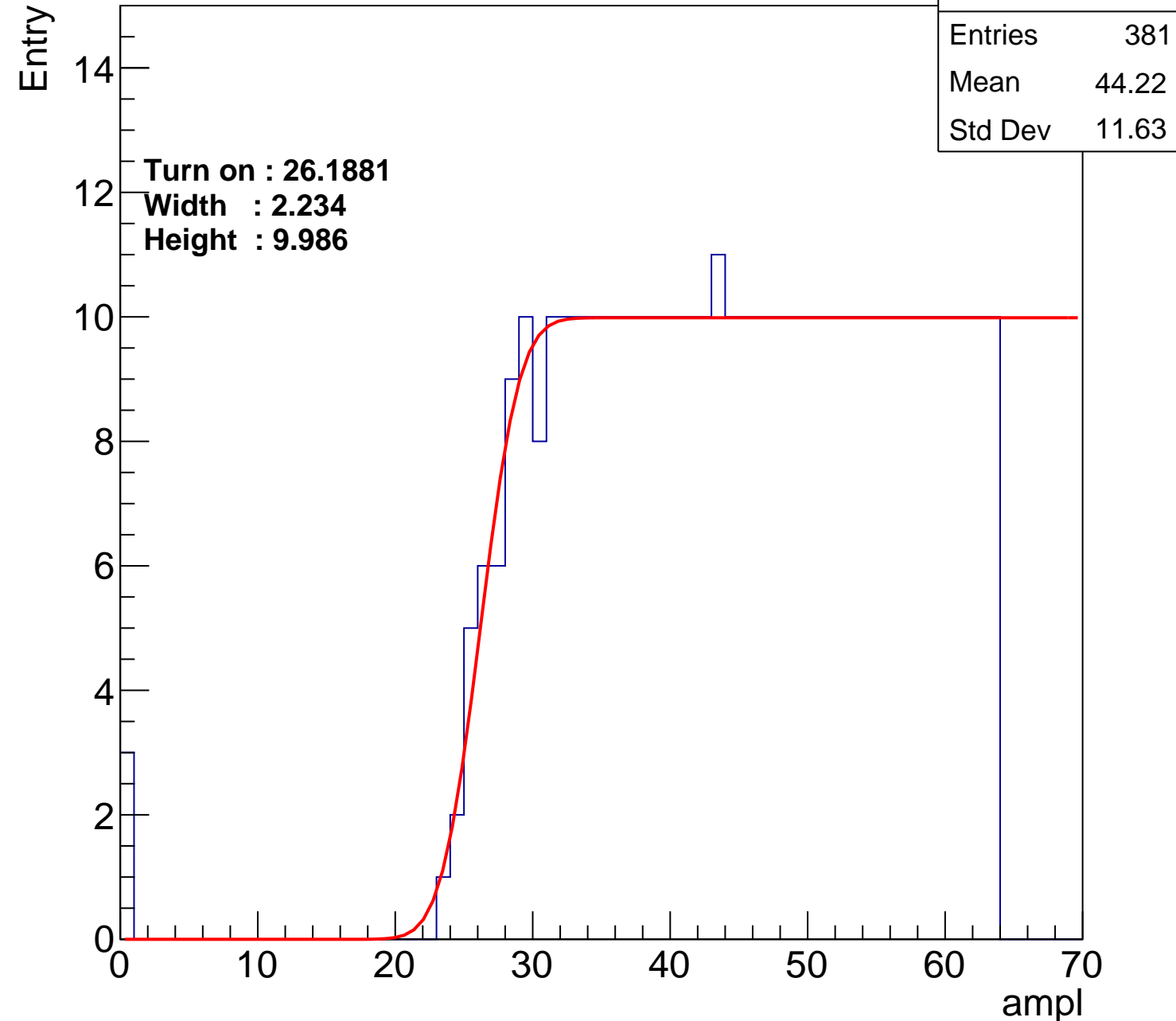
Width : 2.234

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.66
Std Dev	11.84

Turn on : 25.1048

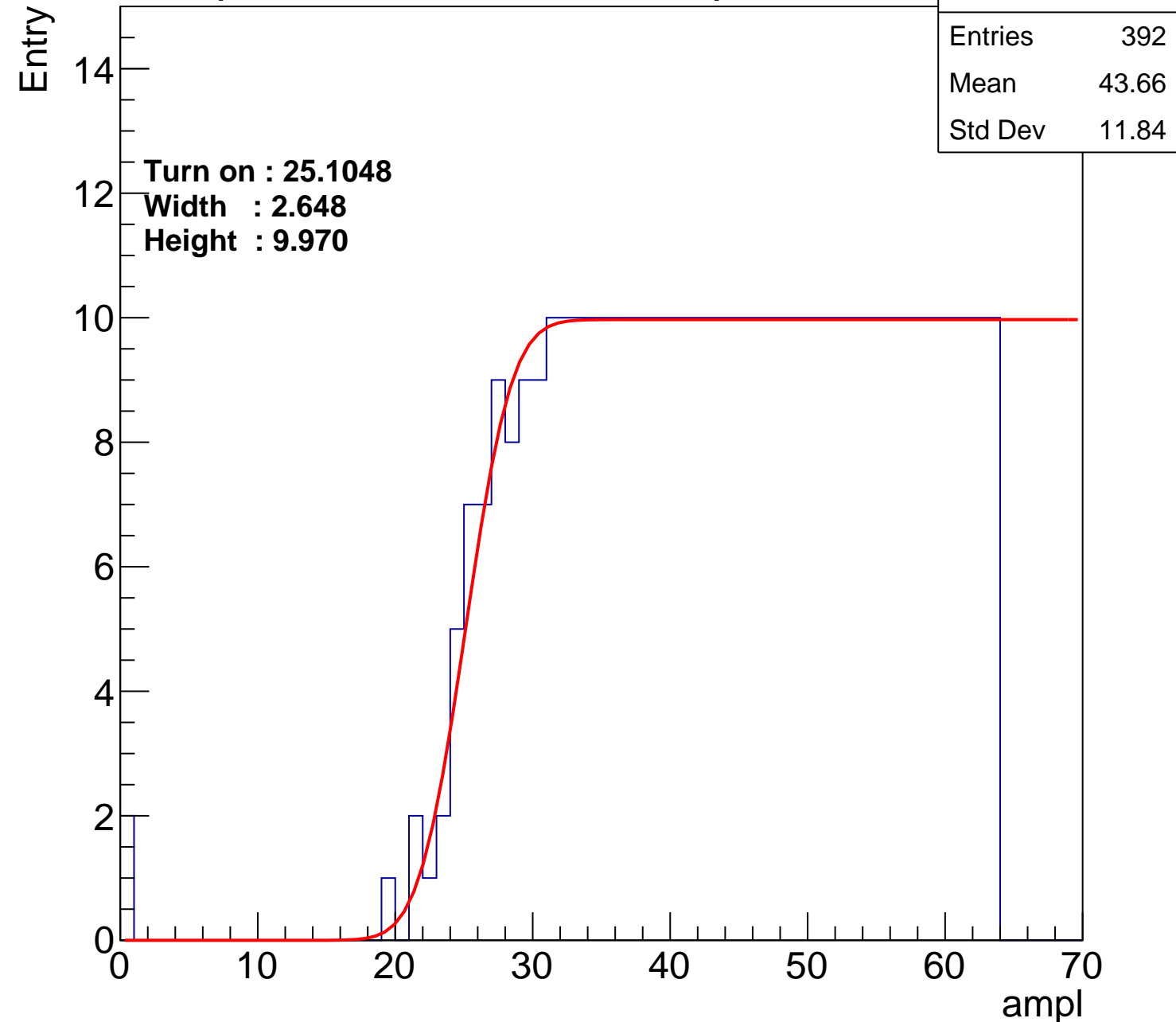
Width : 2.648

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.69
Std Dev	12.07

Turn on : 25.6225

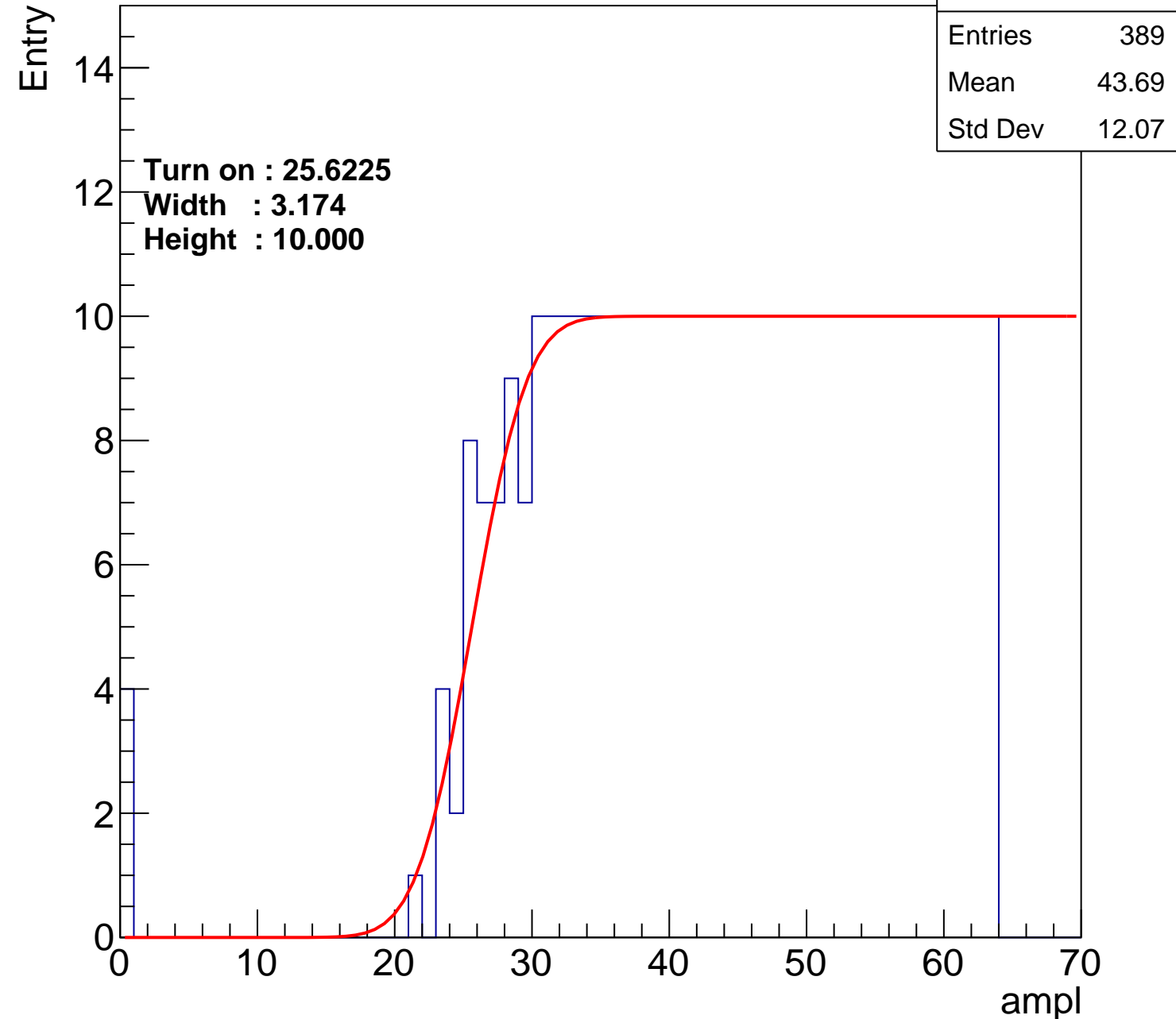
Width : 3.174

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.1
Std Dev	12.25

Turn on : 23.9094

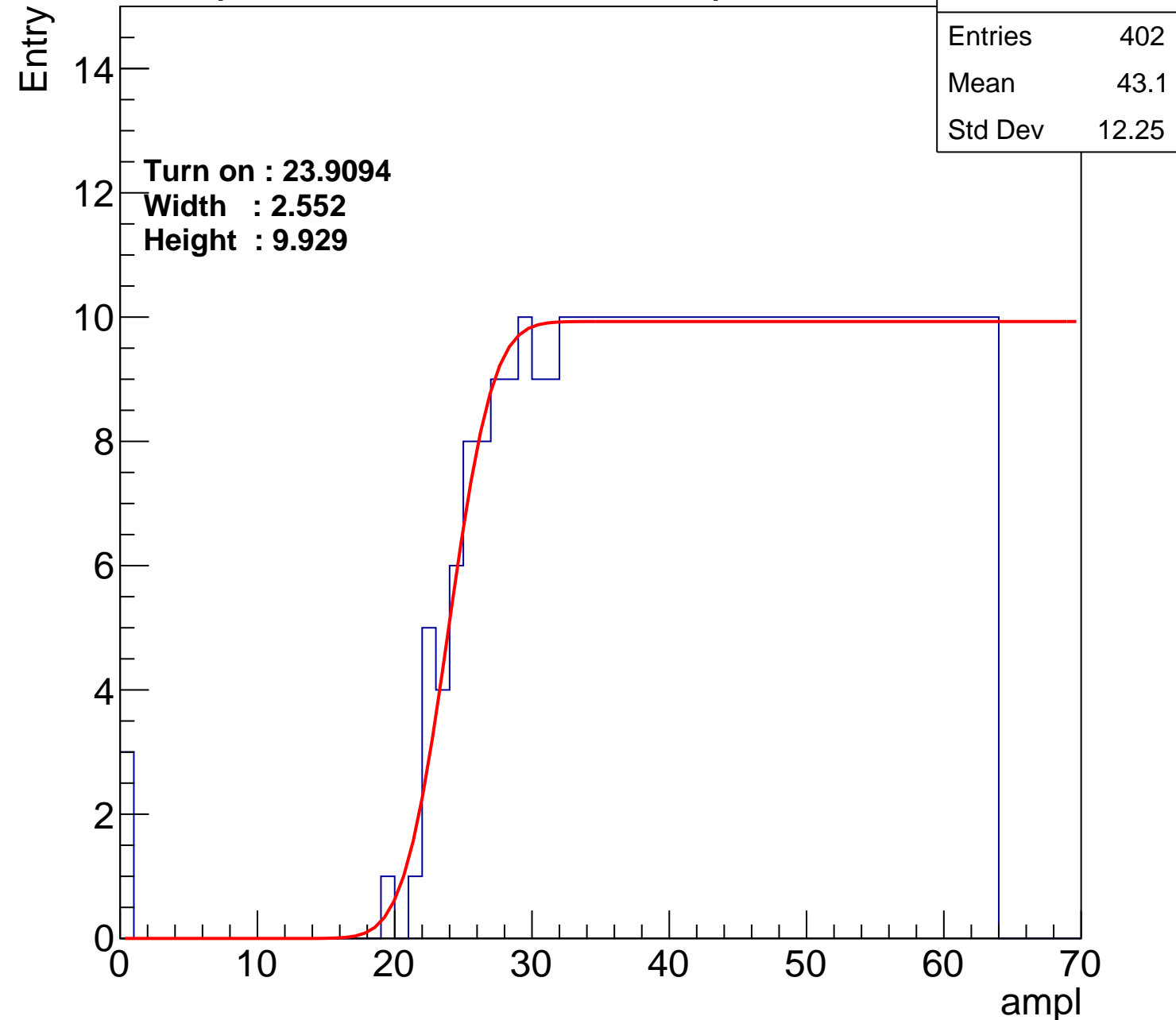
Width : 2.552

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.4135

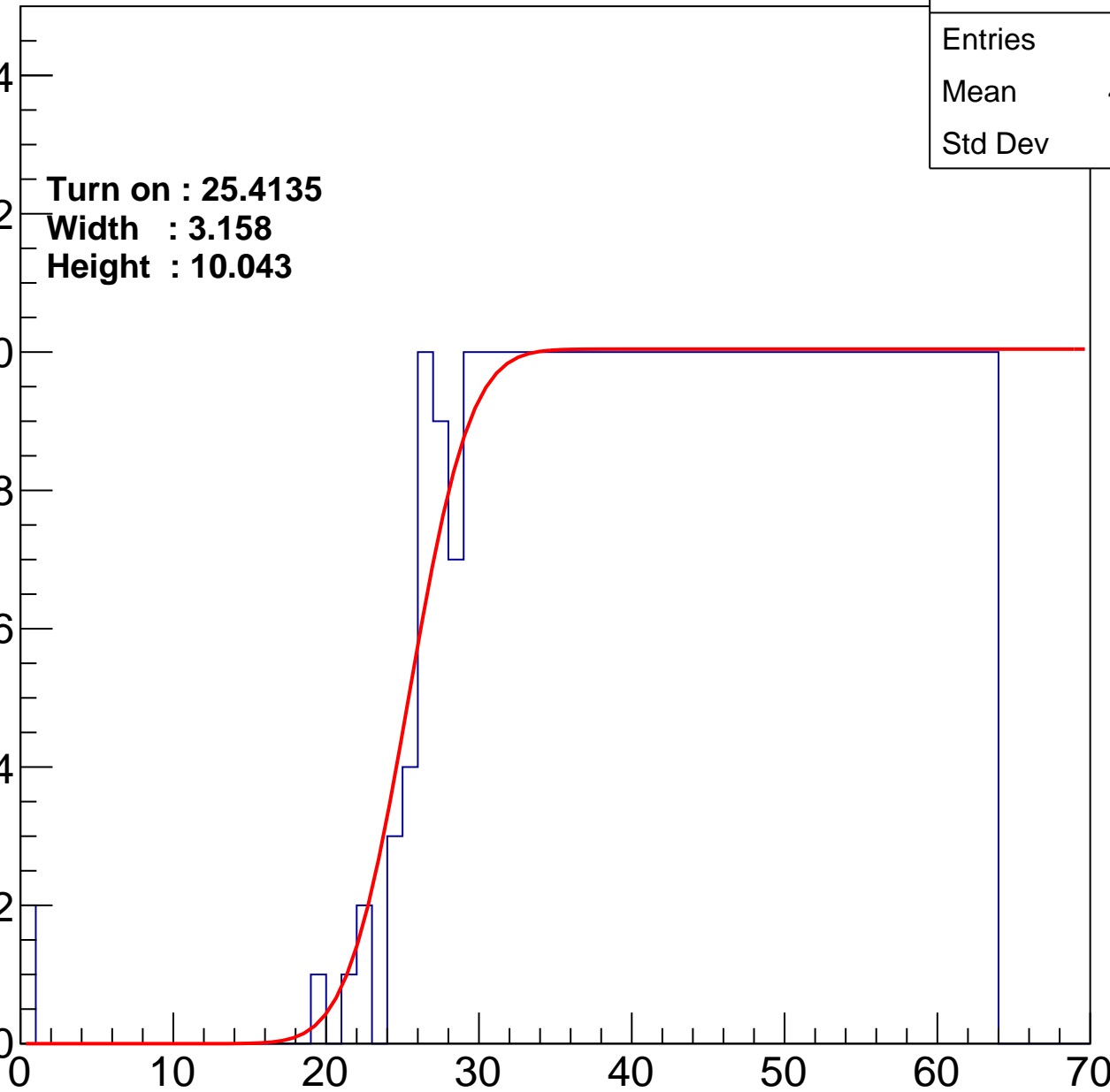
Width : 3.158

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	42.91
Std Dev	12.93

Turn on : 24.8264

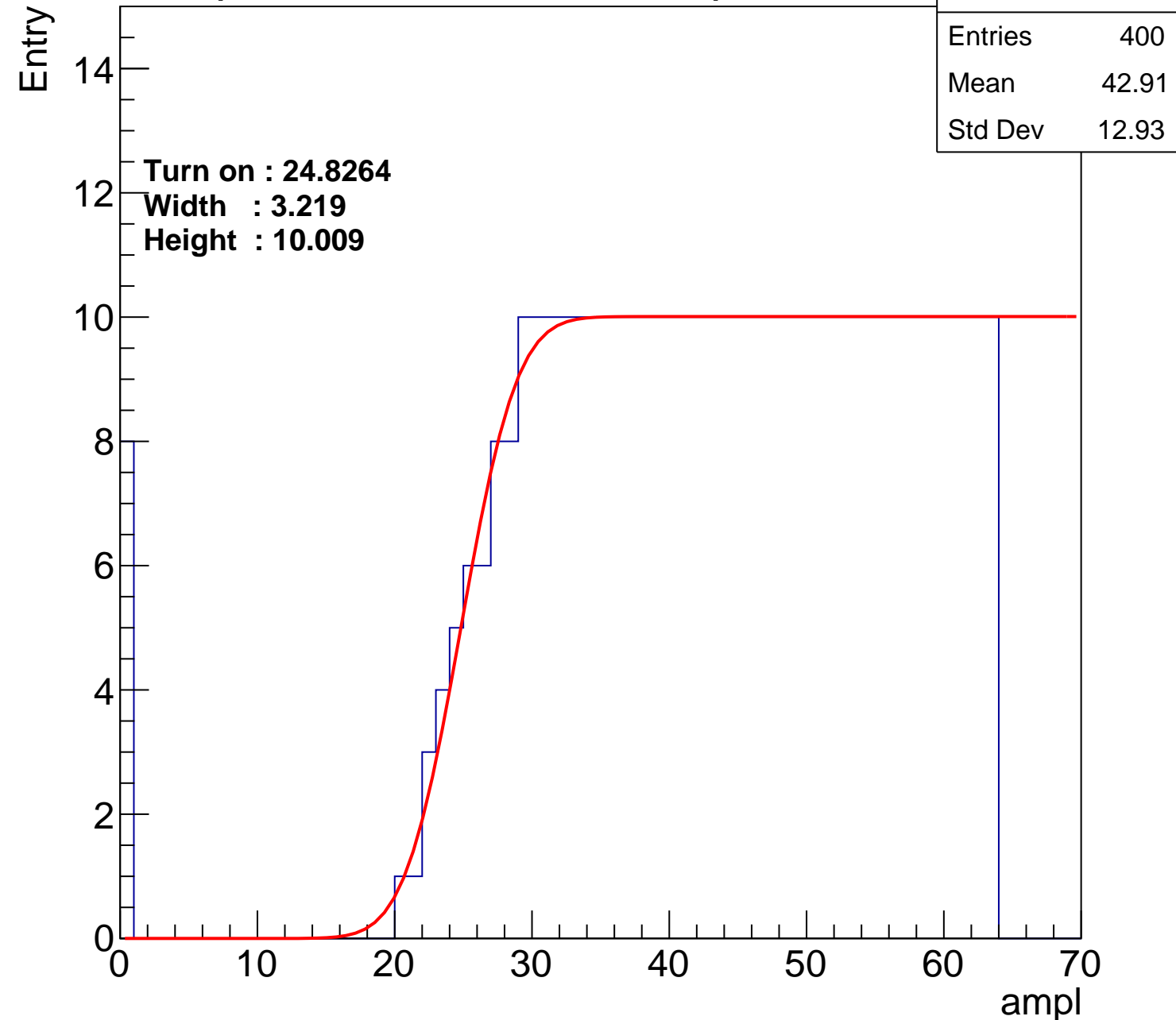
Width : 3.219

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.1
Std Dev	12.27

Turn on : 24.2498

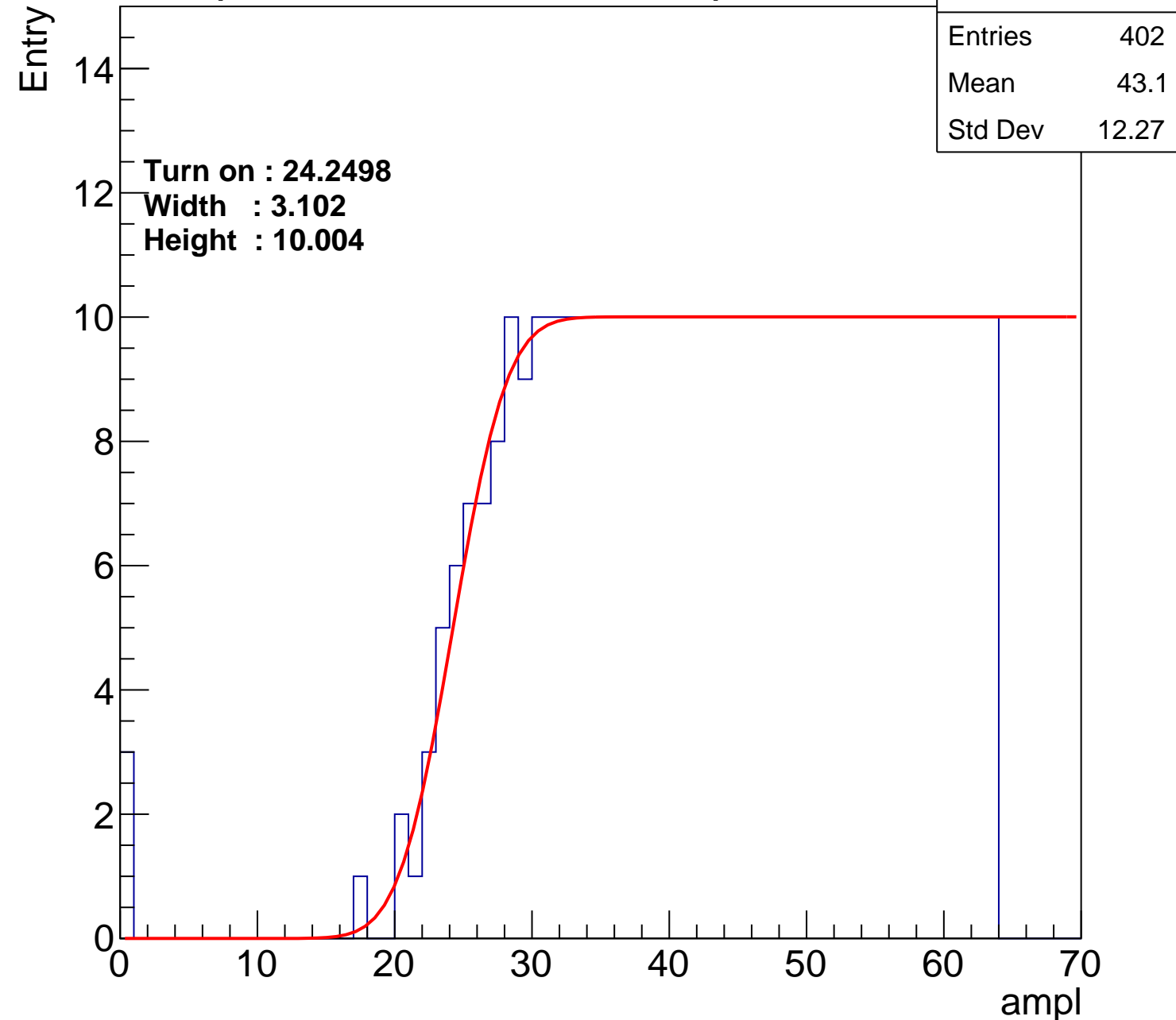
Width : 3.102

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	42.92
Std Dev	12.6

Turn on : 24.3689

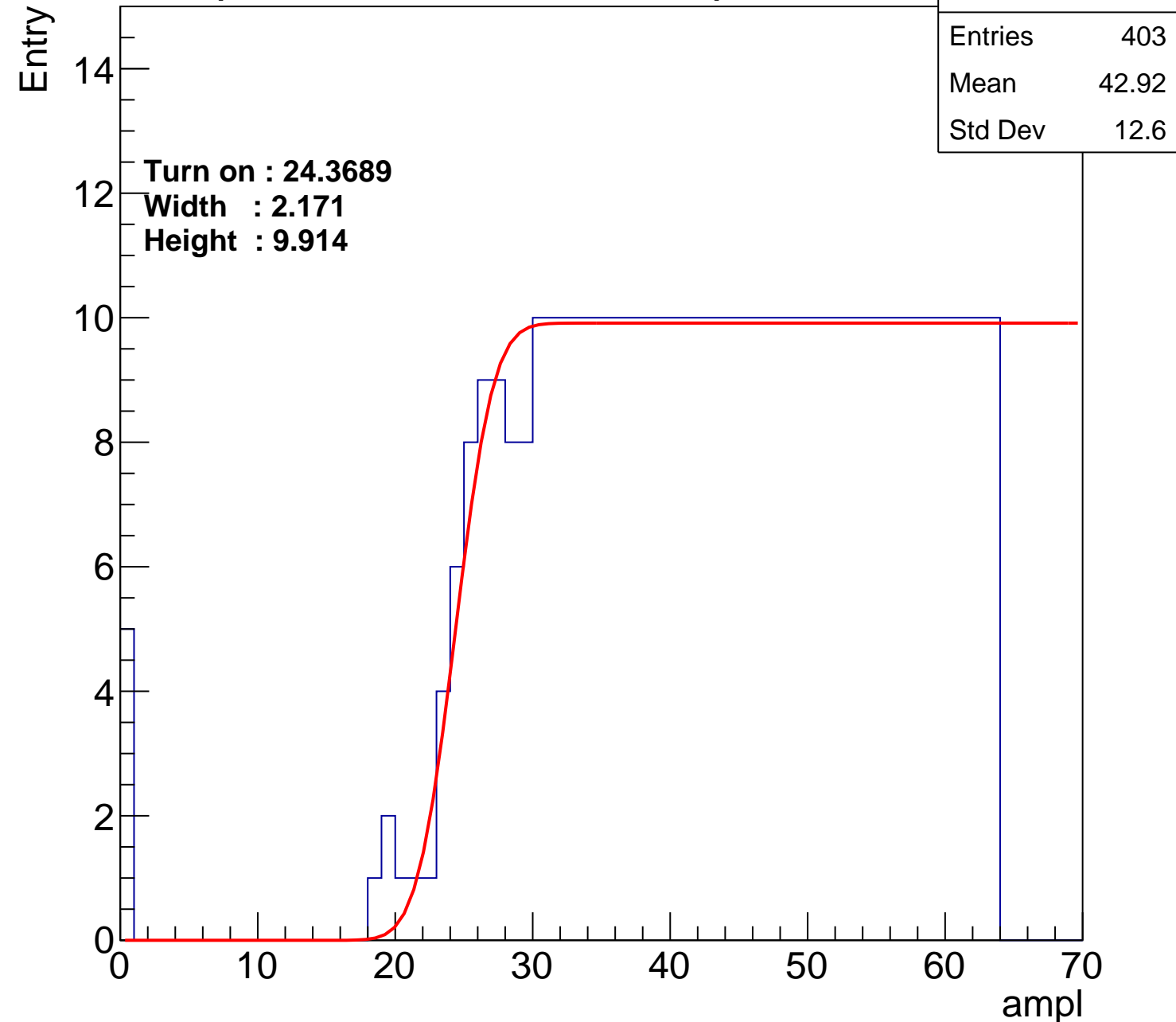
Width : 2.171

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.36
Std Dev	11.27

Turn on : 26.0584

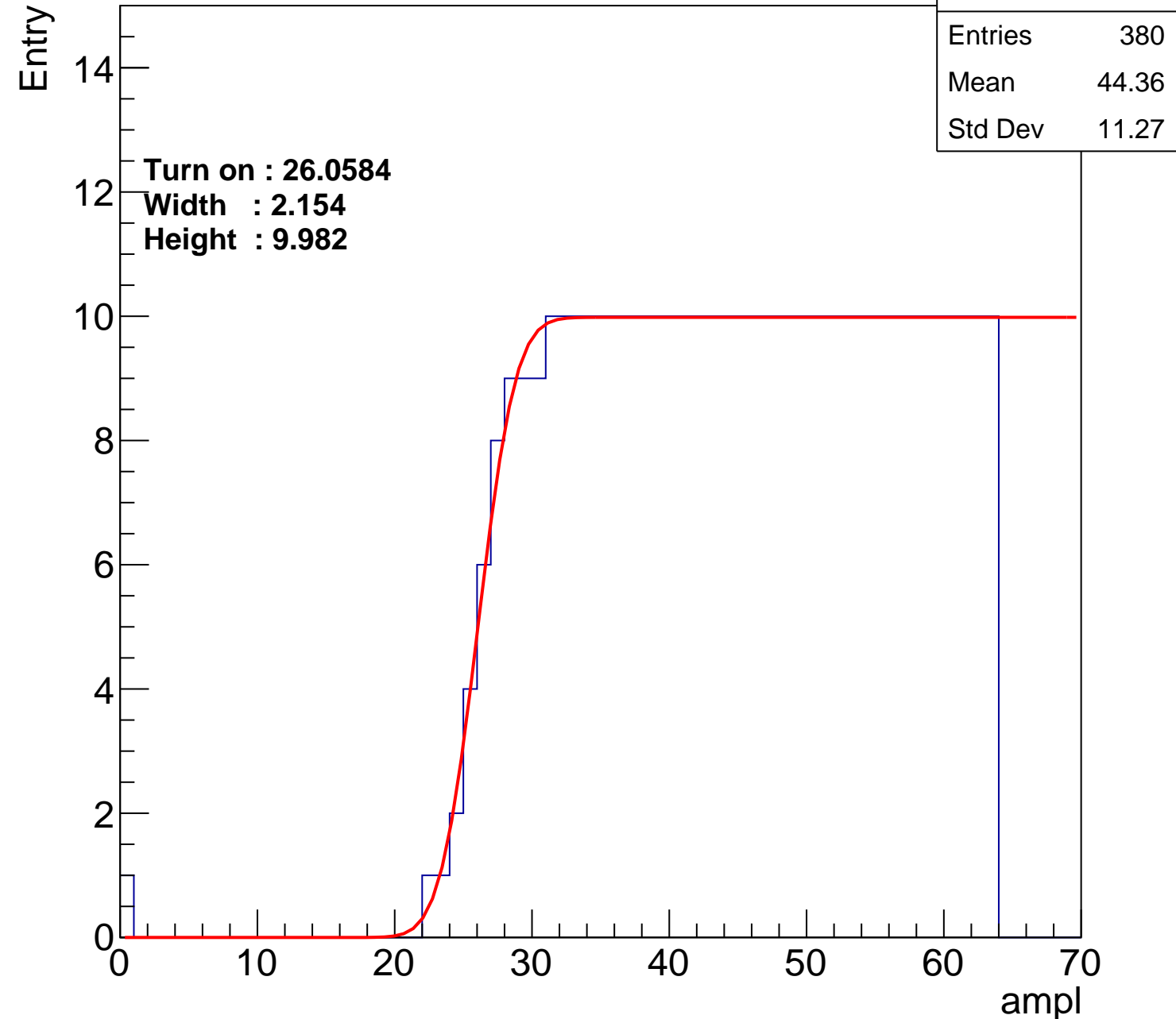
Width : 2.154

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.78
Std Dev	11.86

Turn on : 25.7748

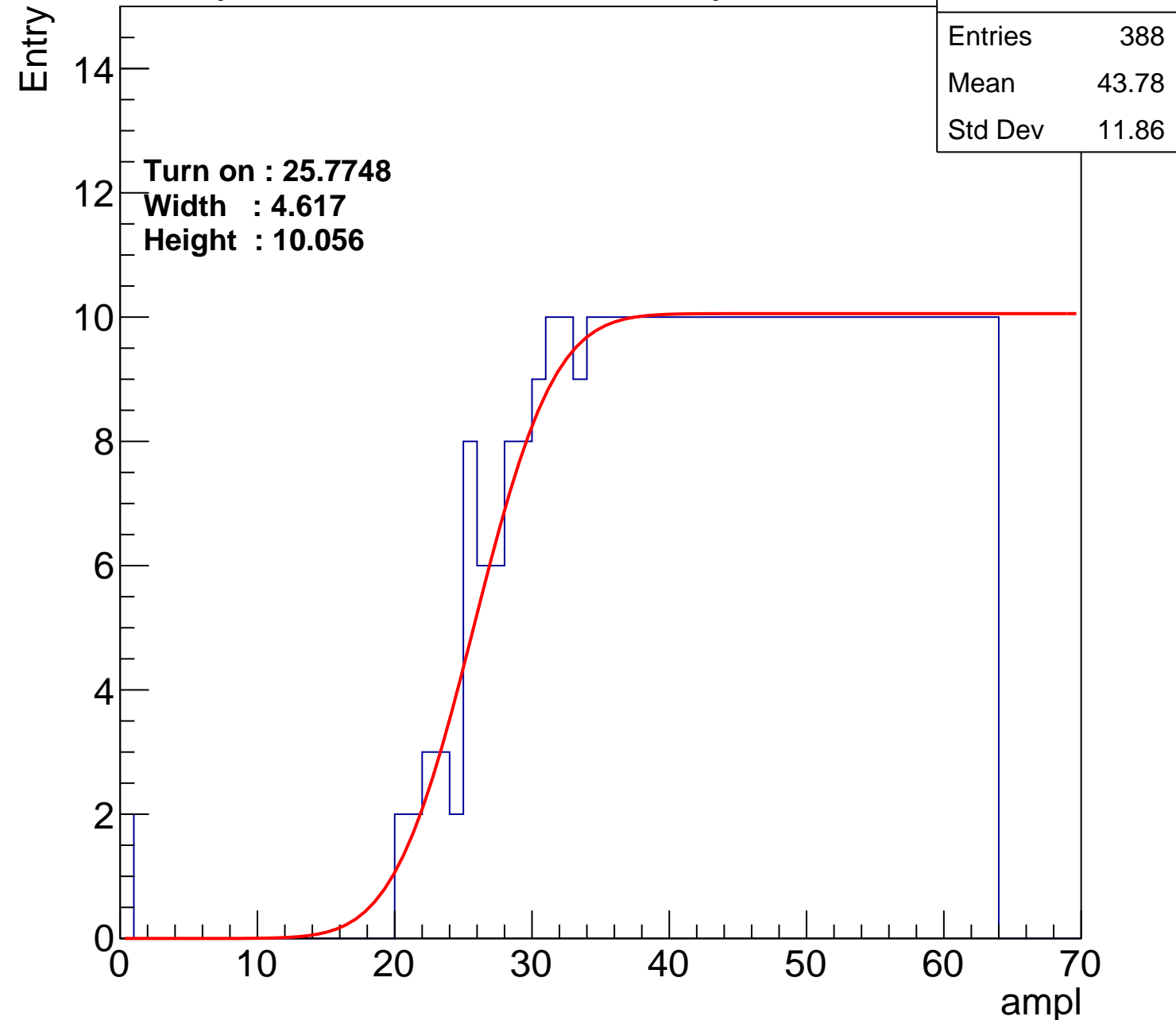
Width : 4.617

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.38
Std Dev	12.14

Turn on : 25.5840

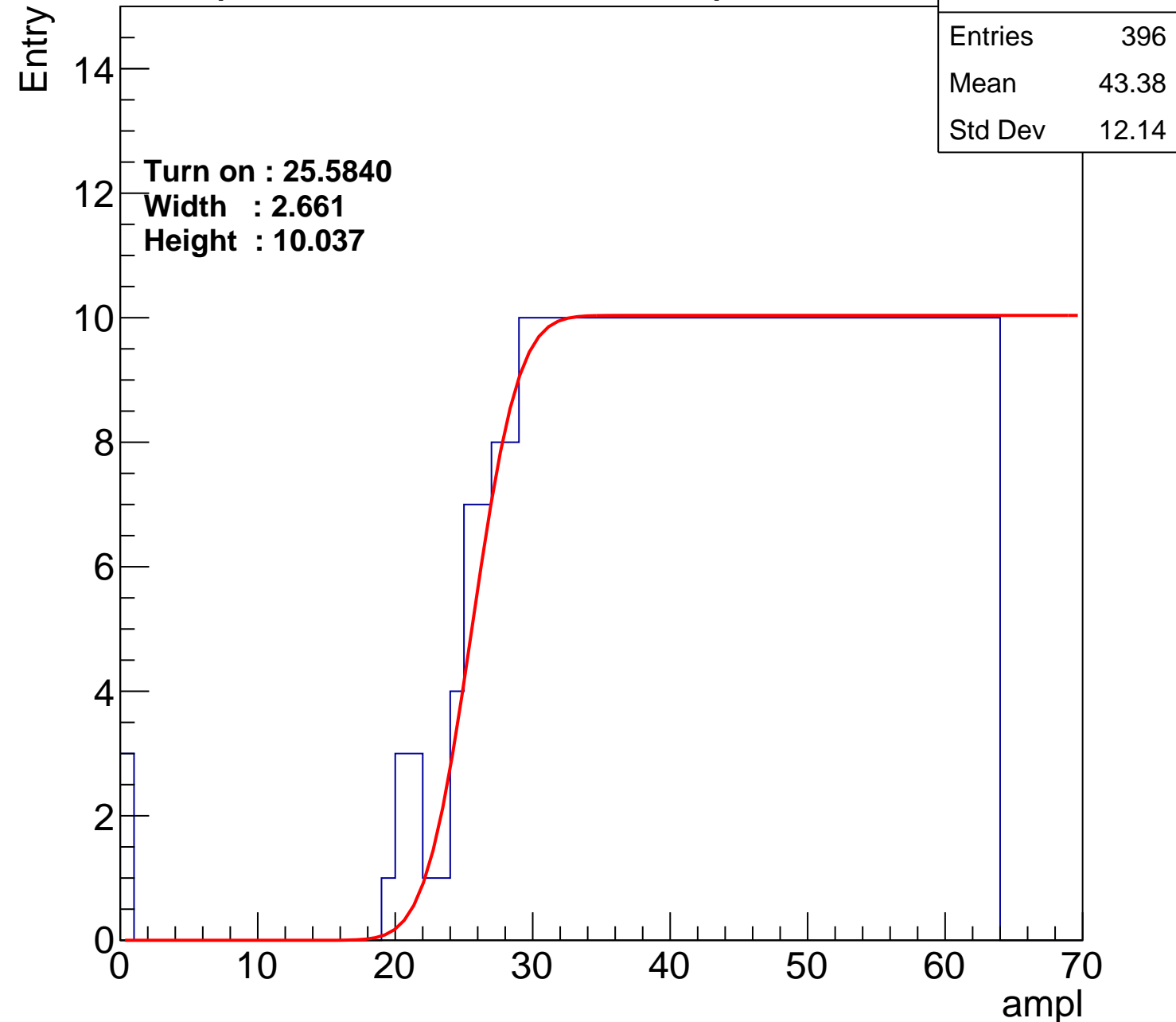
Width : 2.661

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	43.05
Std Dev	12.17

Turn on : 23.2155

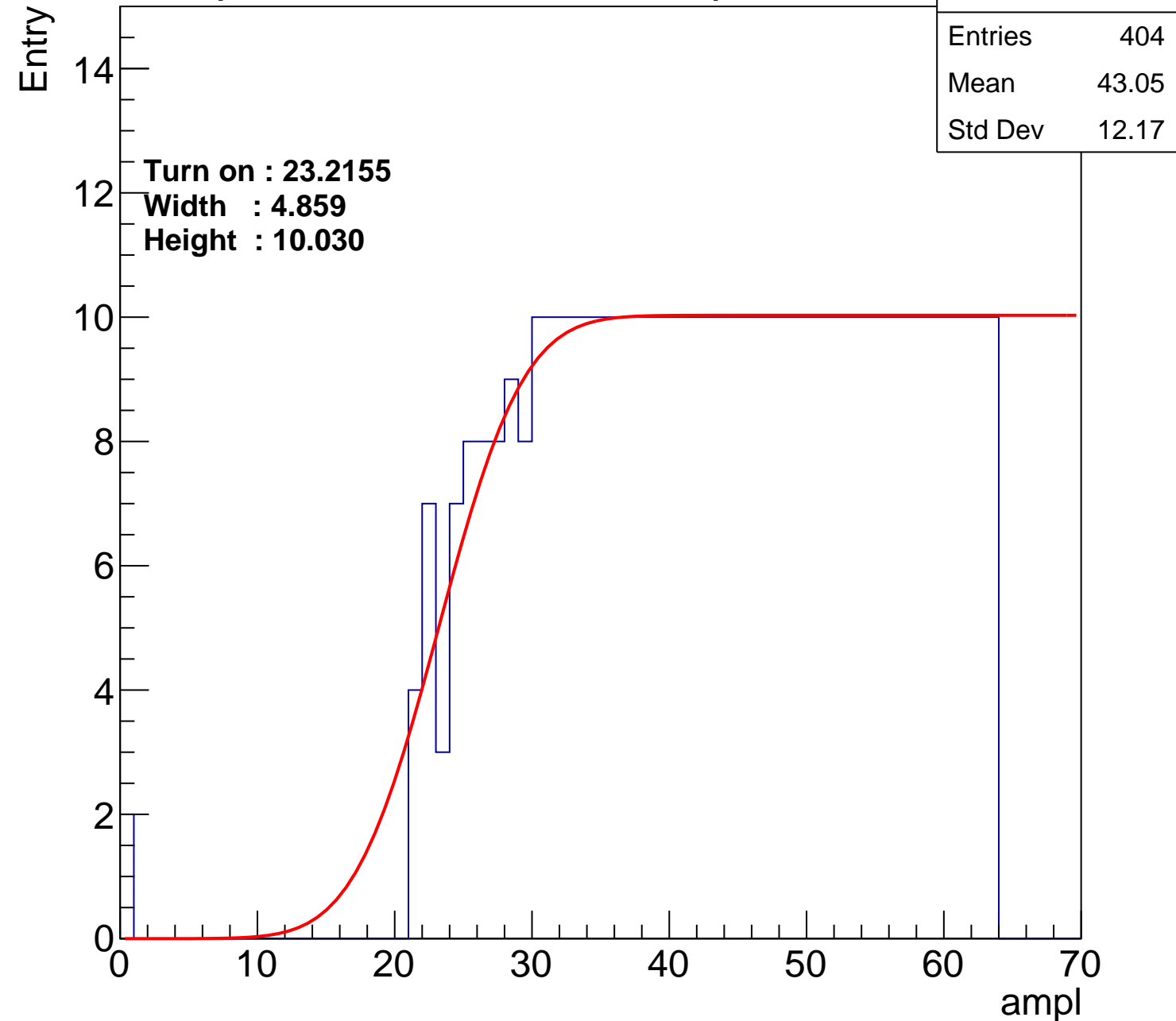
Width : 4.859

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch107

calib_packv5_042523_0143.root, FC#12, port B1

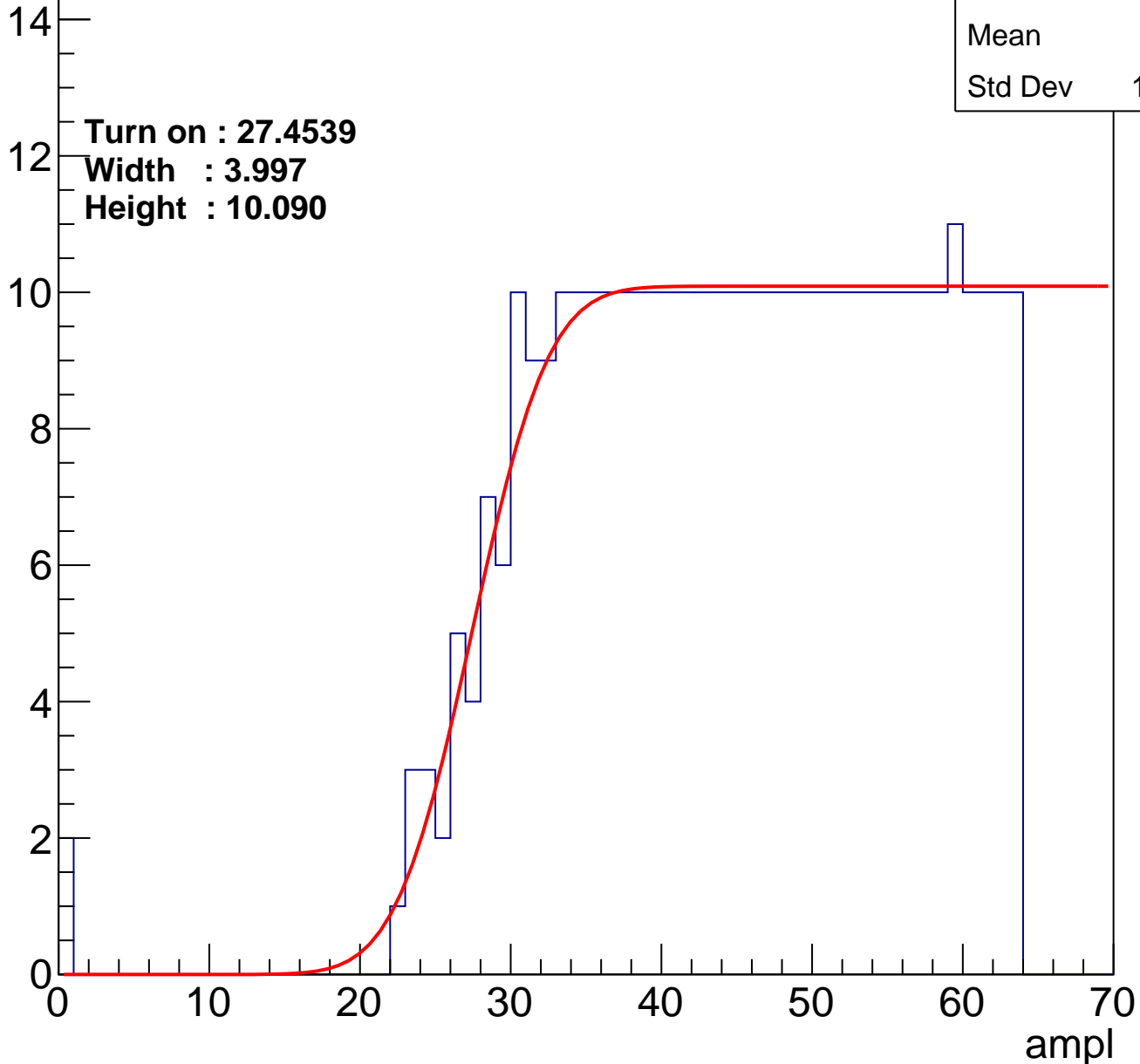
Entries	372
Mean	44.7
Std Dev	11.37

Turn on : 27.4539

Width : 3.997

Height : 10.090

Entry



B0L102S, U16-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.5605

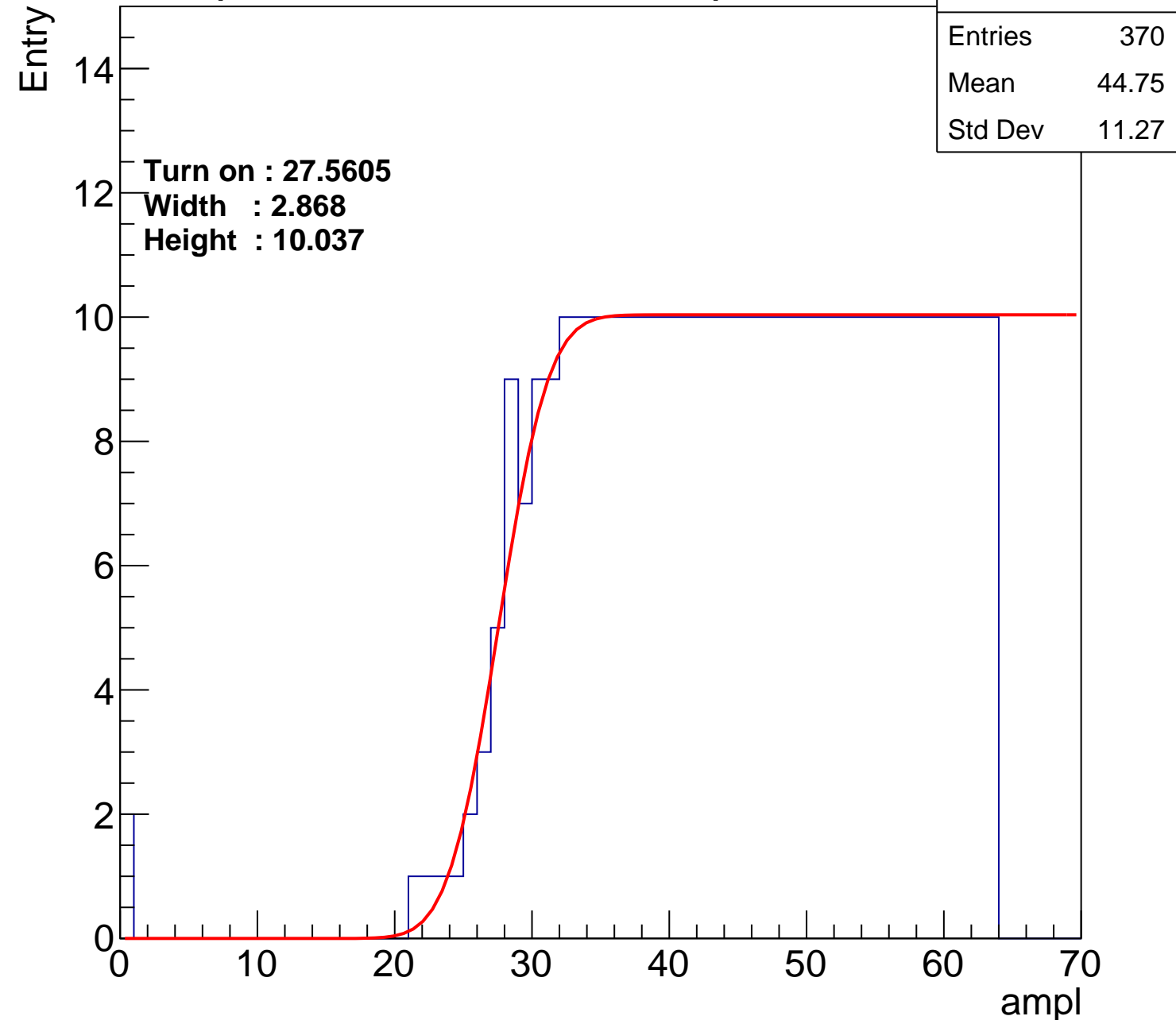
Width : 2.868

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.02
Std Dev	11.94

Turn on : 26.7355

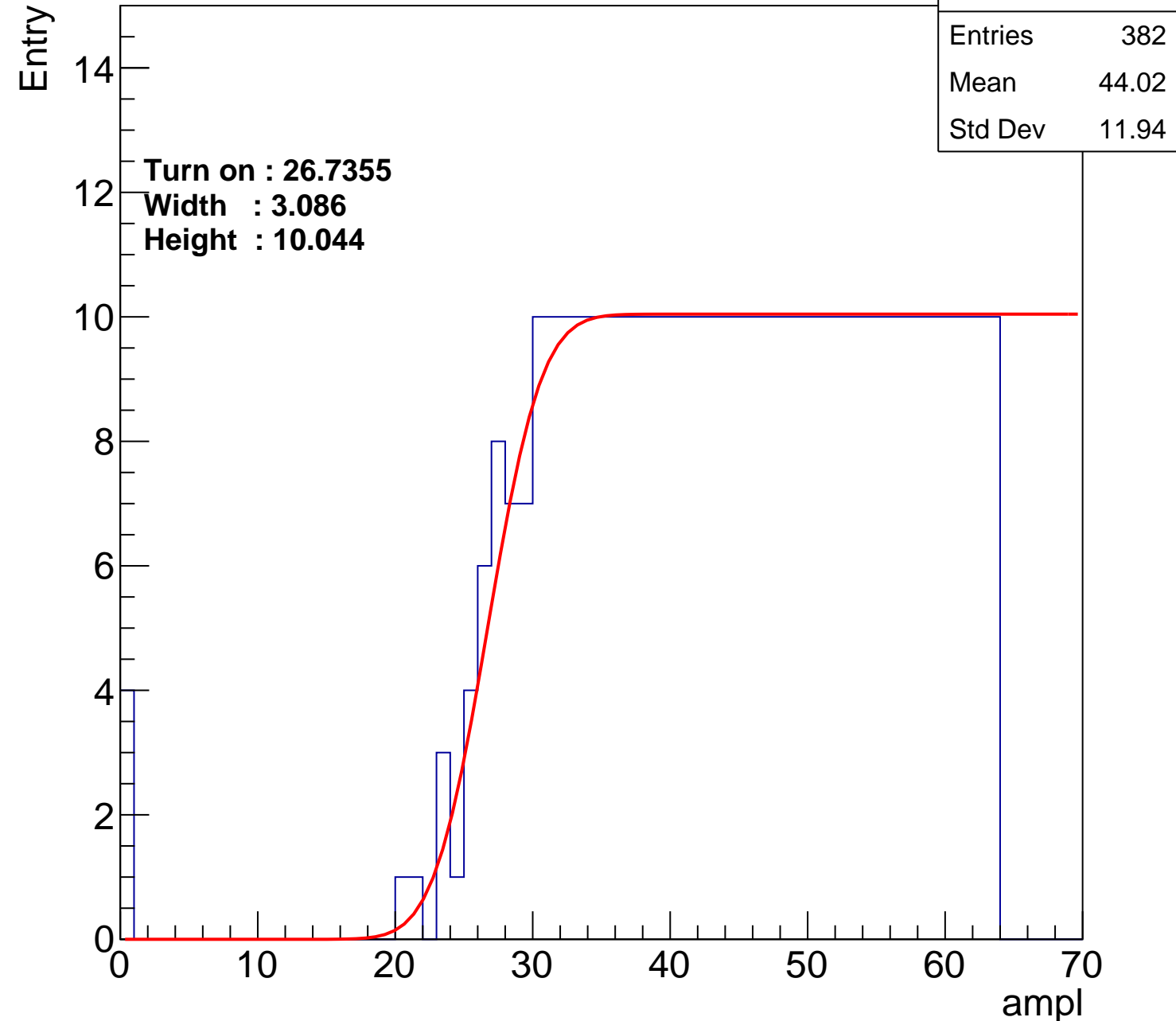
Width : 3.086

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.13
Std Dev	12.37

Turn on : 25.4822

Width : 2.747

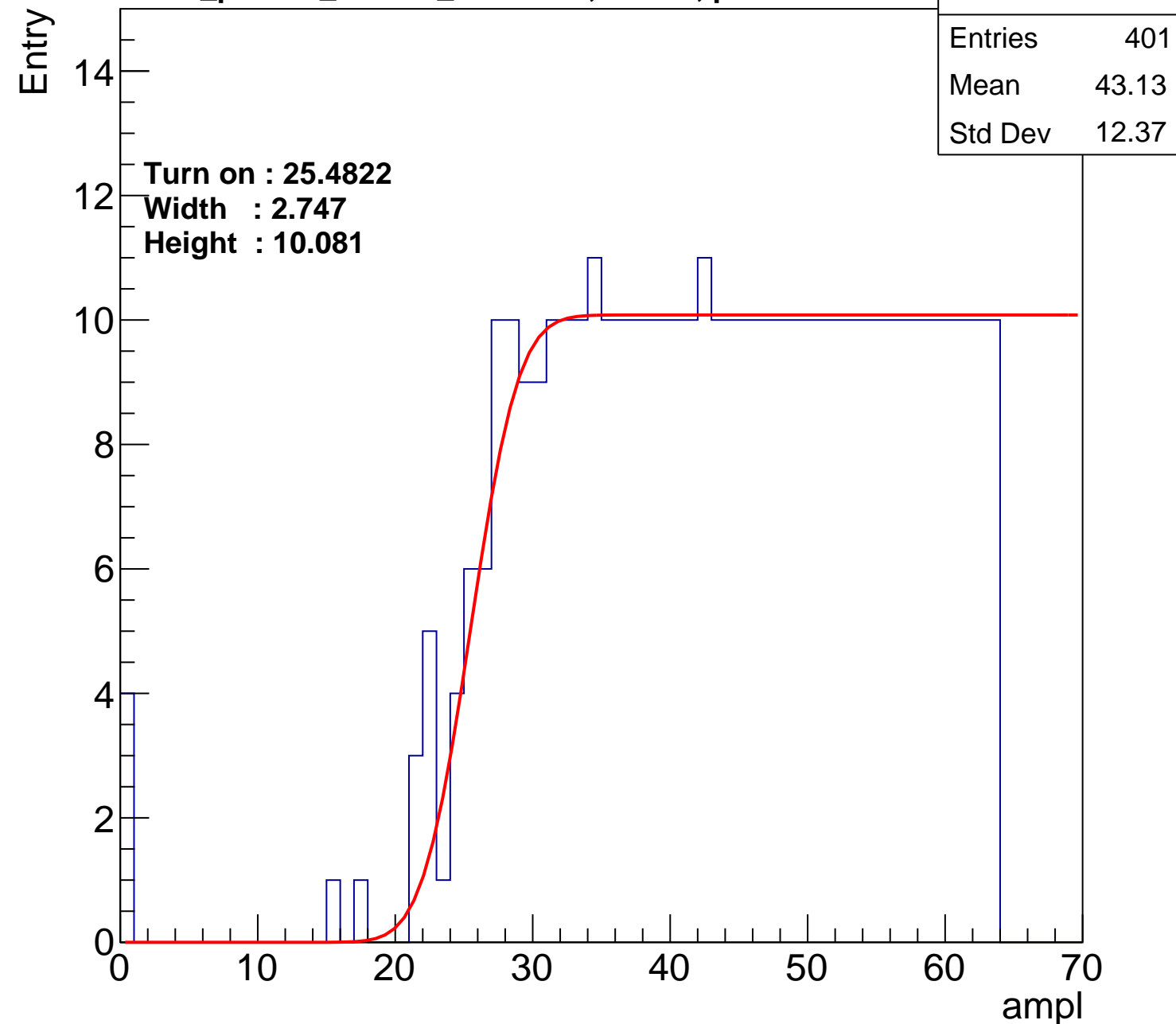
Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U16-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.19
Std Dev	11.09

Turn on : 28.9537

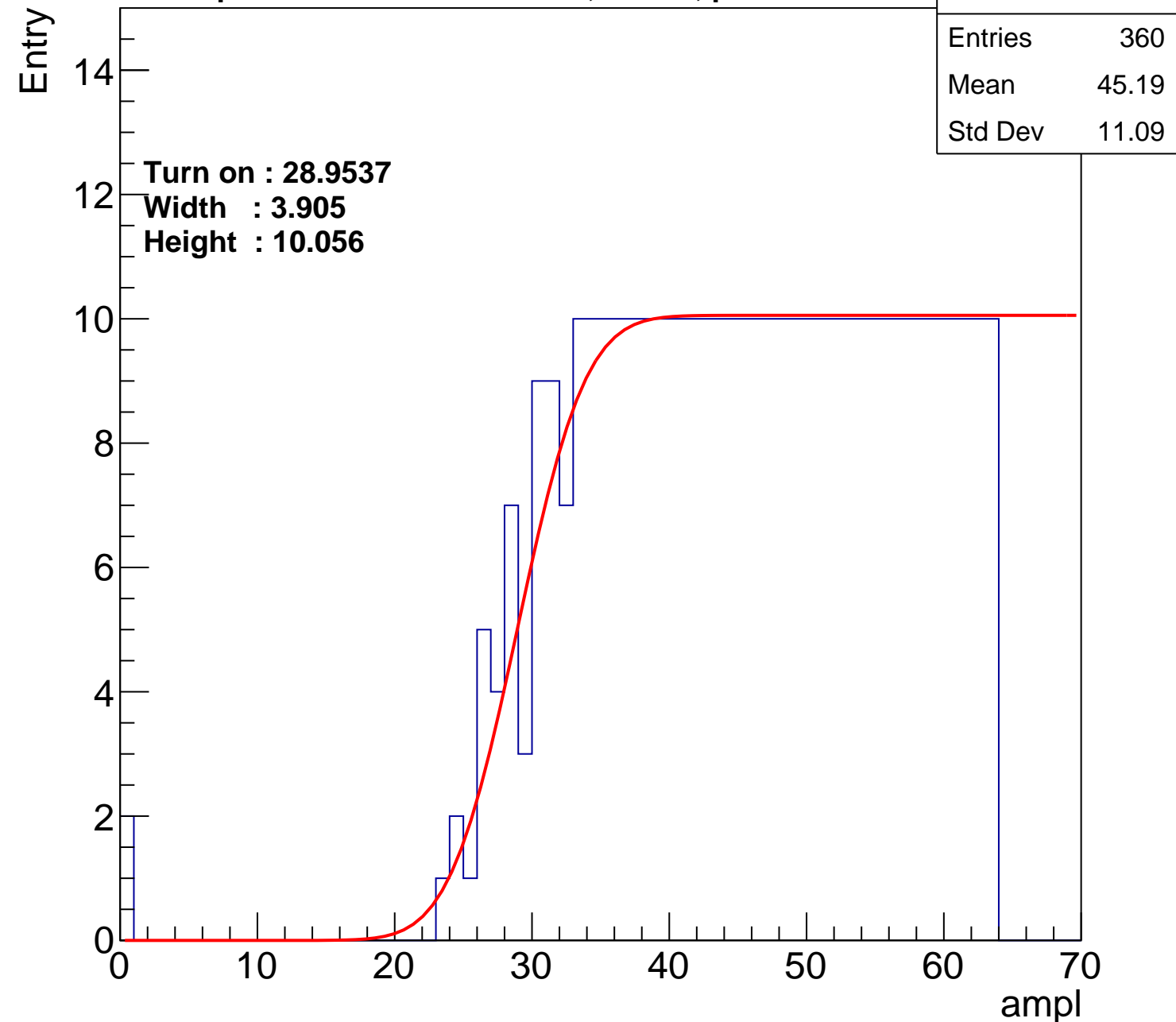
Width : 3.905

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch112

calib_packv5_042523_0143.root, FC#12, port B1

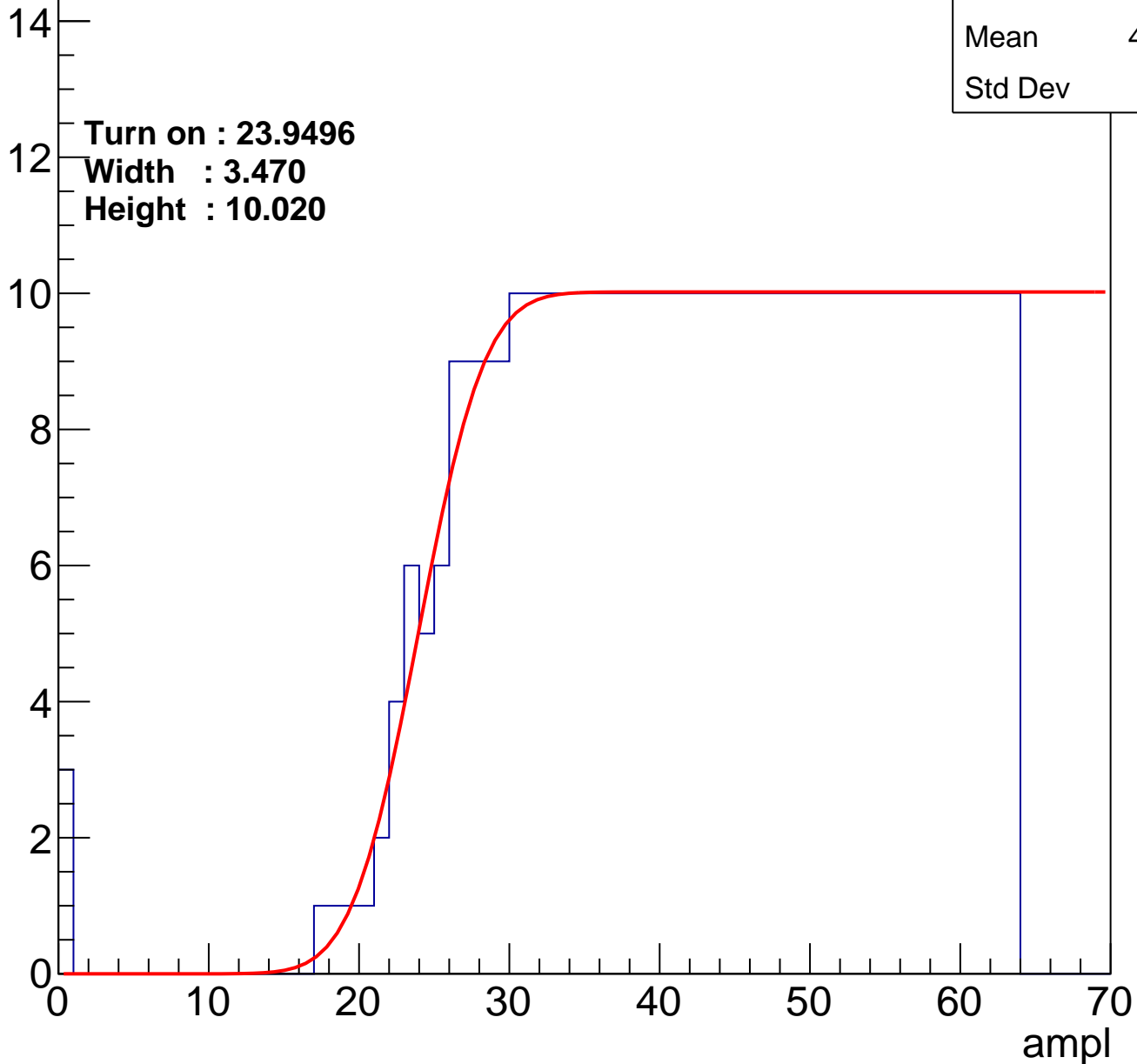
Entries	406
Mean	42.89
Std Dev	12.4

Turn on : 23.9496

Width : 3.470

Height : 10.020

Entry



B0L102S, U16-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.14
Std Dev	12.3

Turn on : 24.5460

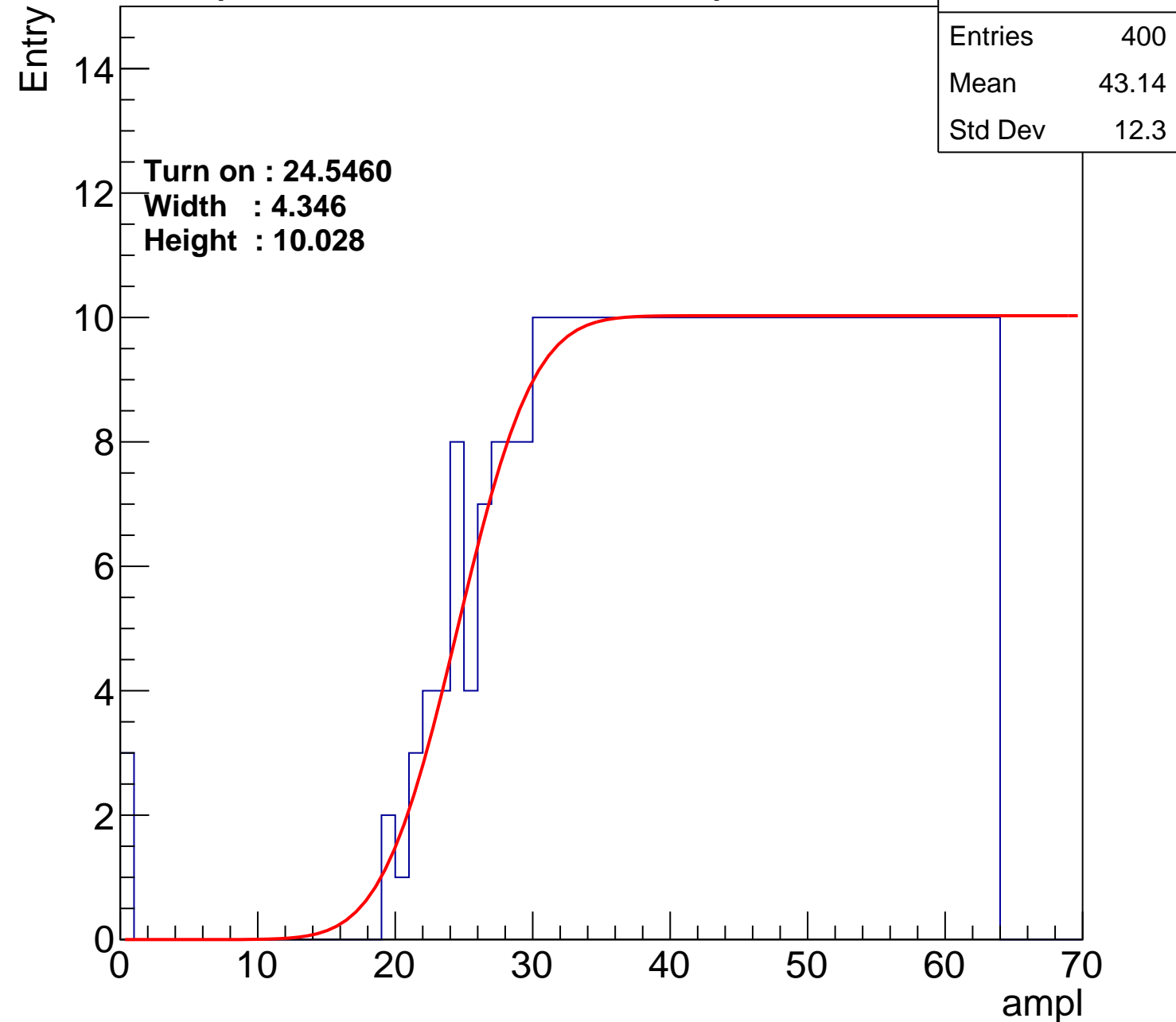
Width : 4.346

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.83
Std Dev	11.9

Turn on : 25.7324

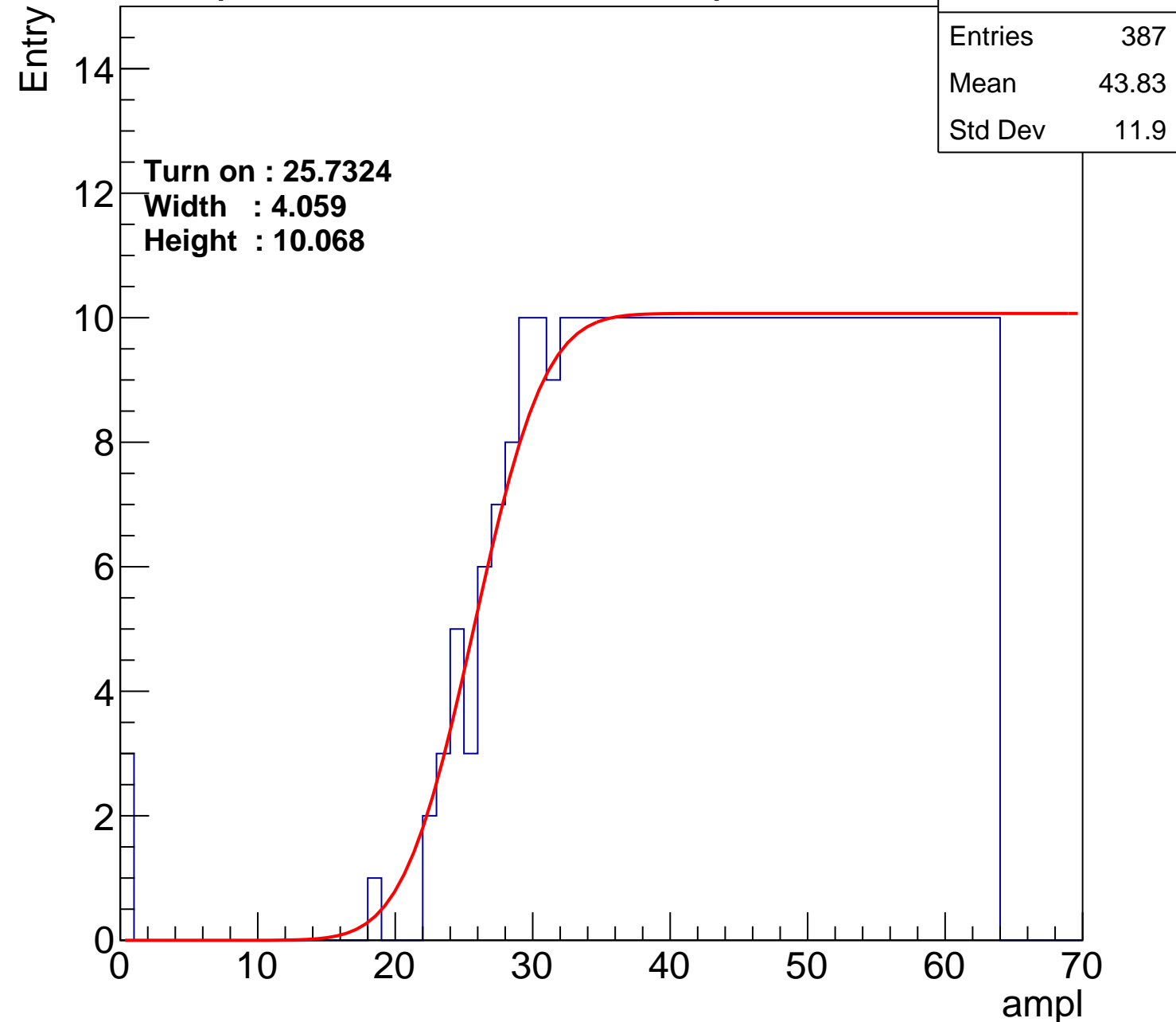
Width : 4.059

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.63
Std Dev	11.33

Turn on : 26.9280

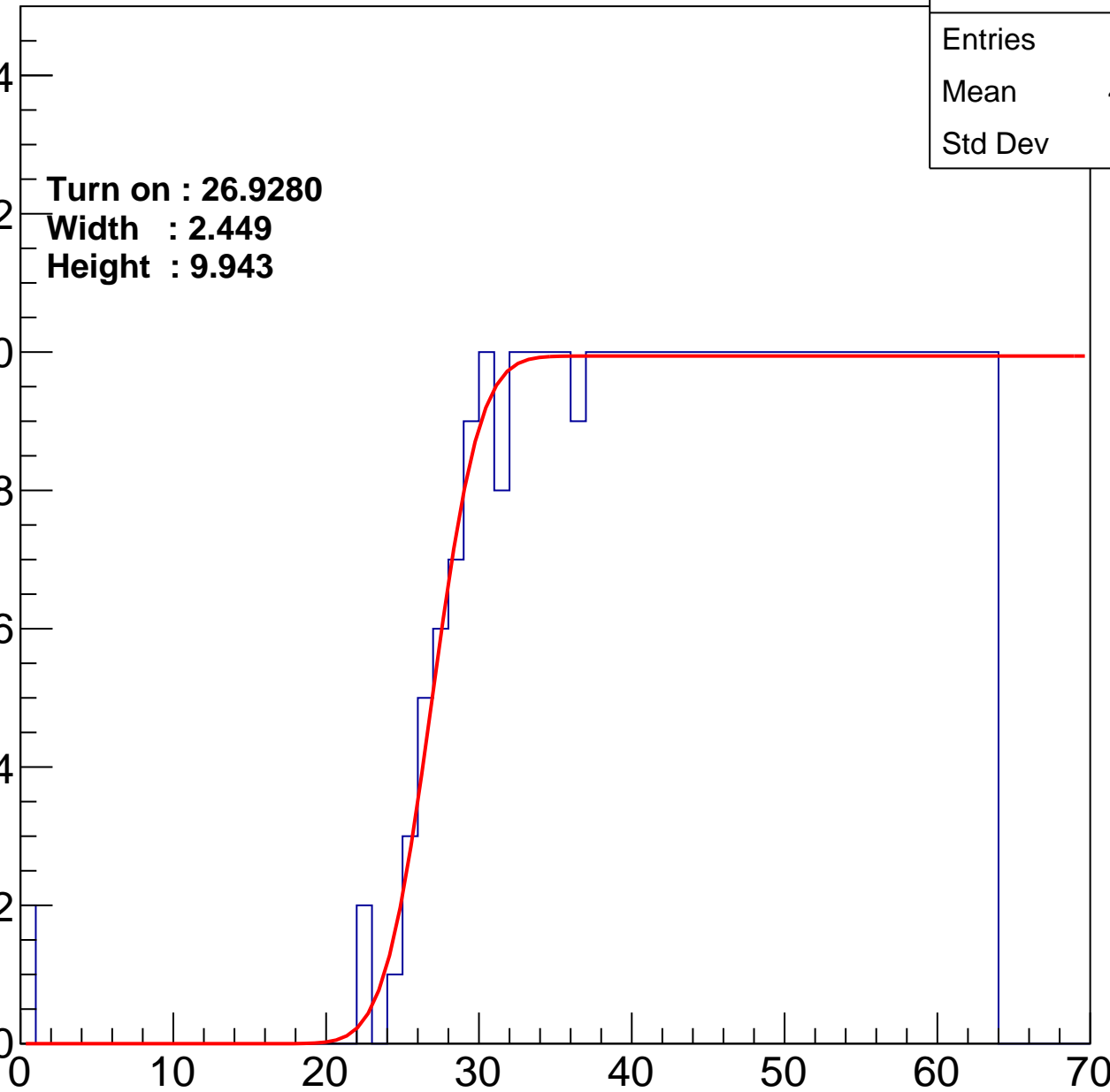
Width : 2.449

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.23
Std Dev	12.27

Turn on : 24.1951

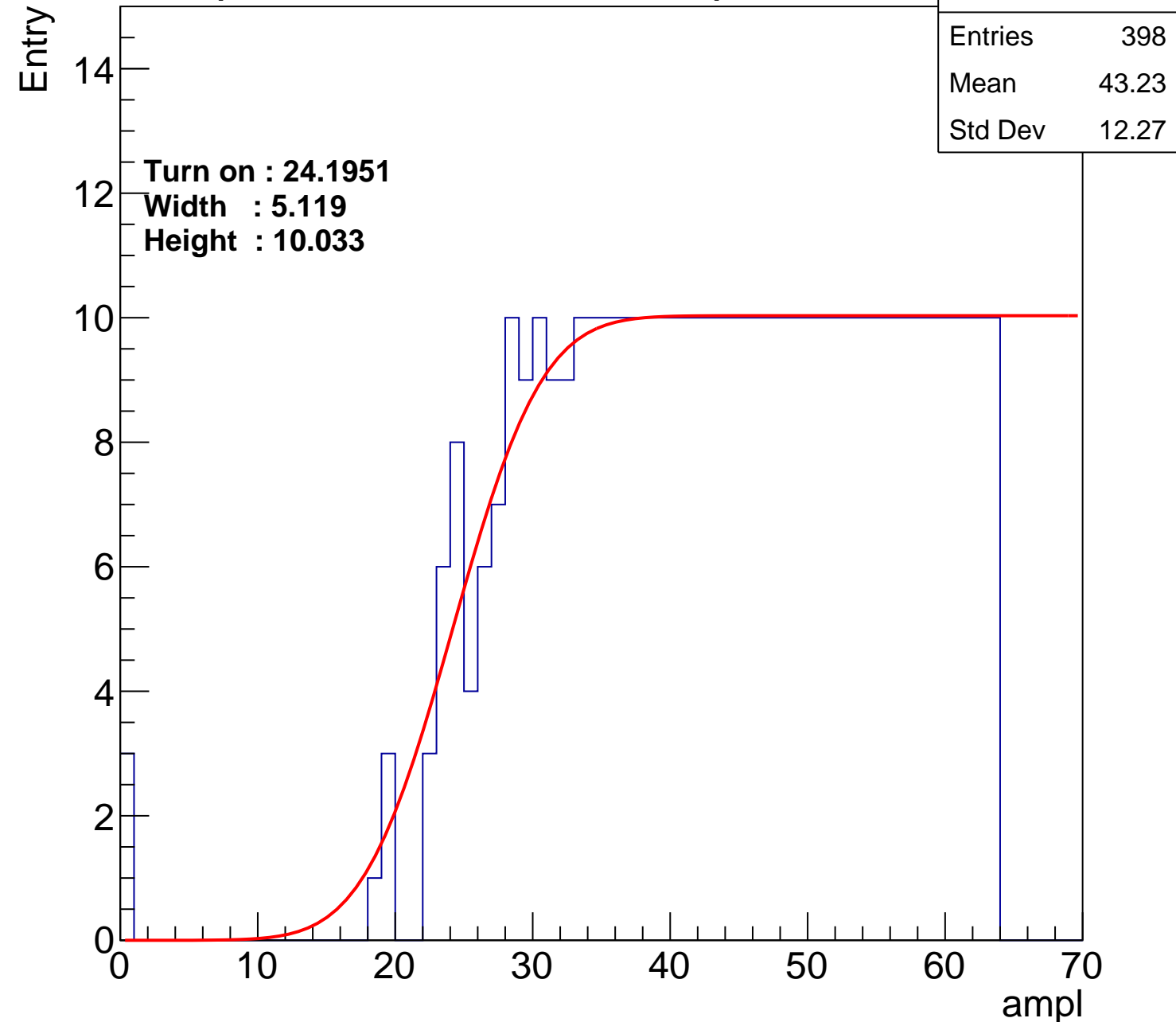
Width : 5.119

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.89
Std Dev	11.8

Turn on : 26.0204

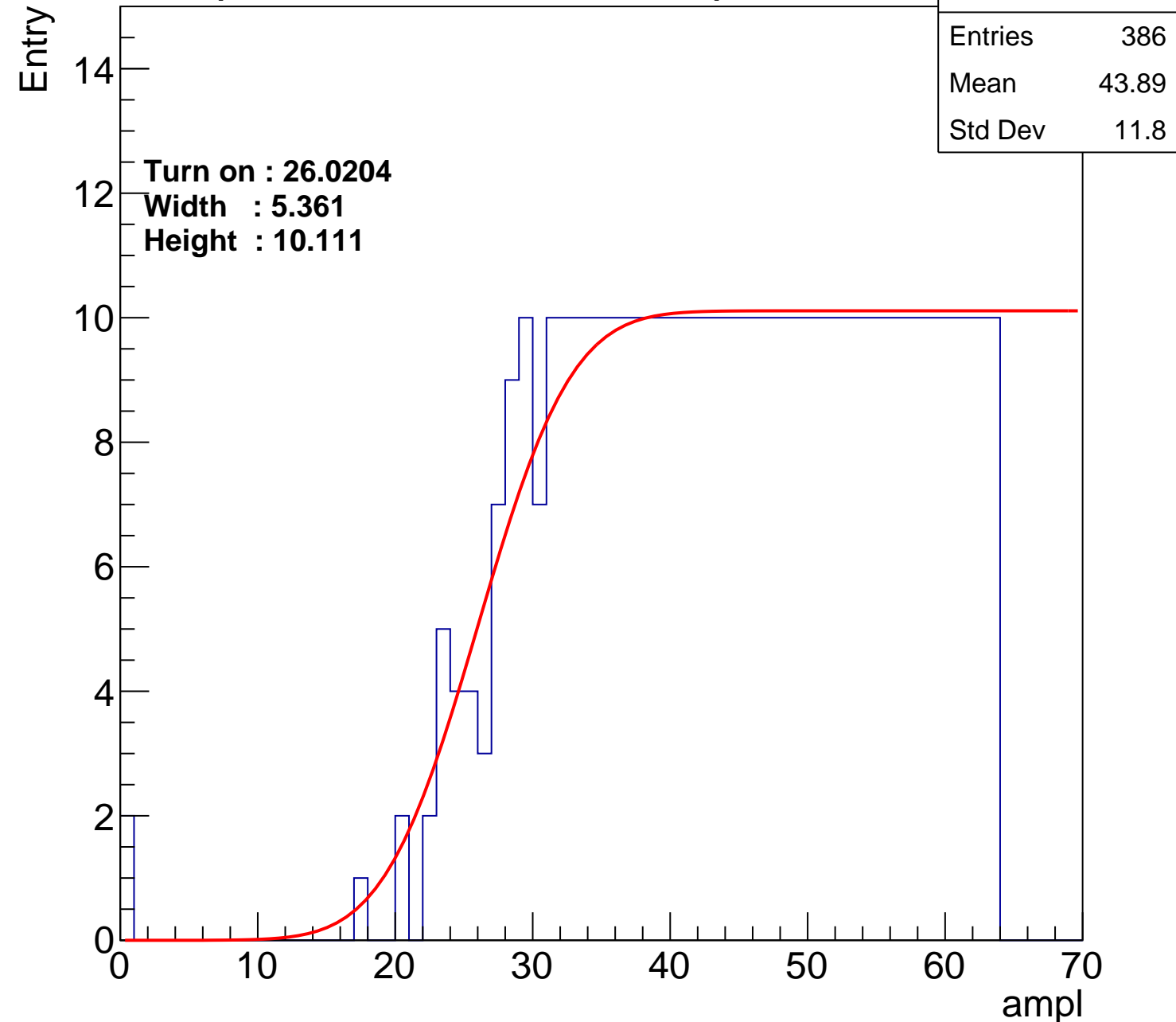
Width : 5.361

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.84
Std Dev	12.41

Turn on : 26.1832

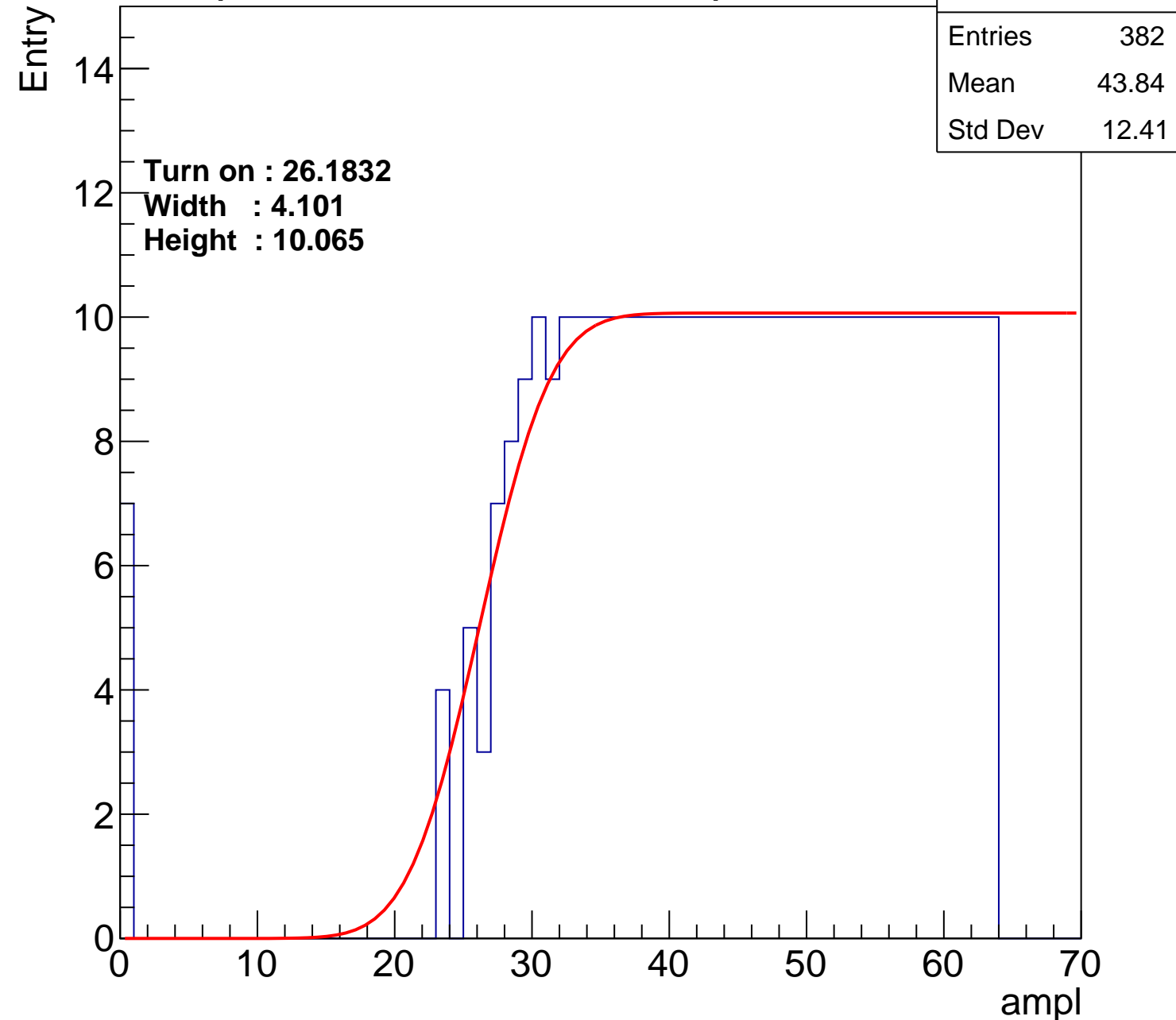
Width : 4.101

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.22
Std Dev	12.45

Turn on : 24.8407

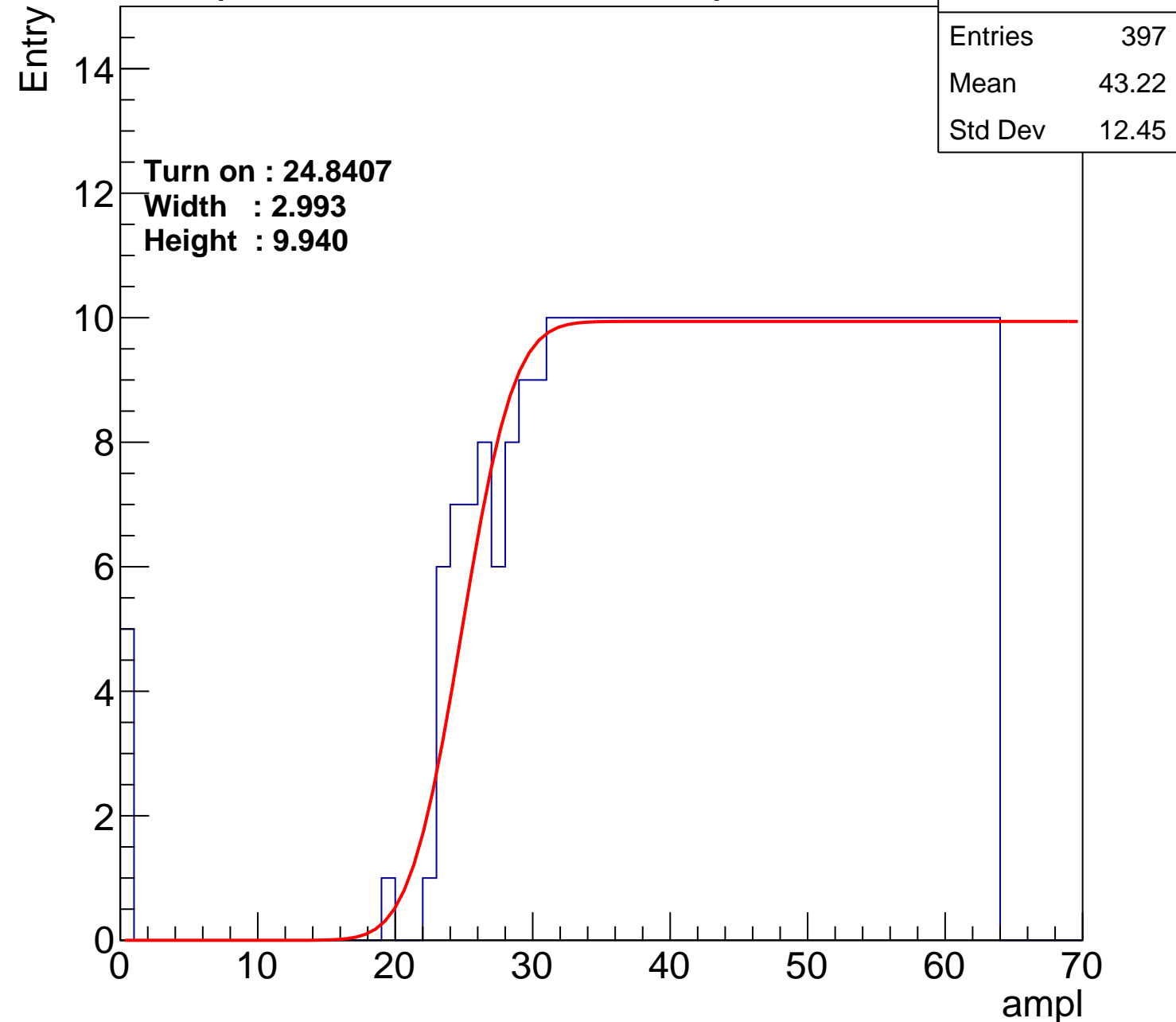
Width : 2.993

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 24.6238

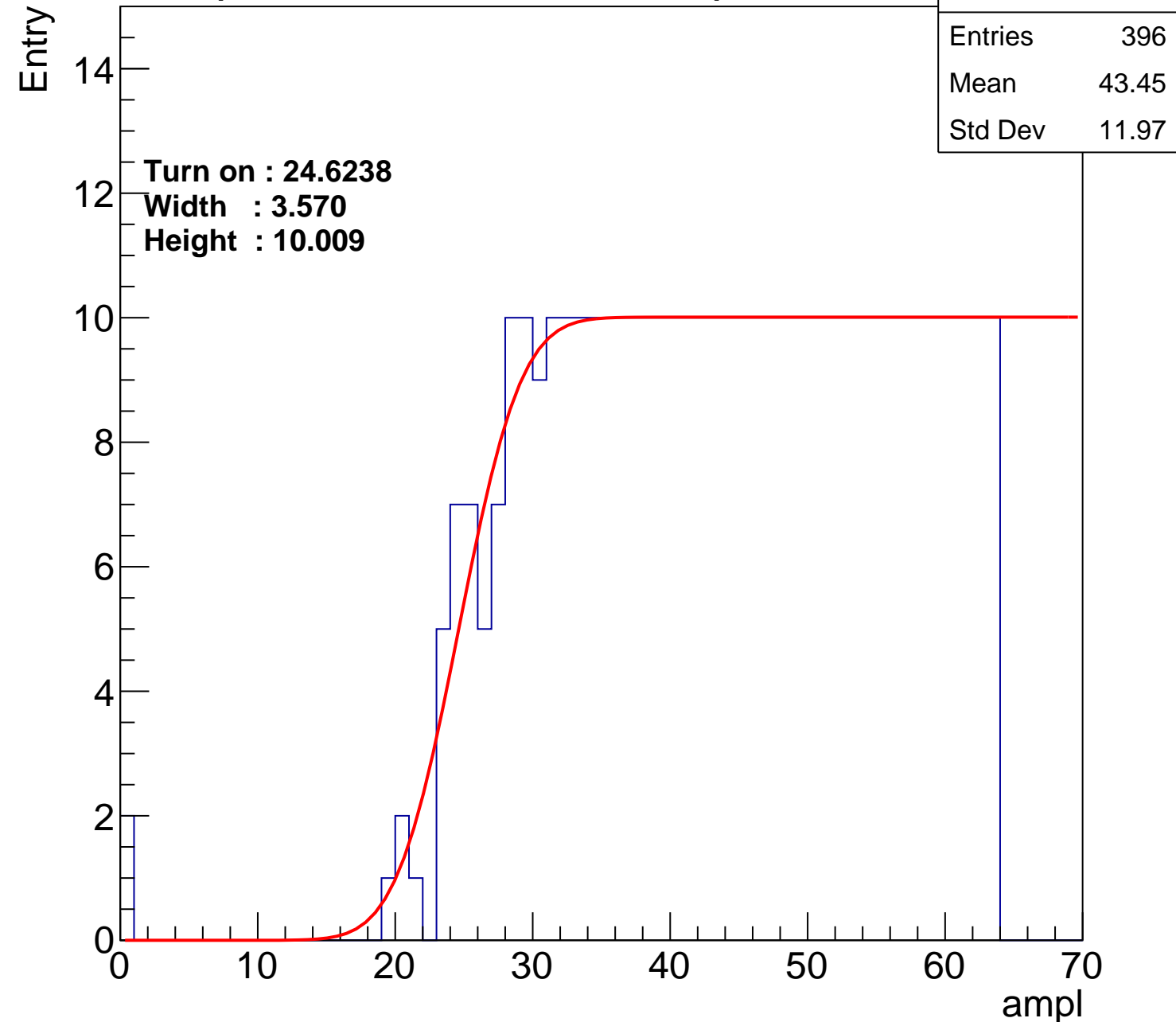
Width : 3.570

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.01
Std Dev	12.2

Turn on : 23.7144

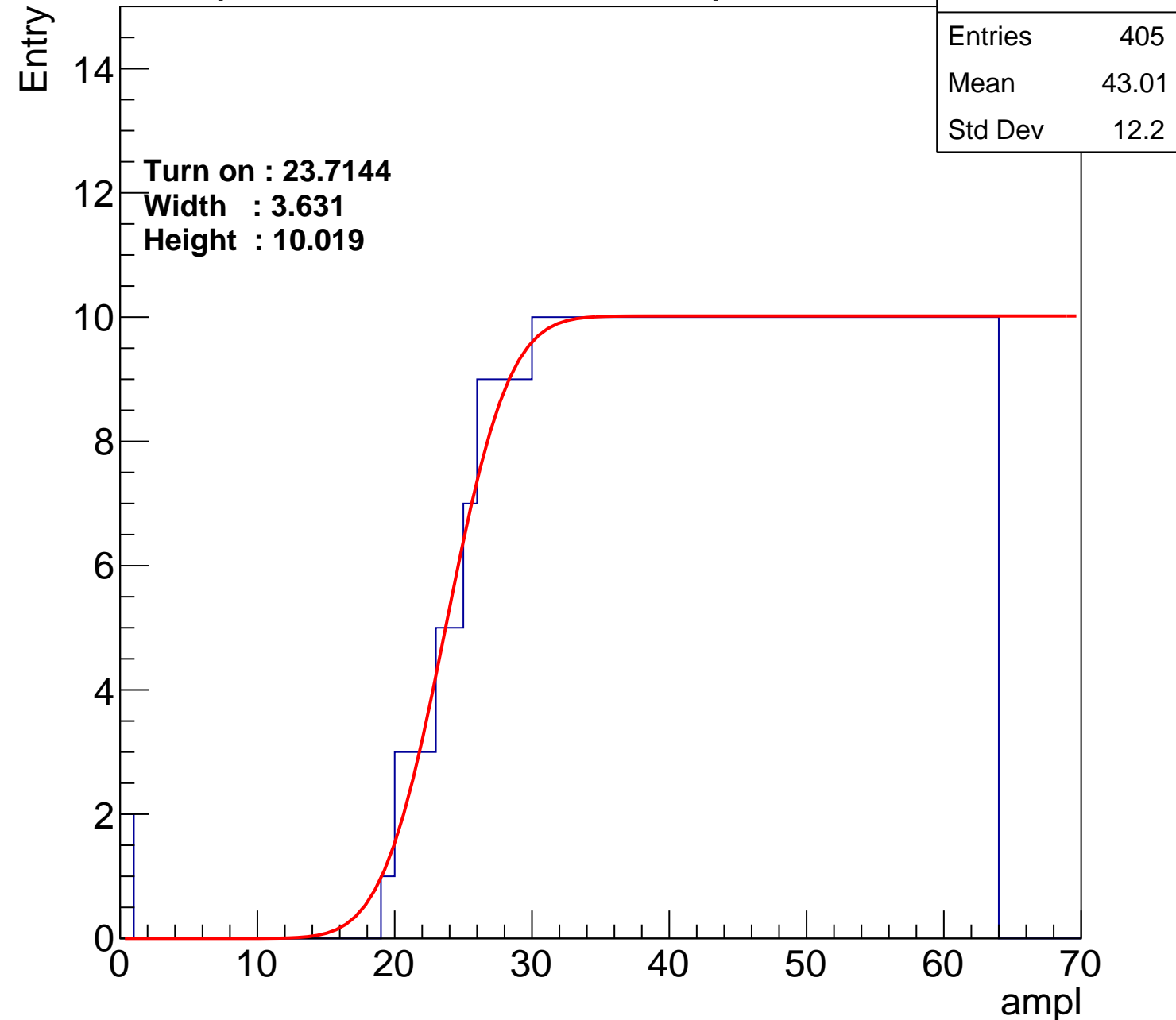
Width : 3.631

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.06
Std Dev	11.86

Turn on : 26.9352

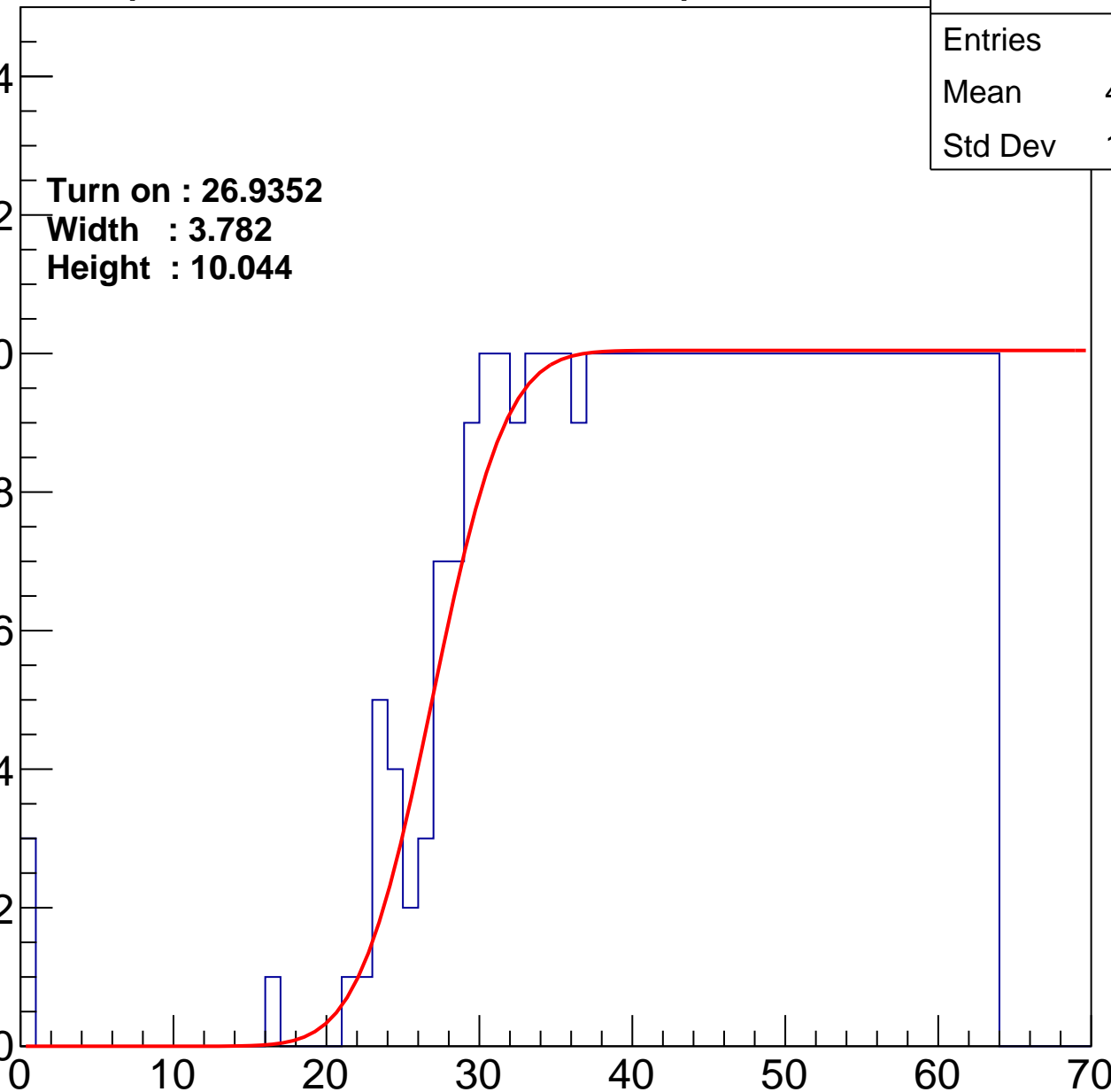
Width : 3.782

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.61
Std Dev	11.86

Turn on : 24.8663

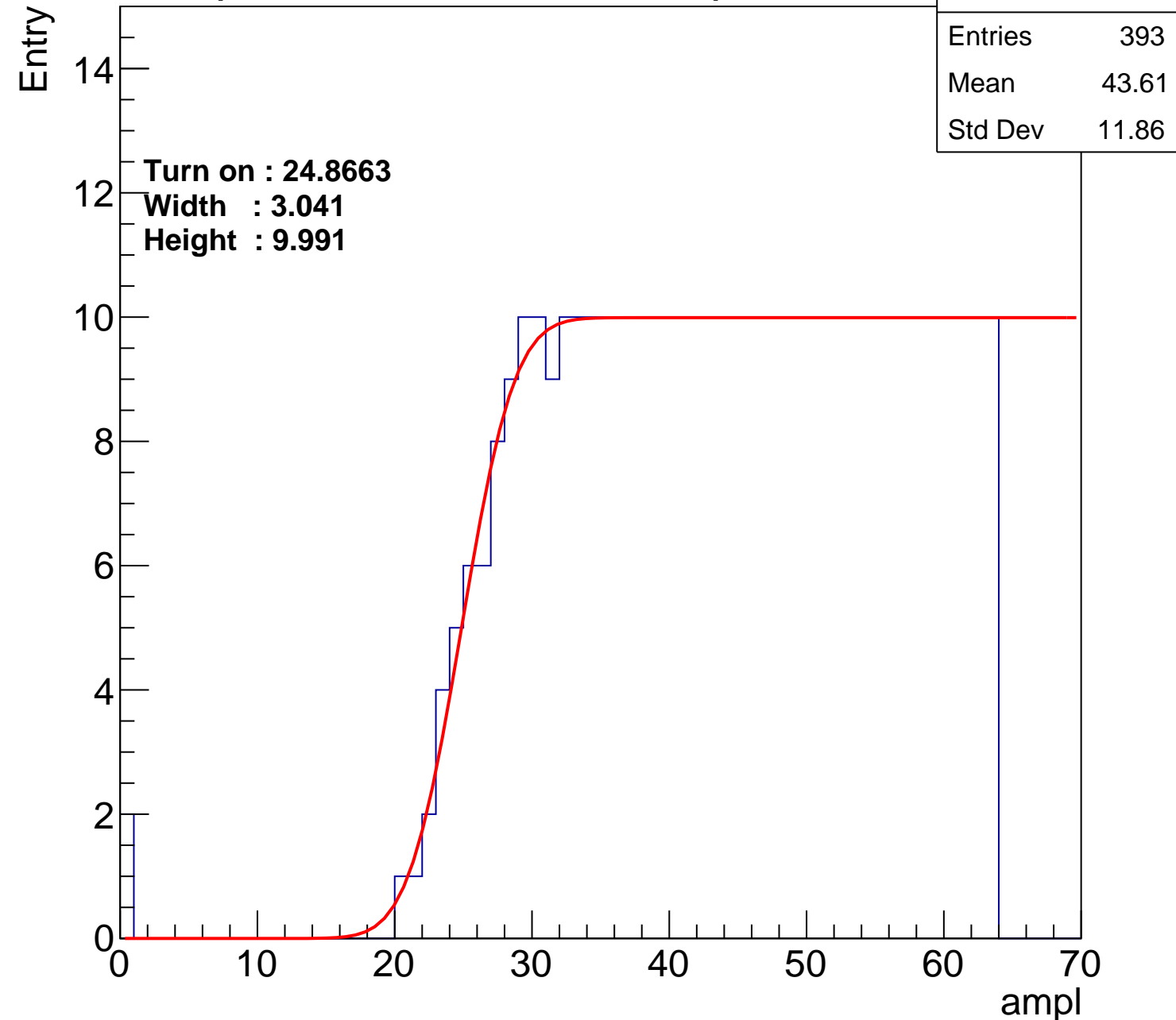
Width : 3.041

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.17
Std Dev	12.48

Turn on : 25.3006

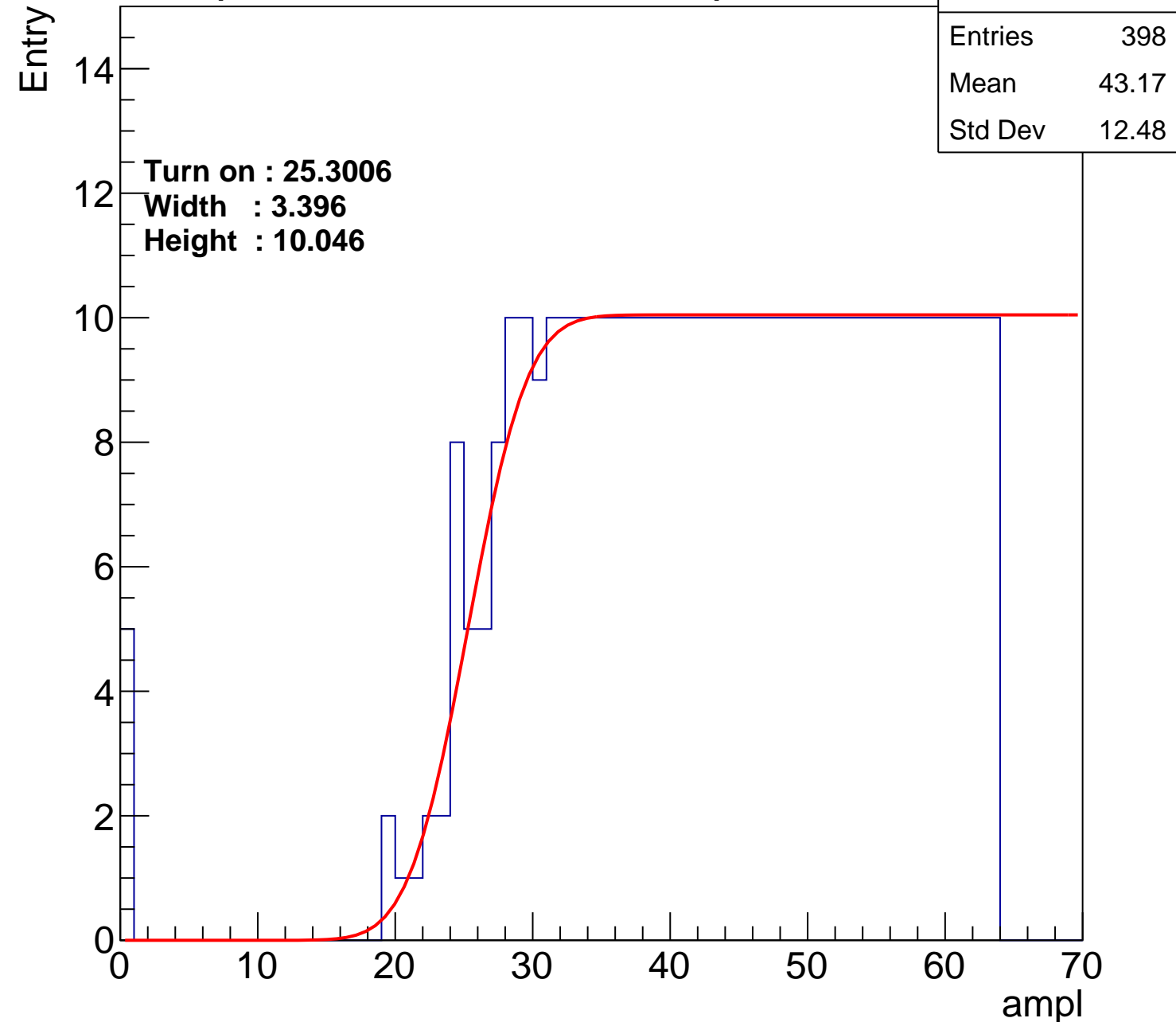
Width : 3.396

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.4567

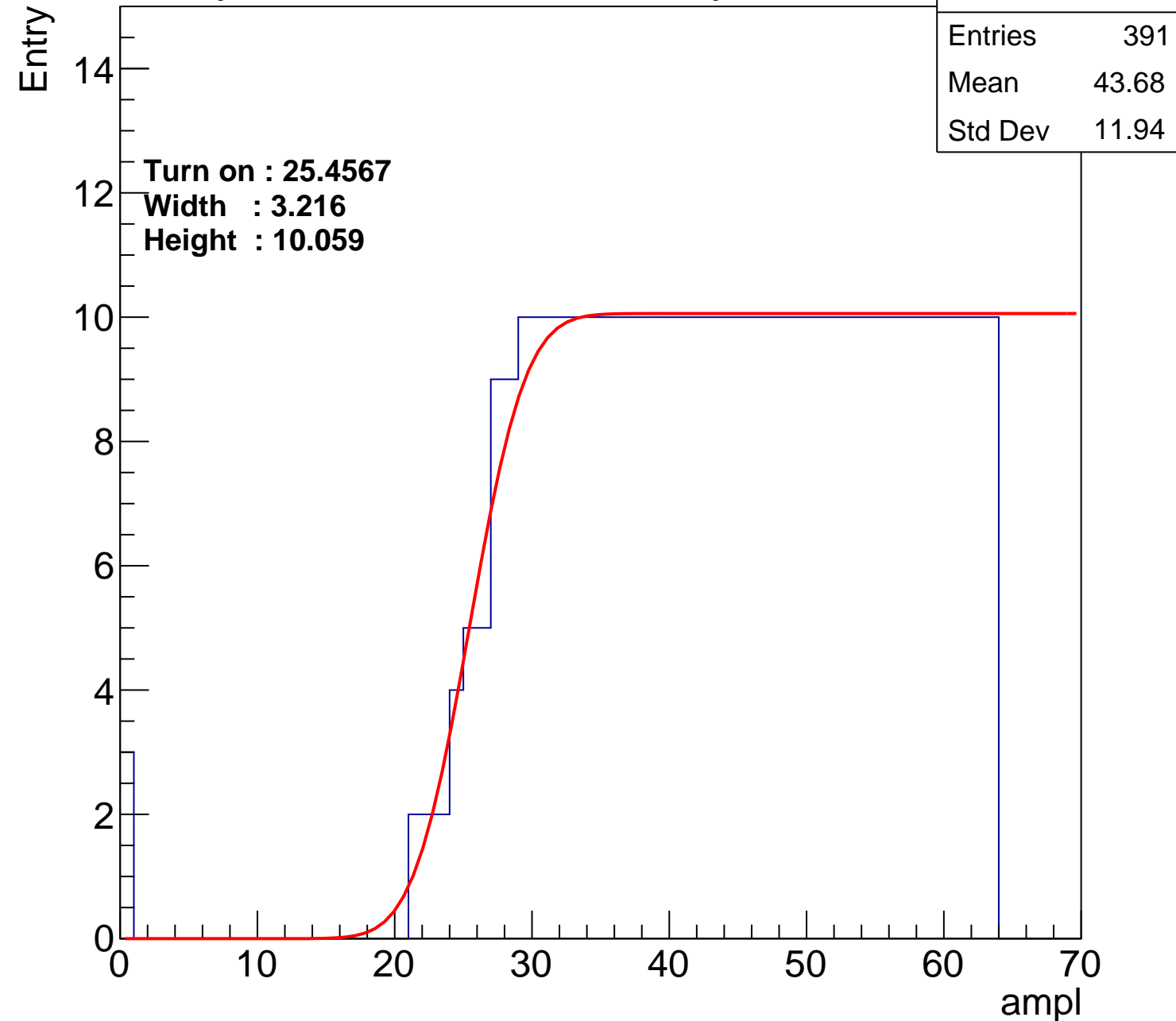
Width : 3.216

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	412
Mean	42.49
Std Dev	12.75

Turn on : 24.2870

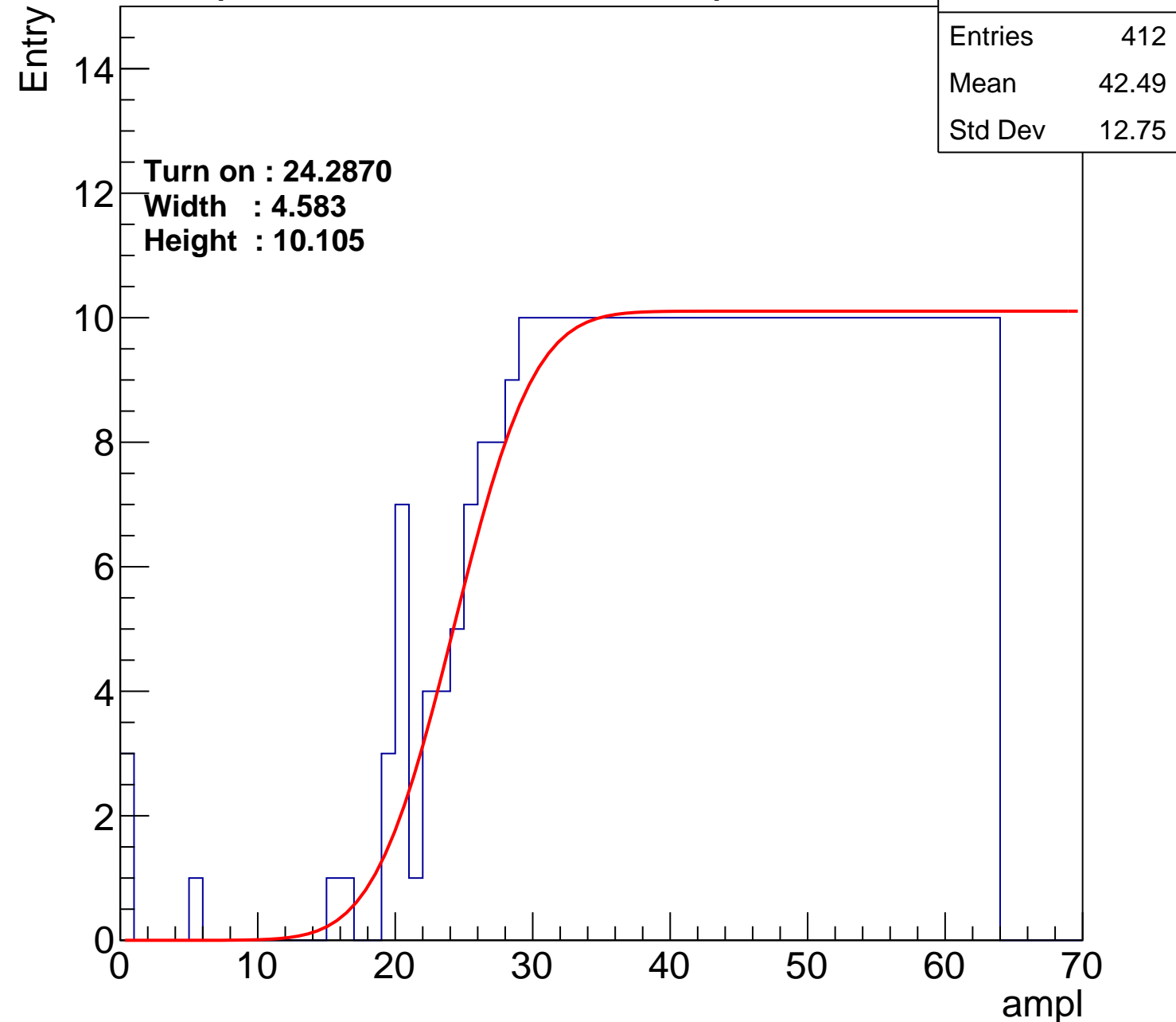
Width : 4.583

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U16-ch127

calib_packv5_042523_0143.root, FC#12, port B1

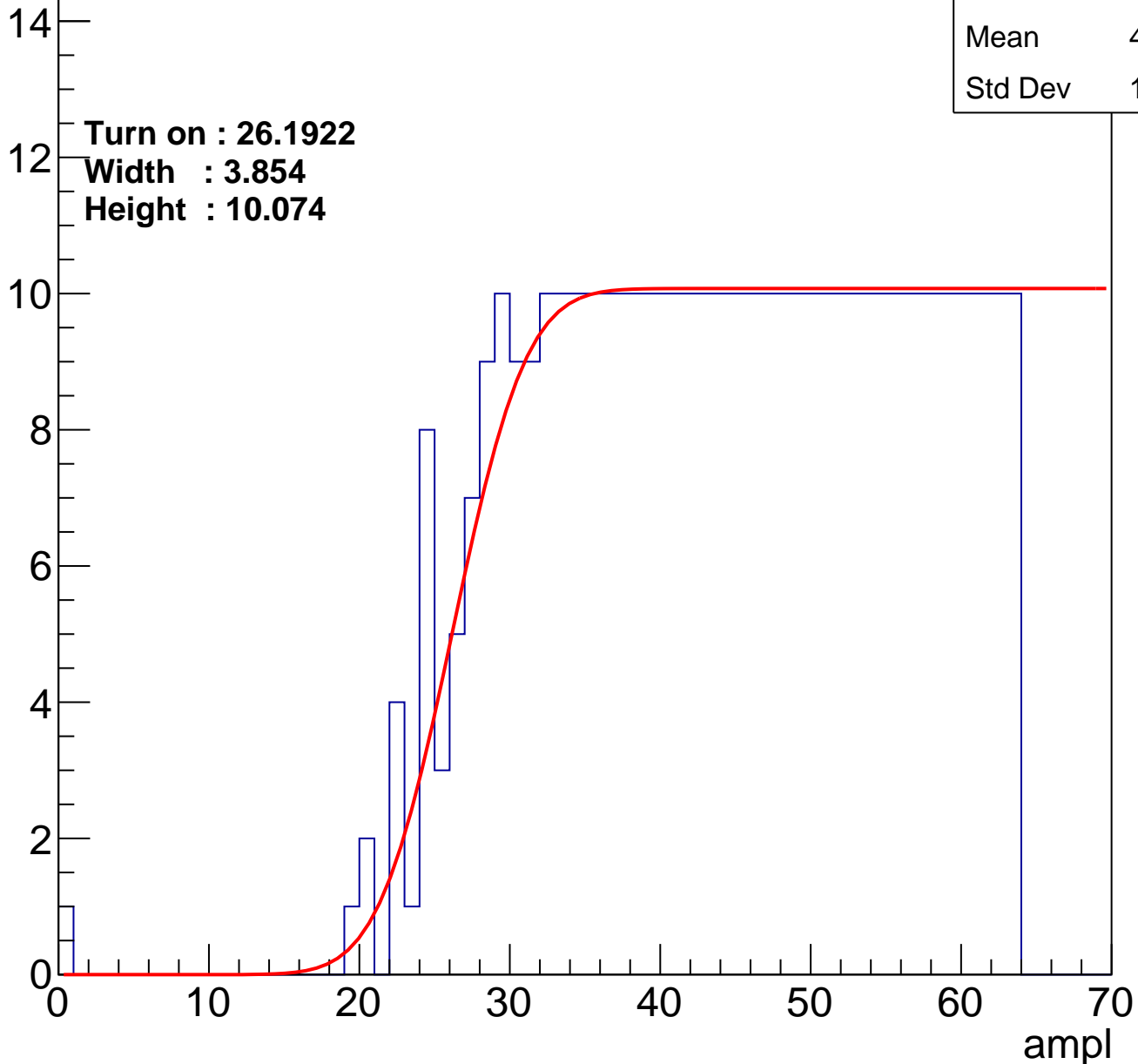
Entries	389
Mean	43.82
Std Dev	11.68

Turn on : 26.1922

Width : 3.854

Height : 10.074

Entry



B0L102S, U16-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.82
Std Dev	11.68

Turn on : 26.1922

Width : 3.854

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl

