



# B0L102S, U21-ch0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.82
Std Dev	11.87

Turn on : 25.6394

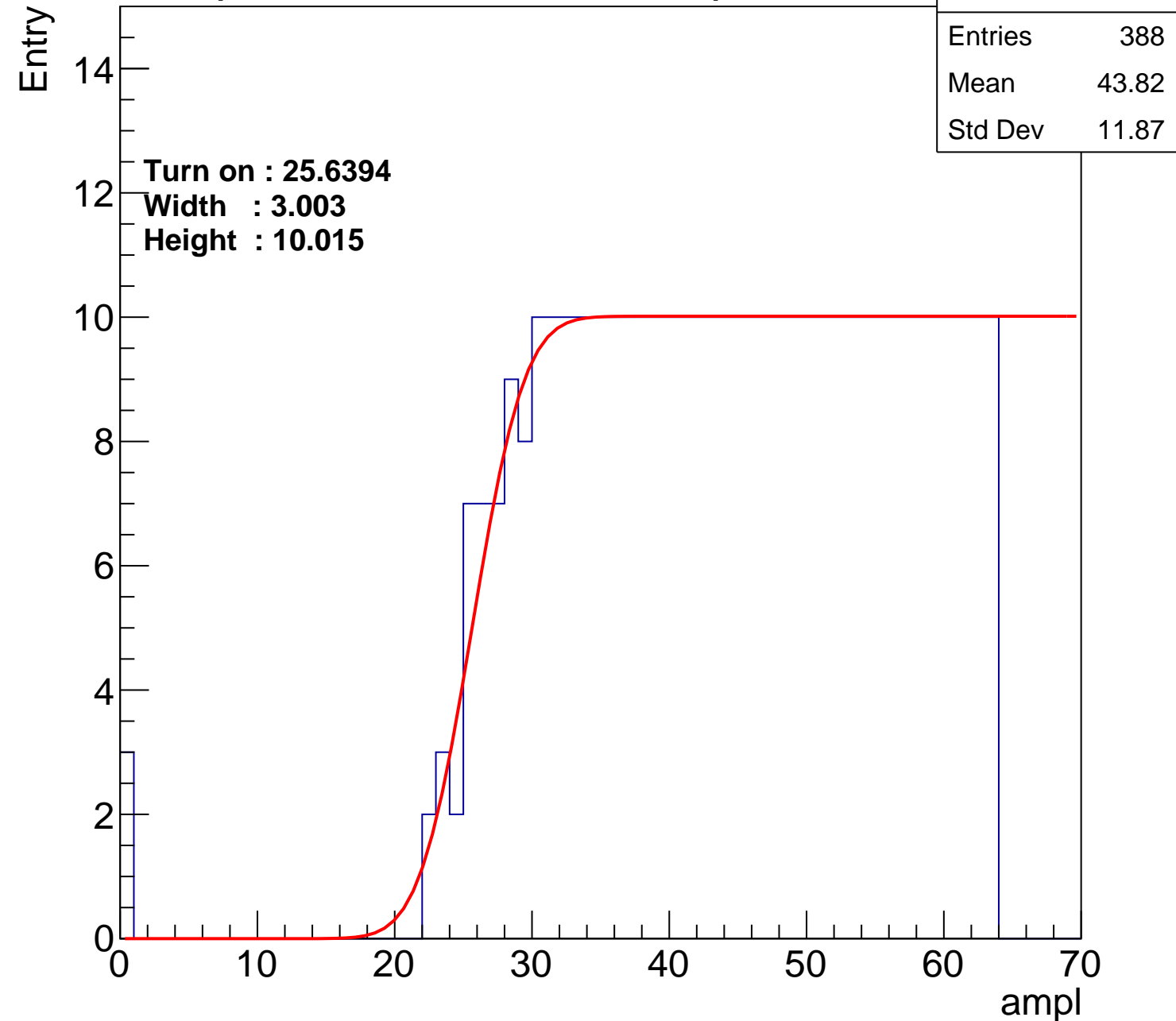
Width : 3.003

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.68
Std Dev	11.31

Turn on : 26.9749

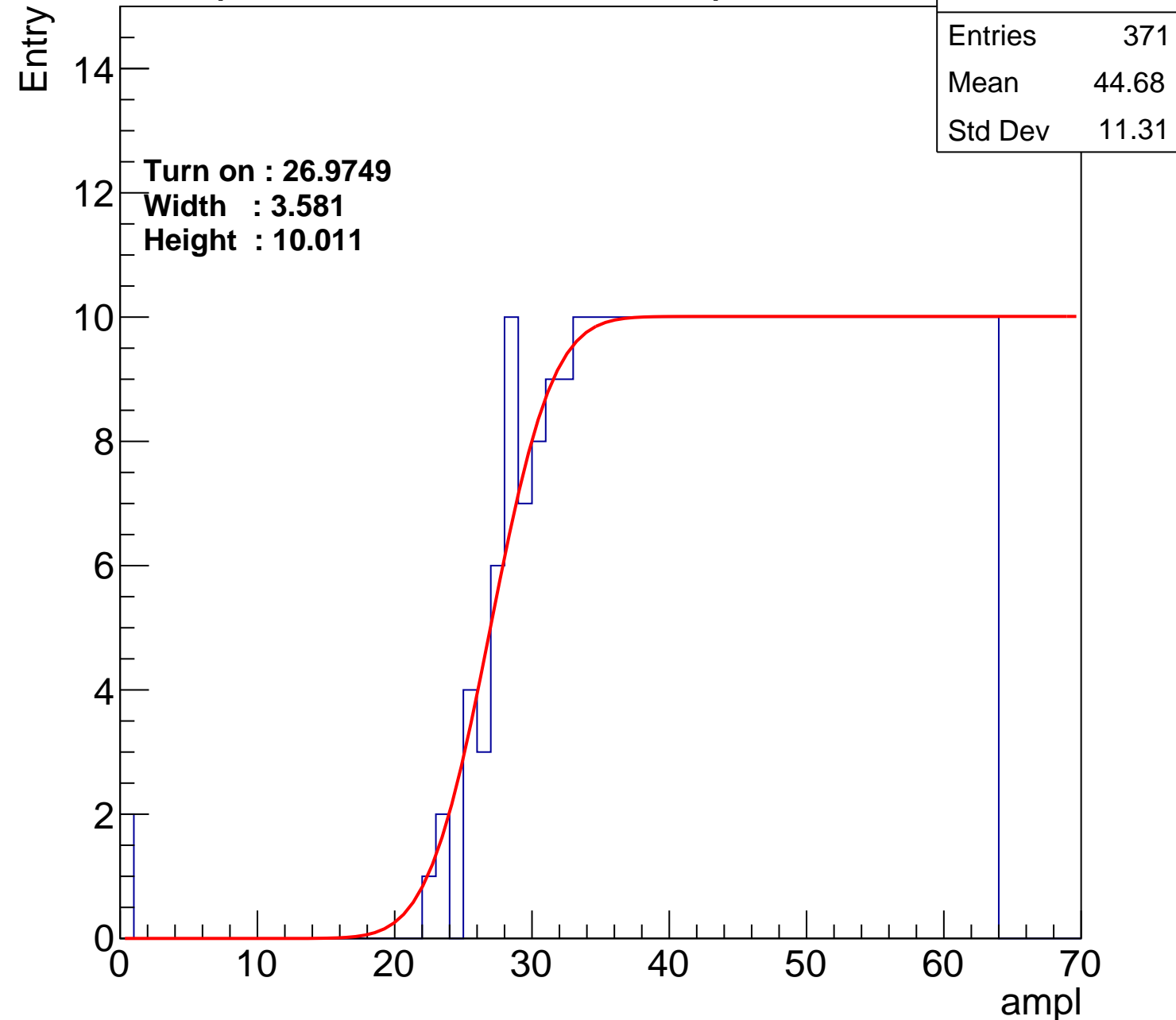
Width : 3.581

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.7209

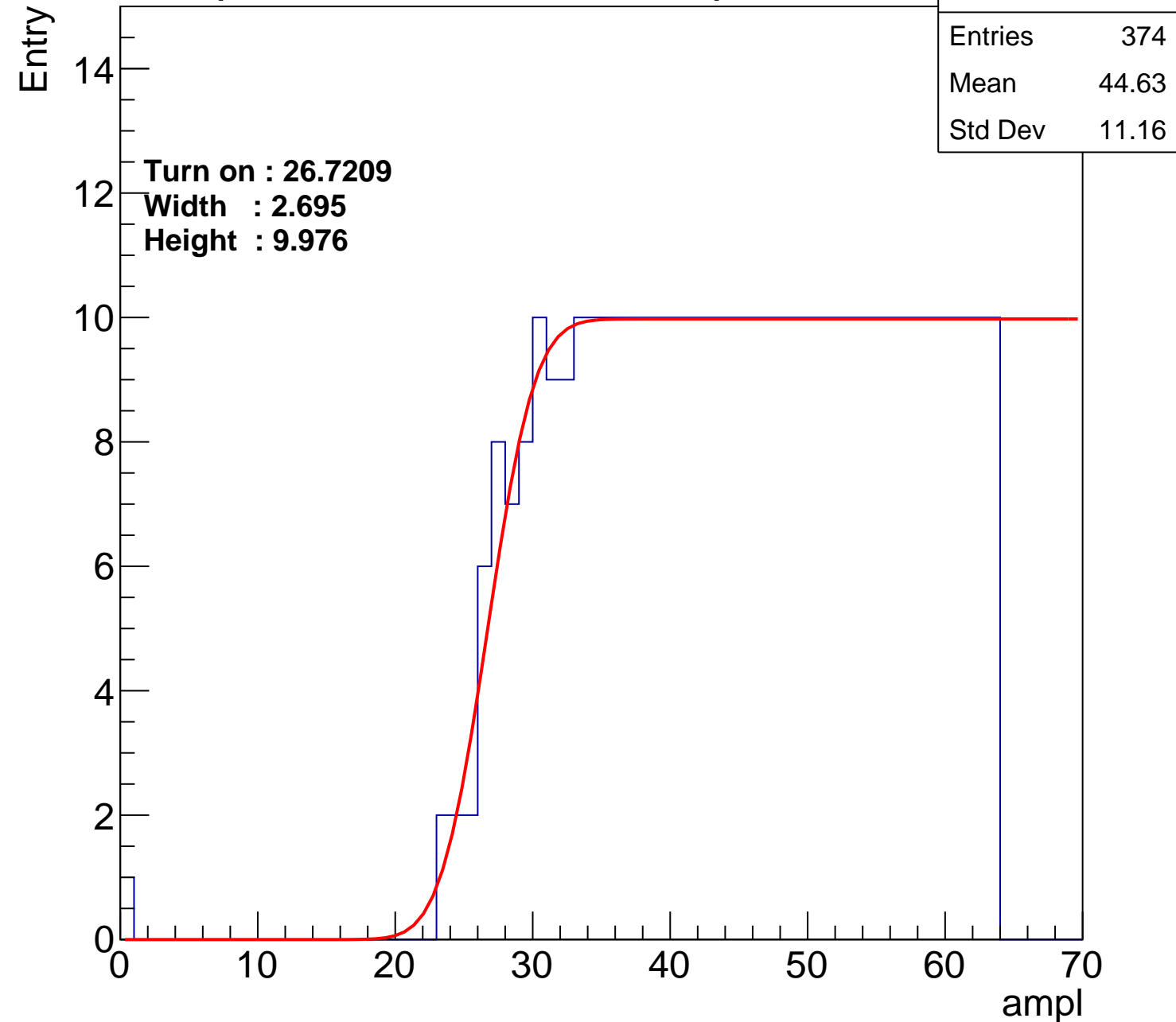
Width : 2.695

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.0003

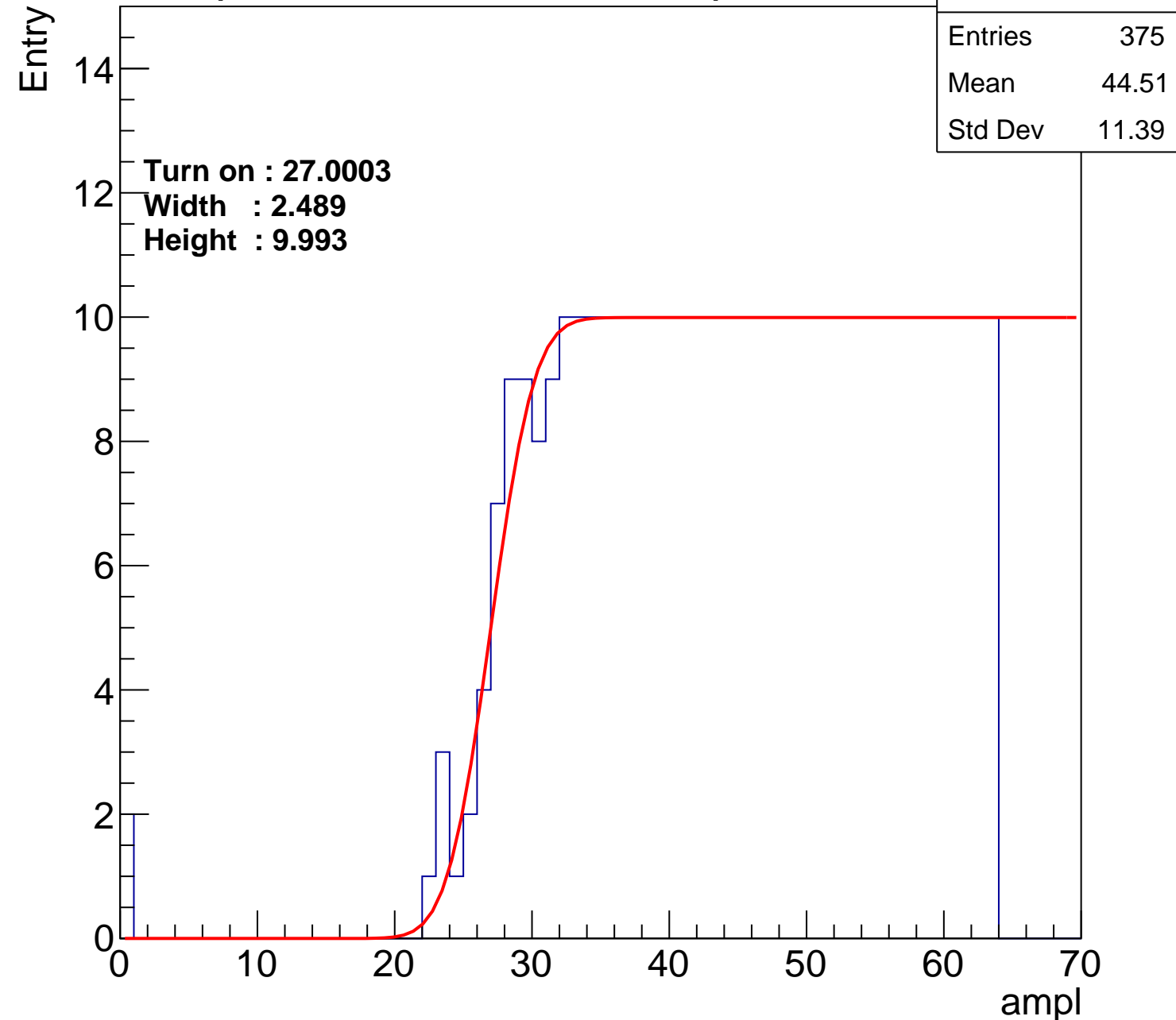
Width : 2.489

Height : 9.993

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.49
Std Dev	11.78

Turn on : 24.2125

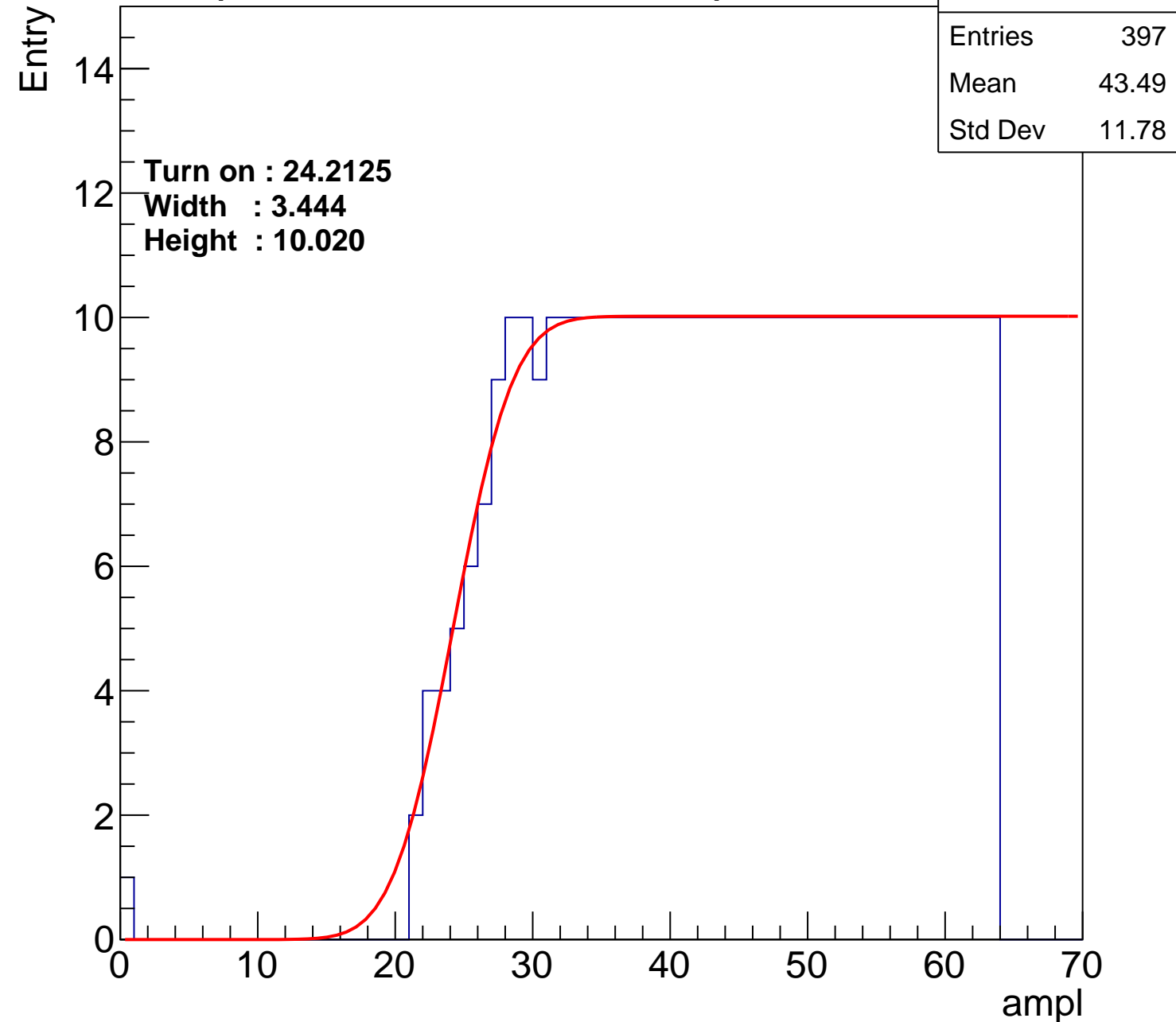
Width : 3.444

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.56
Std Dev	11.89

Turn on : 24.7969

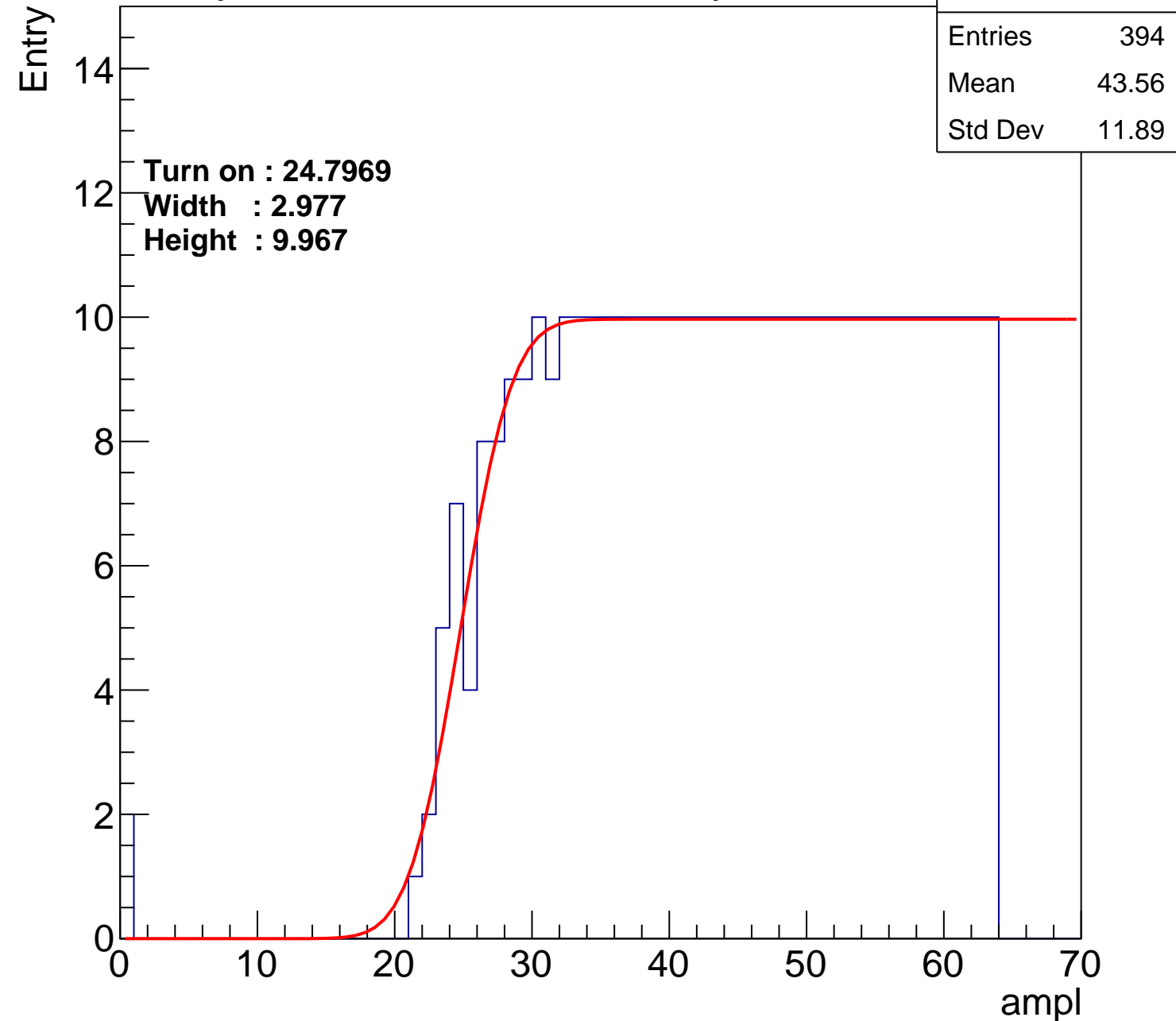
Width : 2.977

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.7
Std Dev	11.75

Turn on : 27.7878

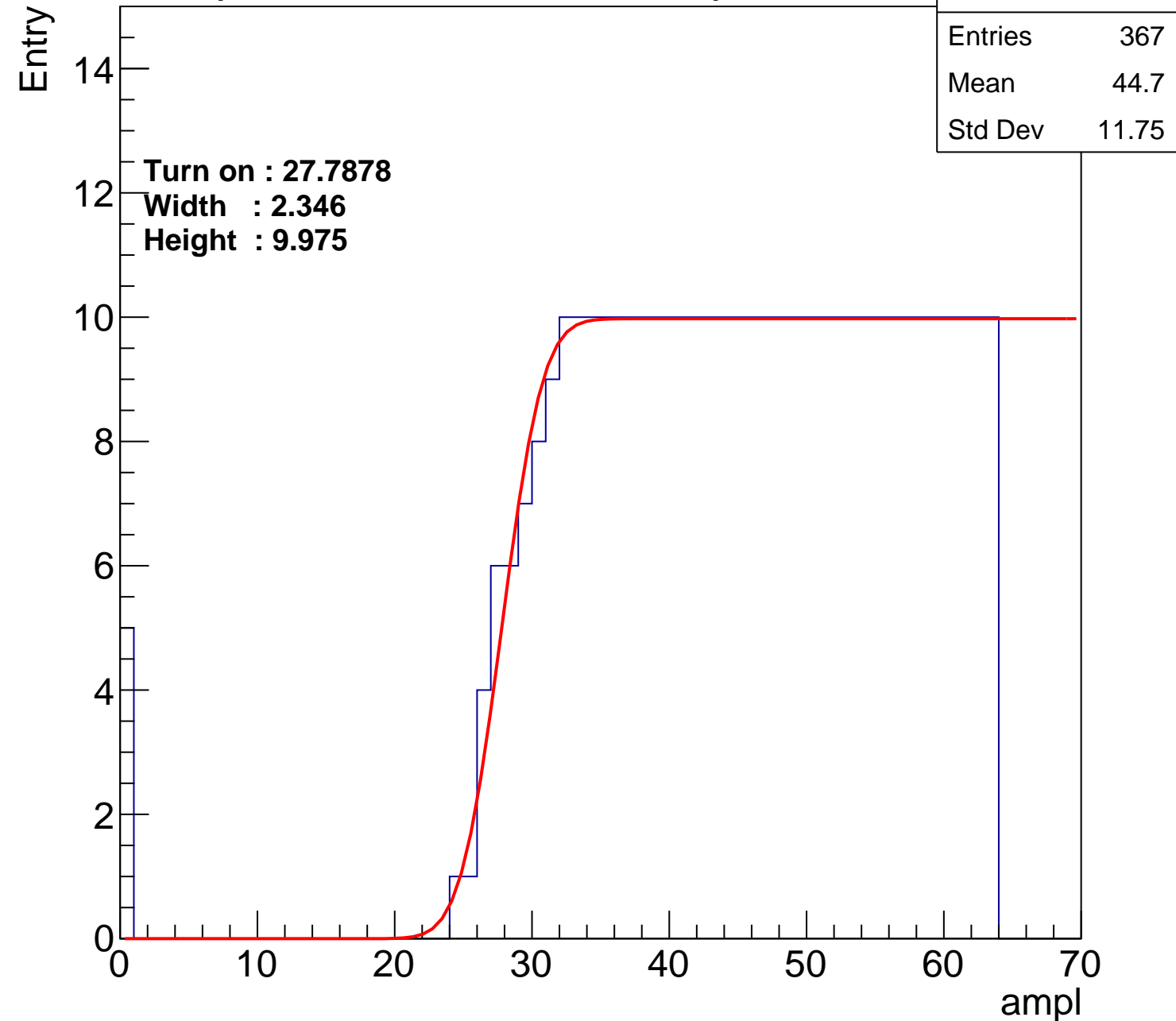
Width : 2.346

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch7

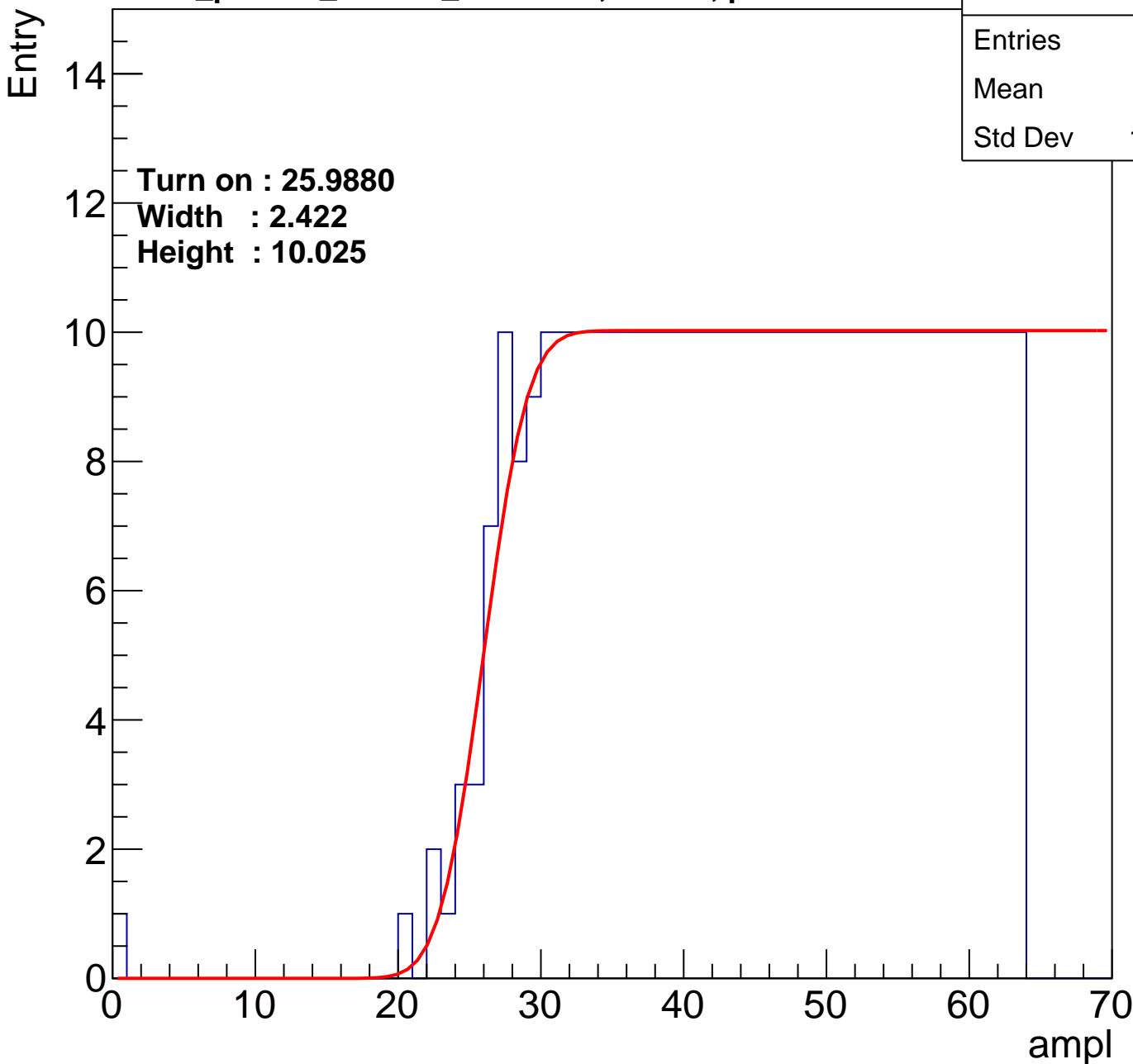
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Entries	385
Mean	44.11
Std Dev	11.43

**Turn on : 25.9880**

**Width : 2.422**

**Height : 10.025**



# B0L102S, U21-ch8

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 25.7881

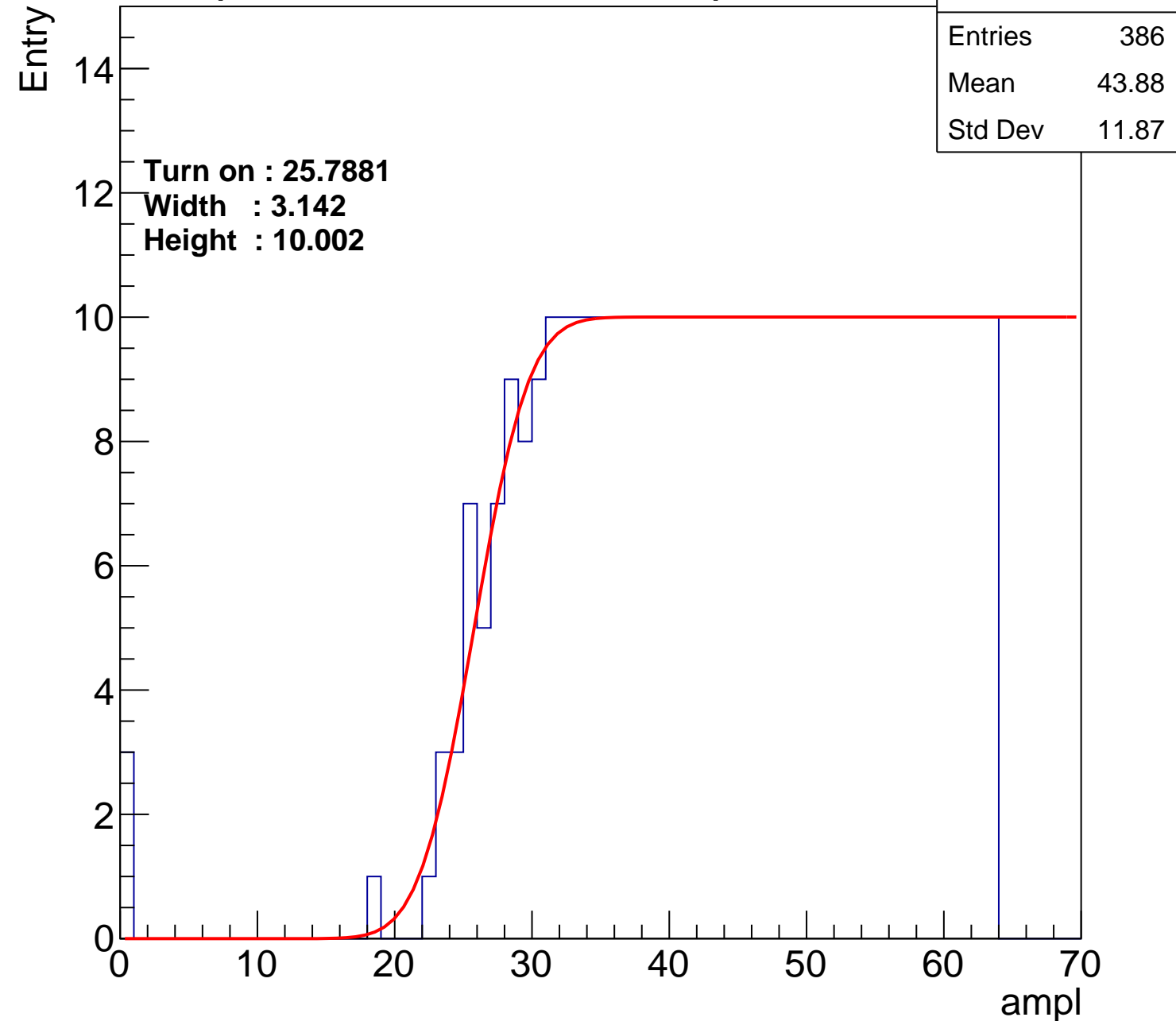
Width : 3.142

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch9

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.6817

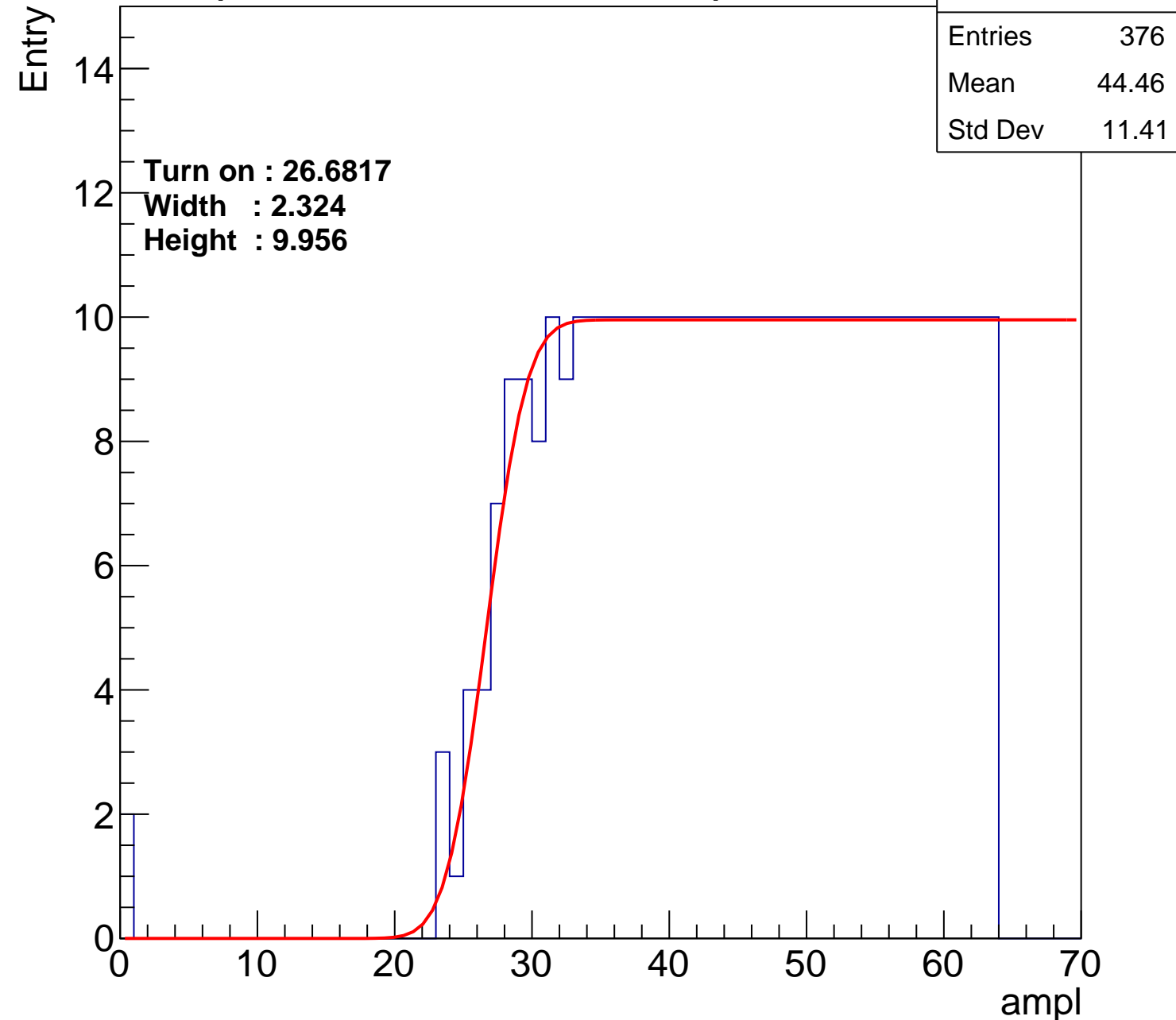
Width : 2.324

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch10

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.7831

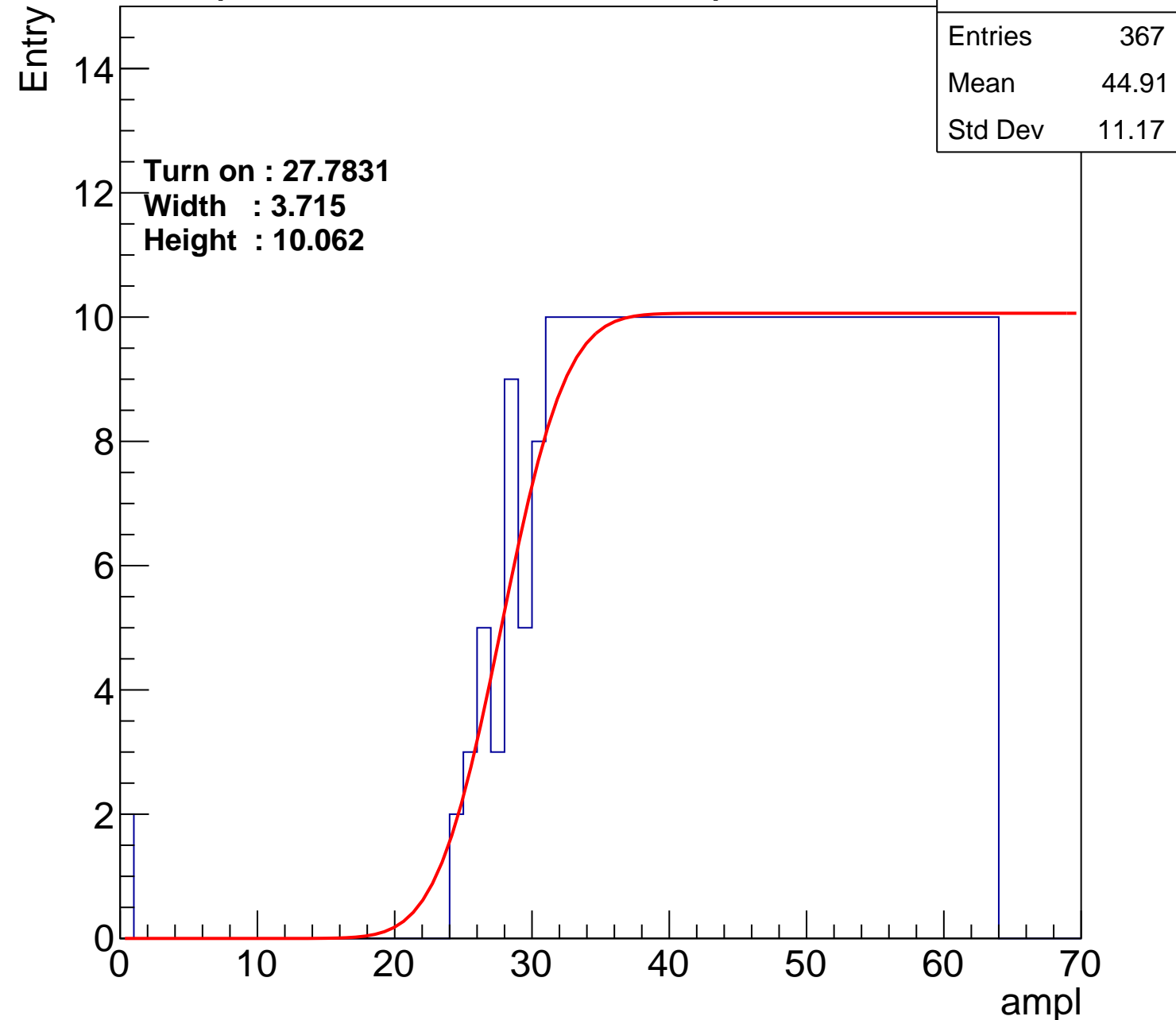
Width : 3.715

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch11

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.4549

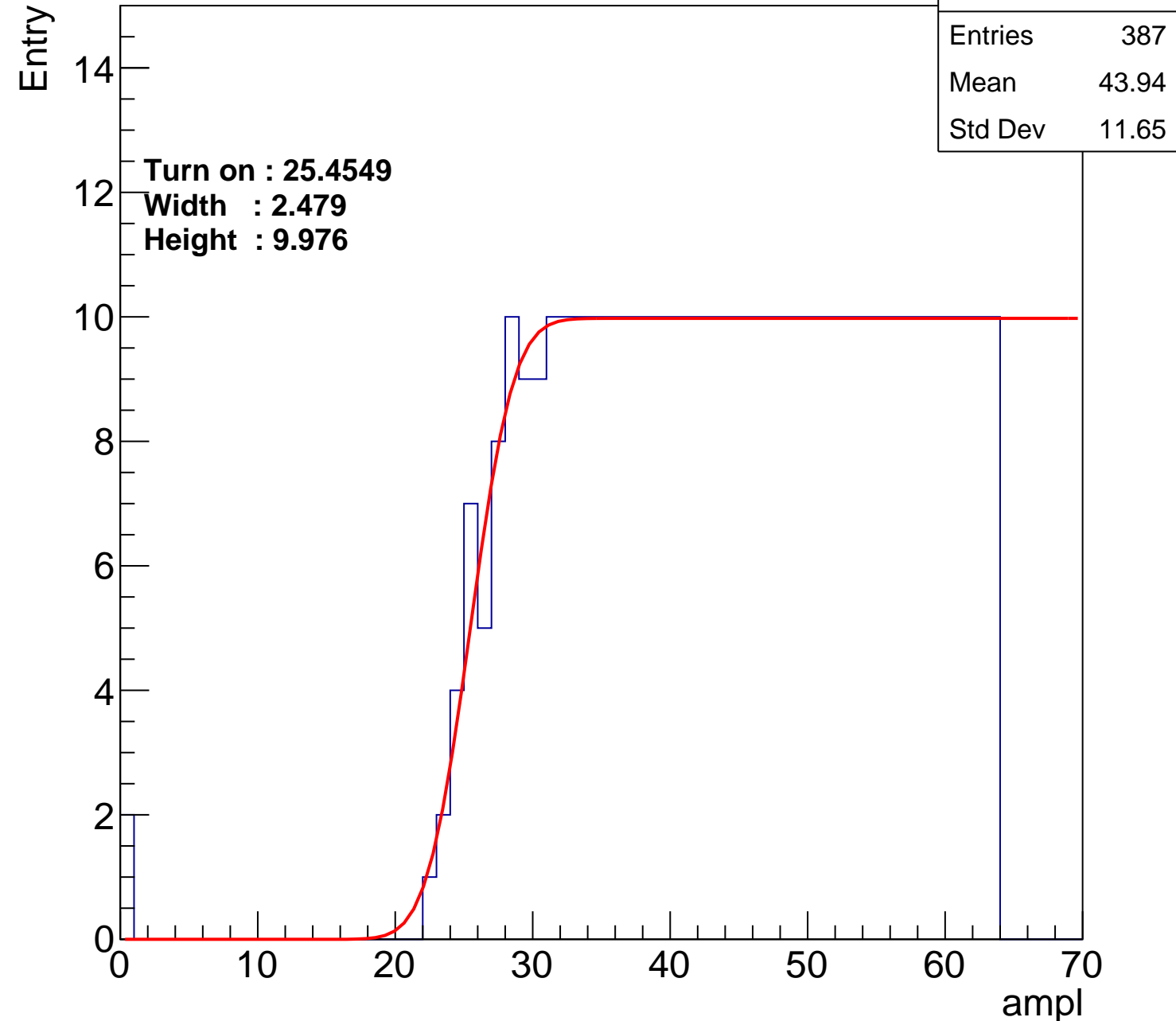
Width : 2.479

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch12

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.89
Std Dev	11.58

Turn on : 26.0088

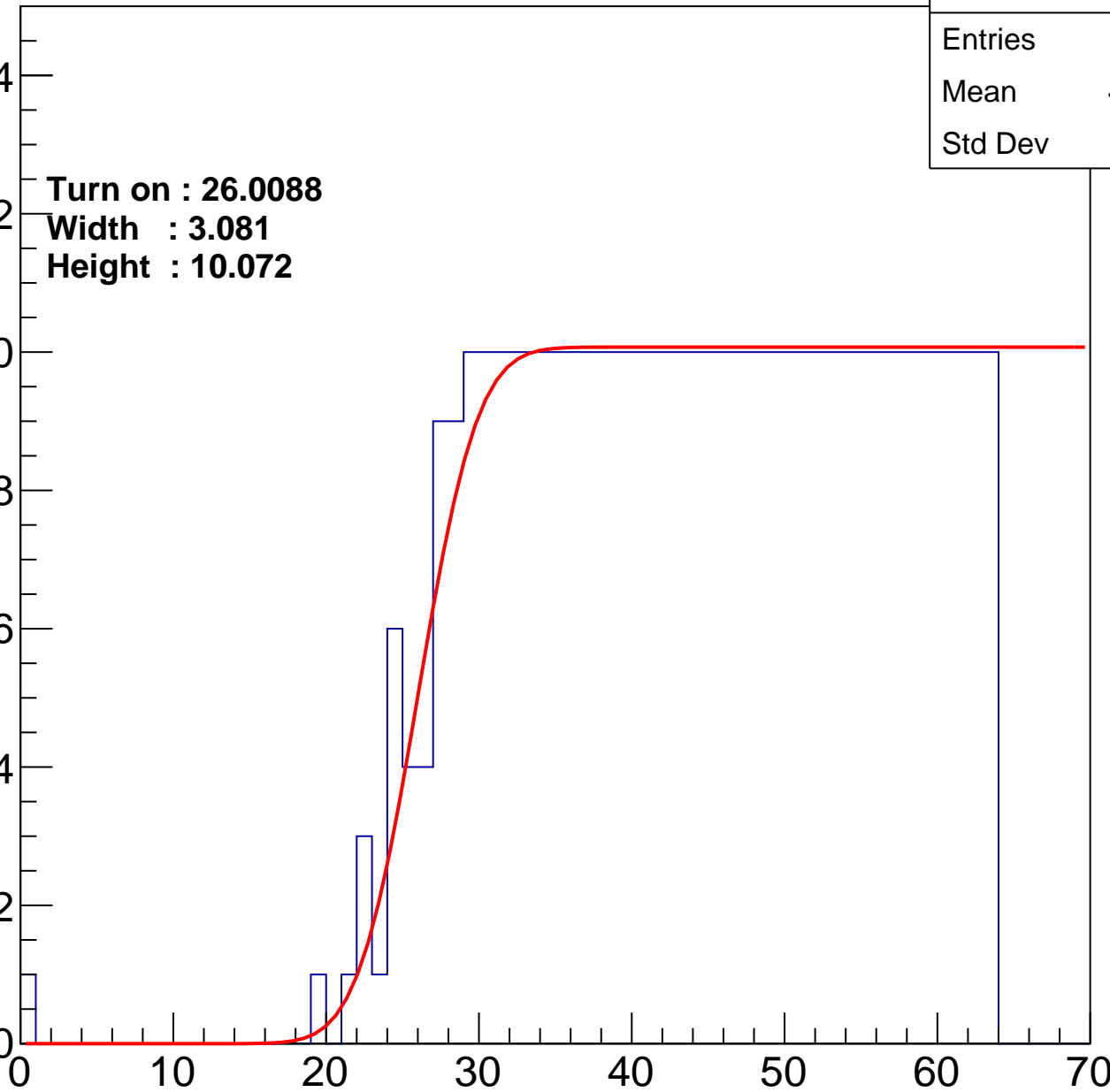
Width : 3.081

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch13

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 26.6843

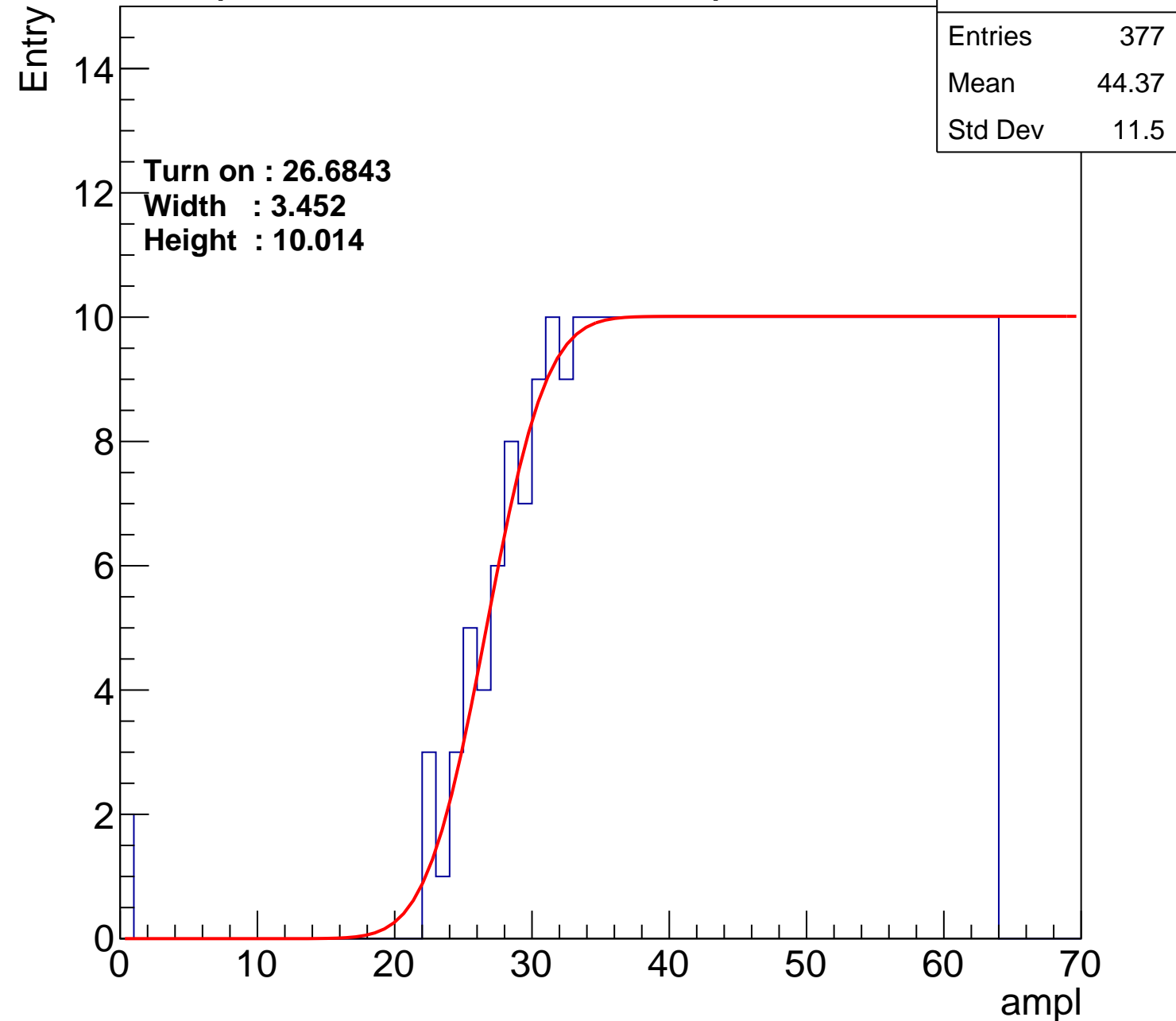
Width : 3.452

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch14

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	406
Mean	42.99
Std Dev	12.17

Turn on : 23.1313

Width : 2.617

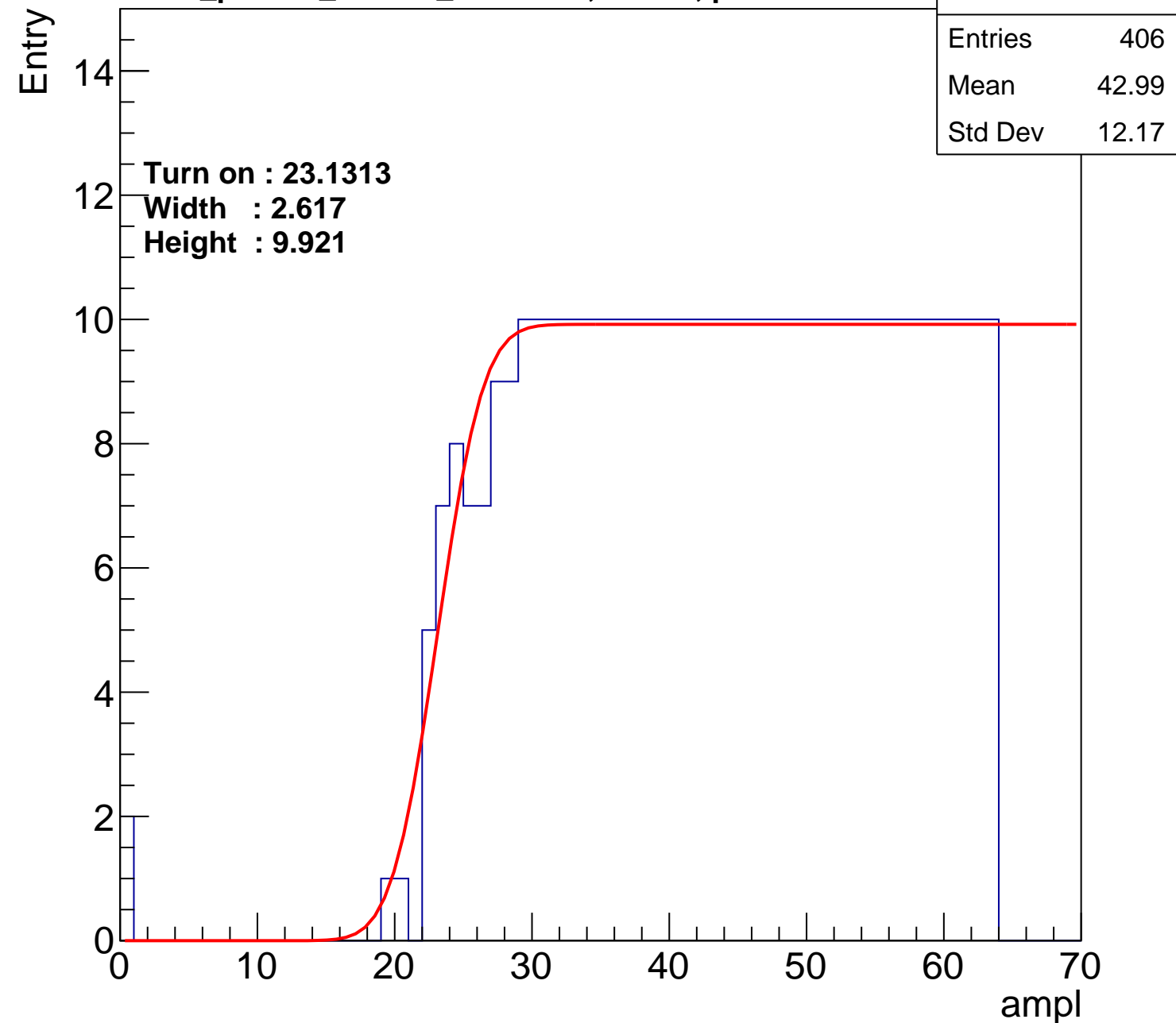
Height : 9.921

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B0L102S, U21-ch15

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 25.3036

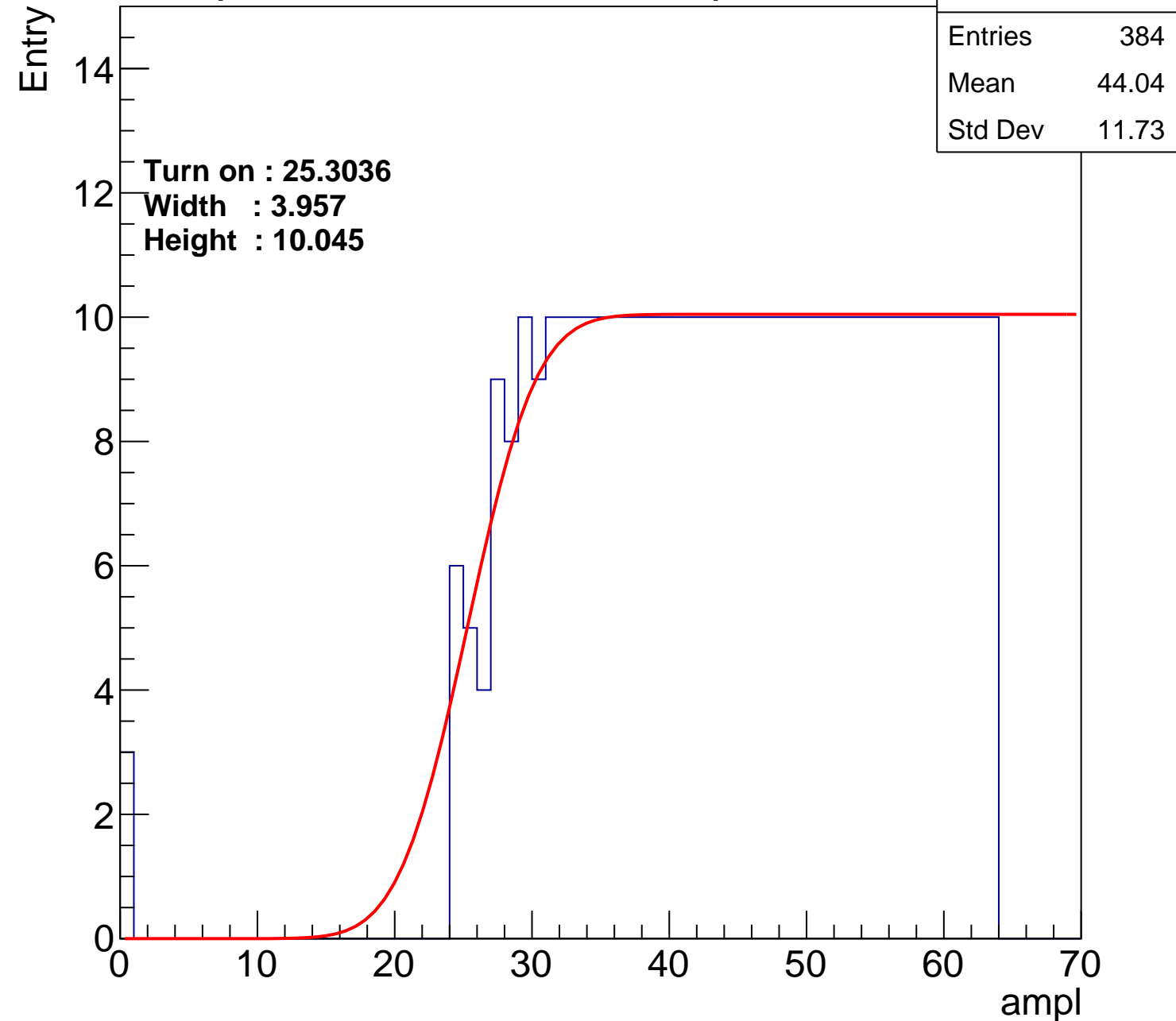
Width : 3.957

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch16

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.4330

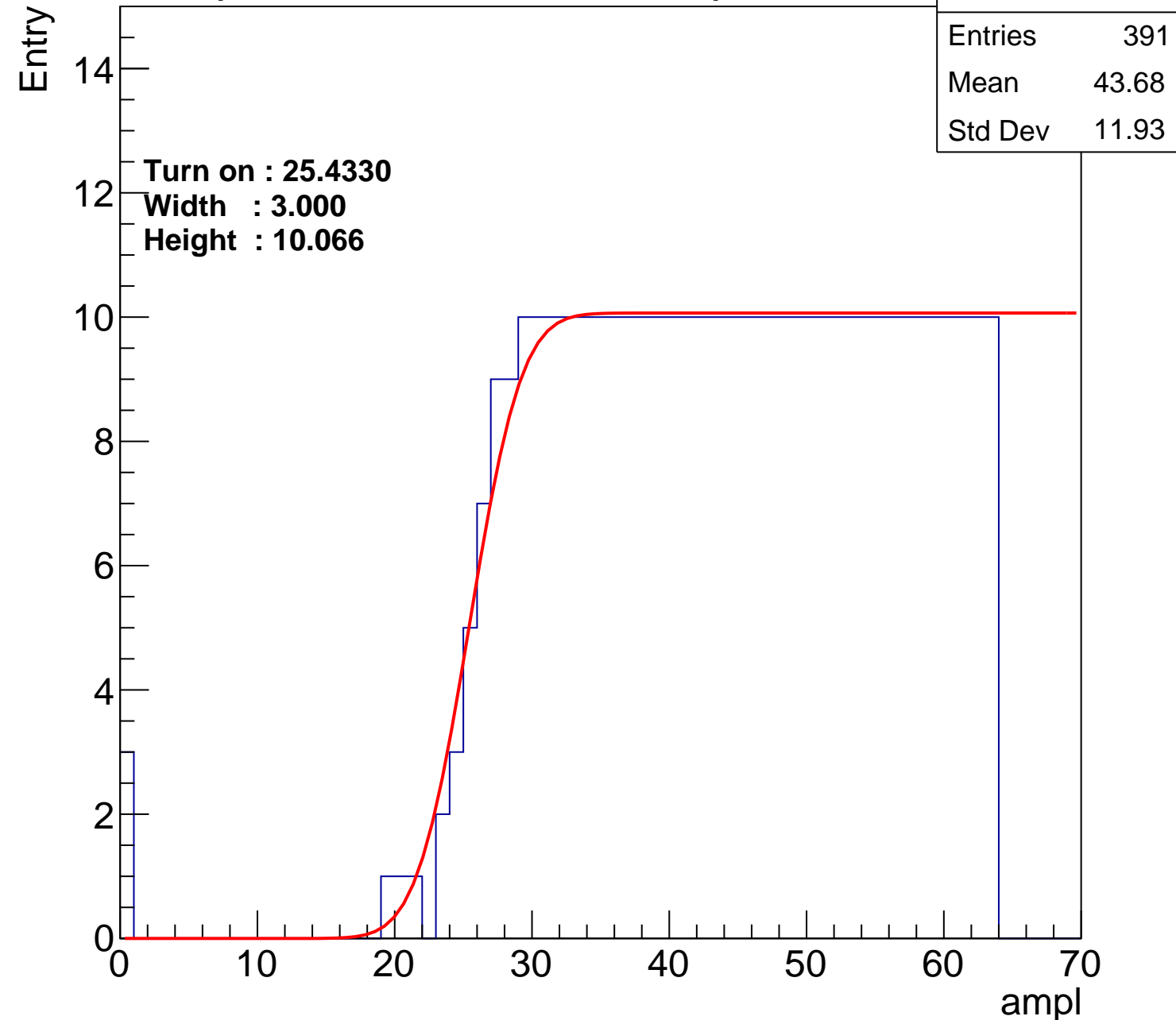
Width : 3.000

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch17

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	44.98
Std Dev	11.31

Turn on : 28.3779

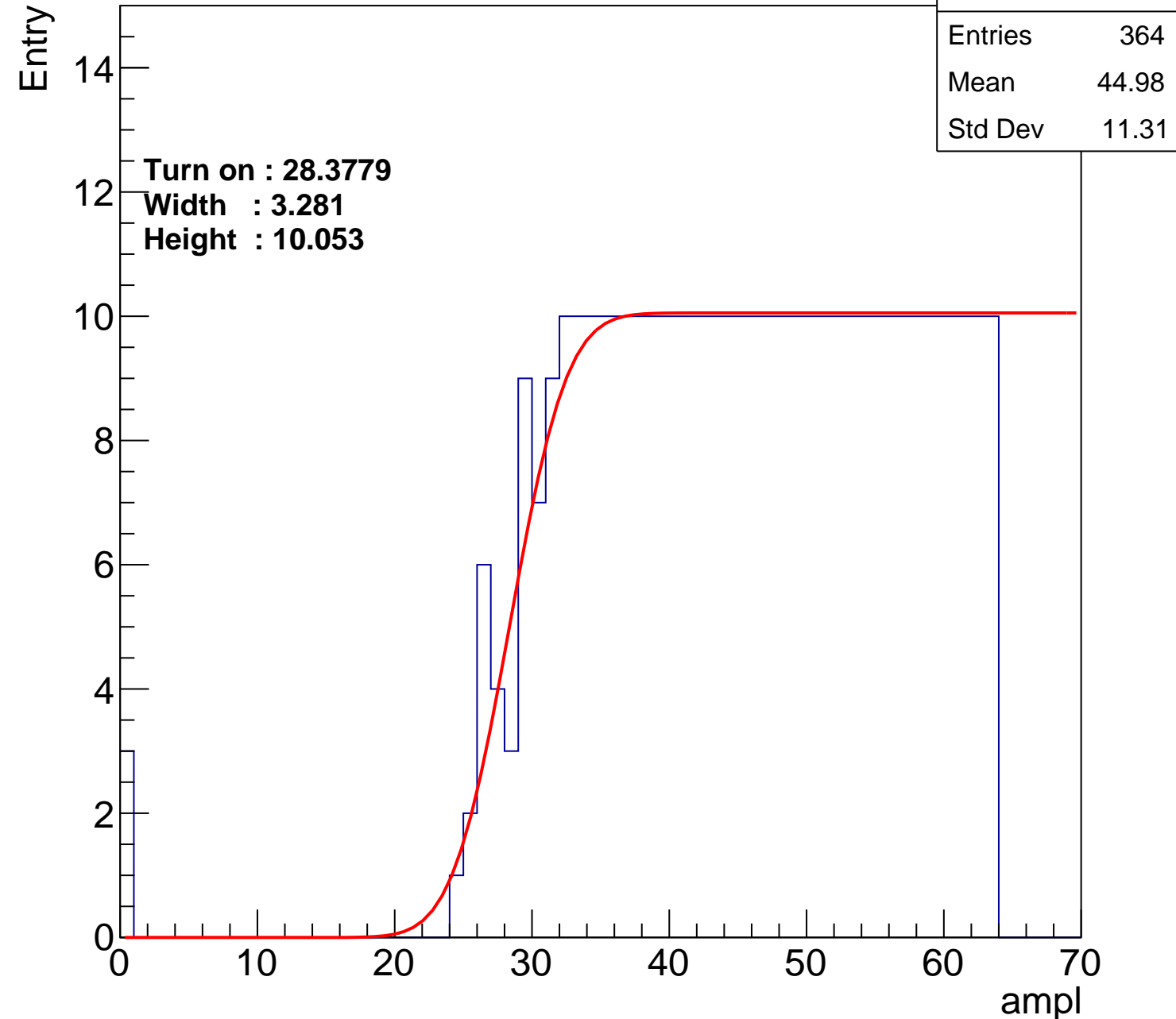
Width : 3.281

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch18

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.8042

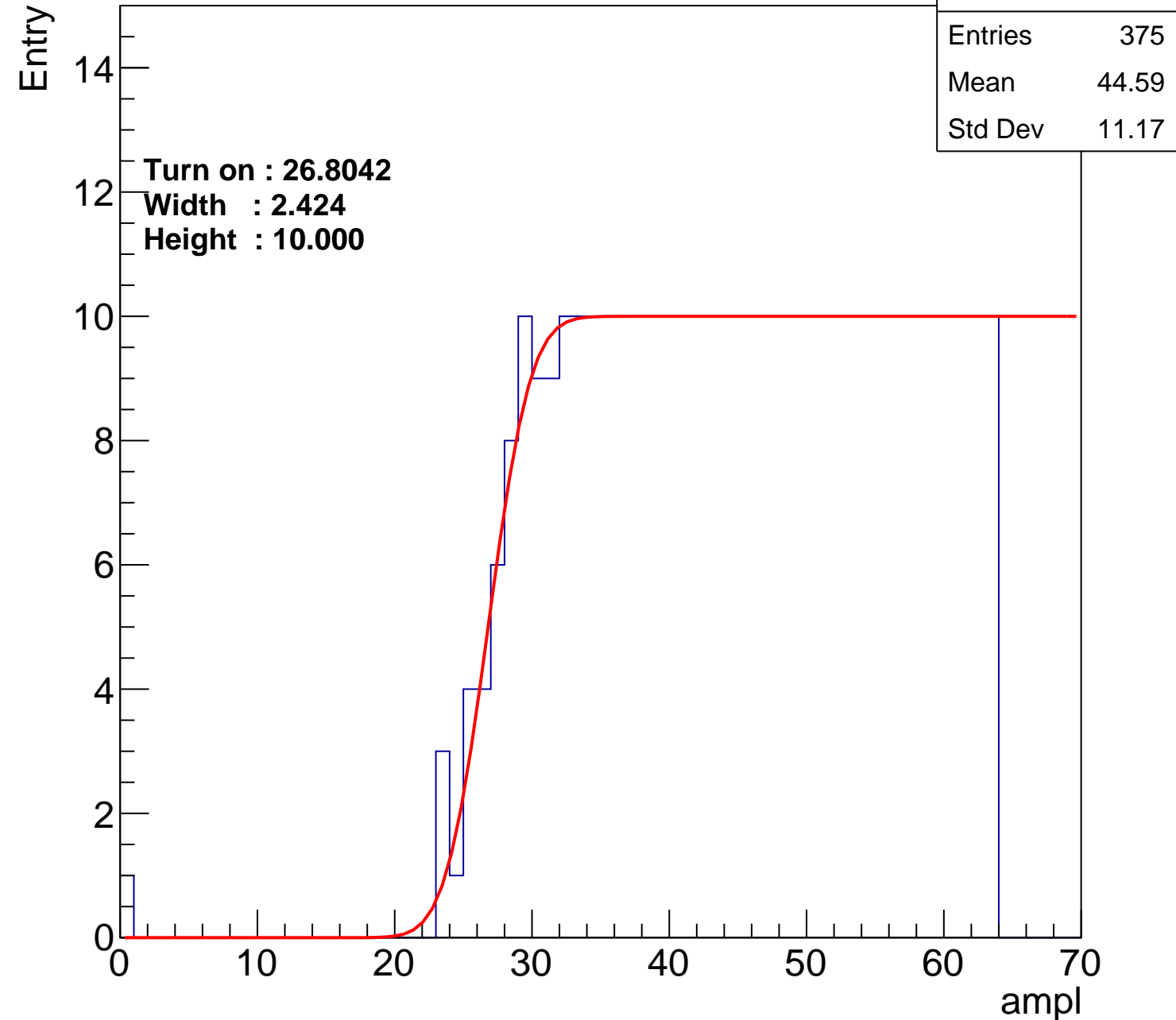
Width : 2.424

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch19

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.2430

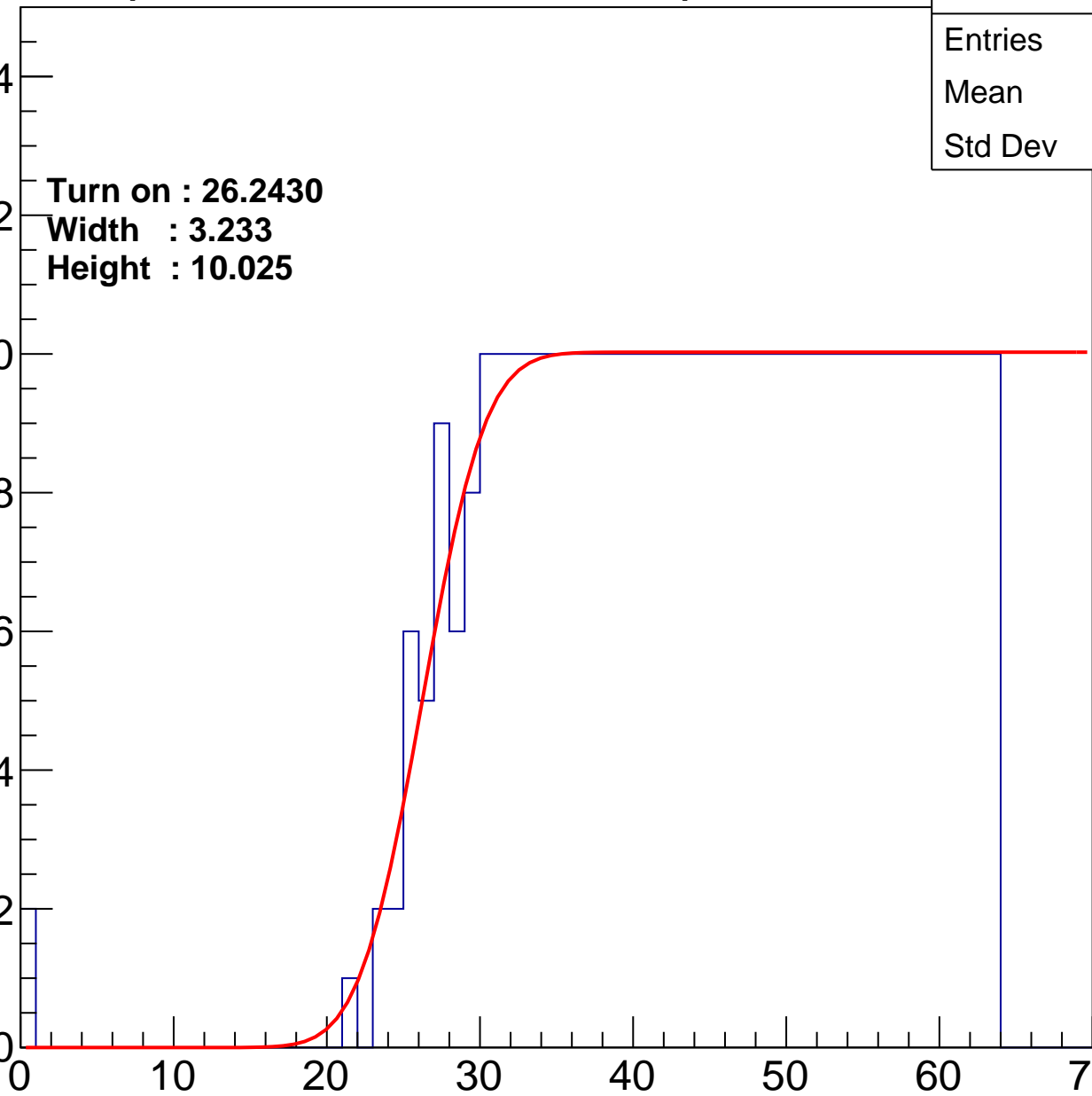
Width : 3.233

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch20

calib\_packv5\_042523\_0143.root, FC#12, port B1

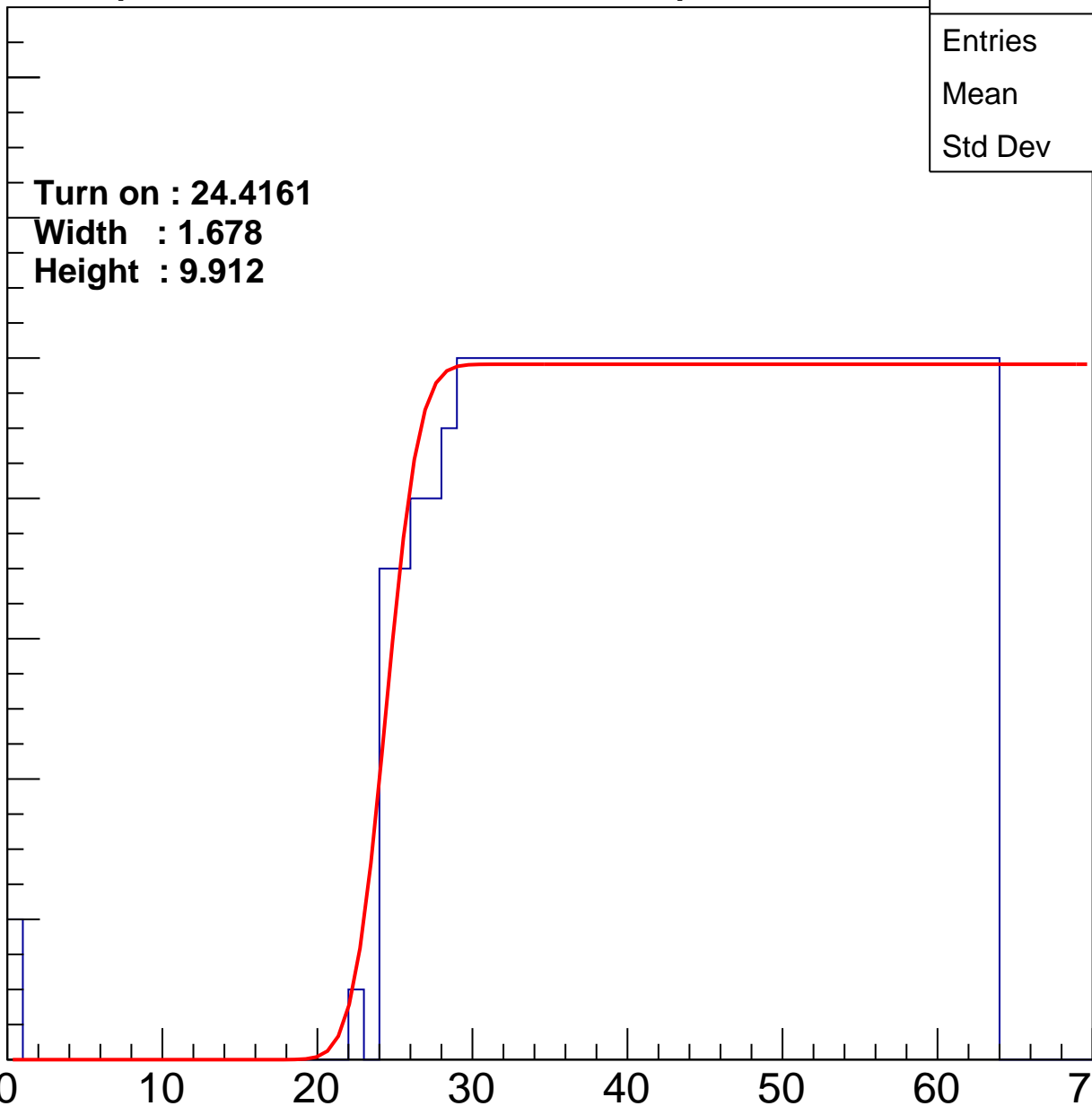
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 24.4161  
Width : 1.678  
Height : 9.912

Entries	392
Mean	43.73
Std Dev	11.73

ampl



# B0L102S, U21-ch21

calib\_packv5\_042523\_0143.root, FC#12, port B1

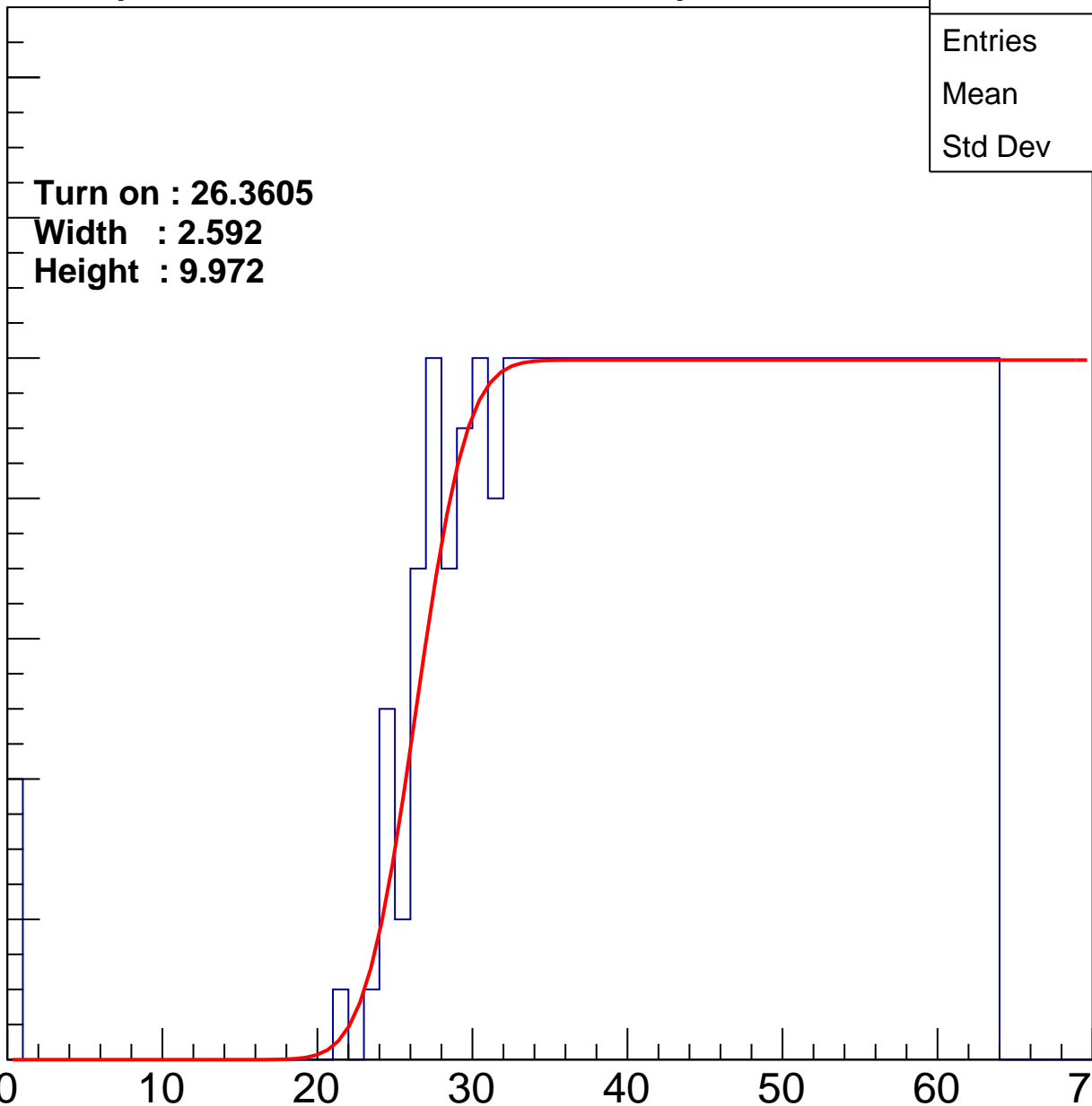
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.3605  
Width : 2.592  
Height : 9.972

Entries	384
Mean	43.93
Std Dev	11.96

ampl



# B0L102S, U21-ch22

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 27.0604

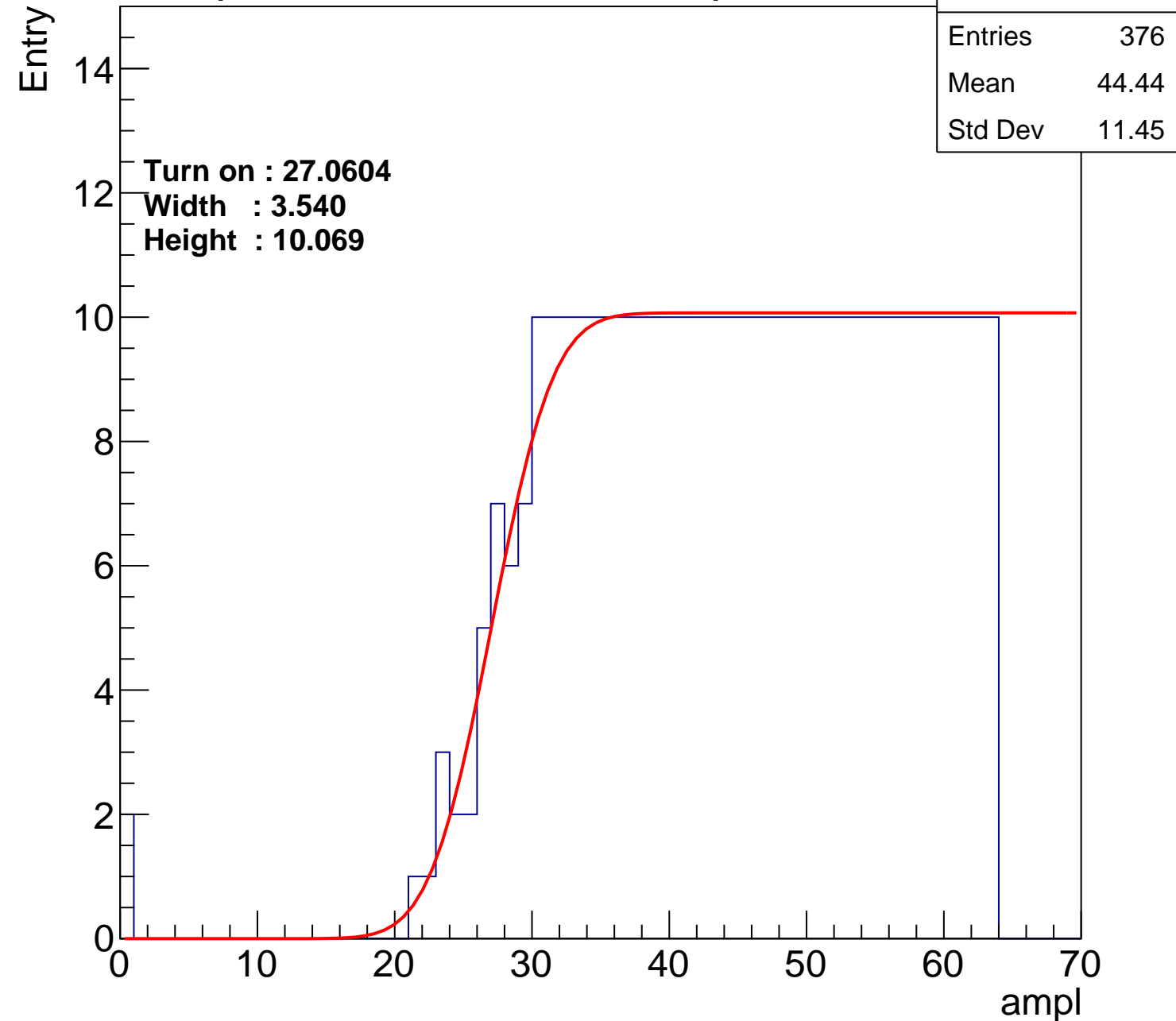
Width : 3.540

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch23

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 28.2031

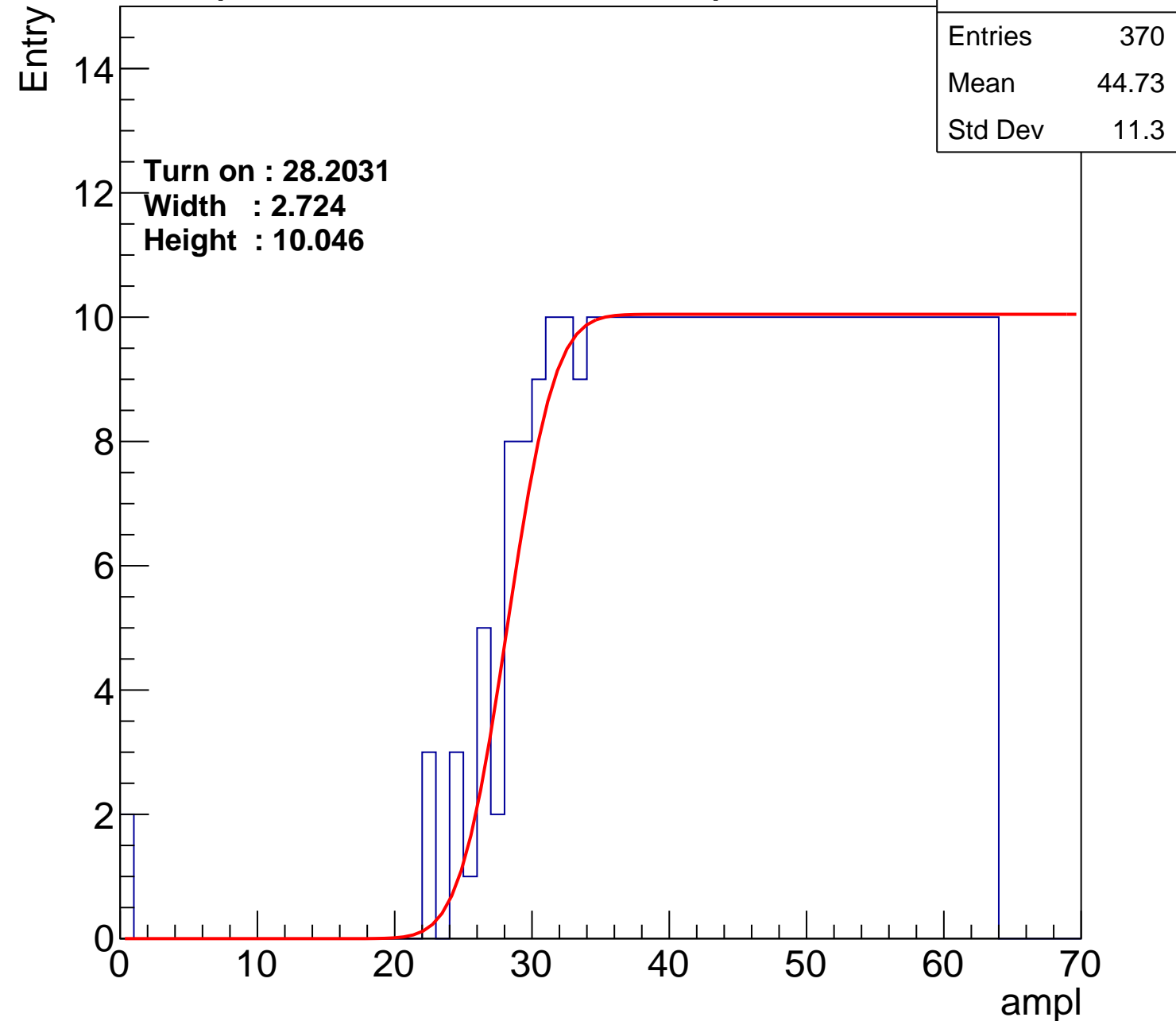
Width : 2.724

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch24

calib\_packv5\_042523\_0143.root, FC#12, port B1

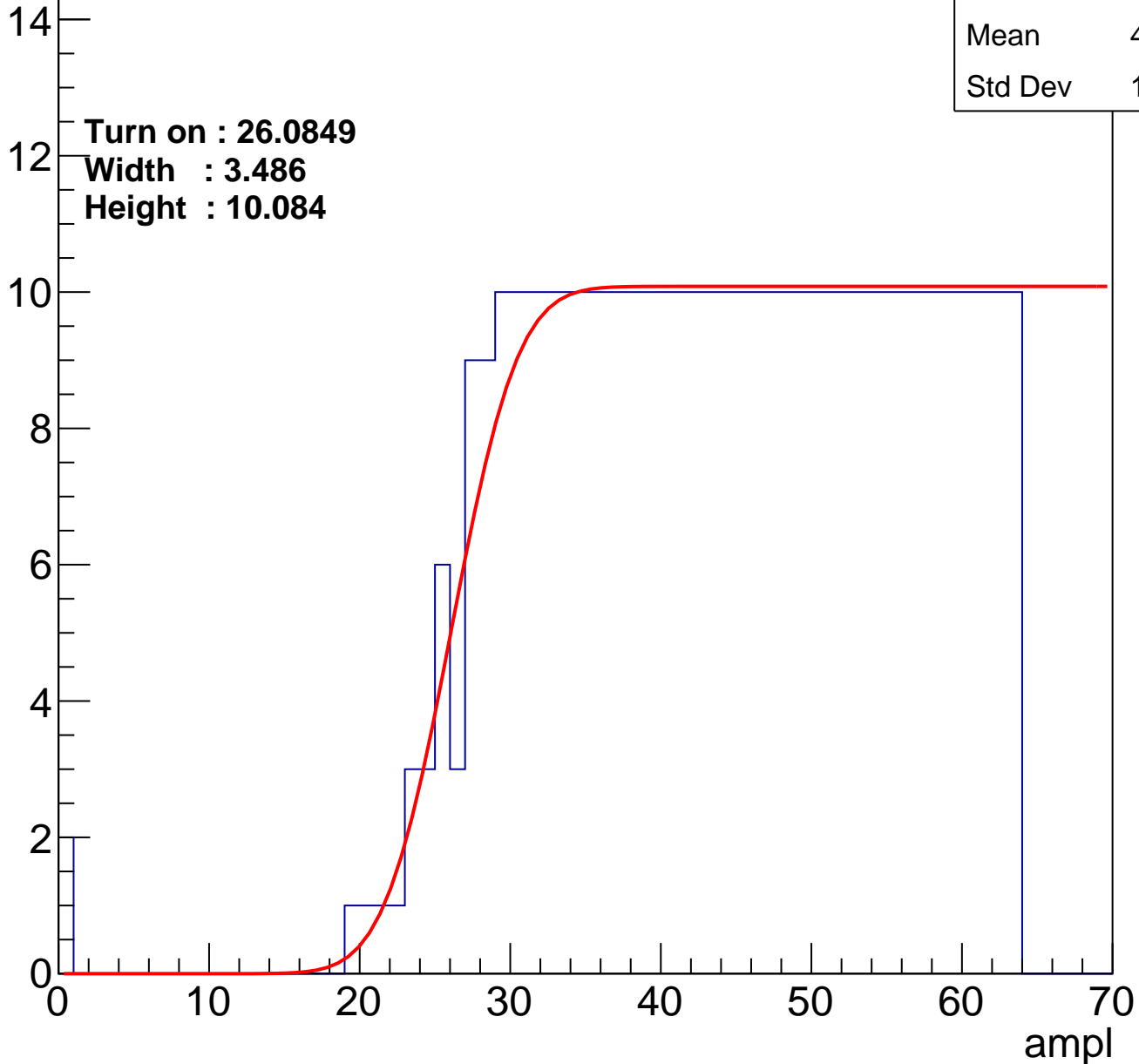
Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 26.0849

Width : 3.486

Height : 10.084

Entry



# B0L102S, U21-ch25

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.1627

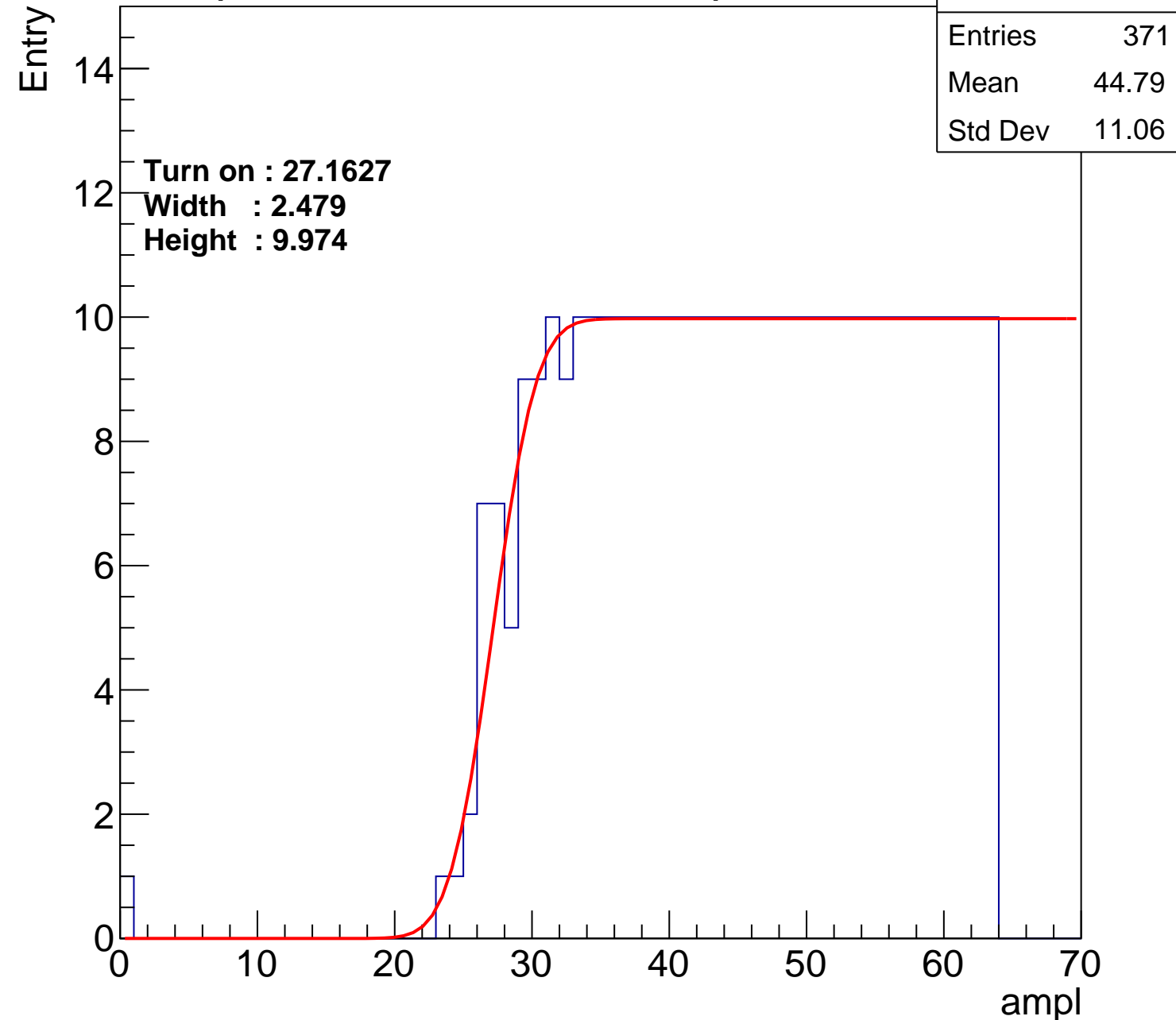
Width : 2.479

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch26

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.6521

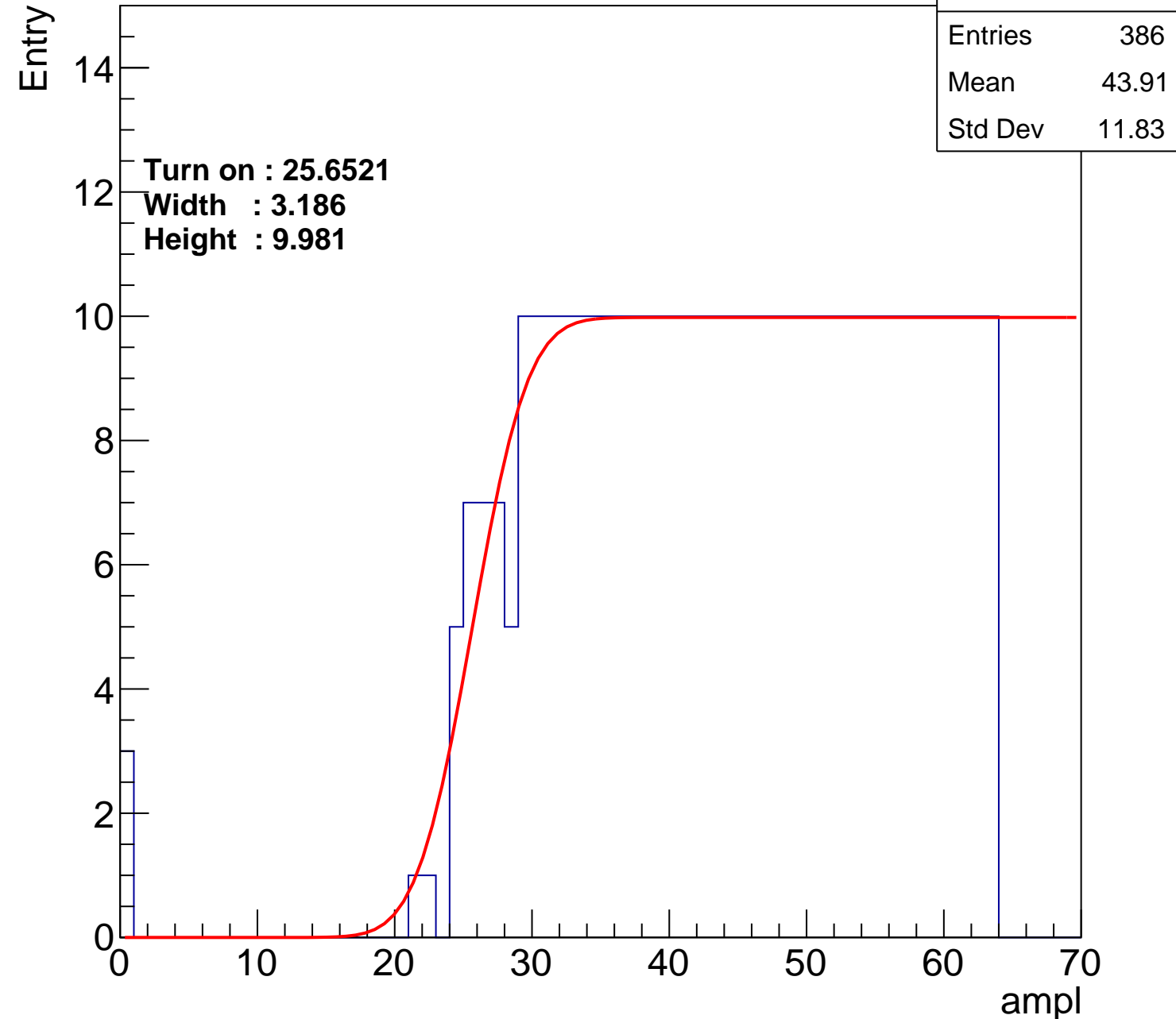
Width : 3.186

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch27

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.7856

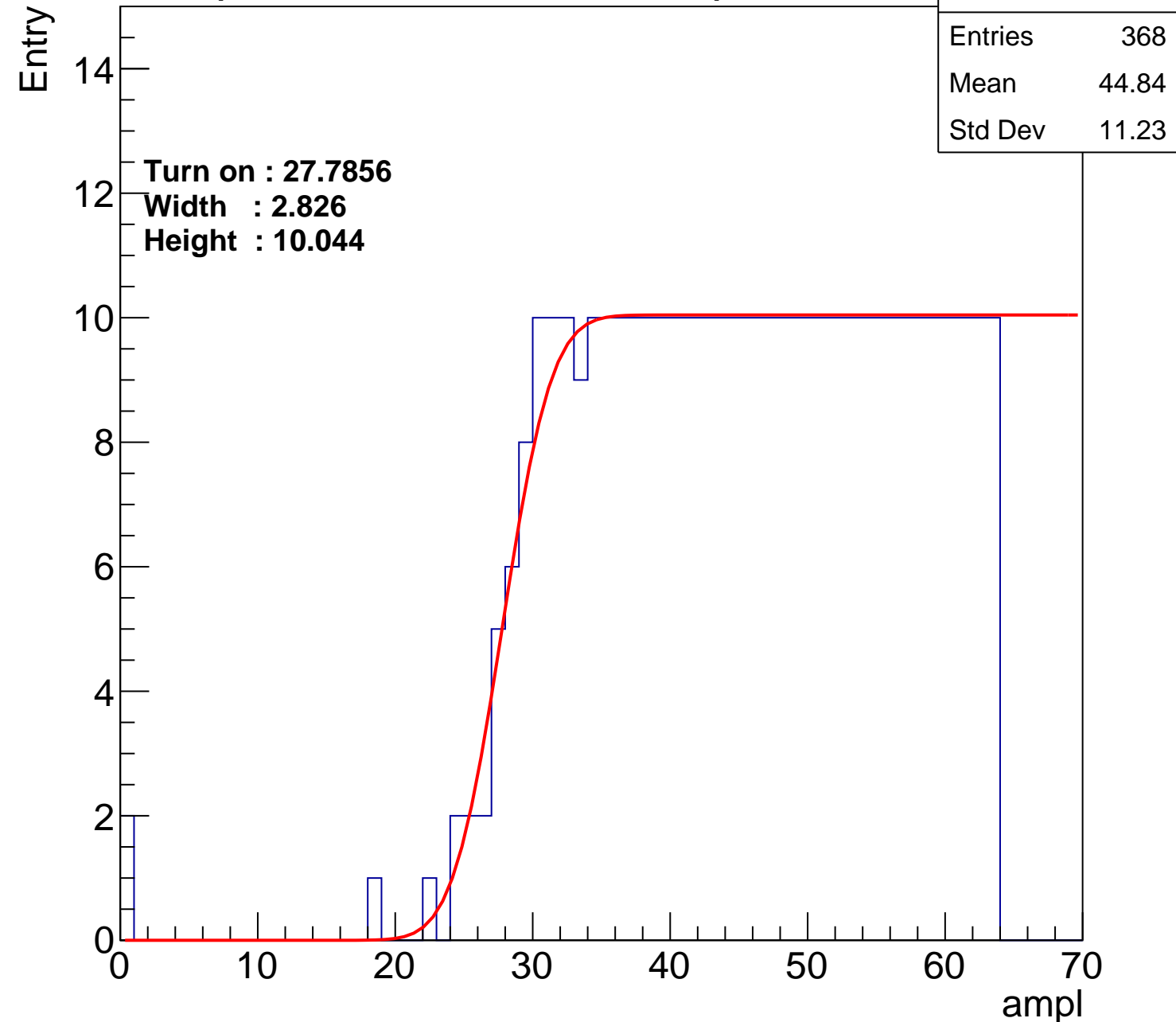
Width : 2.826

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch28

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	406
Mean	42.97
Std Dev	12.27

Turn on : 24.0810

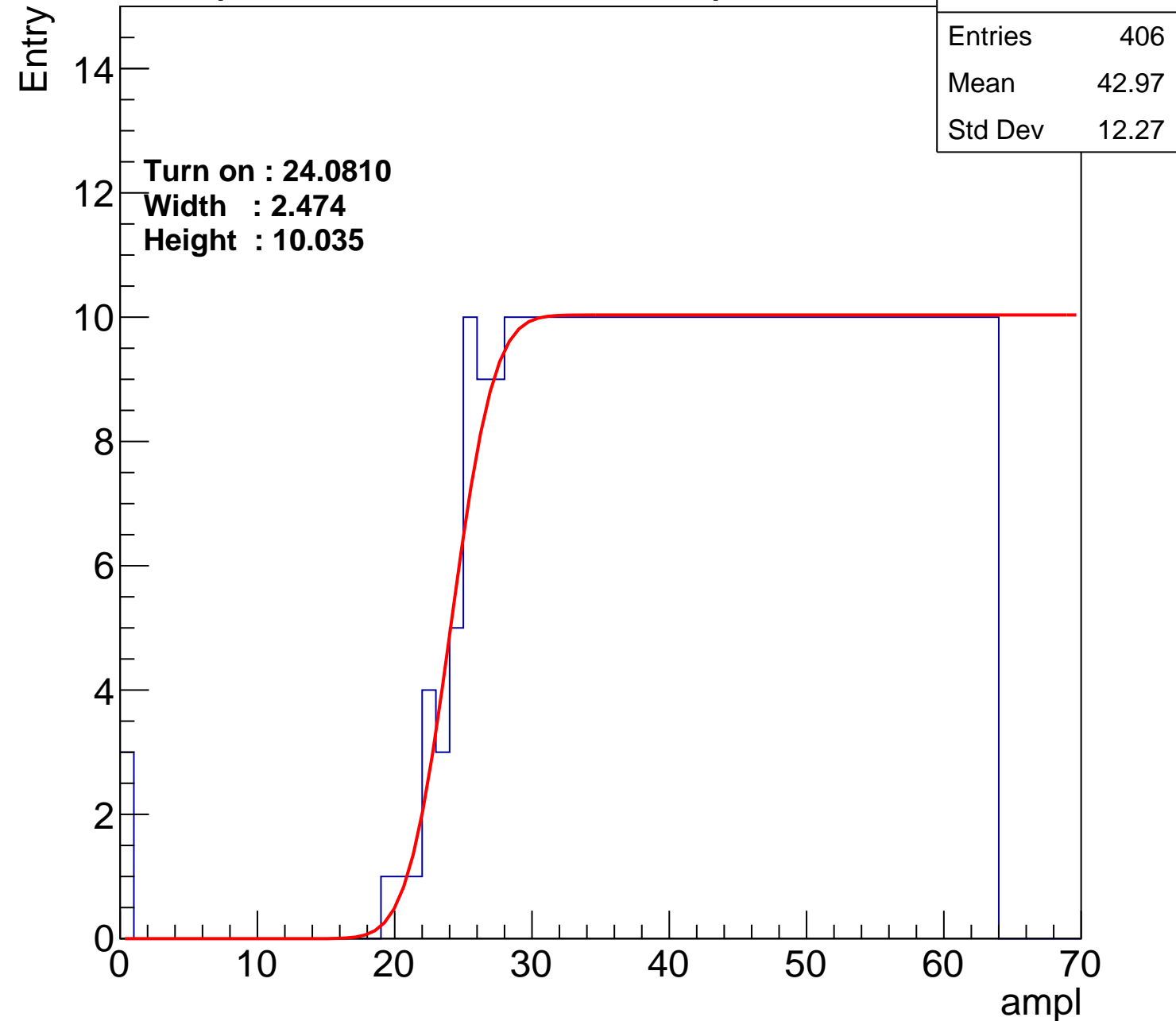
Width : 2.474

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch29

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	354
Mean	45.55
Std Dev	10.84

Turn on : 29.1439

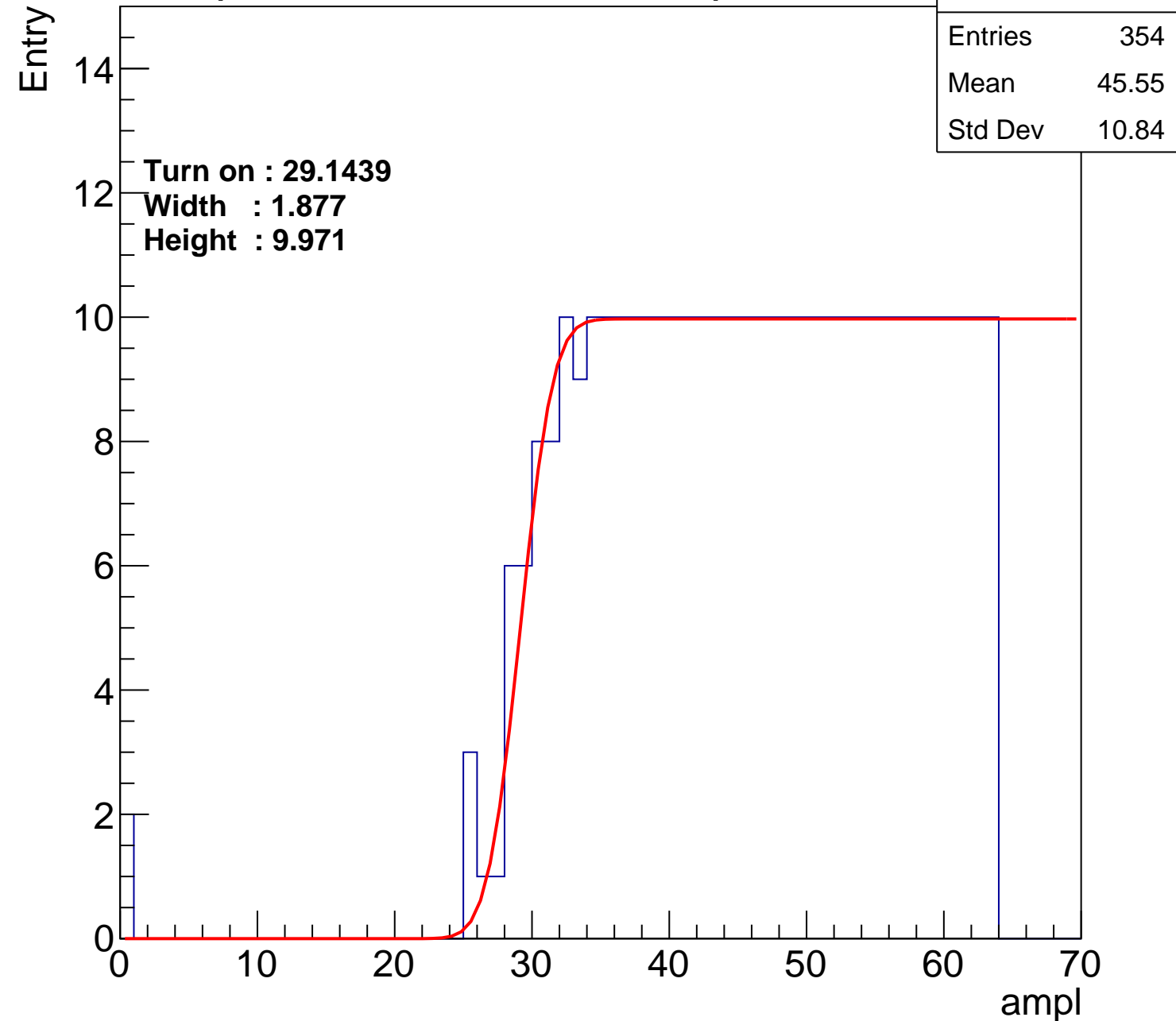
Width : 1.877

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch30

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.42
Std Dev	11.61

**Turn on : 26.9246**

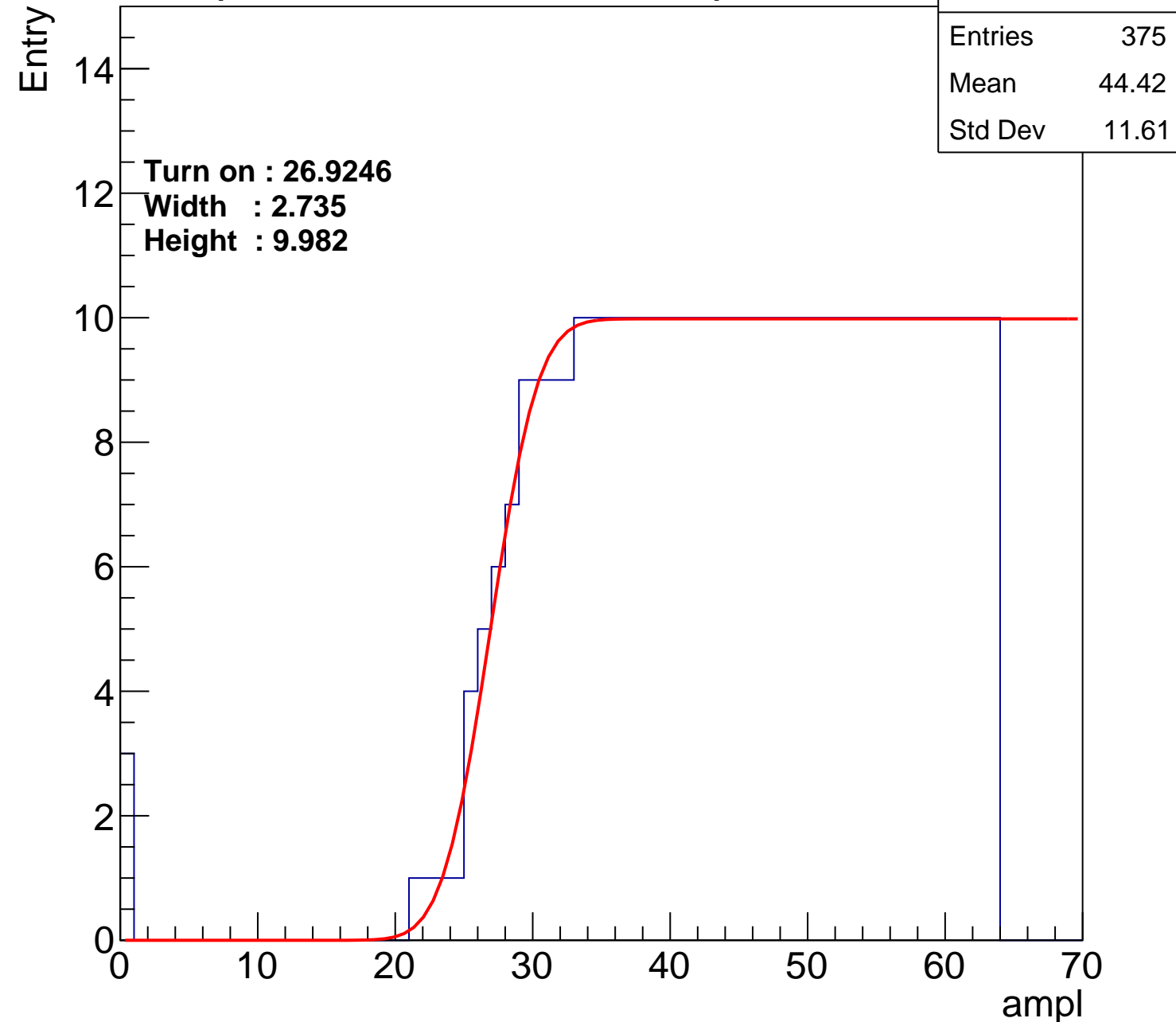
**Width : 2.735**

**Height : 9.982**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch31

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.1507

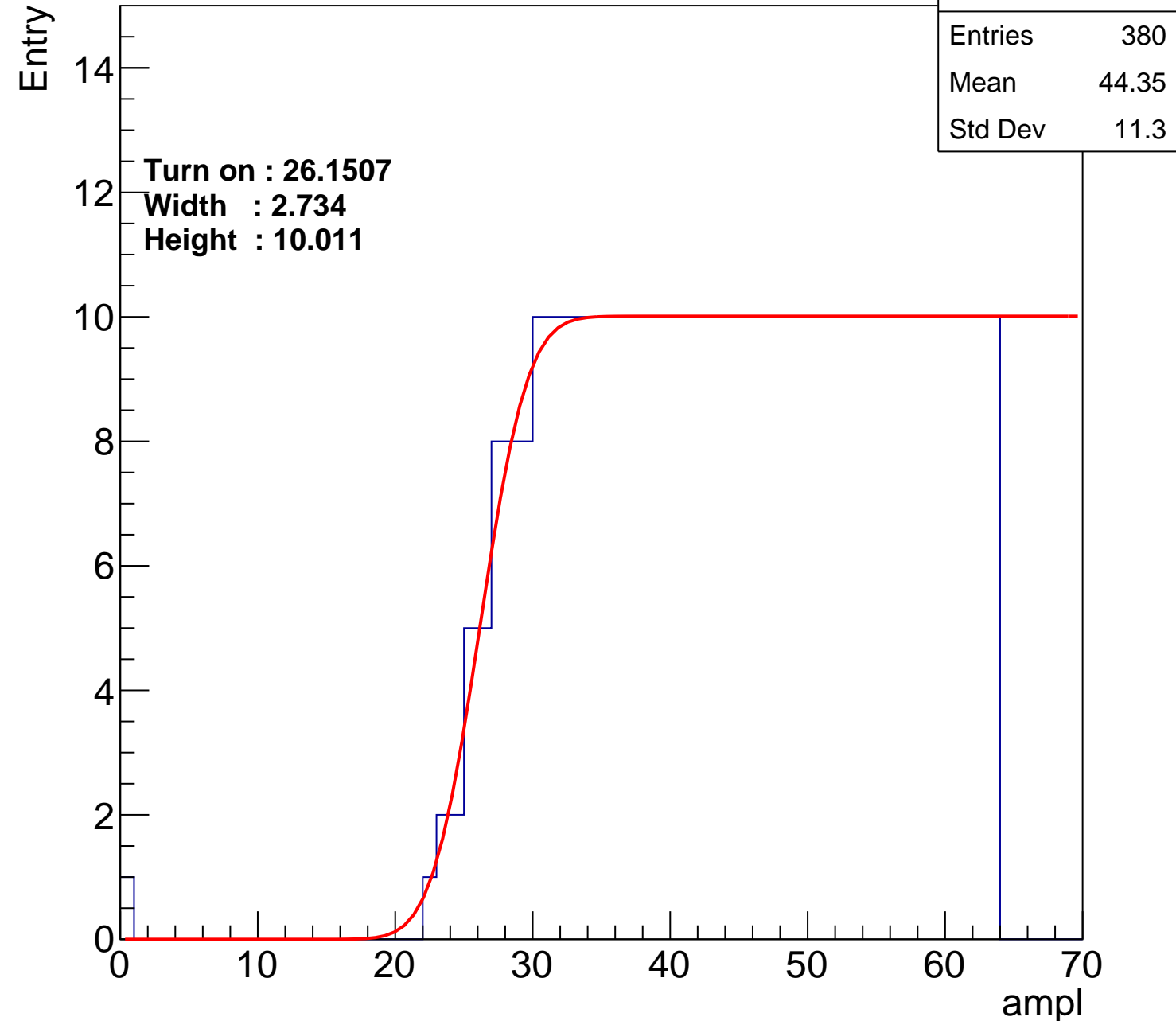
Width : 2.734

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch32

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.45
Std Dev	11.92

Turn on : 25.2013

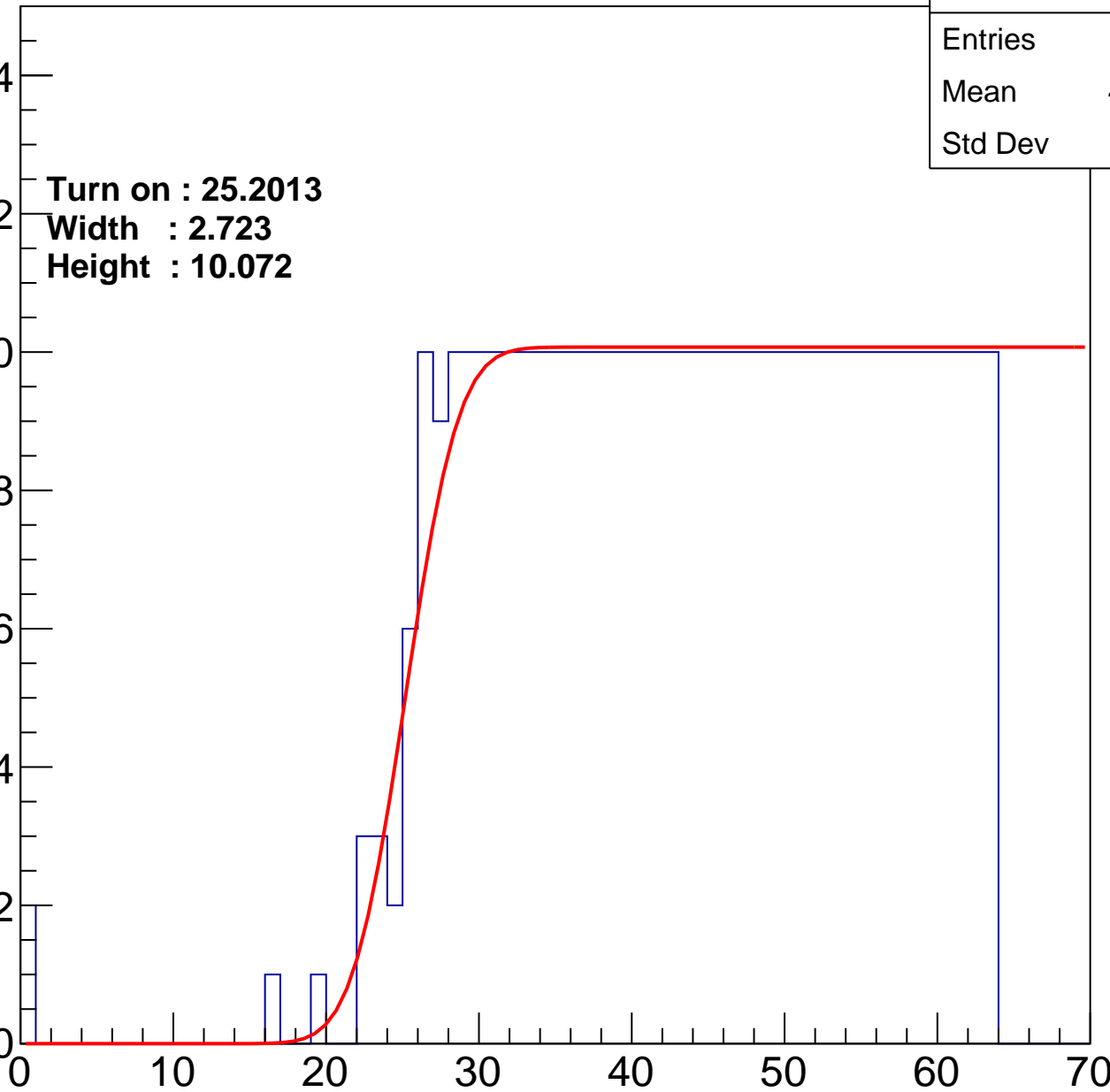
Width : 2.723

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch33

calib\_packv5\_042523\_0143.root, FC#12, port B1

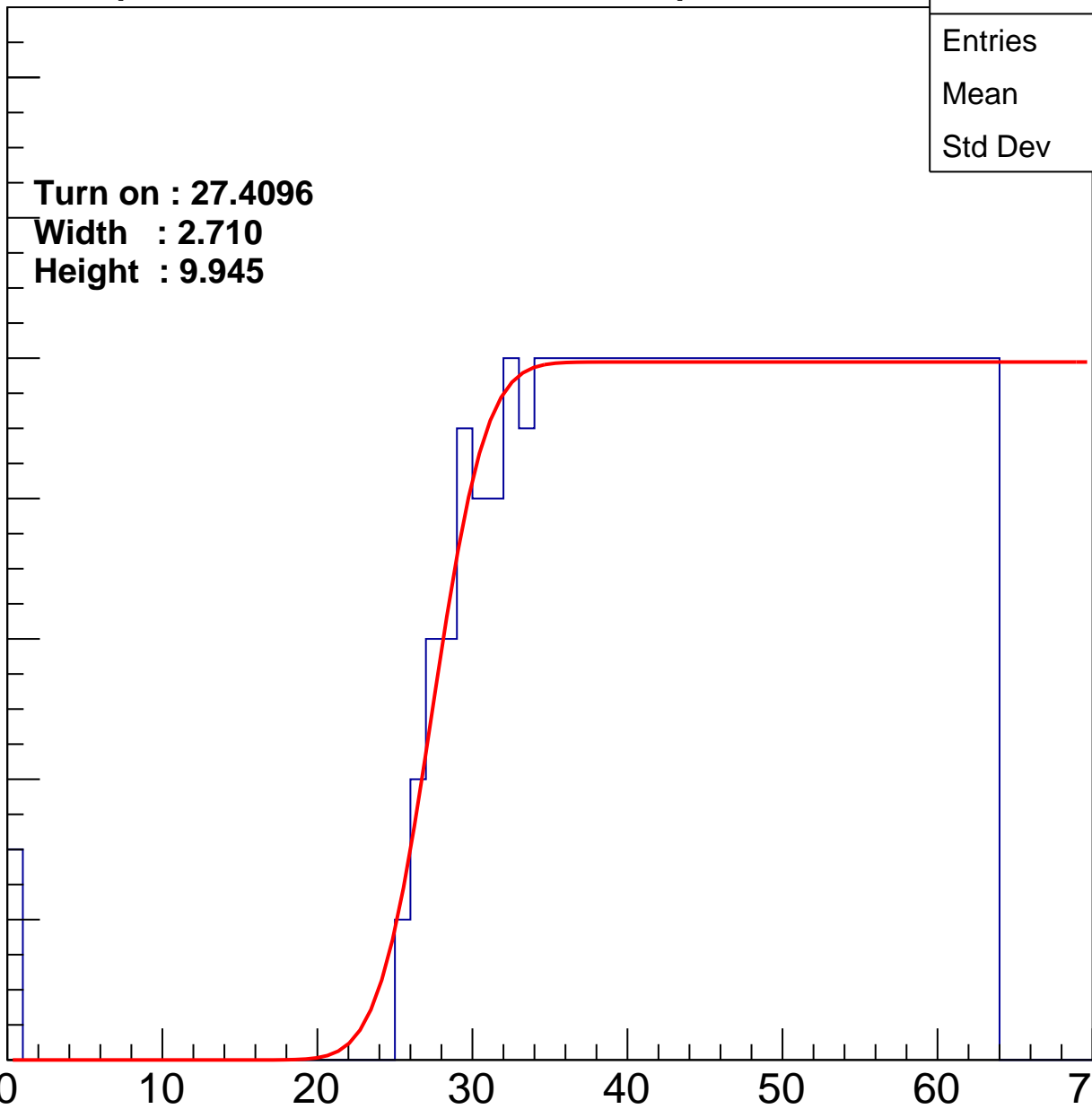
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4096  
Width : 2.710  
Height : 9.945

Entries	365
Mean	44.93
Std Dev	11.32

ampl



# B0L102S, U21-ch34

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.0432

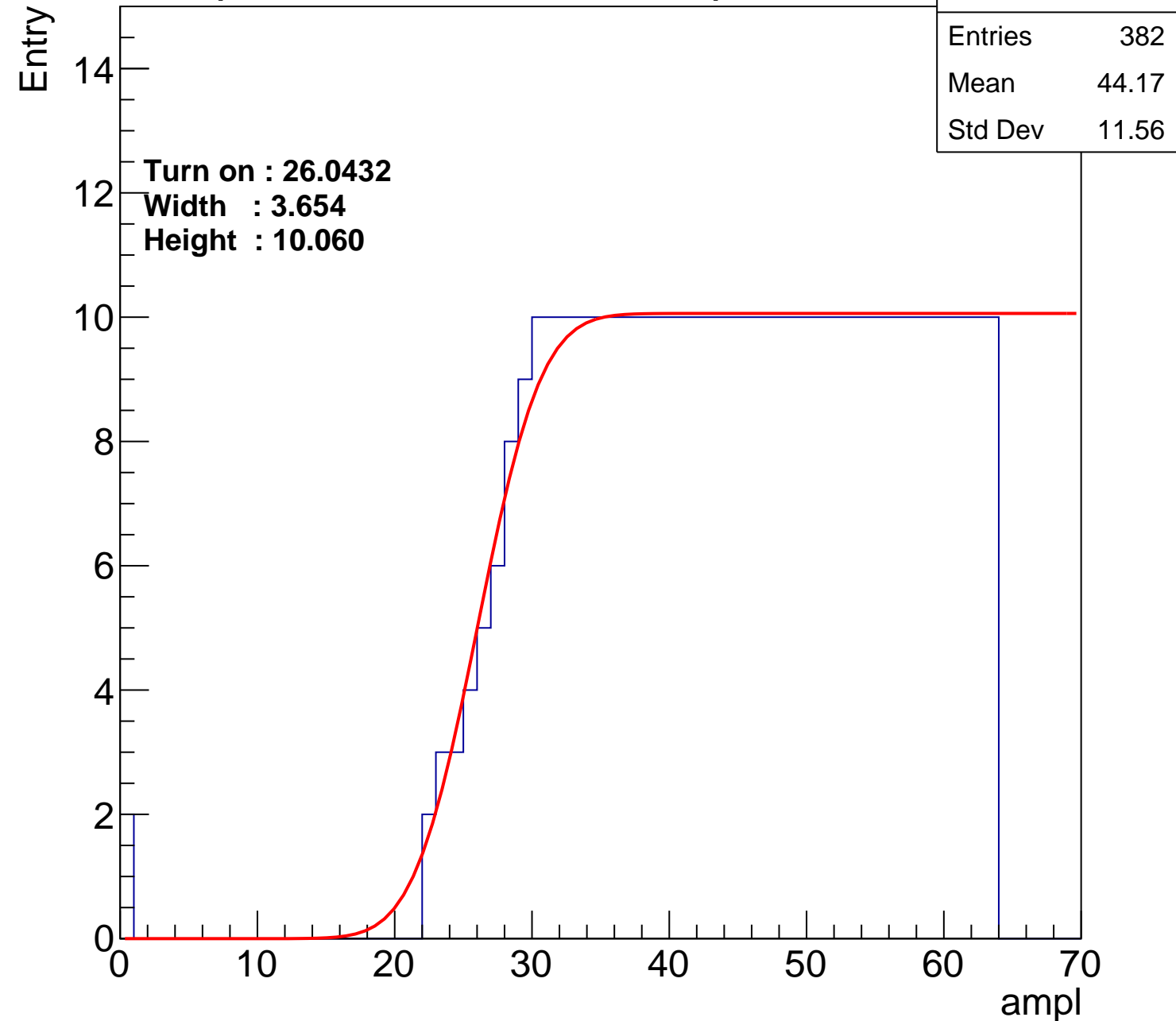
Width : 3.654

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch35

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	356
Mean	45.54
Std Dev	10.64

Turn on : 28.1520

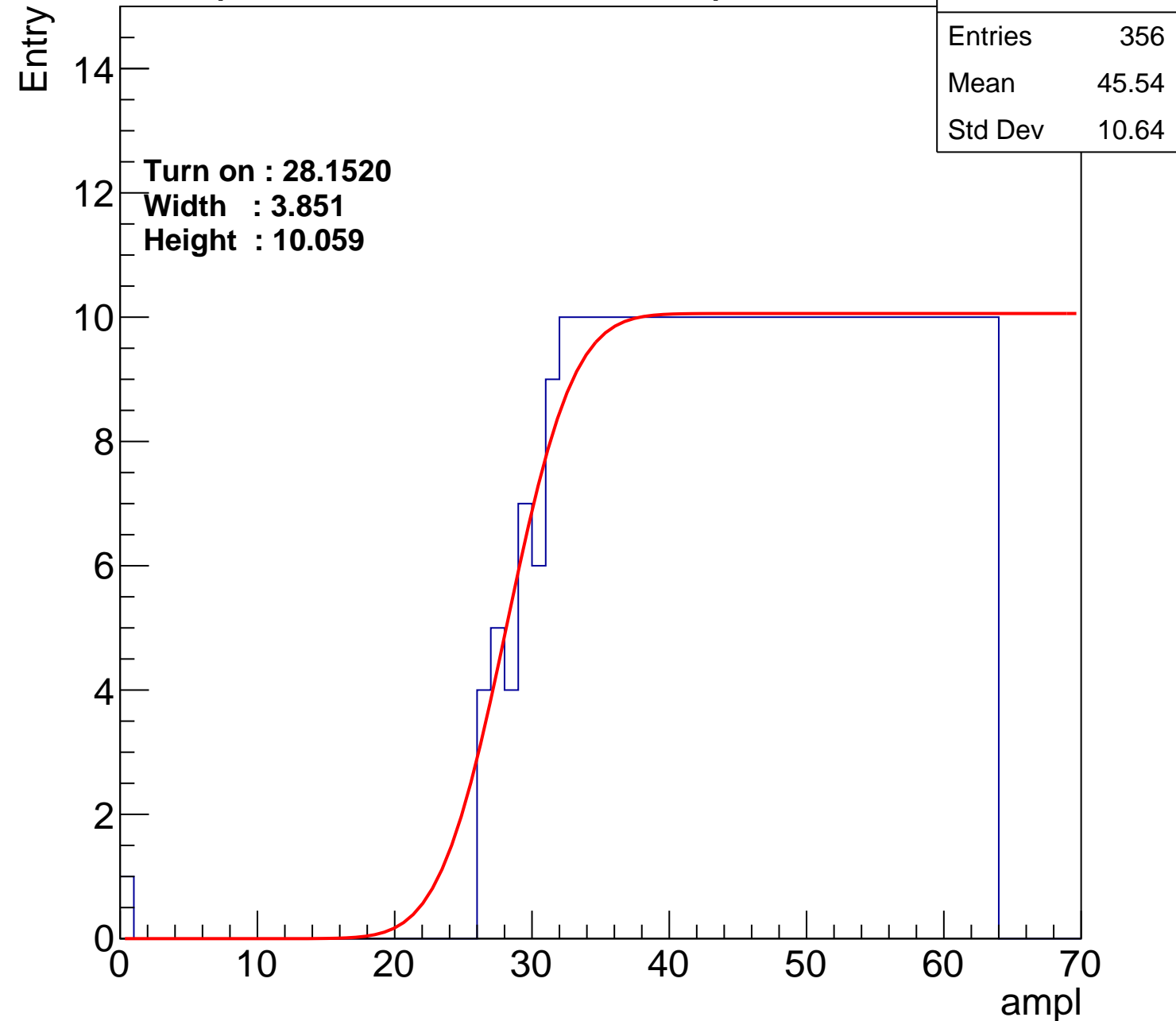
Width : 3.851

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch36

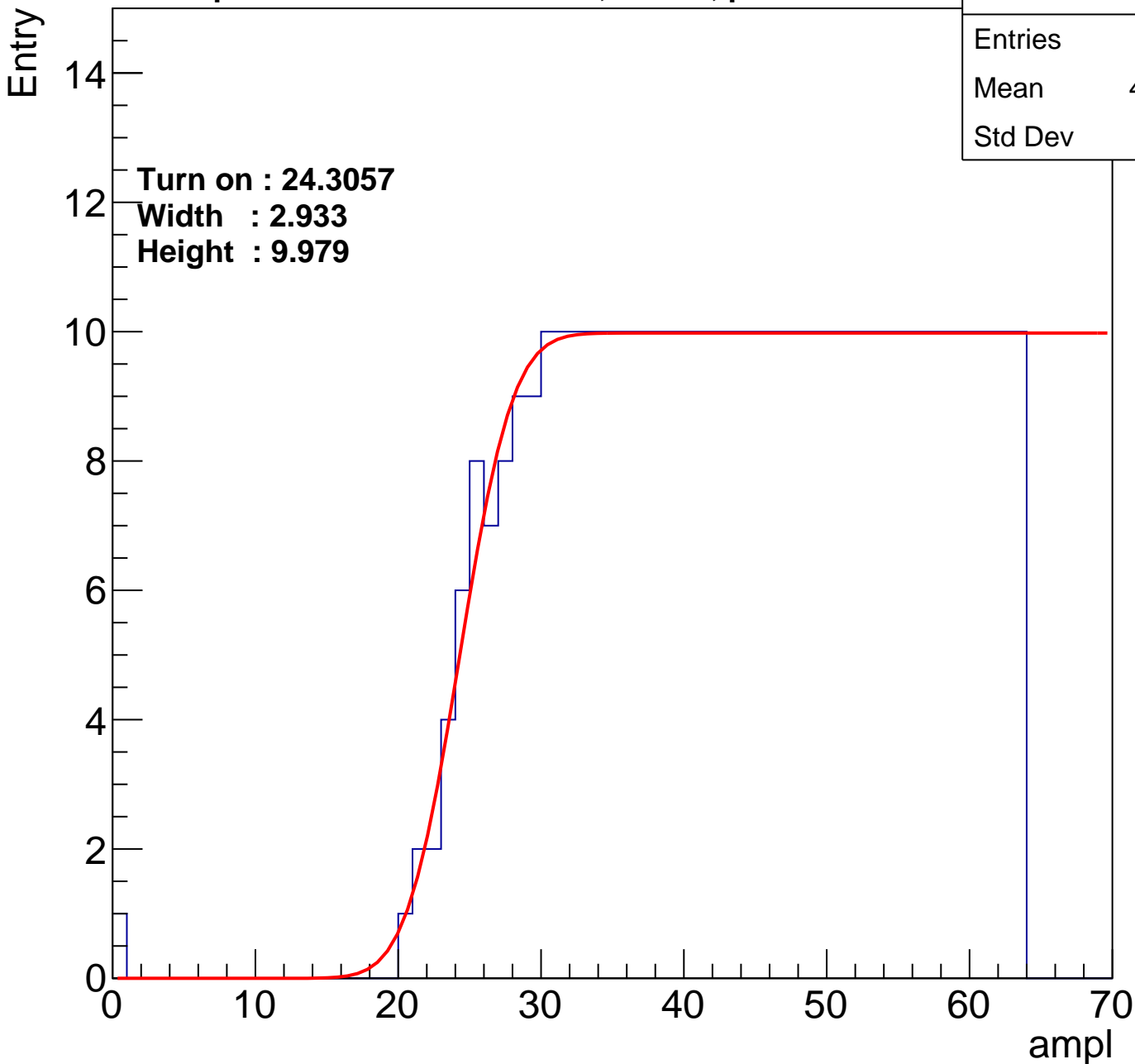
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Entries	397
Mean	43.48
Std Dev	11.8

**Turn on : 24.3057**

**Width : 2.933**

**Height : 9.979**



# B0L102S, U21-ch37

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.4060

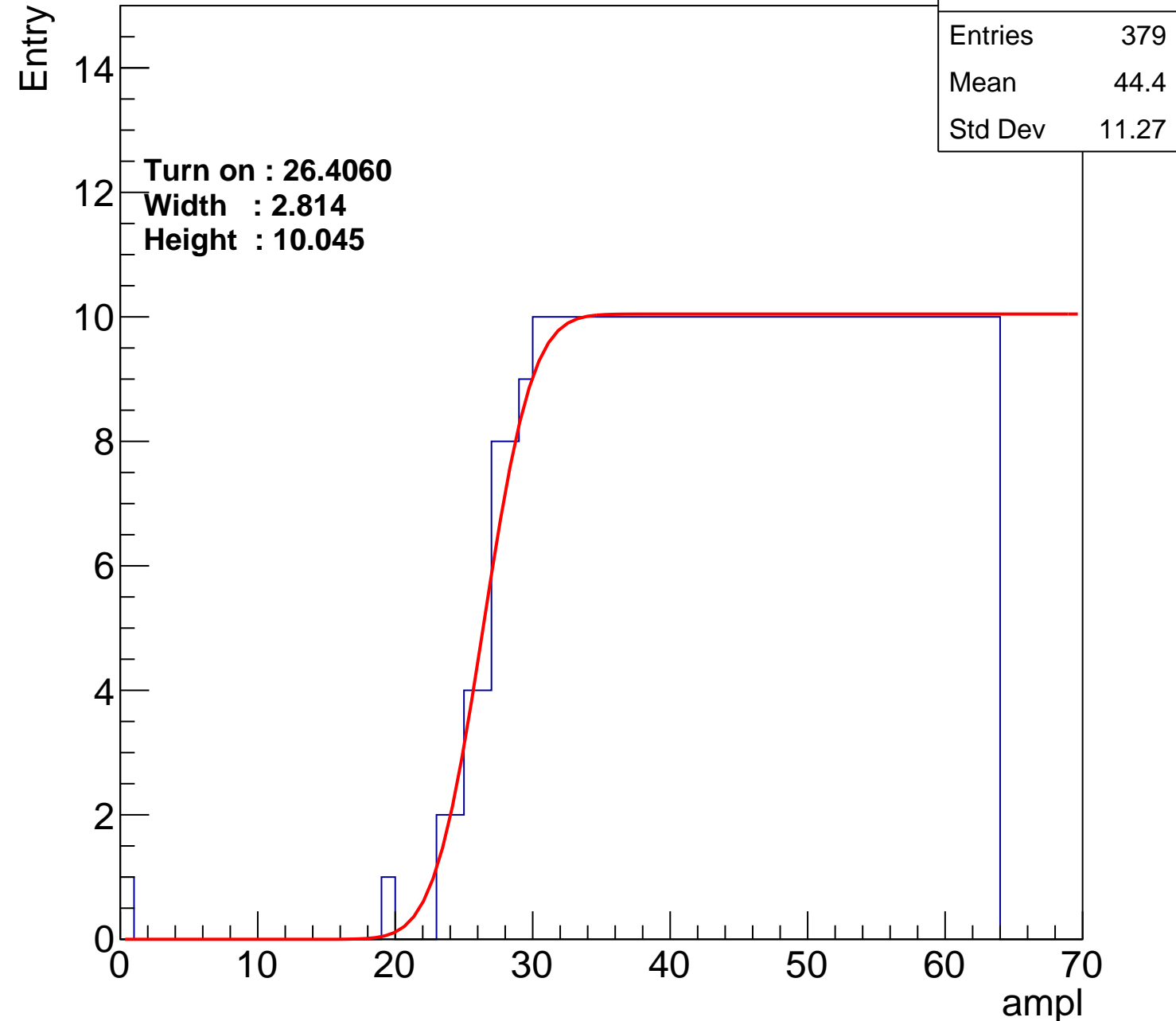
Width : 2.814

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch38

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 24.5395

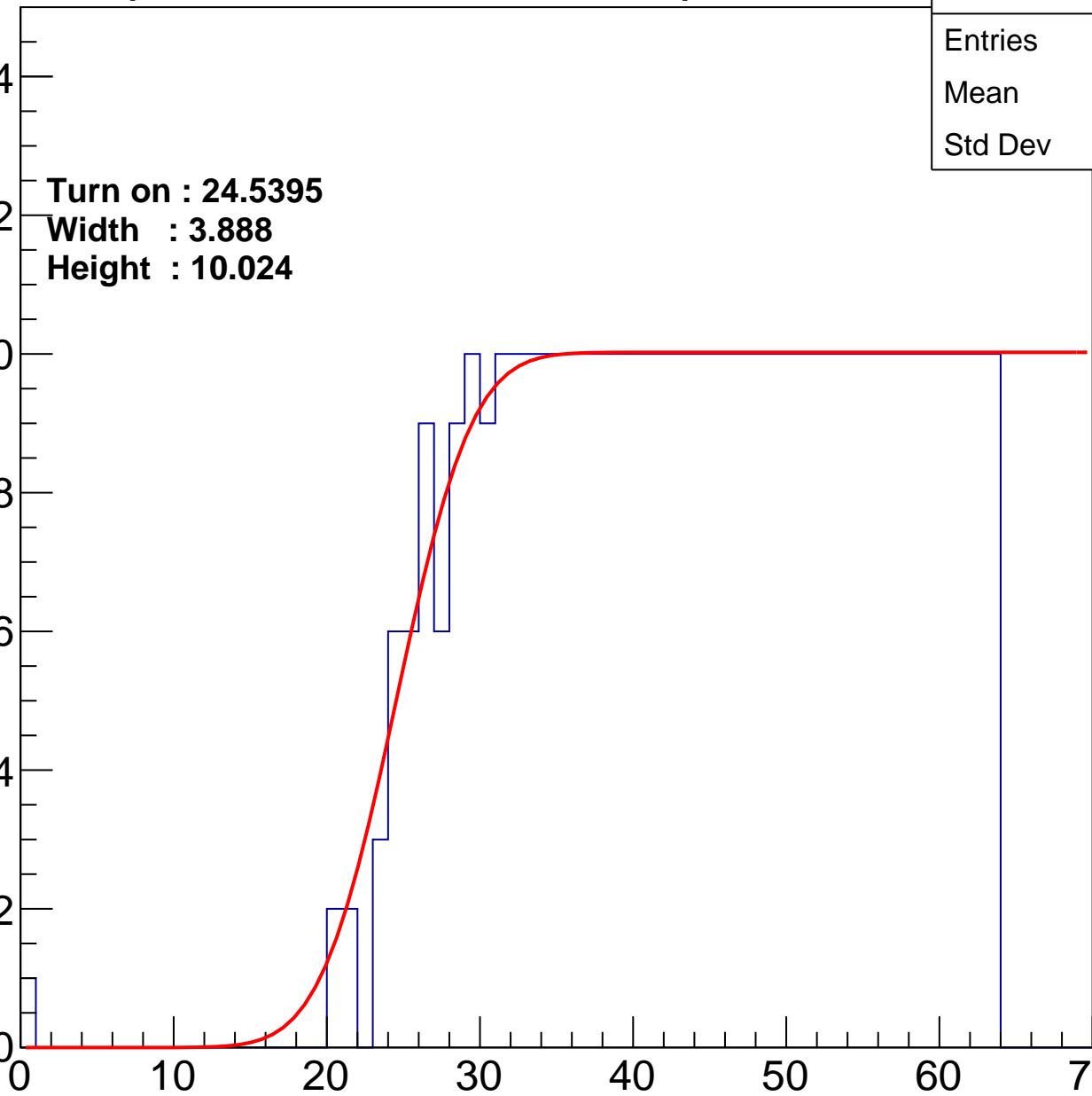
Width : 3.888

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch39

calib\_packv5\_042523\_0143.root, FC#12, port B1

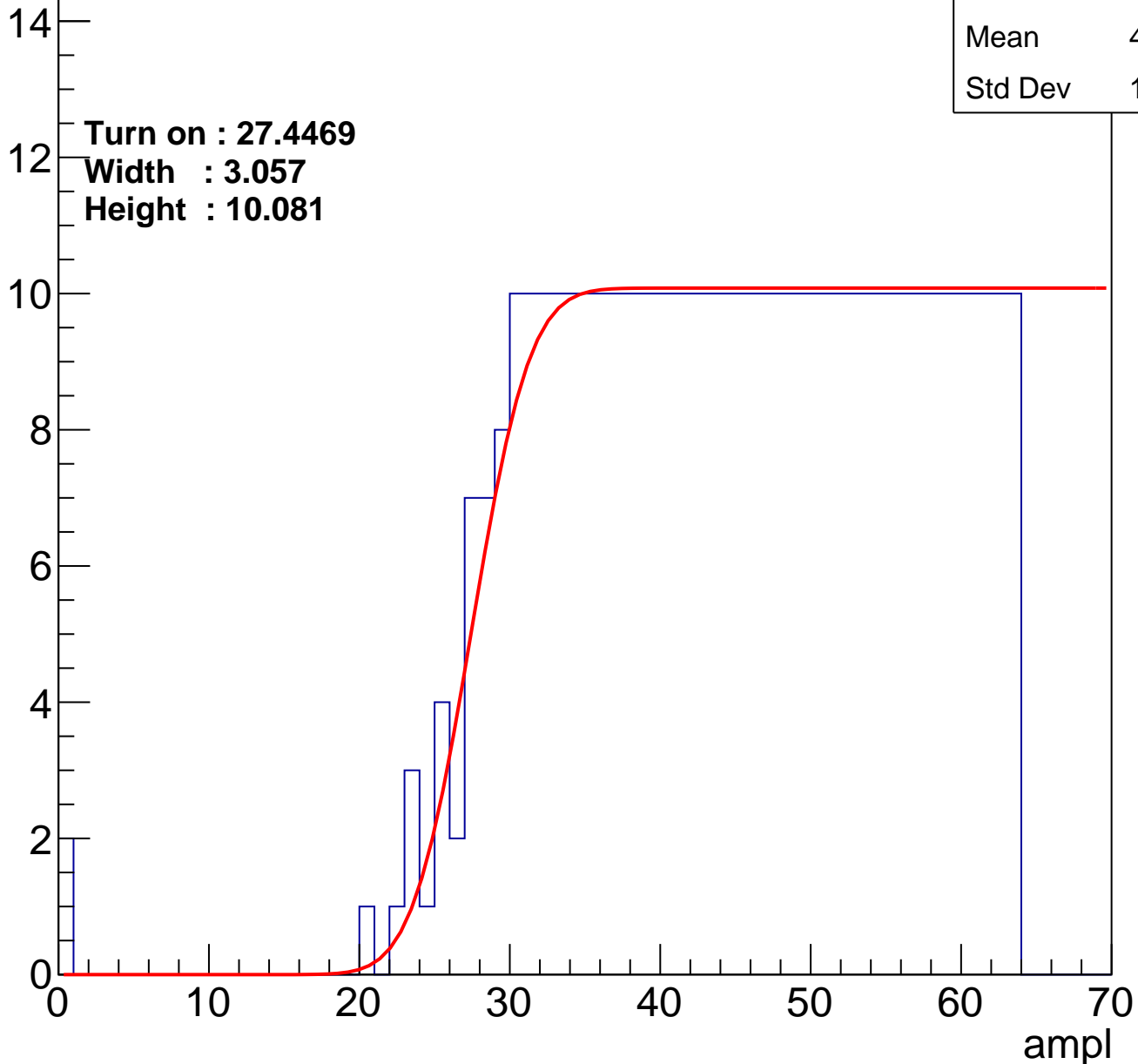
Entries	376
Mean	44.45
Std Dev	11.43

Turn on : 27.4469

Width : 3.057

Height : 10.081

Entry



# B0L102S, U21-ch40

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.42
Std Dev	11.91

Turn on : 24.6837

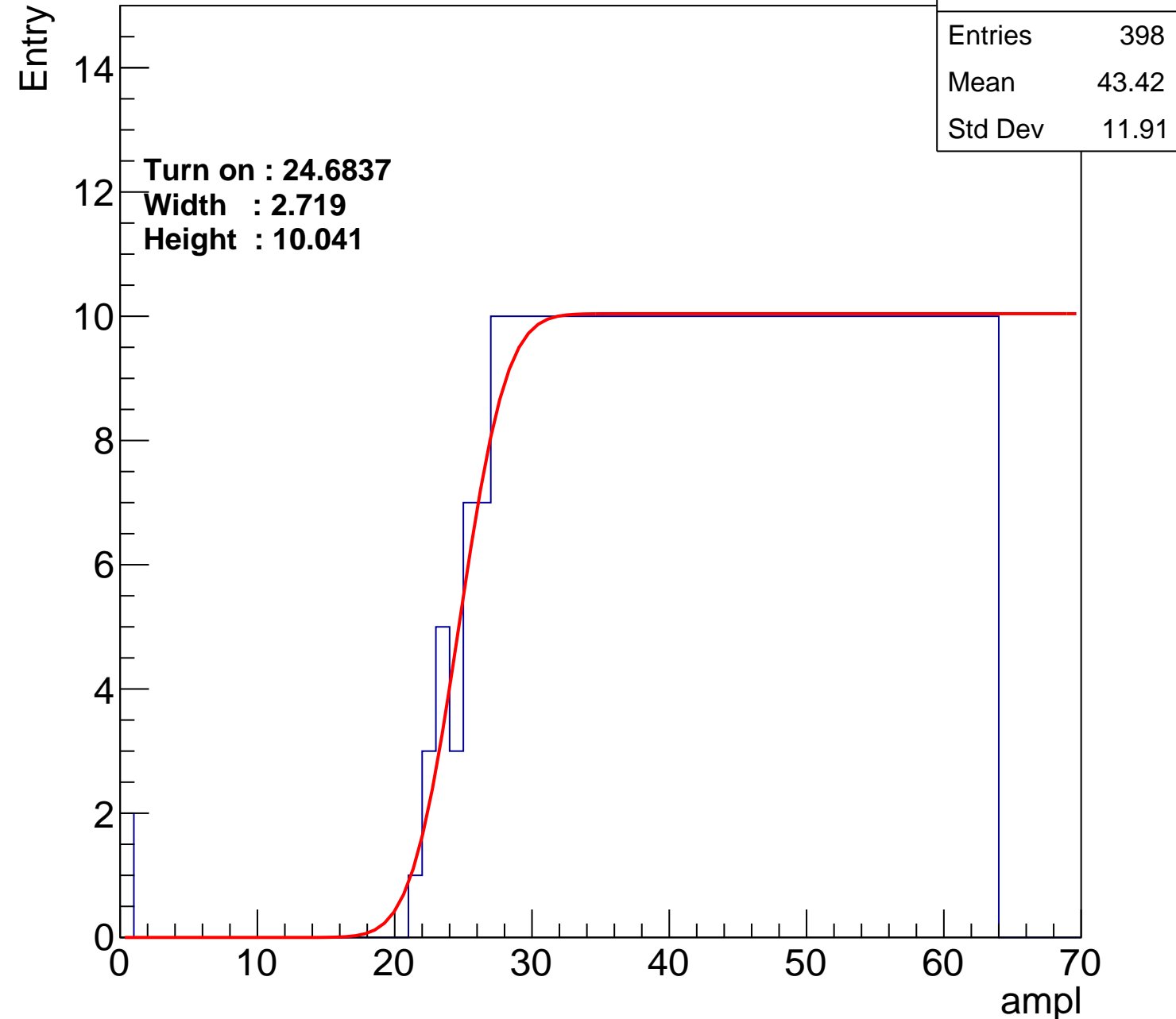
Width : 2.719

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch41

calib\_packv5\_042523\_0143.root, FC#12, port B1

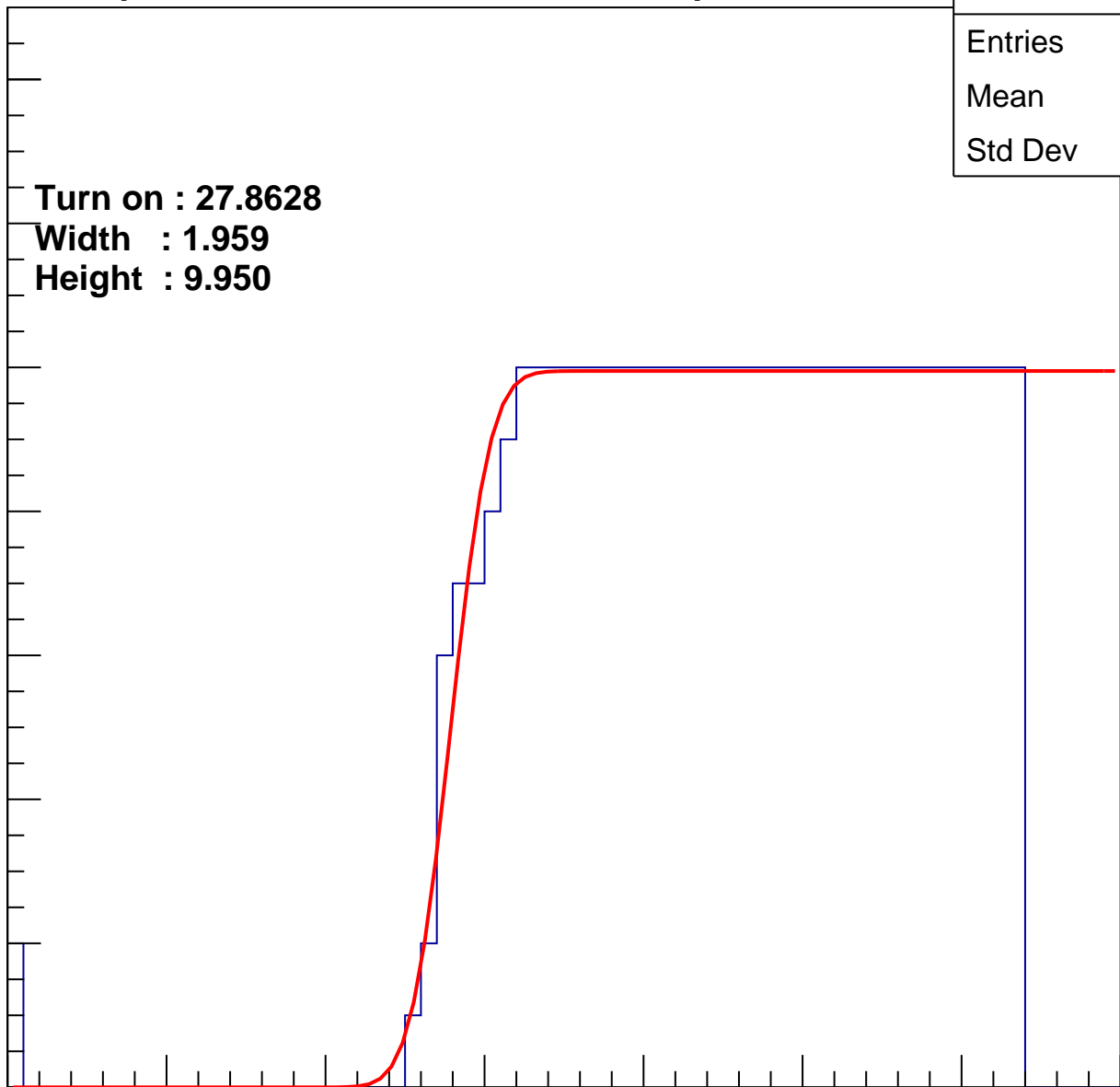
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.8628  
Width : 1.959  
Height : 9.950

Entries	362
Mean	45.19
Std Dev	11

ampl



# B0L102S, U21-ch42

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.19
Std Dev	11.54

**Turn on : 25.9463**

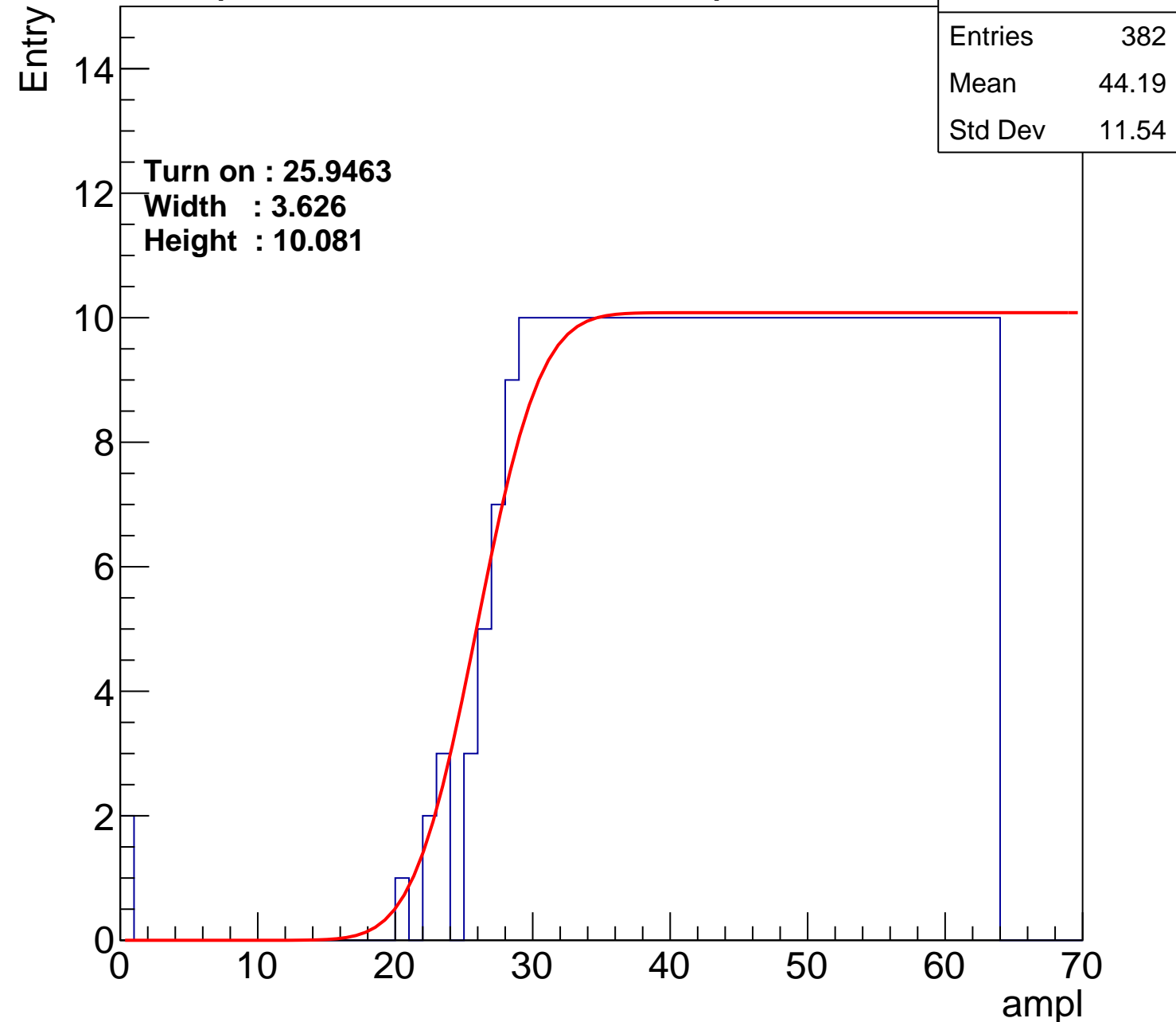
**Width : 3.626**

**Height : 10.081**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch43

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.9750

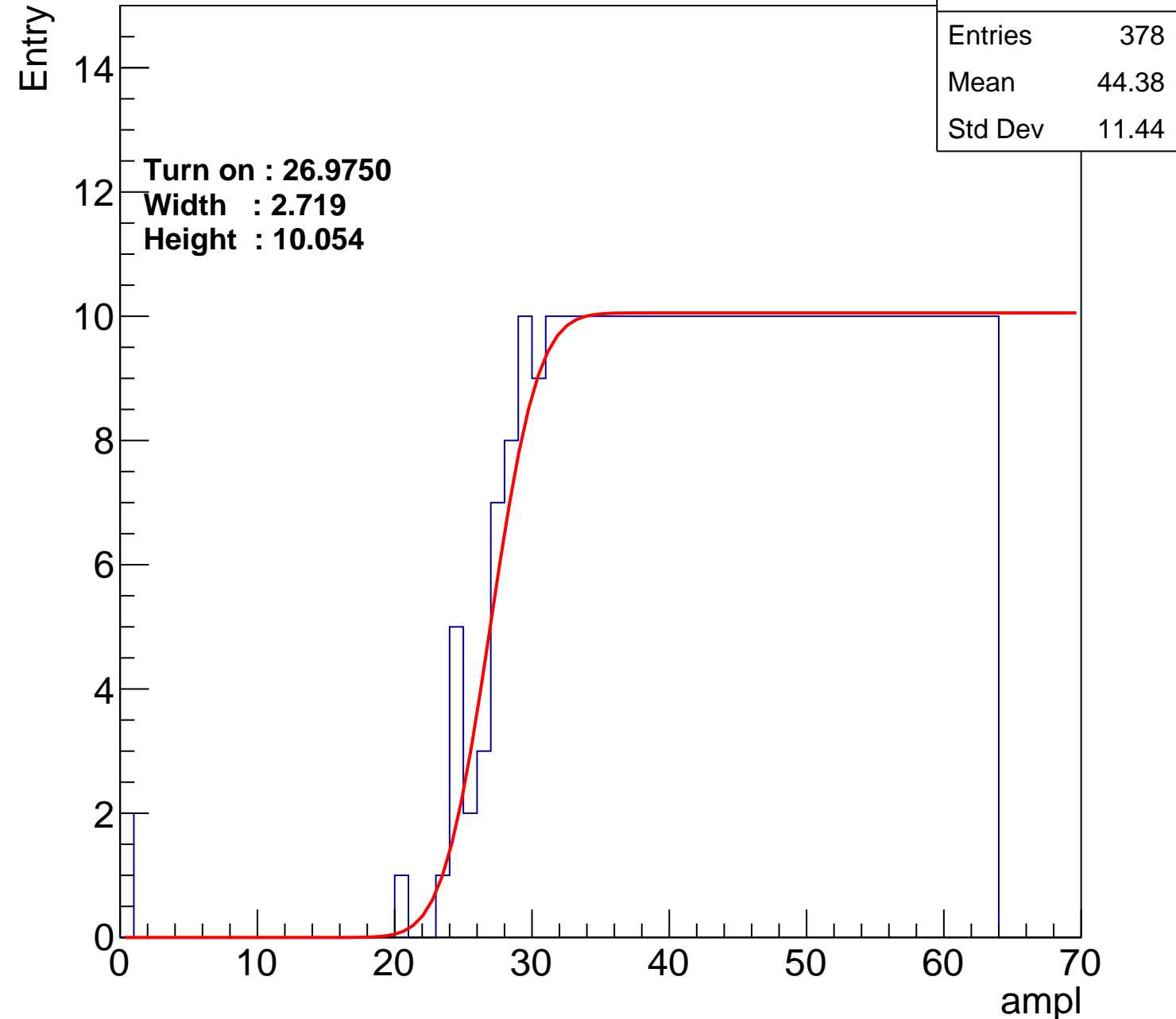
Width : 2.719

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch44

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 25.4311

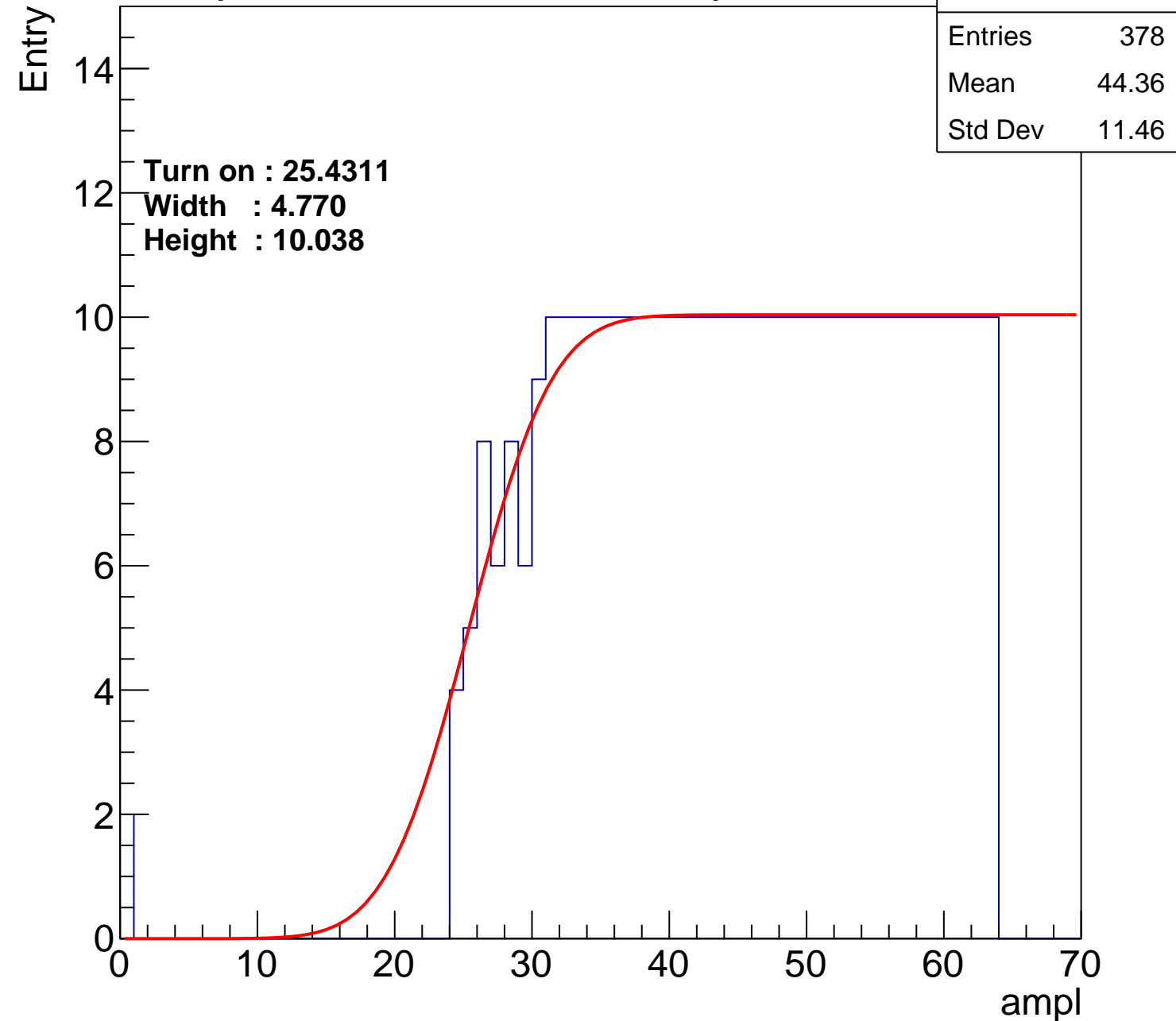
Width : 4.770

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch45

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.7683

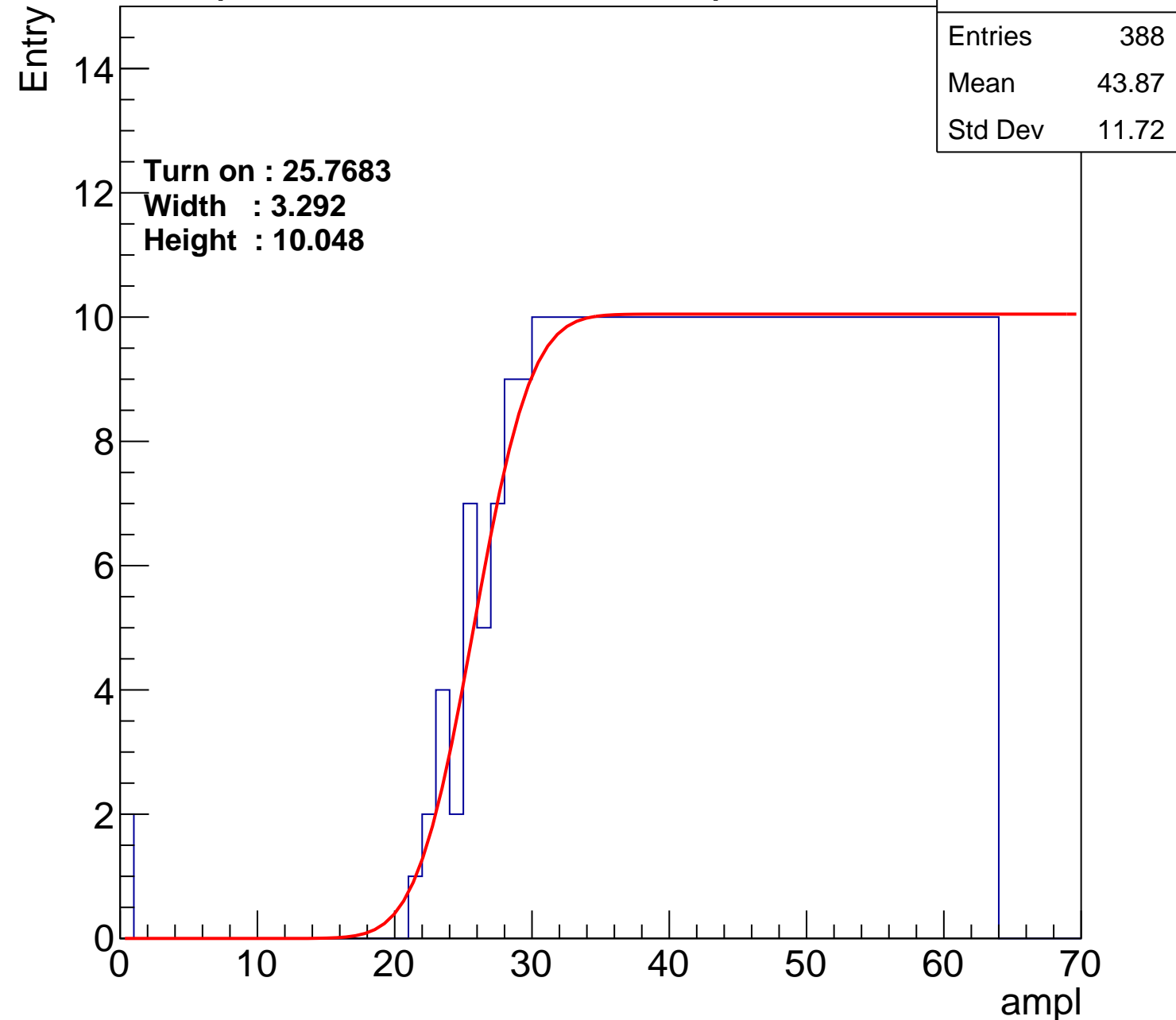
Width : 3.292

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch46

calib\_packv5\_042523\_0143.root, FC#12, port B1

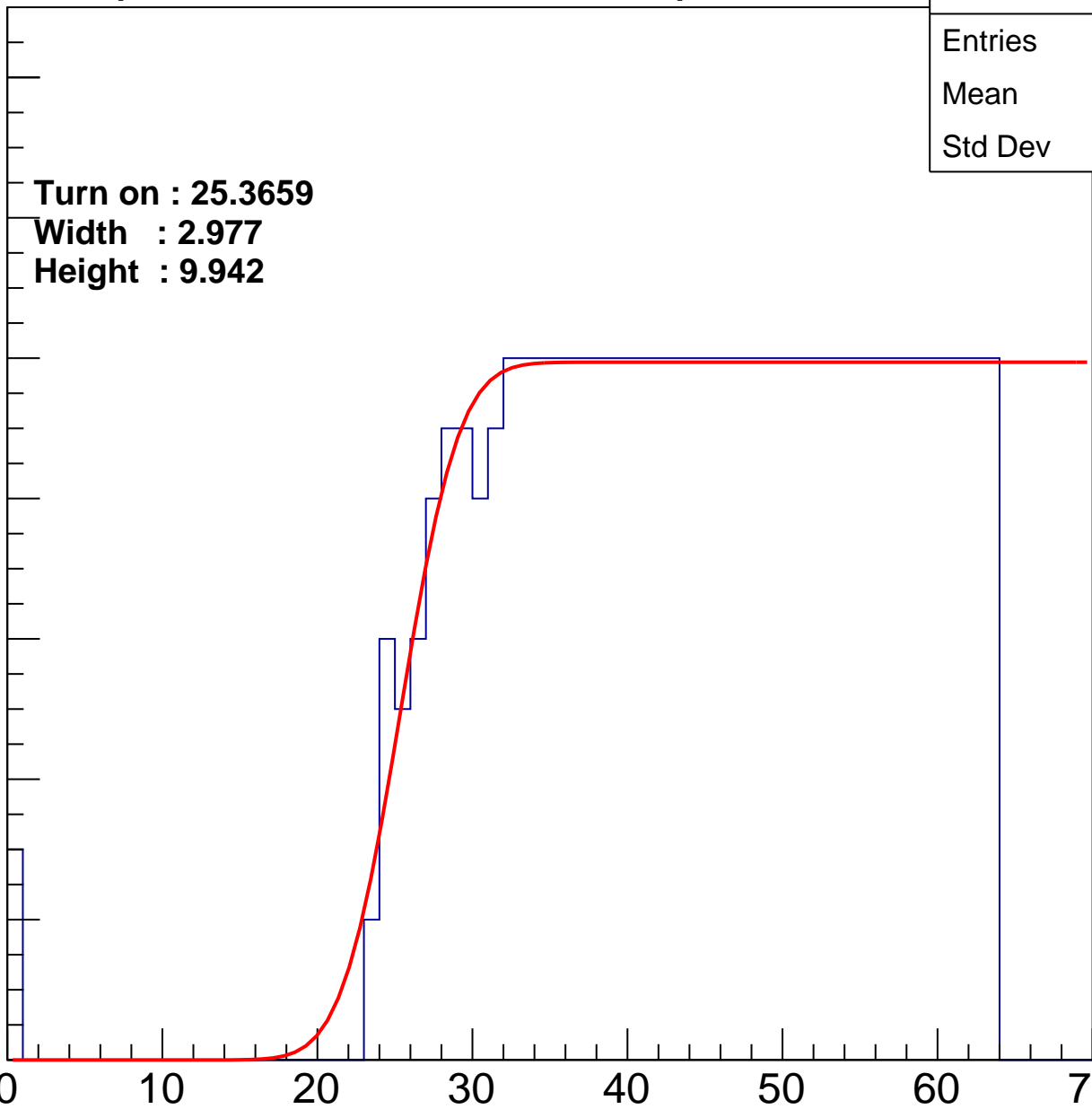
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.3659  
Width : 2.977  
Height : 9.942

Entries	385
Mean	43.95
Std Dev	11.82

ampl





# B0L102S, U21-ch47

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 24.9811

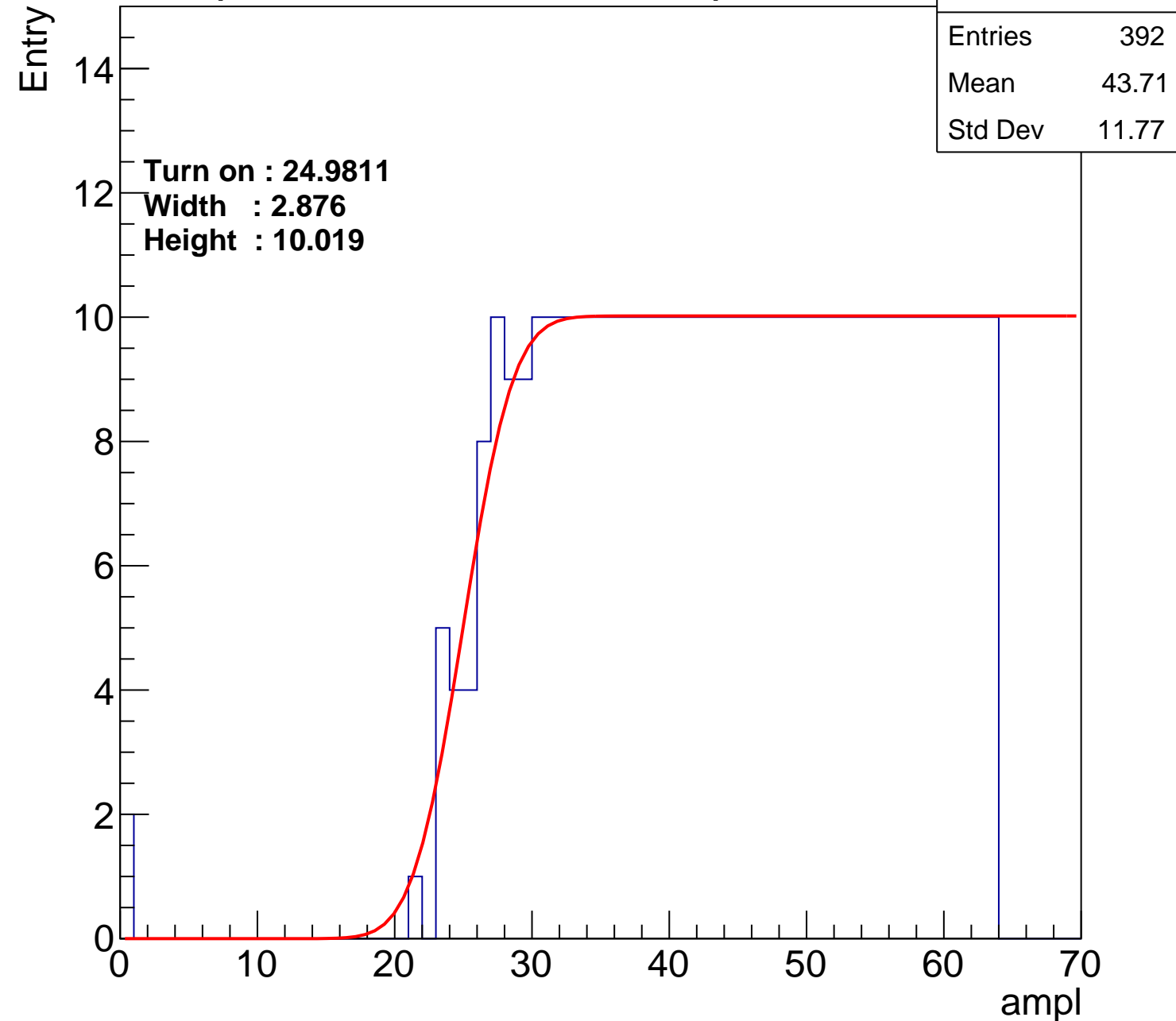
Width : 2.876

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch48

calib\_packv5\_042523\_0143.root, FC#12, port B1

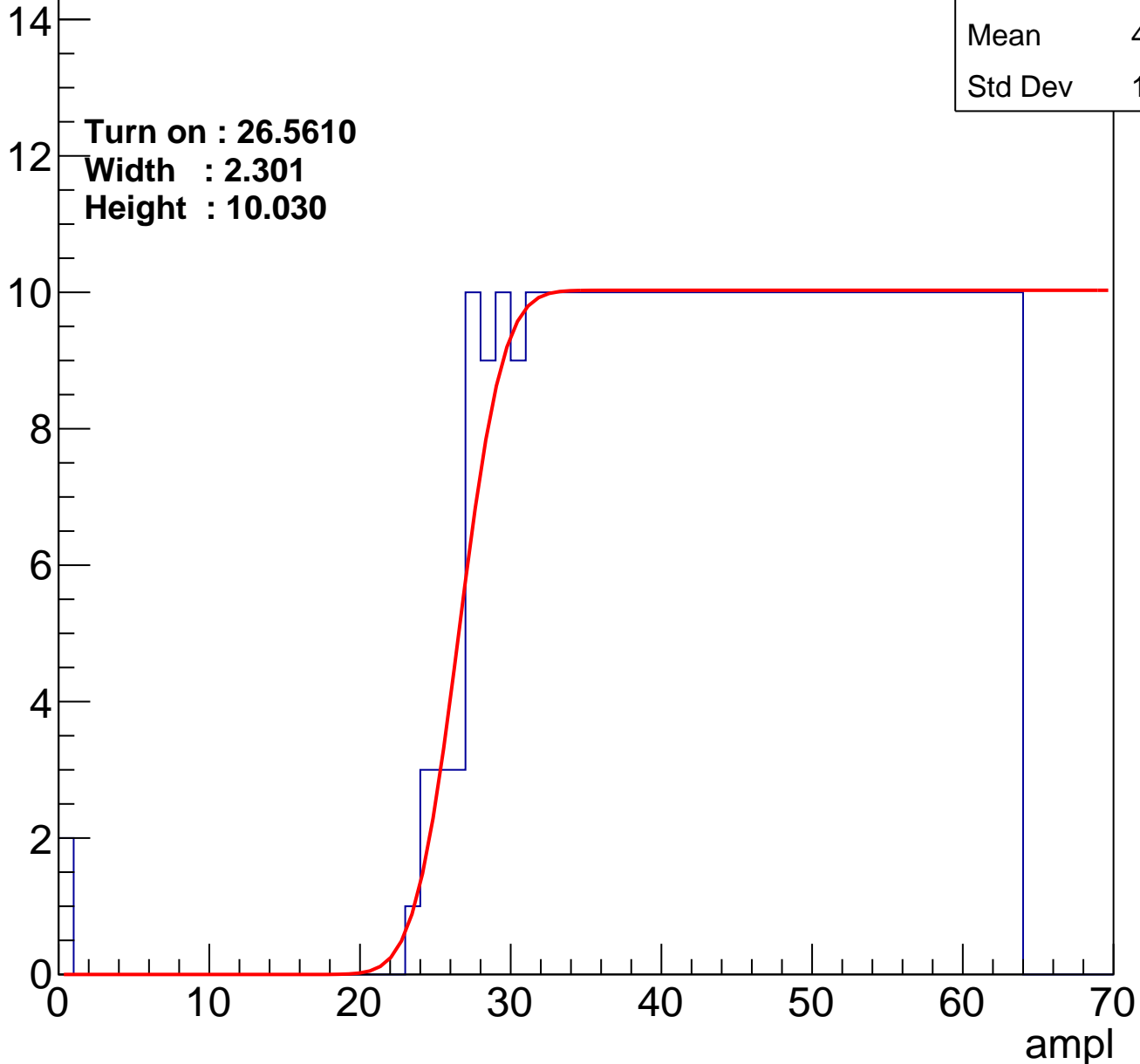
Entries	380
Mean	44.32
Std Dev	11.43

Turn on : 26.5610

Width : 2.301

Height : 10.030

Entry



# B0L102S, U21-ch49

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.5
Std Dev	11.35

**Turn on : 26.5800**

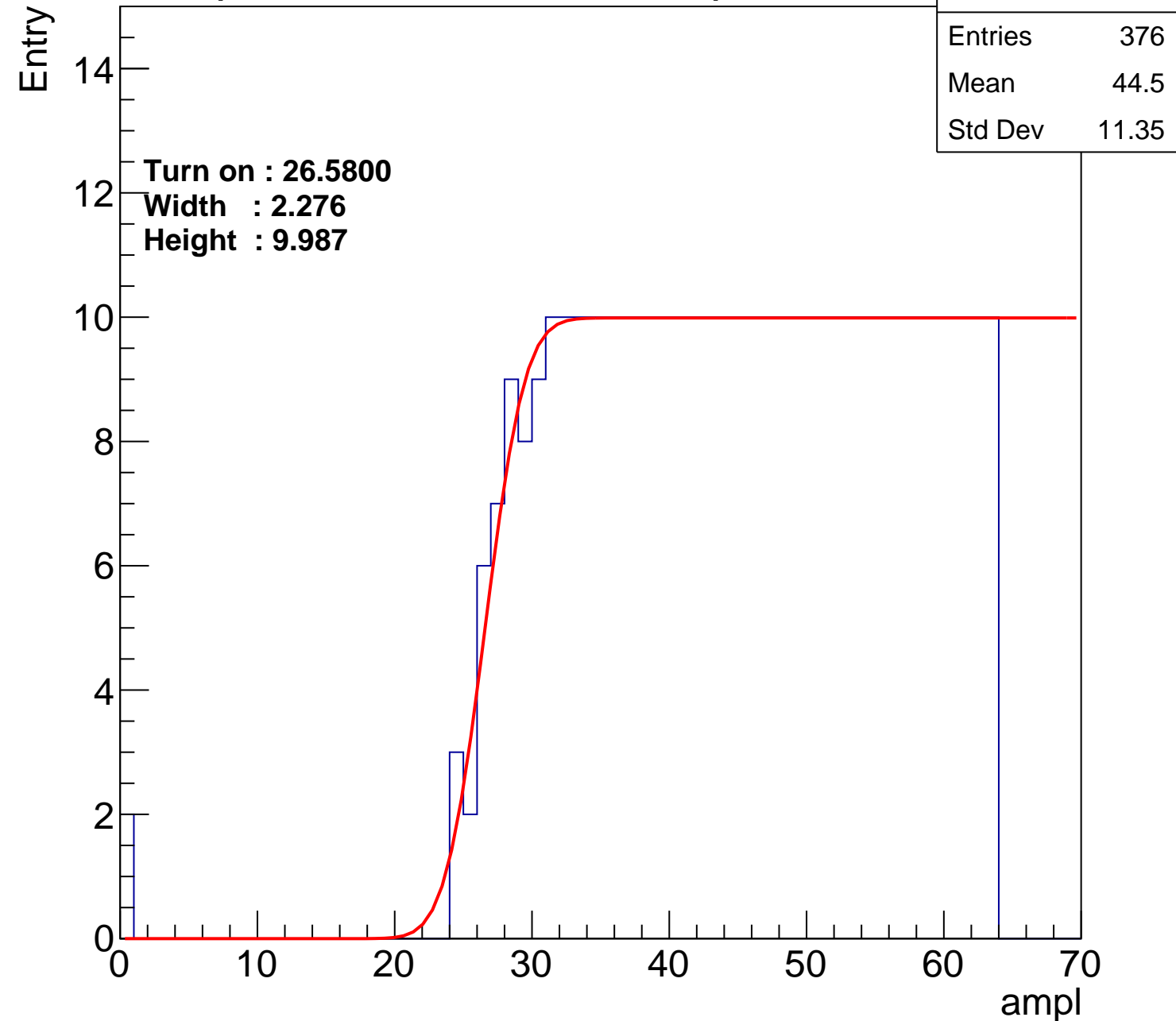
**Width : 2.276**

**Height : 9.987**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch50

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.52
Std Dev	11.86

Turn on : 24.5832

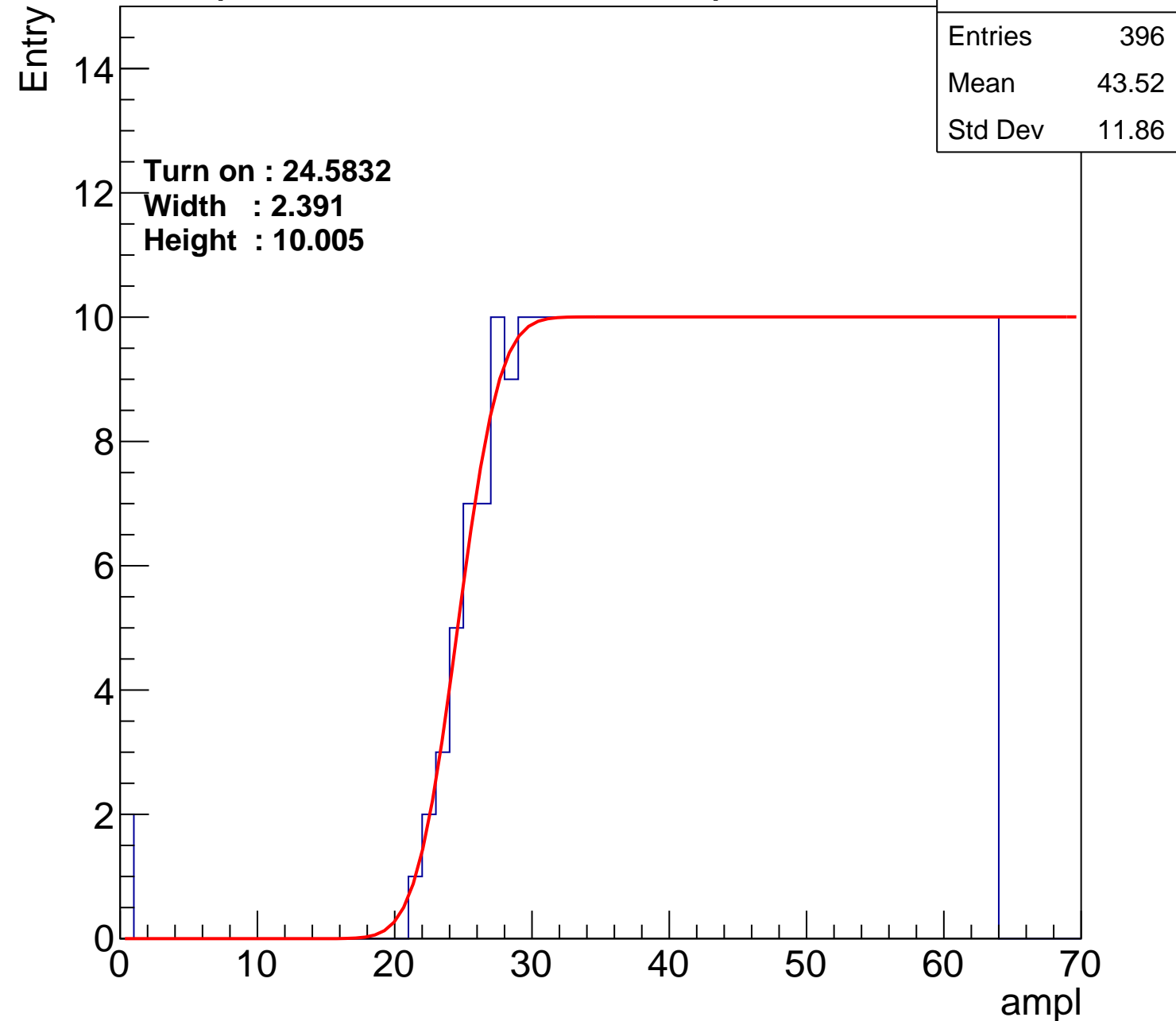
Width : 2.391

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch51

calib\_packv5\_042523\_0143.root, FC#12, port B1

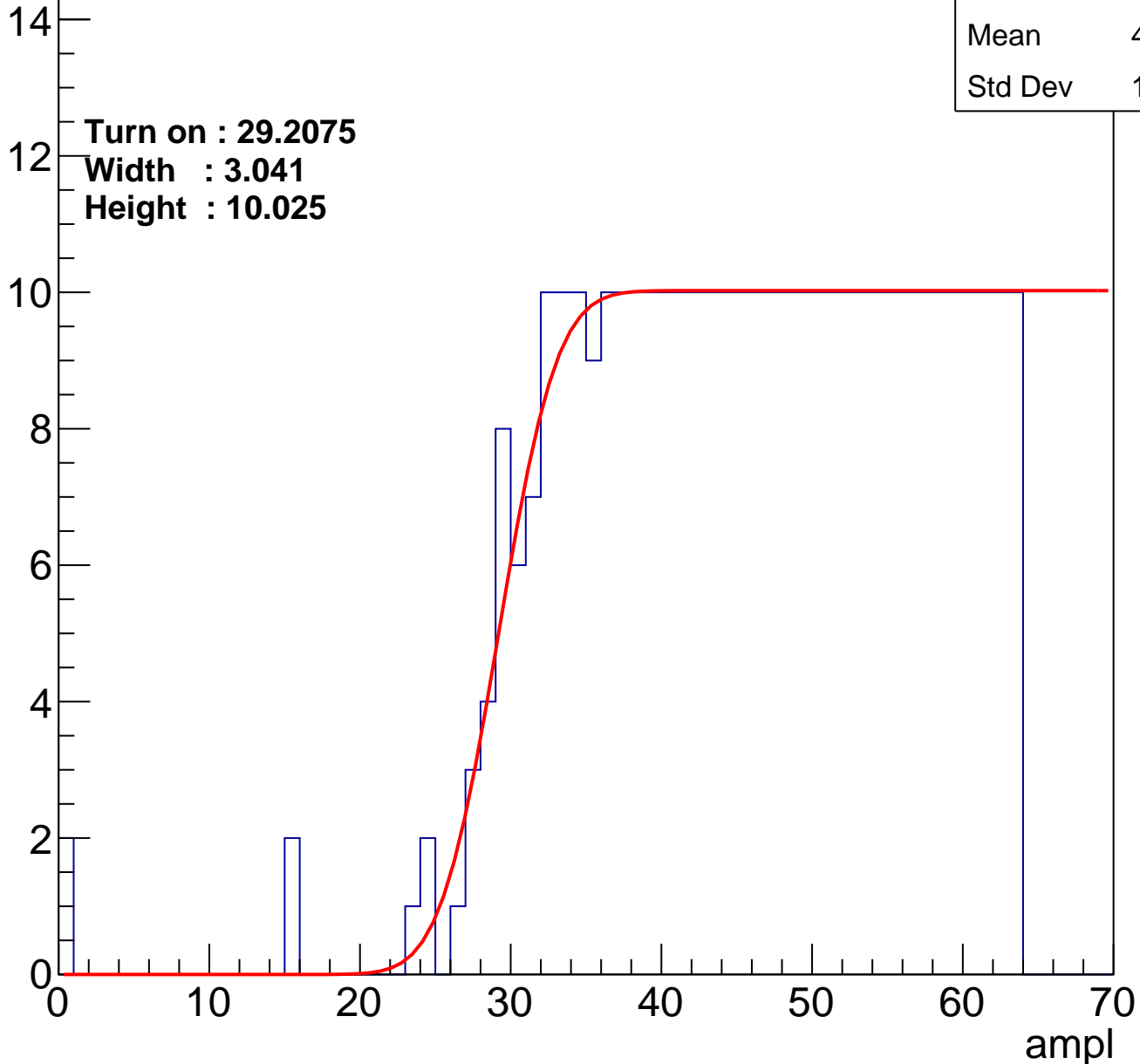
Entries	355
Mean	45.39
Std Dev	11.08

Turn on : 29.2075

Width : 3.041

Height : 10.025

Entry



# B0L102S, U21-ch52

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.4940

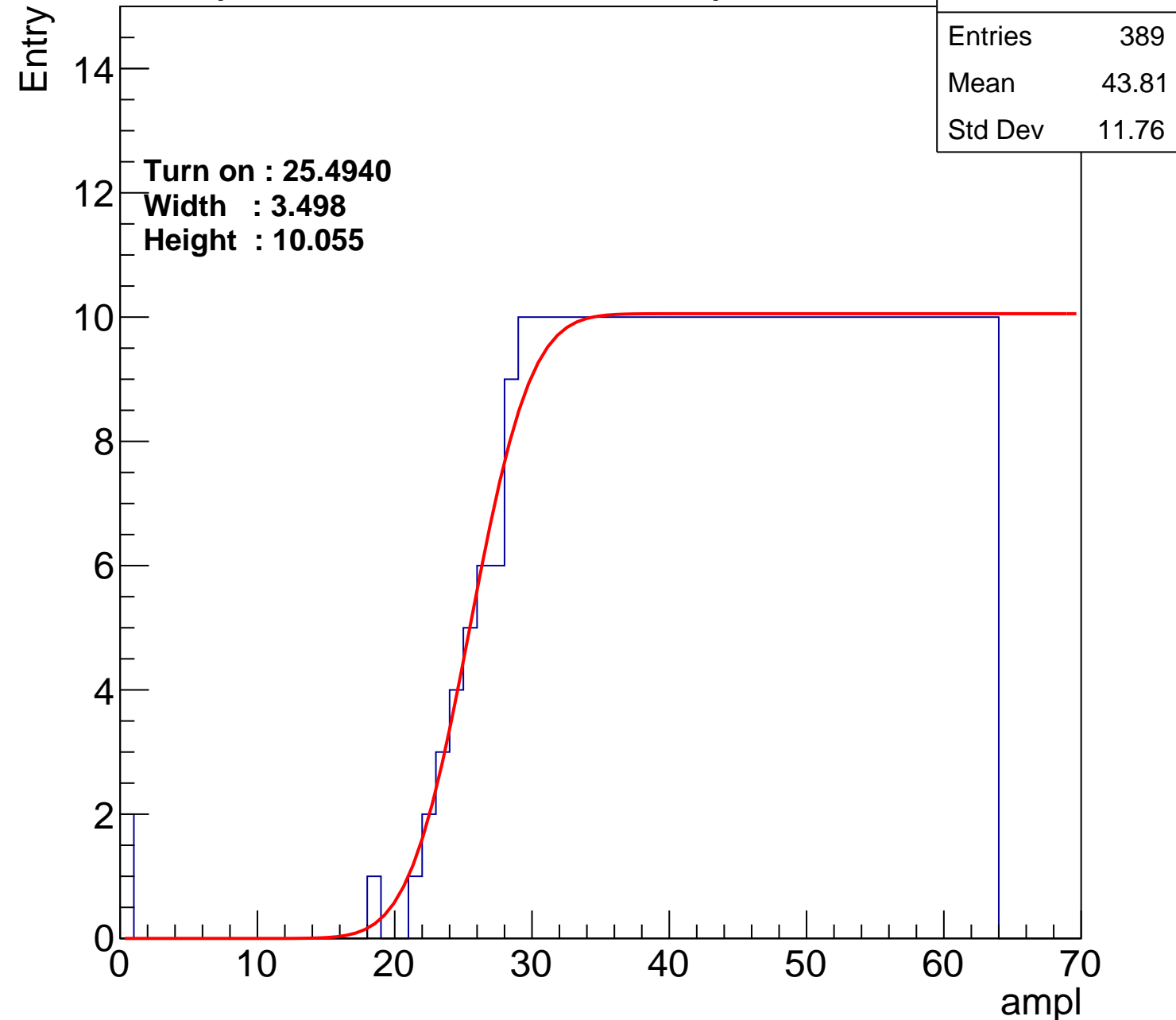
Width : 3.498

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch53

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.62
Std Dev	11.17

Turn on : 26.5778

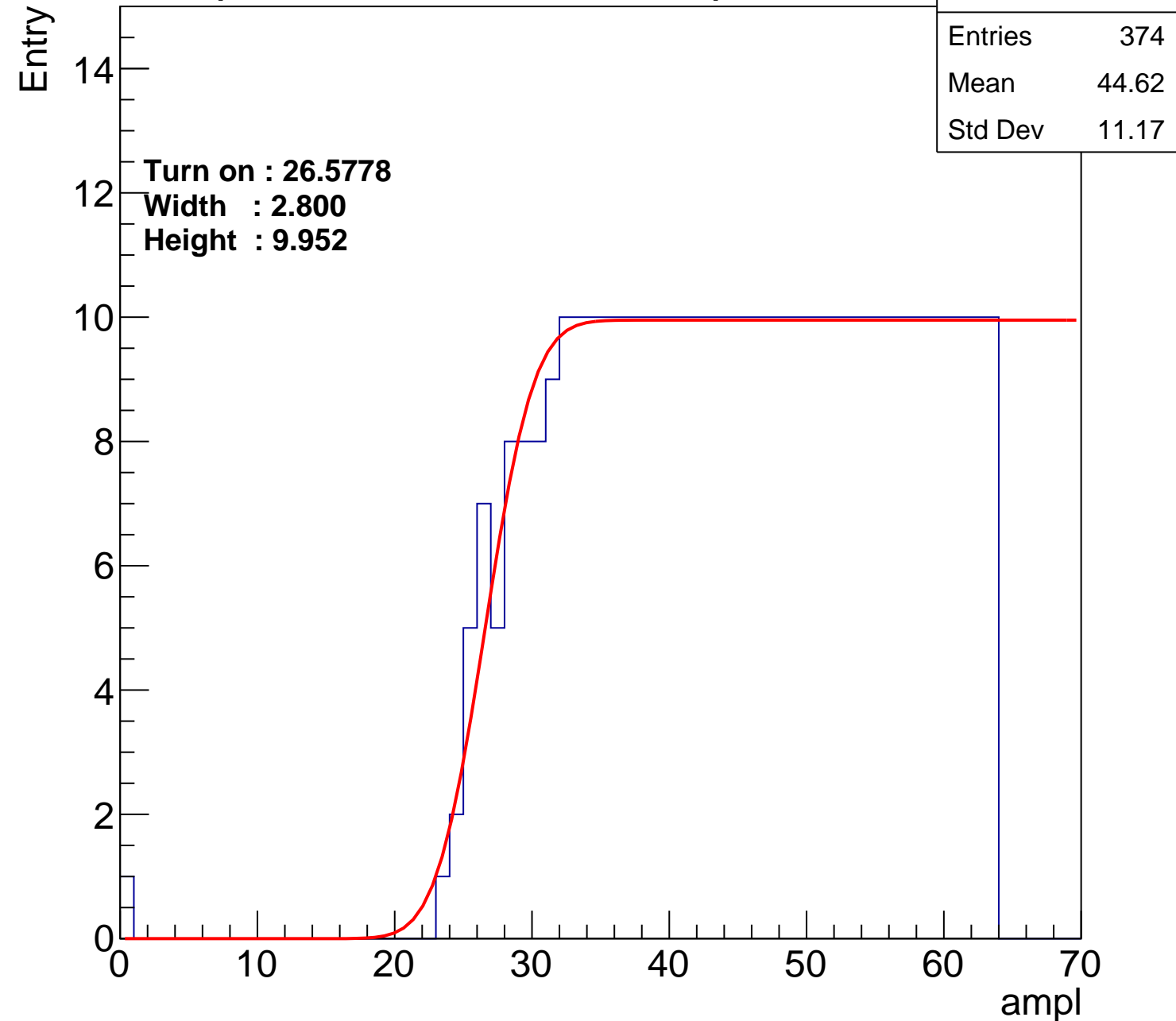
Width : 2.800

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch54

calib\_packv5\_042523\_0143.root, FC#12, port B1

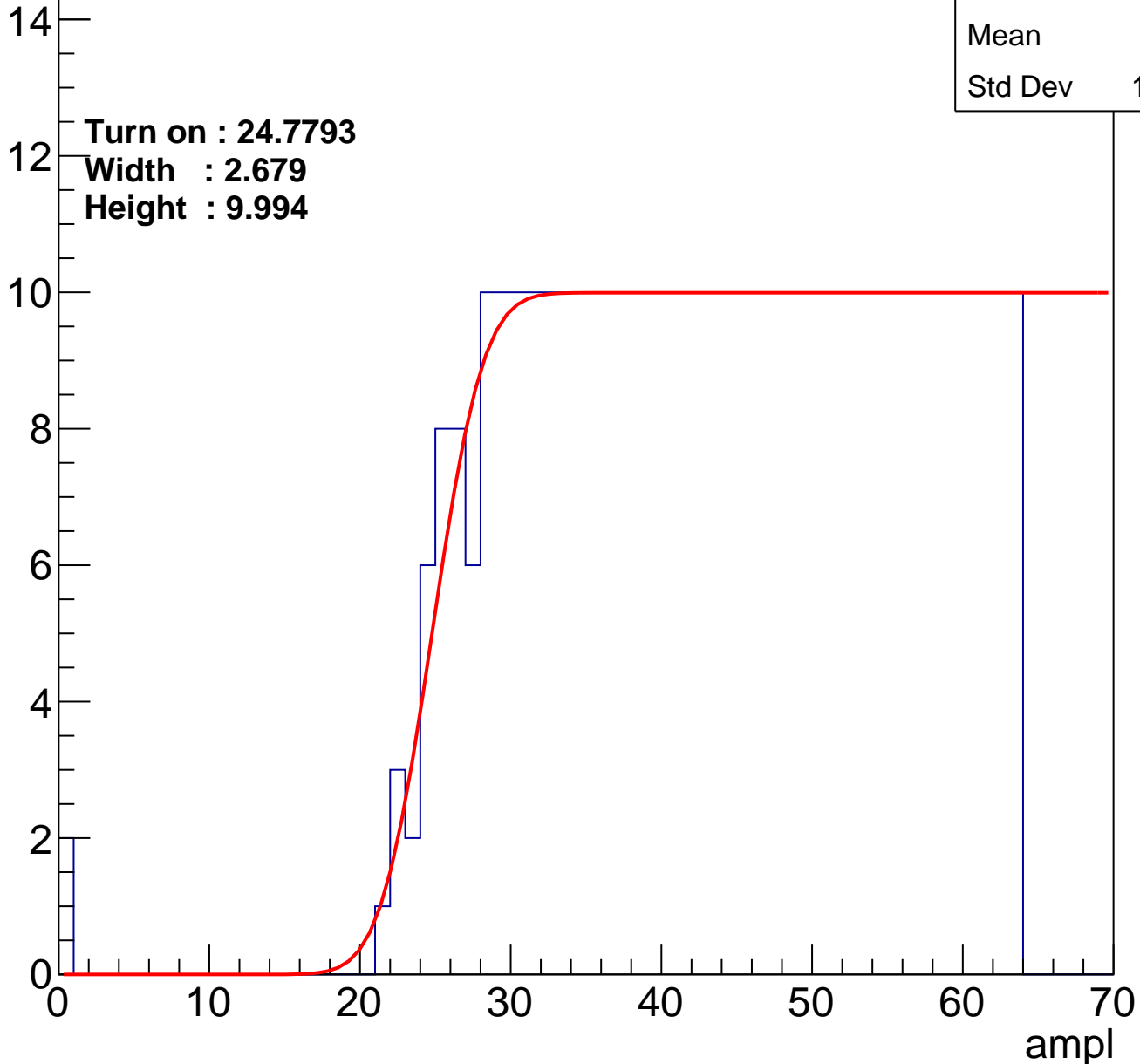
Entries	396
Mean	43.5
Std Dev	11.88

Turn on : 24.7793

Width : 2.679

Height : 9.994

Entry





# B0L102S, U21-ch55

calib\_packv5\_042523\_0143.root, FC#12, port B1

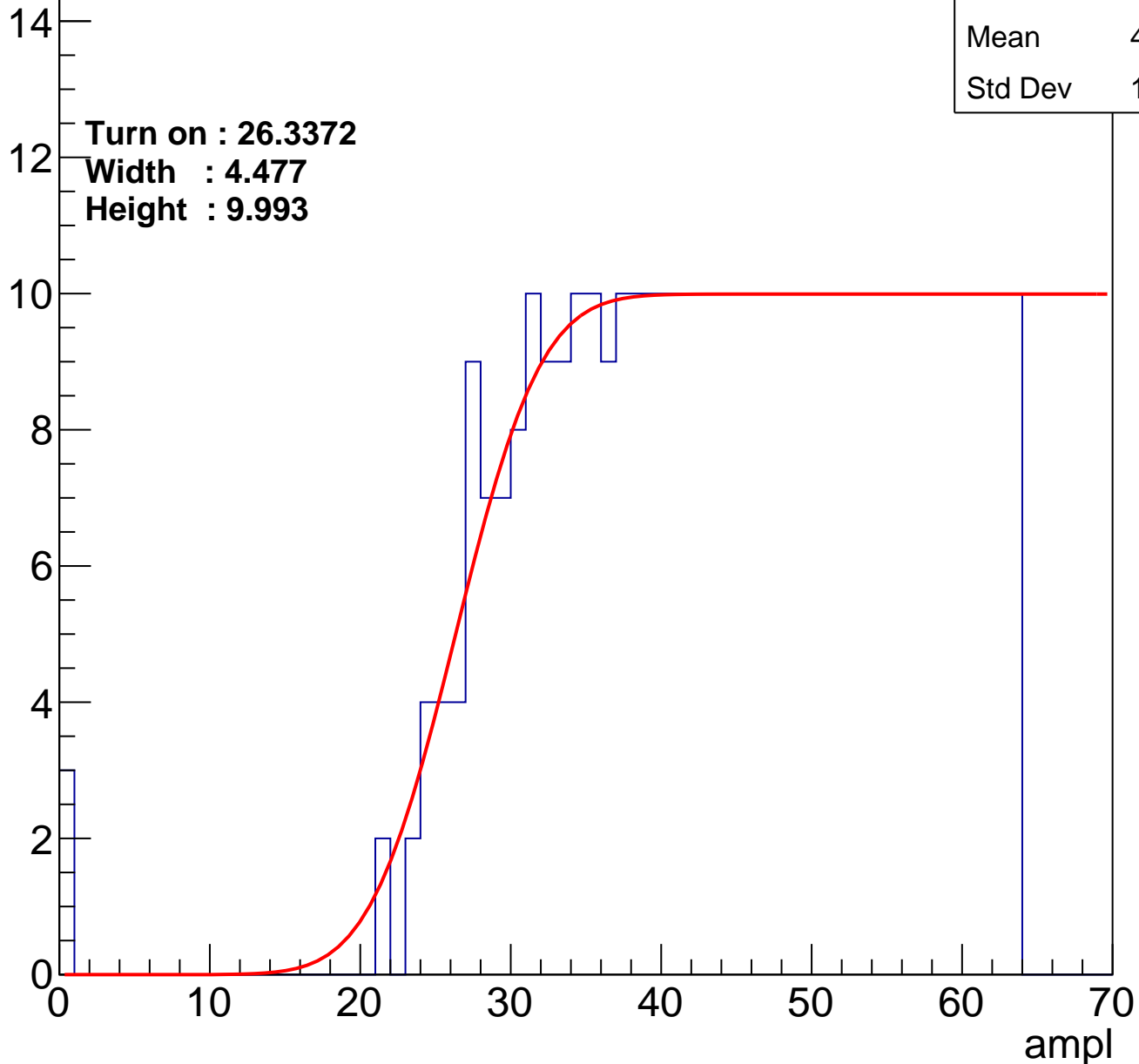
Entries	377
Mean	44.24
Std Dev	11.76

Turn on : 26.3372

Width : 4.477

Height : 9.993

Entry



# B0L102S, U21-ch56

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.74
Std Dev	11.78

**Turn on : 25.1916**

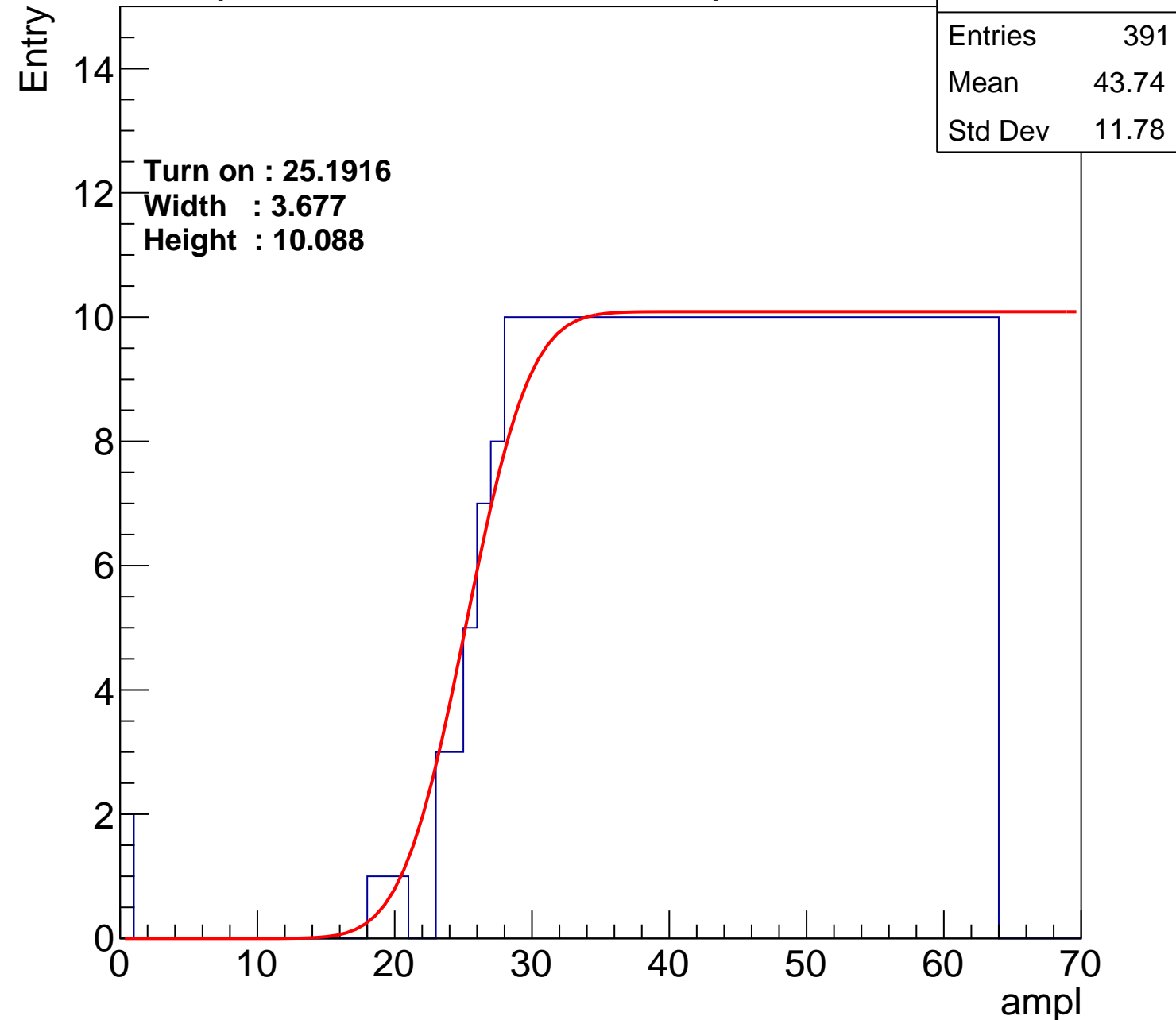
**Width : 3.677**

**Height : 10.088**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch57

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.64
Std Dev	11.28

Turn on : 26.8771

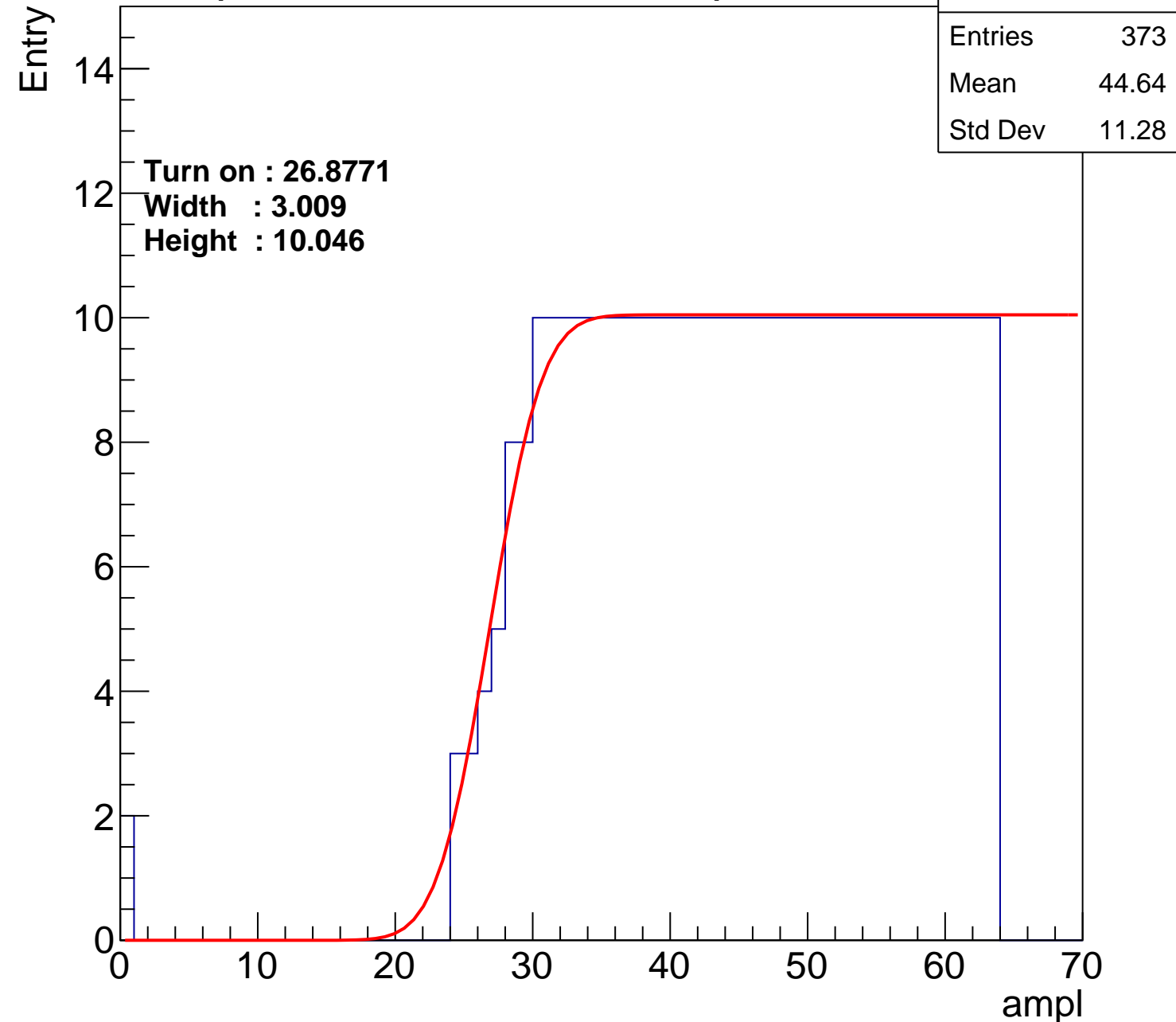
Width : 3.009

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch58

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.41
Std Dev	11.58

Turn on : 26.9059

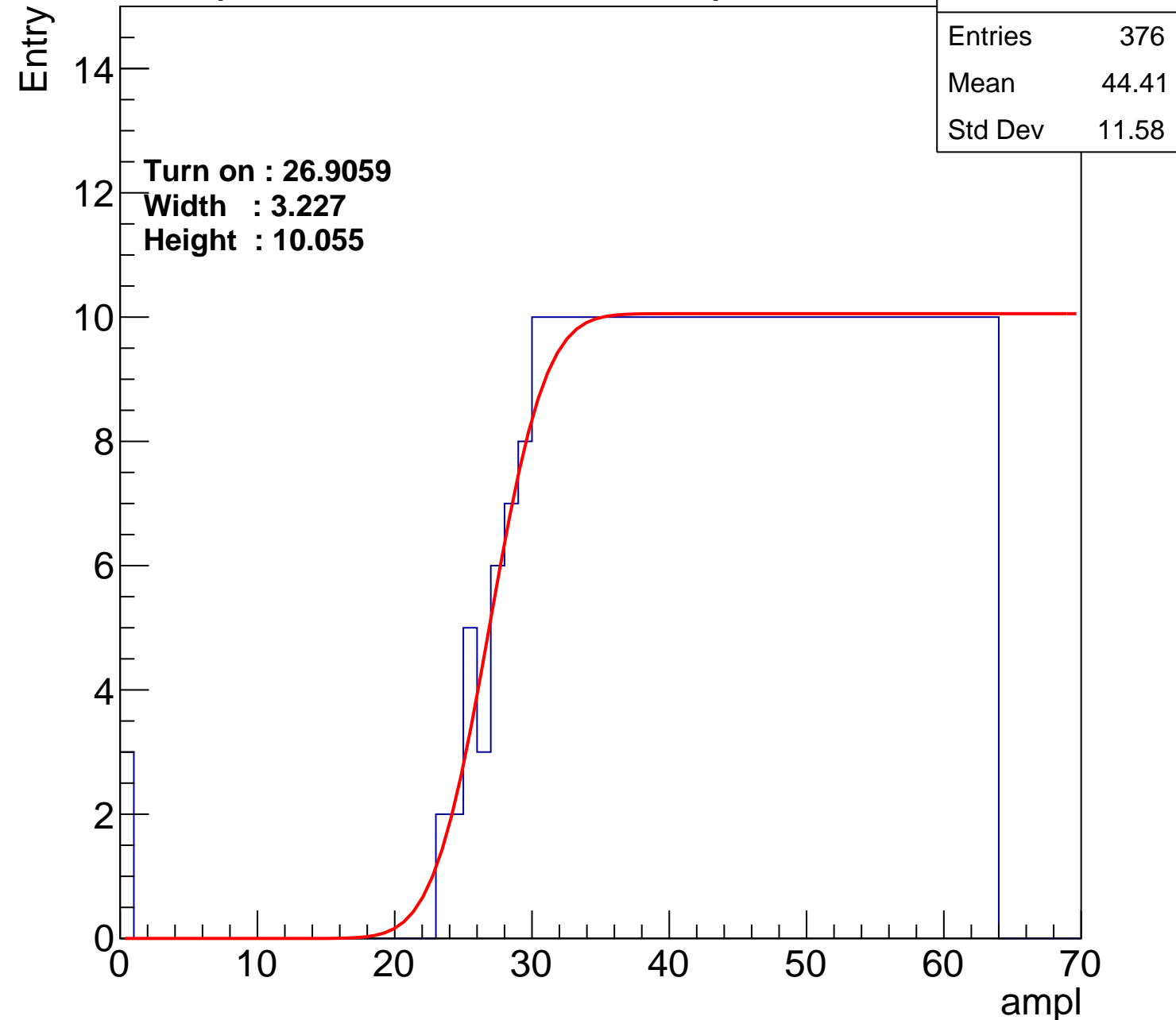
Width : 3.227

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch59

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.64
Std Dev	11.28

Turn on : 26.9832

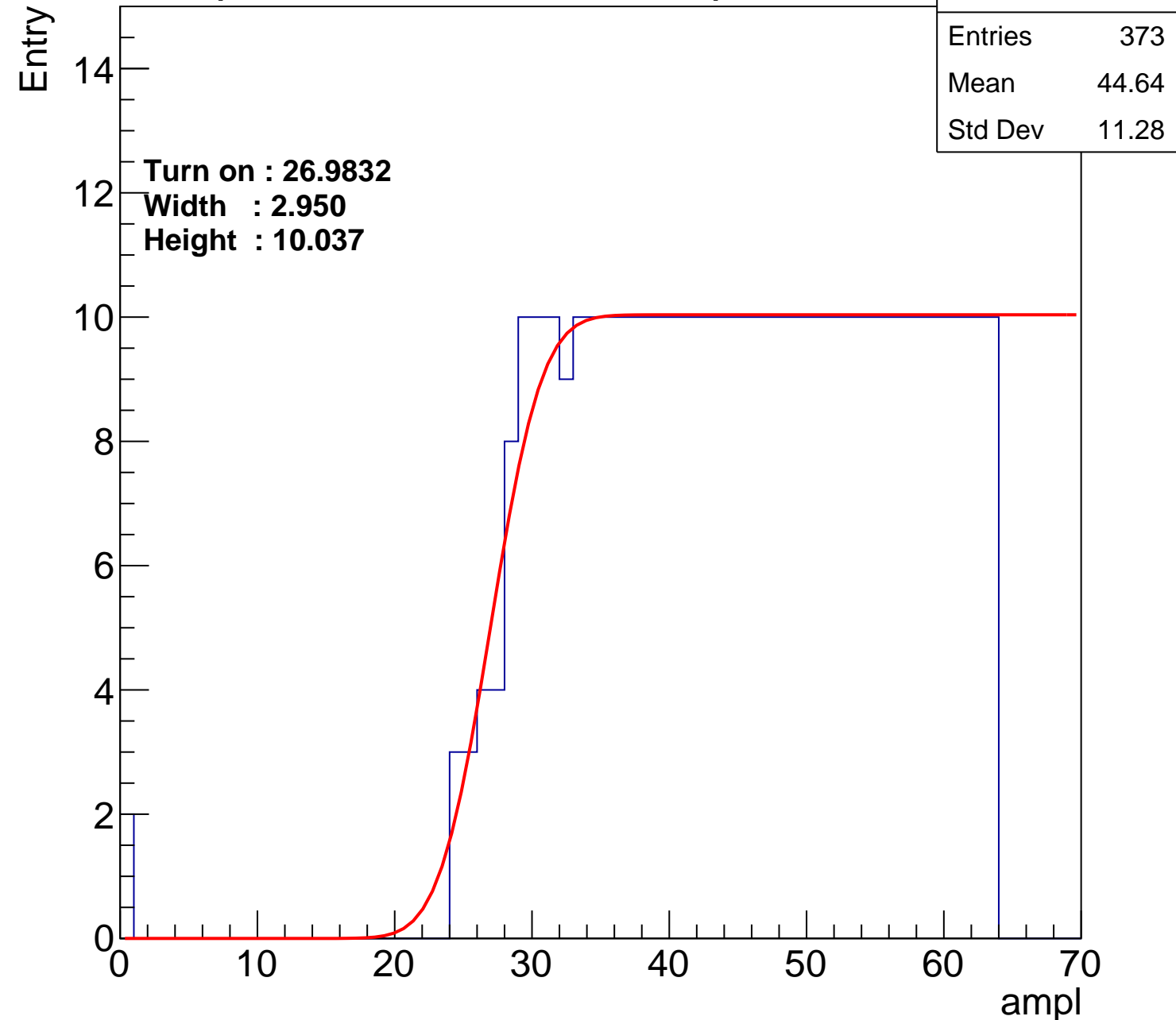
Width : 2.950

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch60

calib\_packv5\_042523\_0143.root, FC#12, port B1

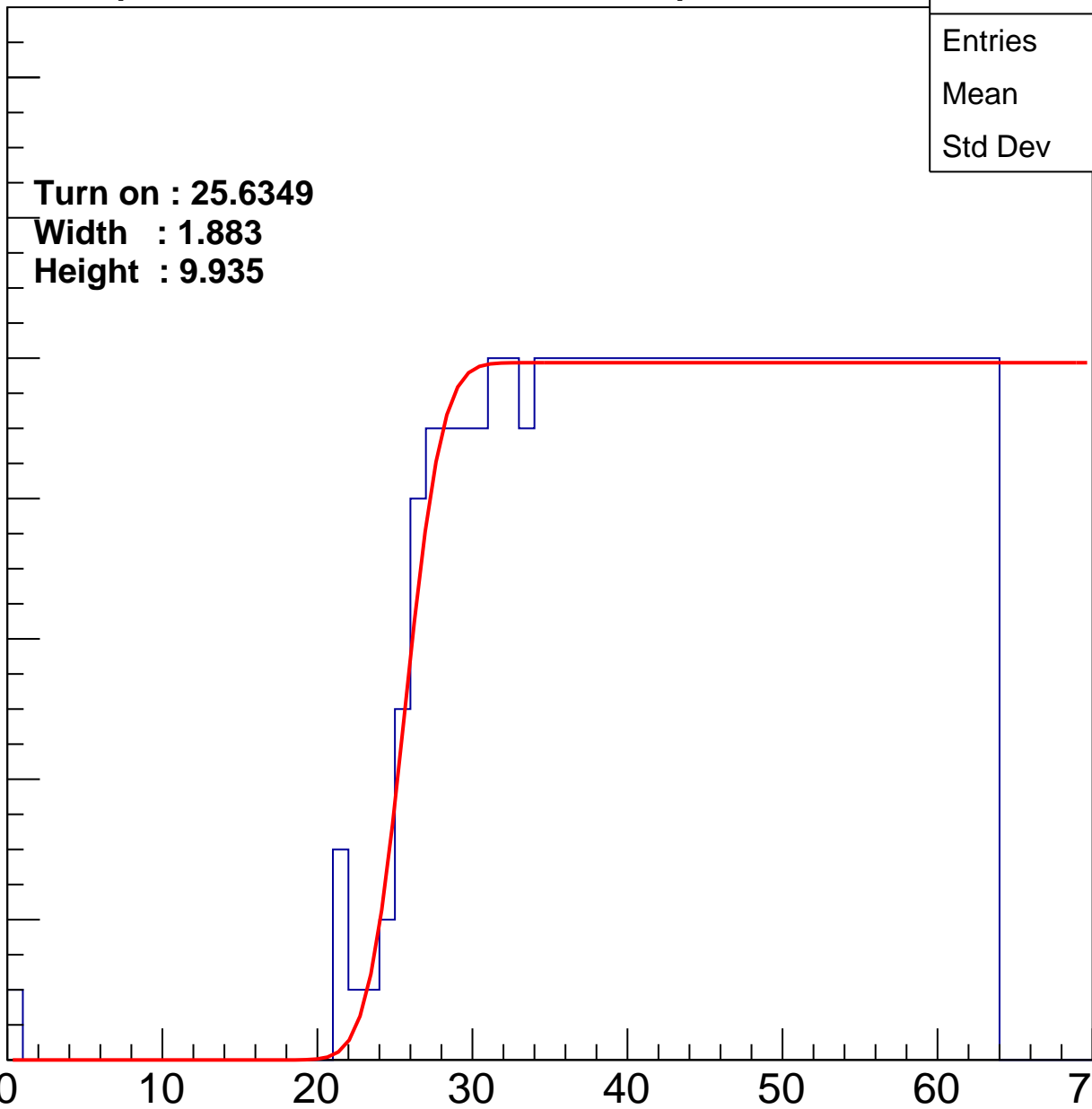
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.6349  
Width : 1.883  
Height : 9.935

Entries	386
Mean	44.02
Std Dev	11.51

ampl



# B0L102S, U21-ch61

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.87
Std Dev	10.98

Turn on : 27.2509

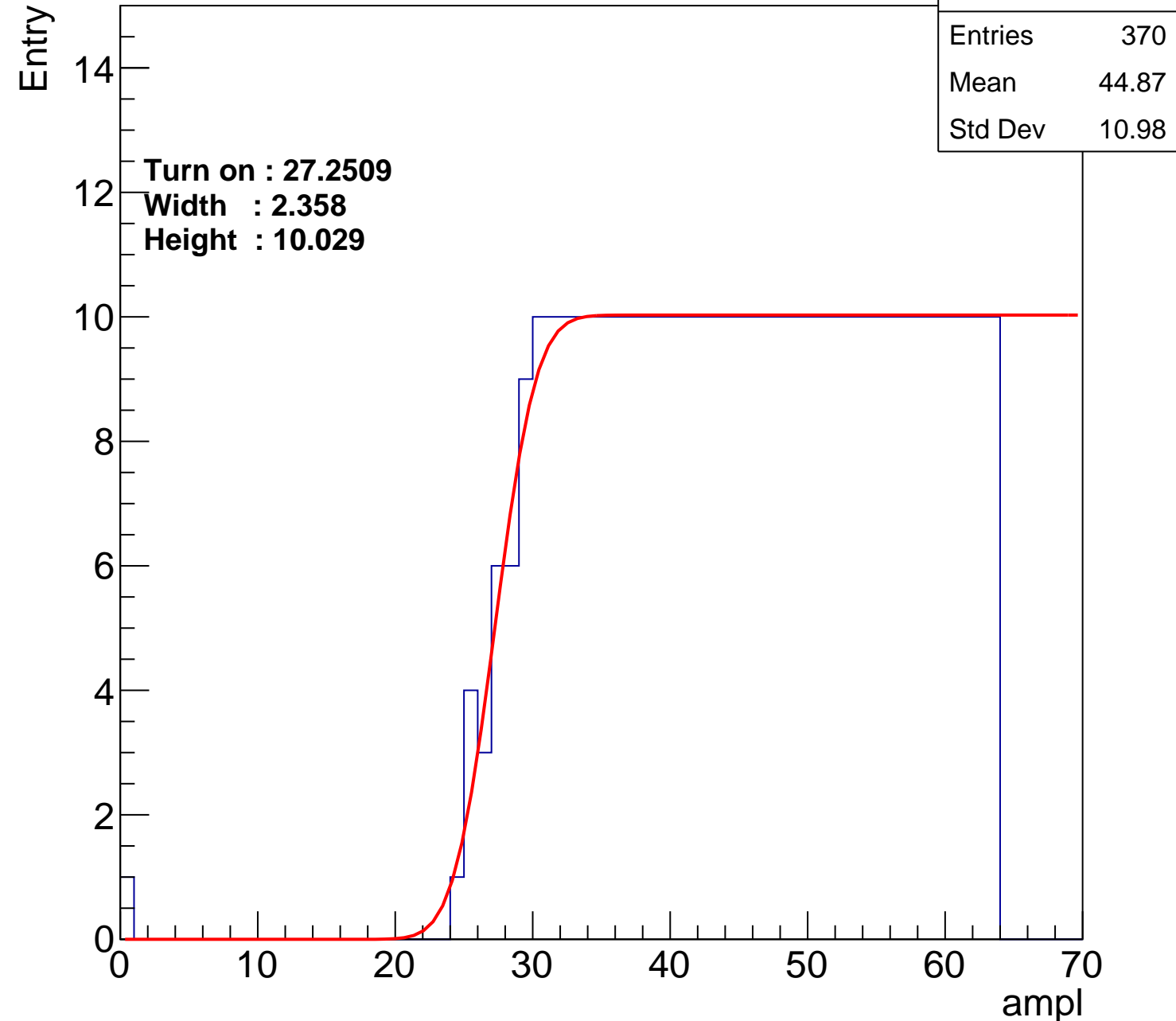
Width : 2.358

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch62

calib\_packv5\_042523\_0143.root, FC#12, port B1

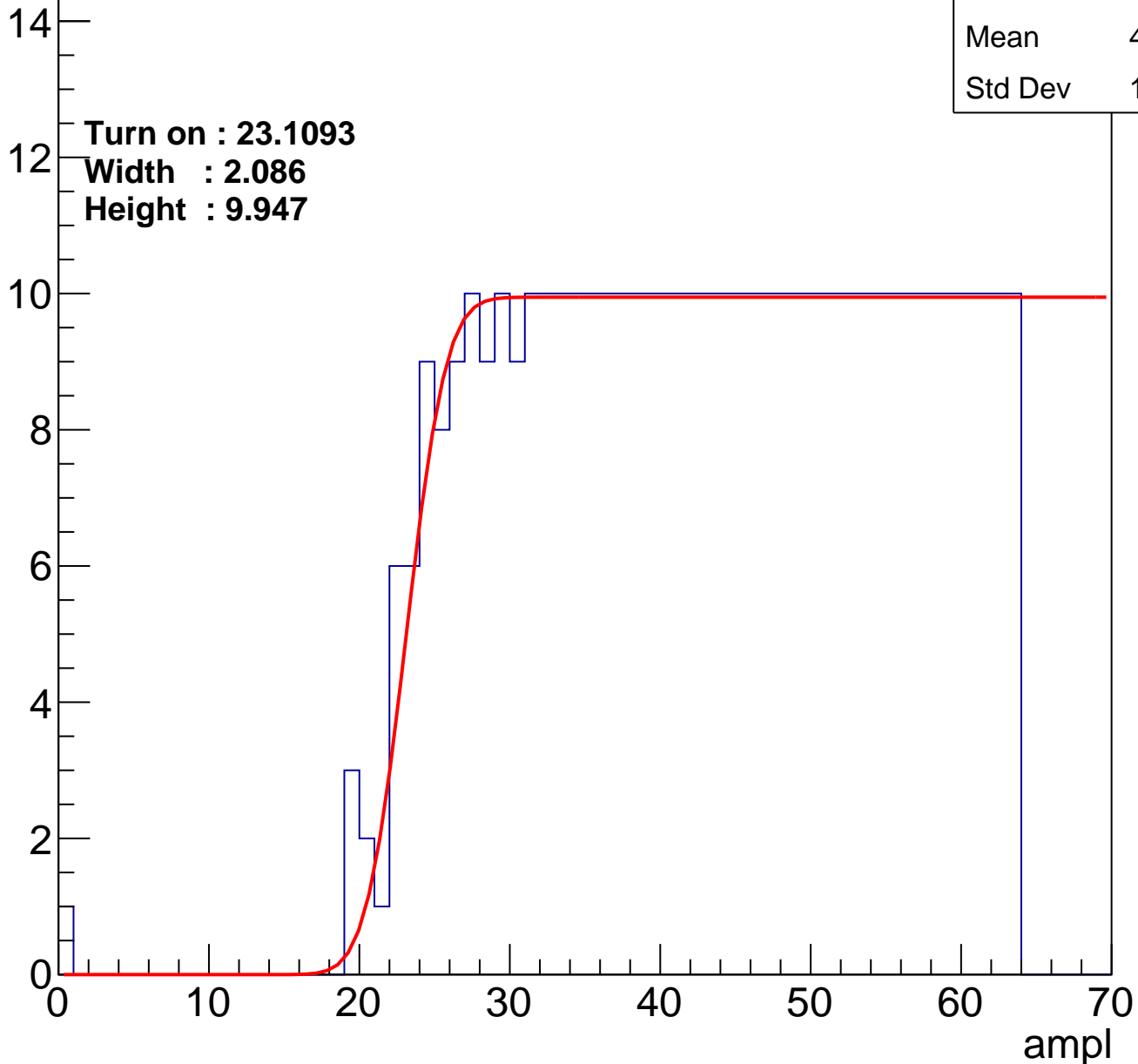
Entries	413
Mean	42.69
Std Dev	12.24

Turn on : 23.1093

Width : 2.086

Height : 9.947

Entry





# B0L102S, U21-ch63

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.31
Std Dev	12.14

Turn on : 24.5868

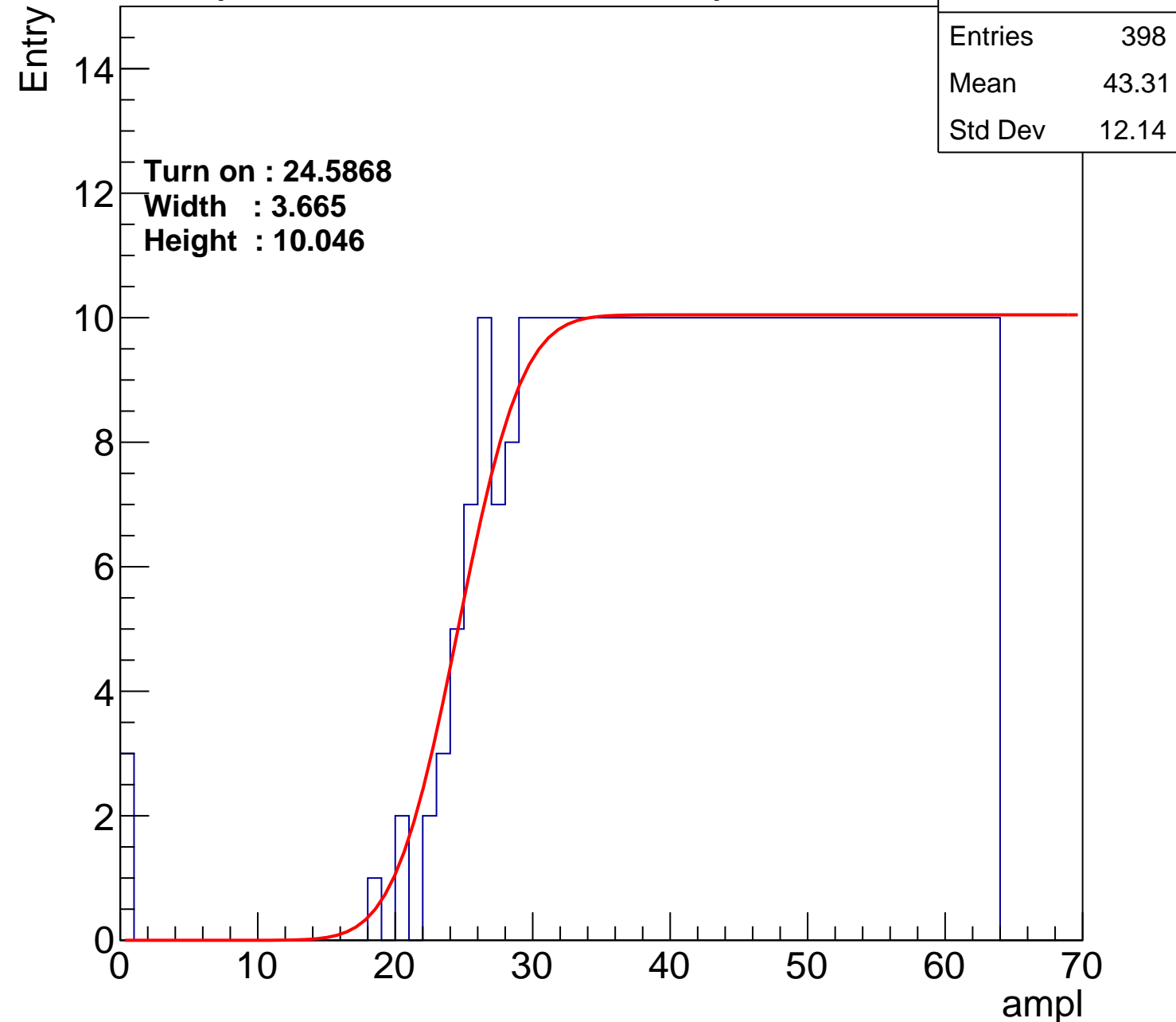
Width : 3.665

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch64

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.07
Std Dev	11.91

Turn on : 26.4818

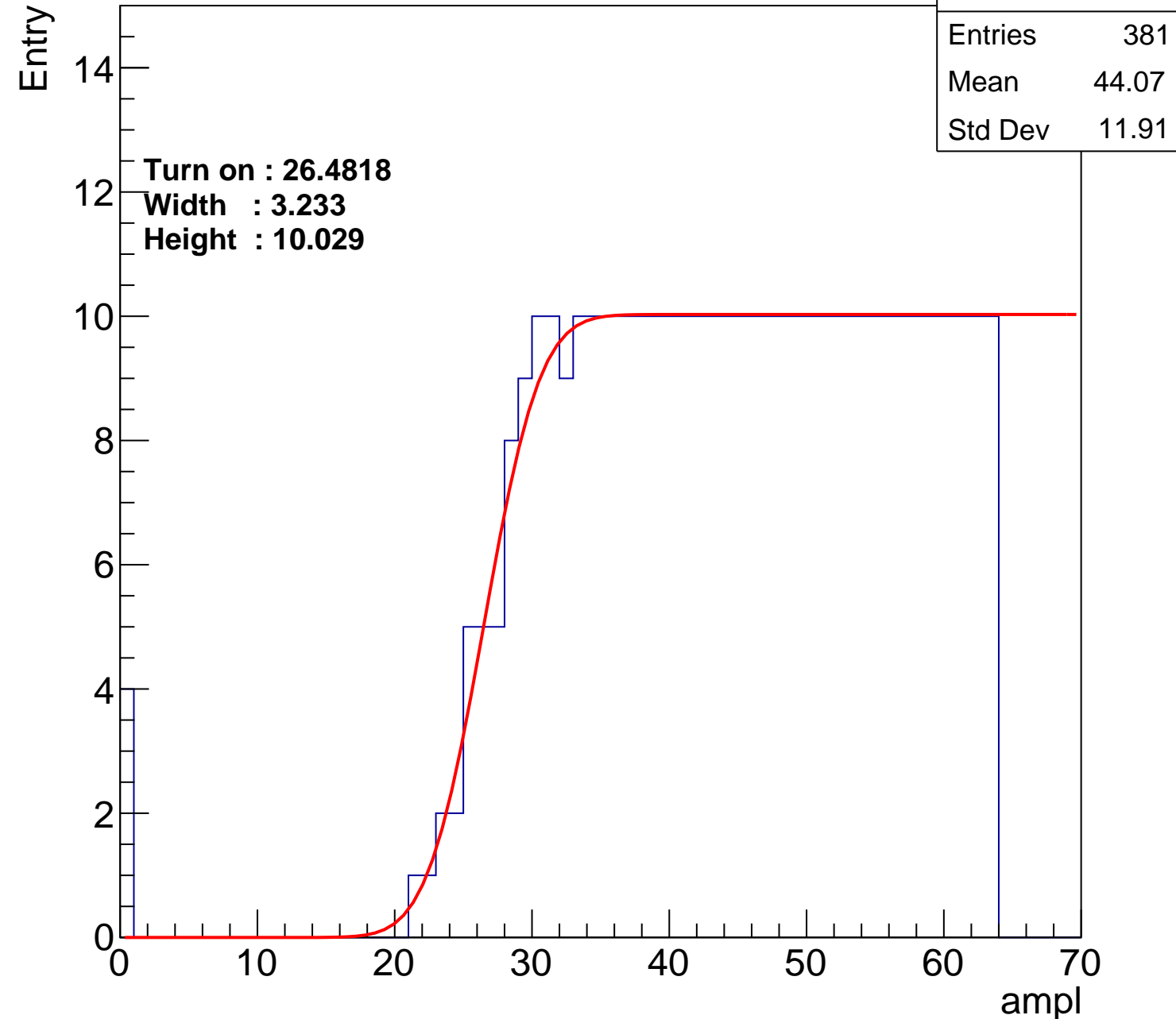
Width : 3.233

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch65

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.3888

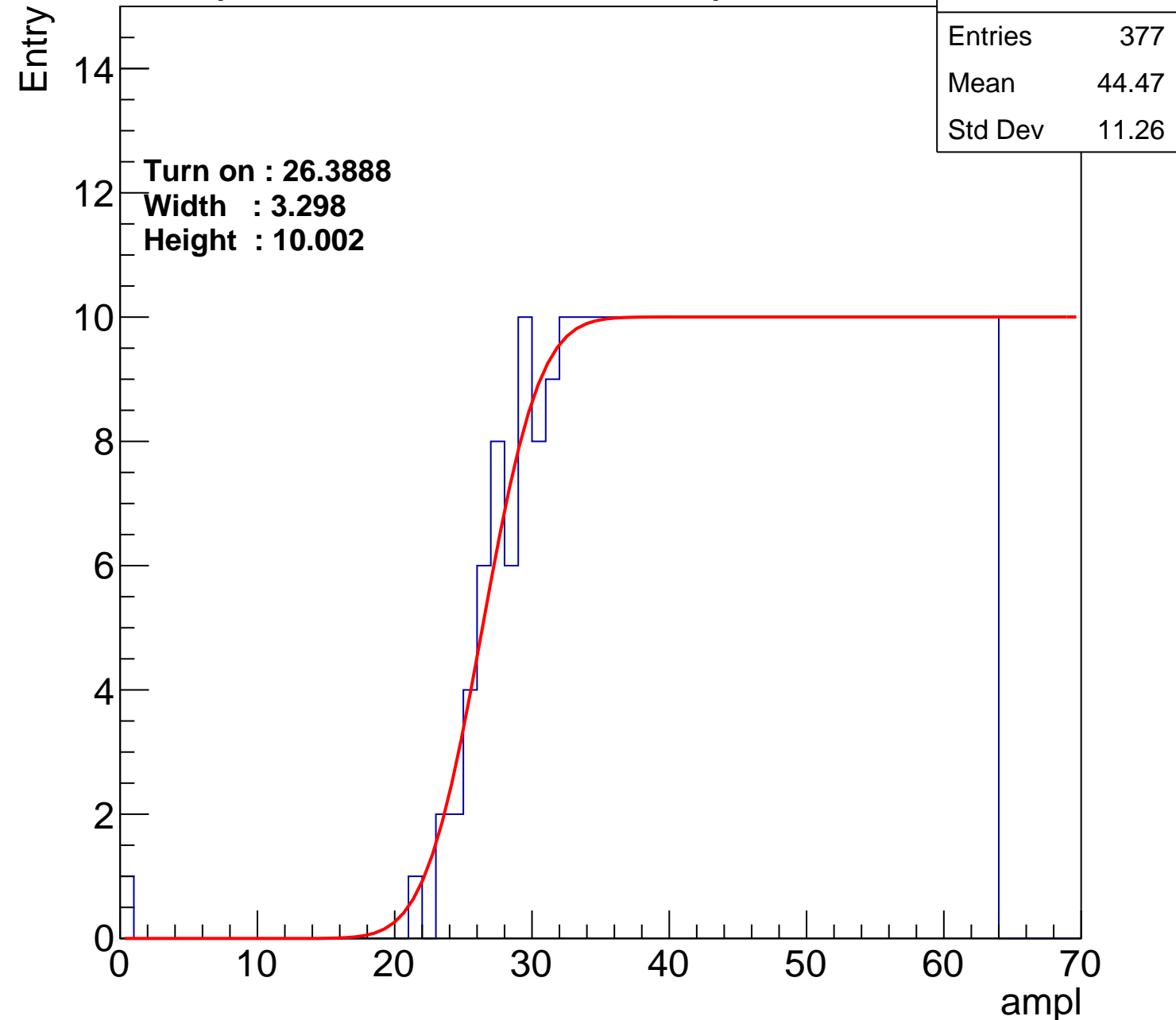
Width : 3.298

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch66

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.73
Std Dev	12.08

Turn on : 25.8547

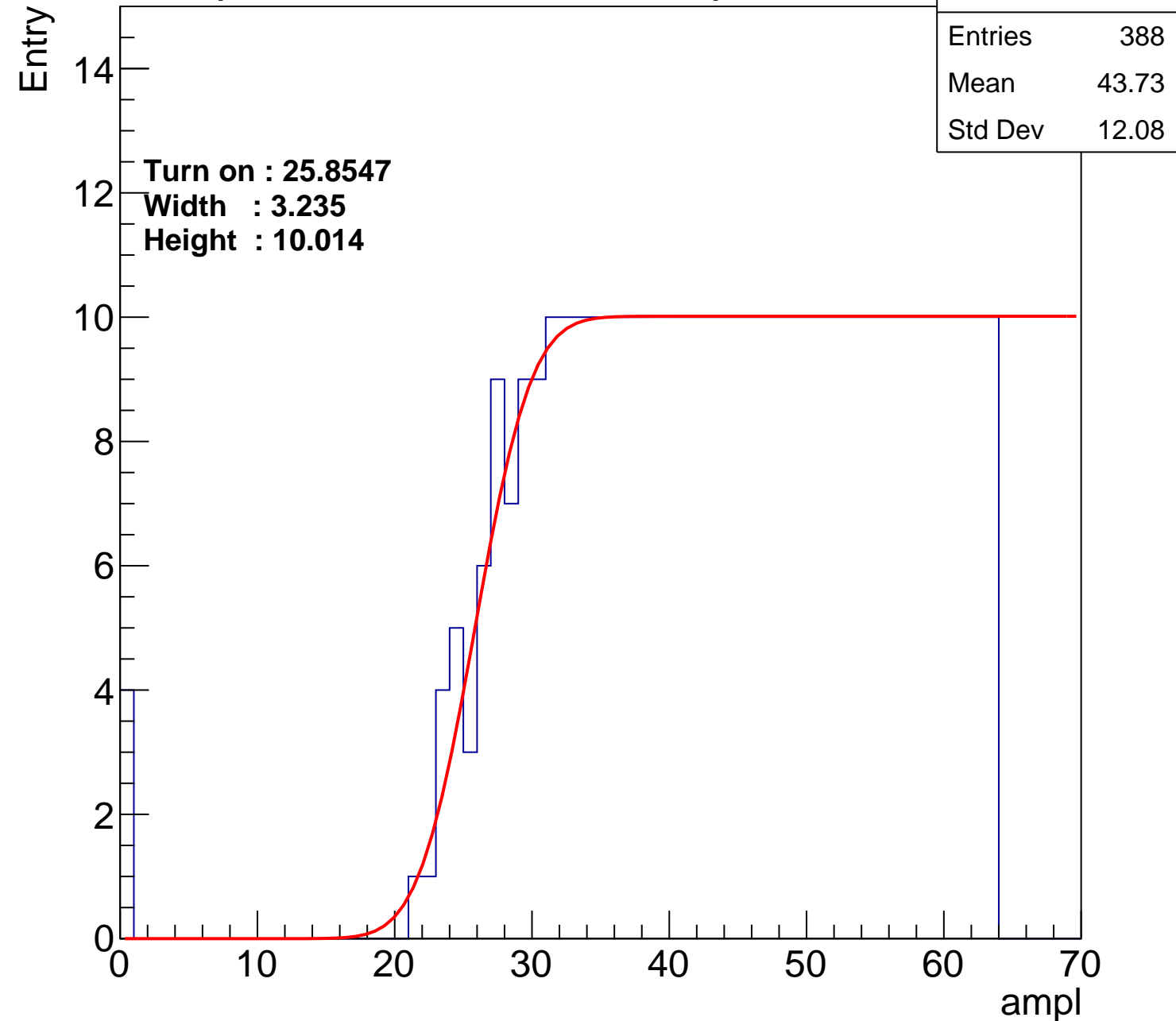
Width : 3.235

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch67

calib\_packv5\_042523\_0143.root, FC#12, port B1

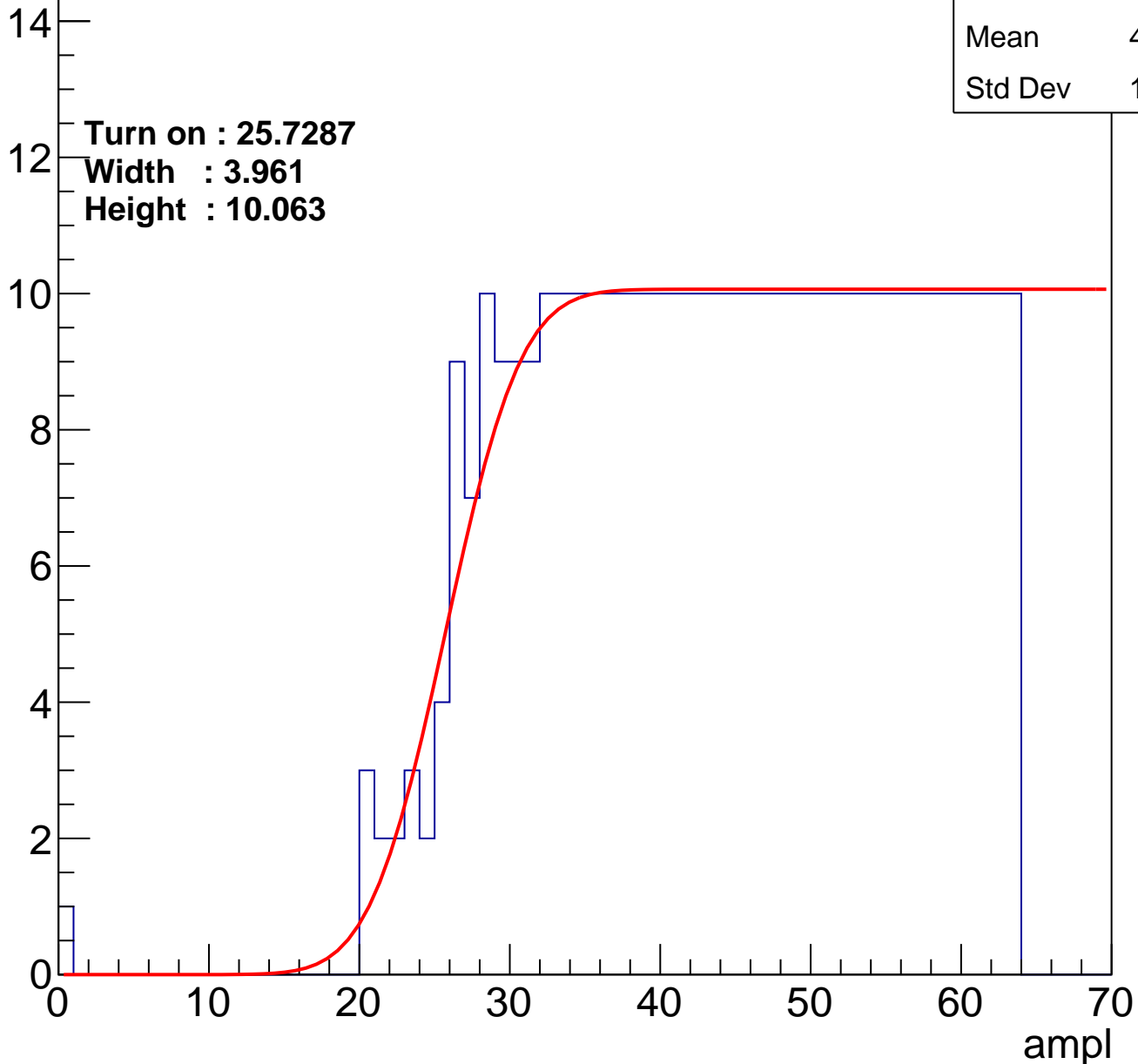
Entries	390
Mean	43.78
Std Dev	11.68

Turn on : 25.7287

Width : 3.961

Height : 10.063

Entry



# B0L102S, U21-ch68

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.3049

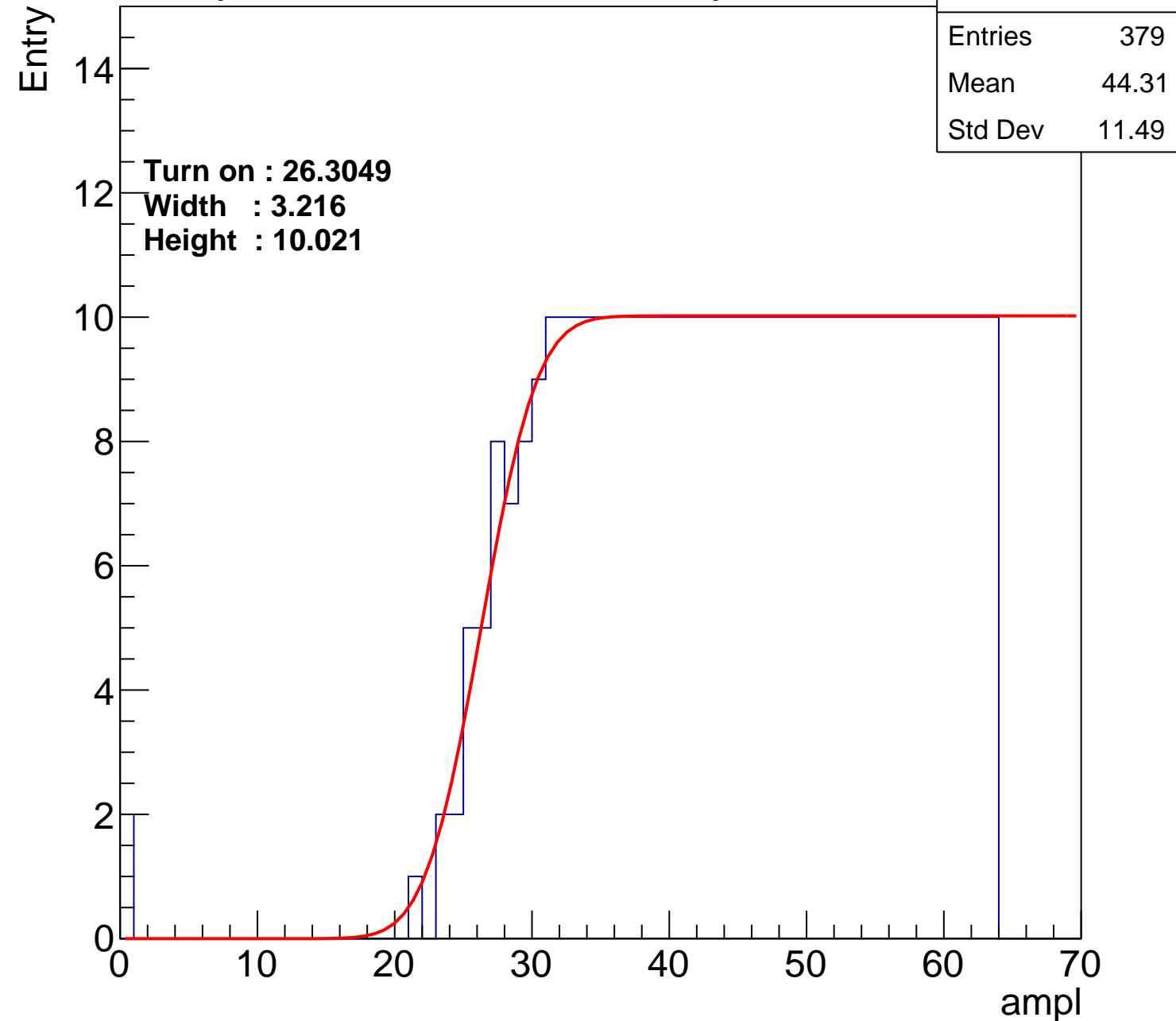
Width : 3.216

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch69

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	375
Mean	44.39
Std Dev	11.74

Turn on : 27.4773

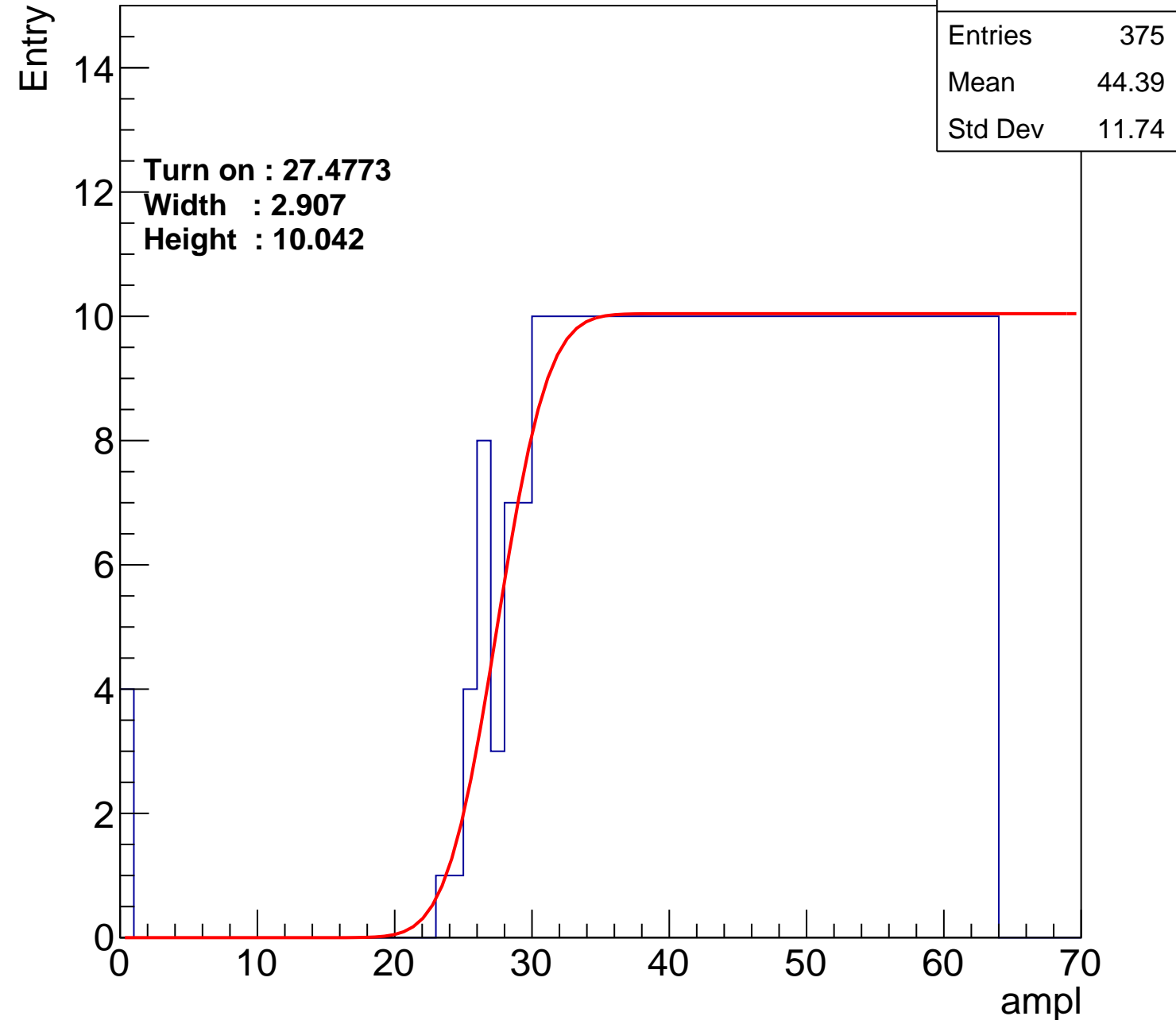
Width : 2.907

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch70

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.26
Std Dev	11.44

Turn on : 26.6838

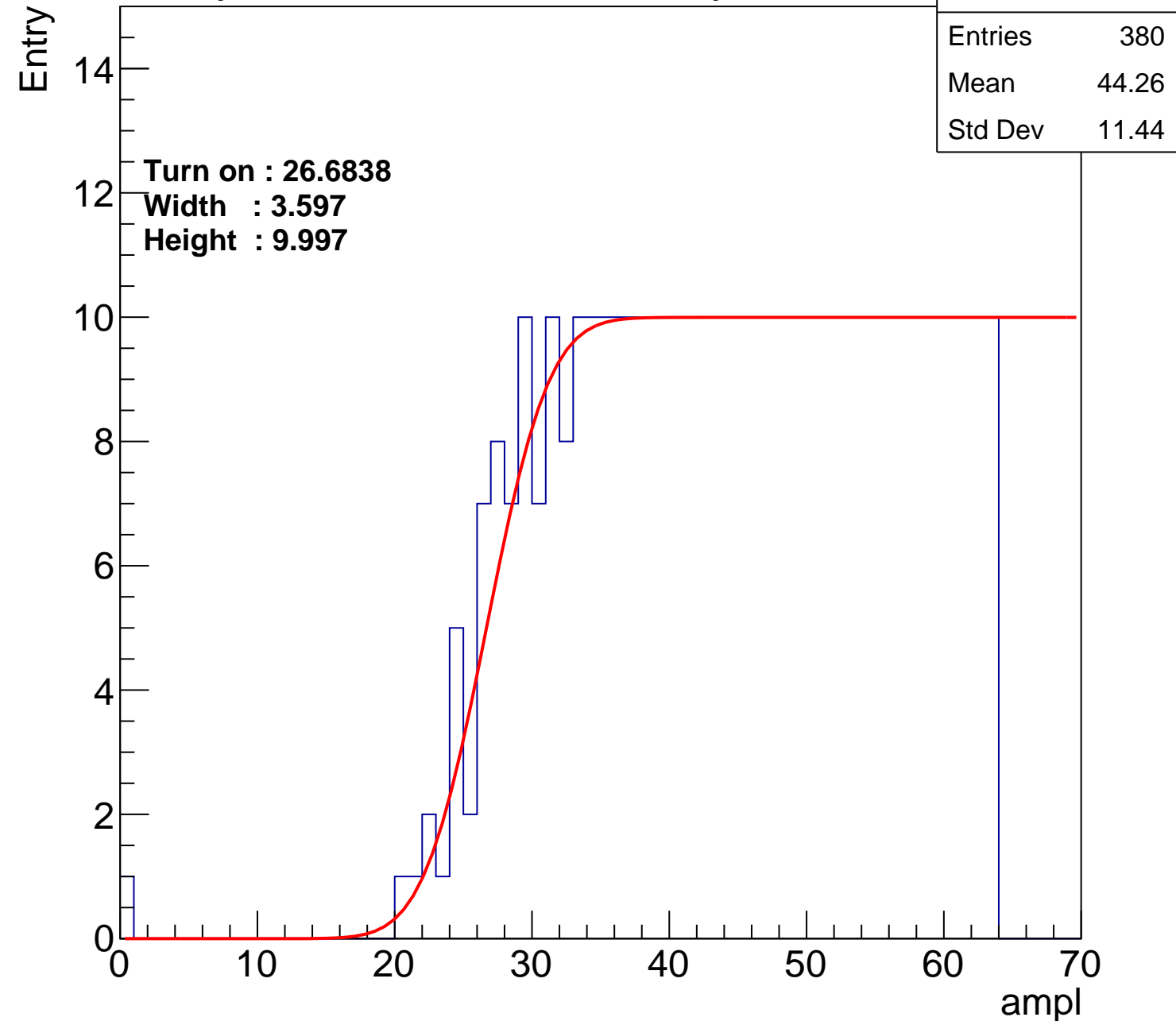
Width : 3.597

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch71

calib\_packv5\_042523\_0143.root, FC#12, port B1

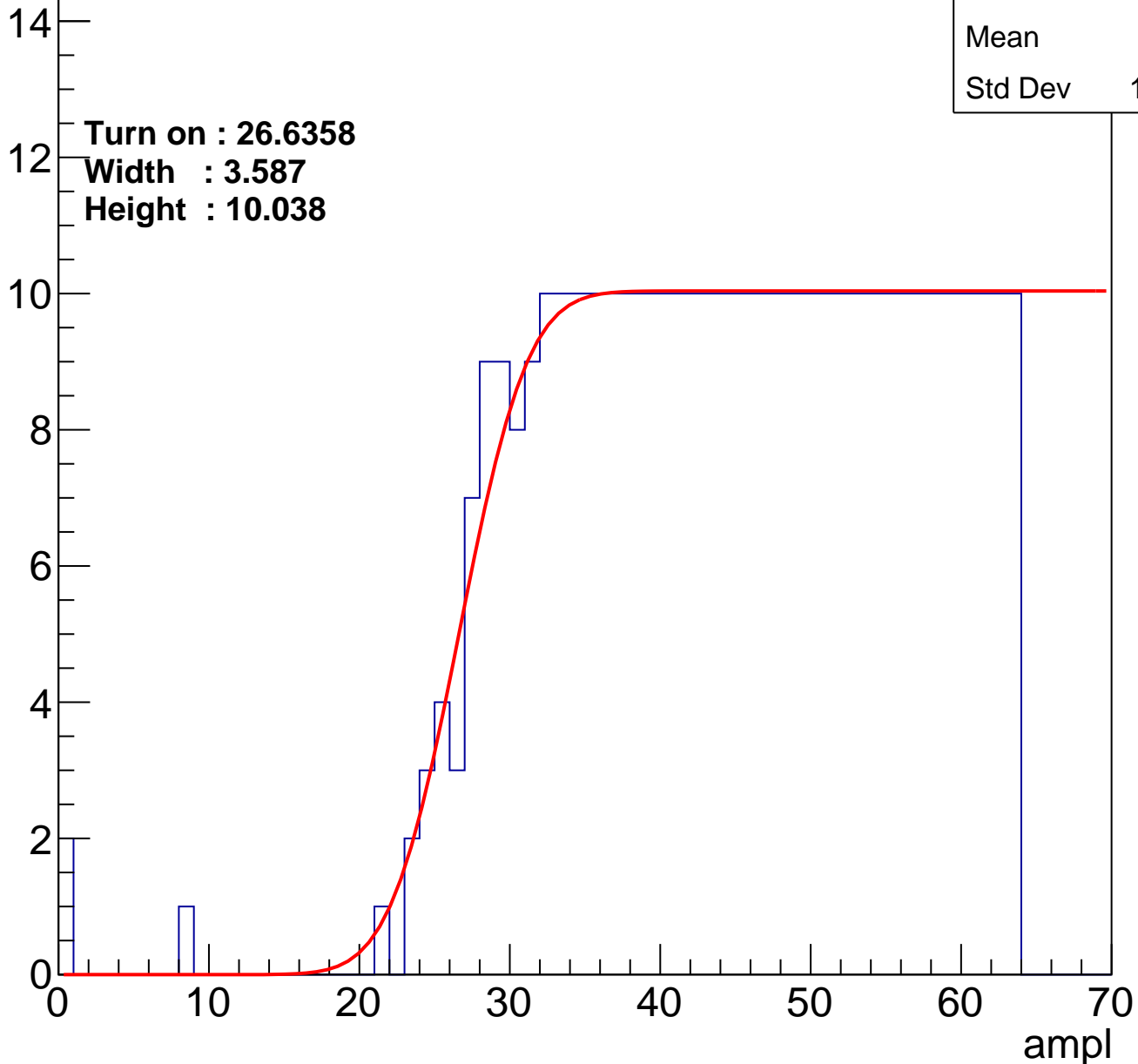
Entries	378
Mean	44.3
Std Dev	11.59

Turn on : 26.6358

Width : 3.587

Height : 10.038

Entry



# B0L102S, U21-ch72

calib\_packv5\_042523\_0143.root, FC#12, port B1

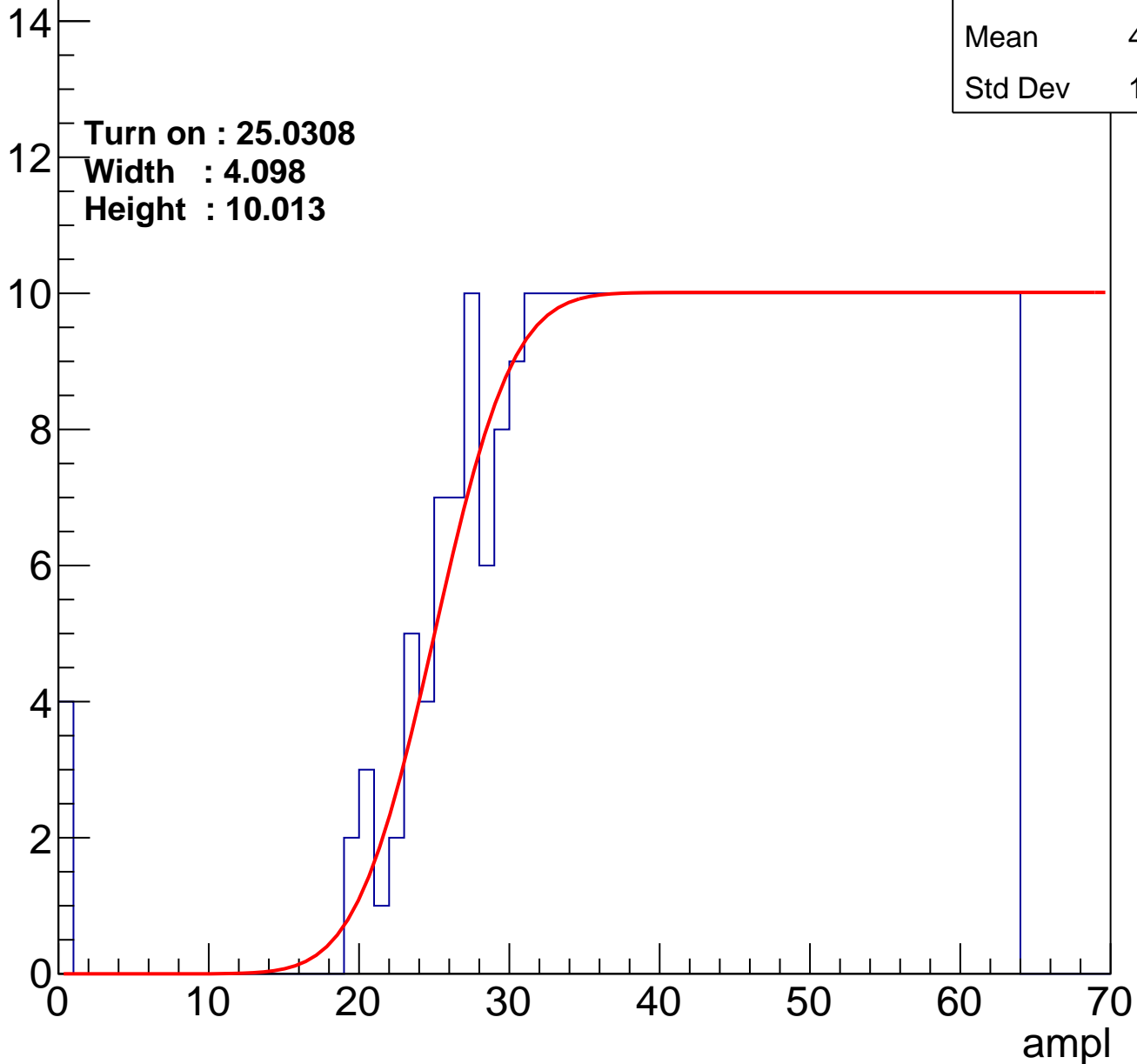
Entries	398
Mean	43.17
Std Dev	12.42

Turn on : 25.0308

Width : 4.098

Height : 10.013

Entry



# B0L102S, U21-ch73

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	358
Mean	45.47
Std Dev	10.65

Turn on : 28.5303

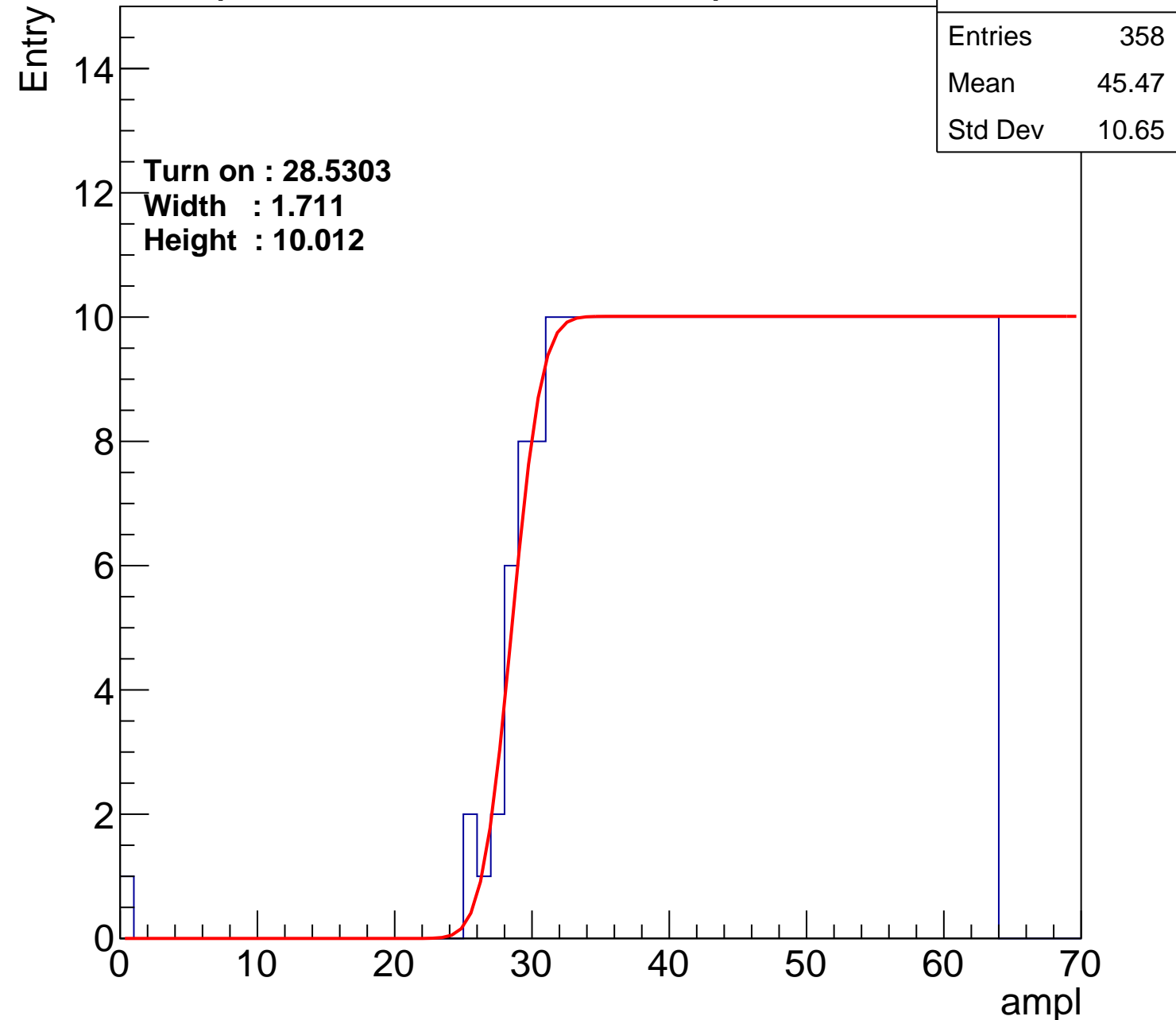
Width : 1.711

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch74

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	391
Mean	43.53
Std Dev	12.23

Turn on : 25.6554

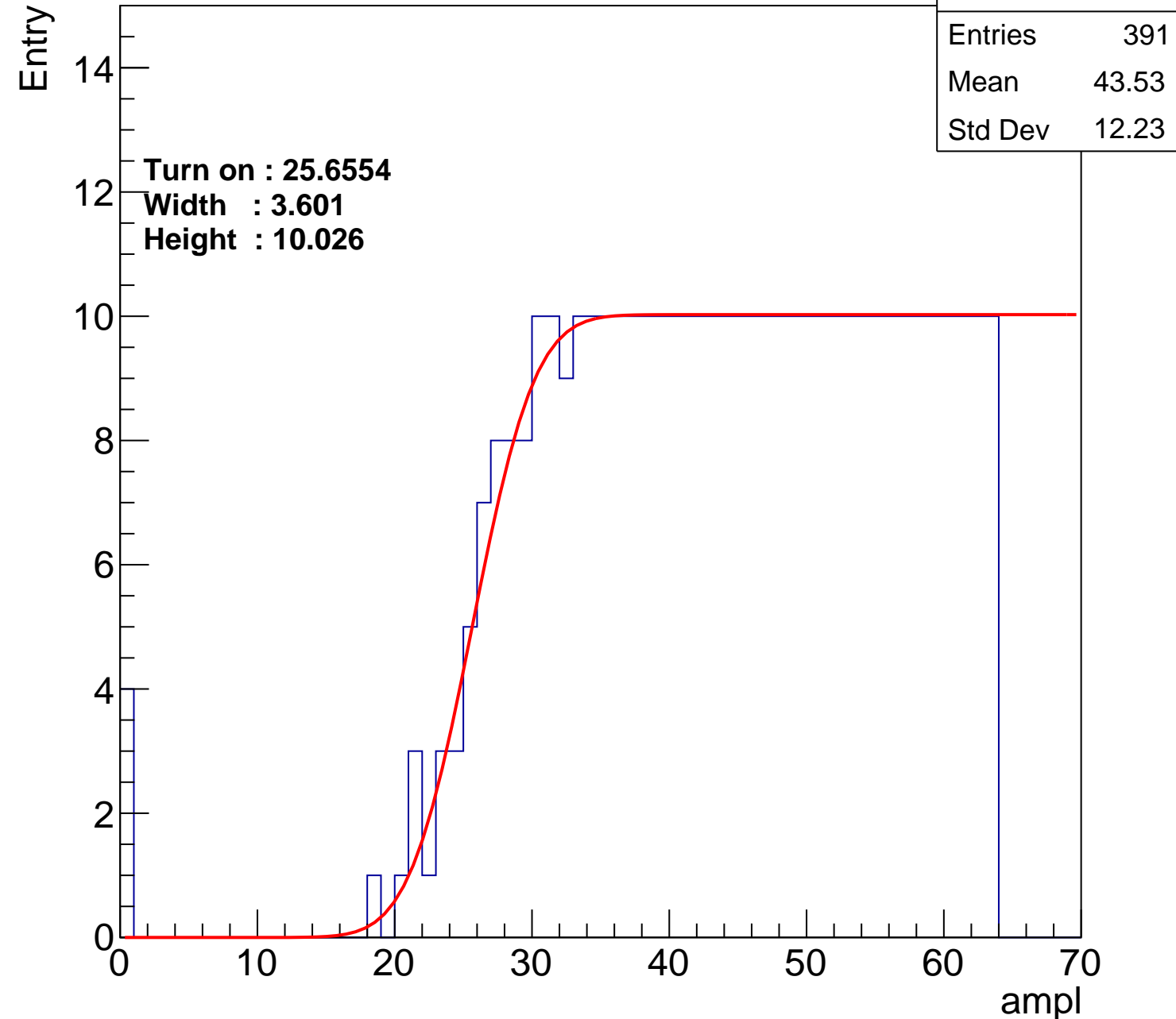
Width : 3.601

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch75

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.53
Std Dev	11.84

Turn on : 25.0355

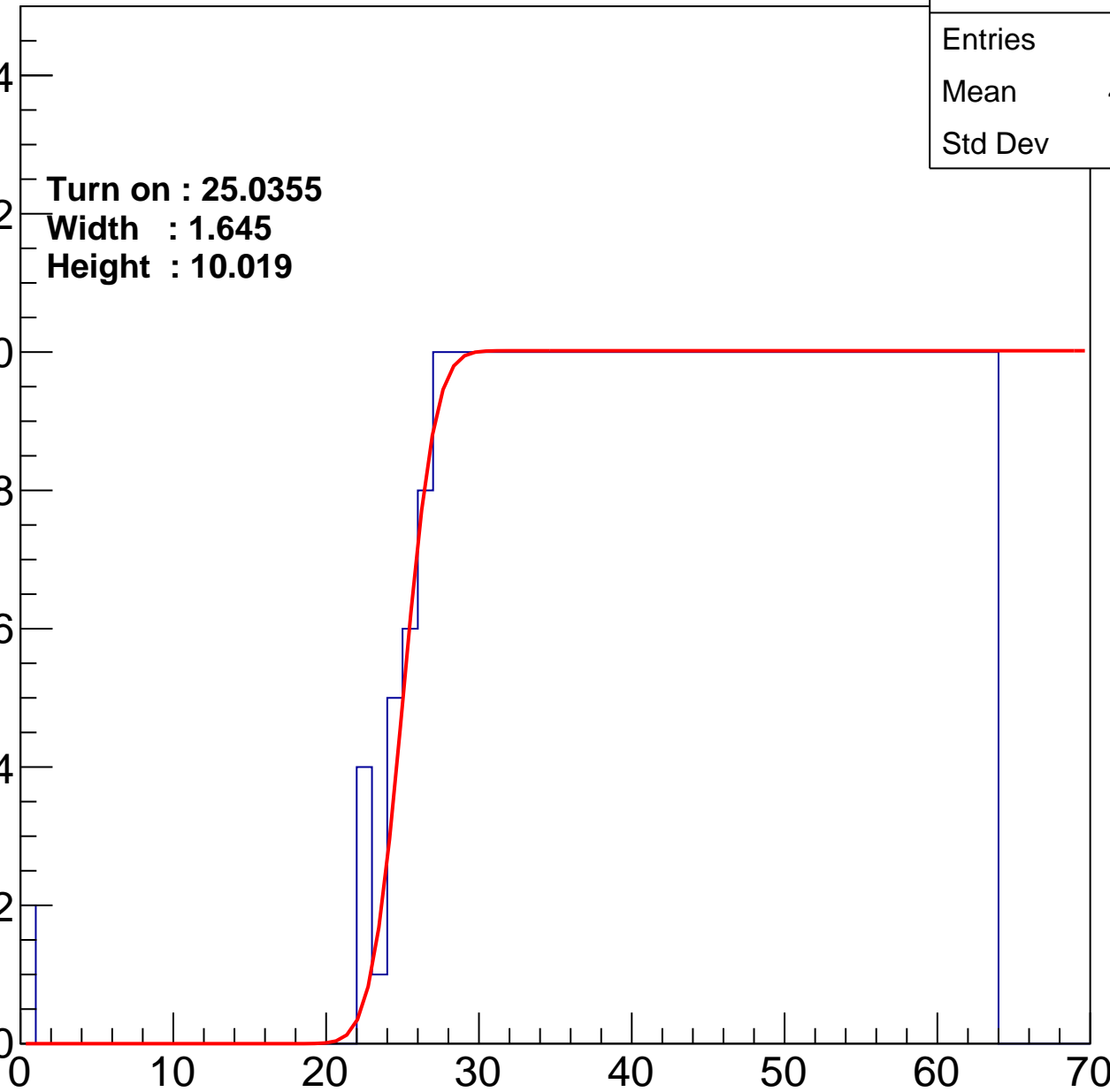
Width : 1.645

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch76

calib\_packv5\_042523\_0143.root, FC#12, port B1

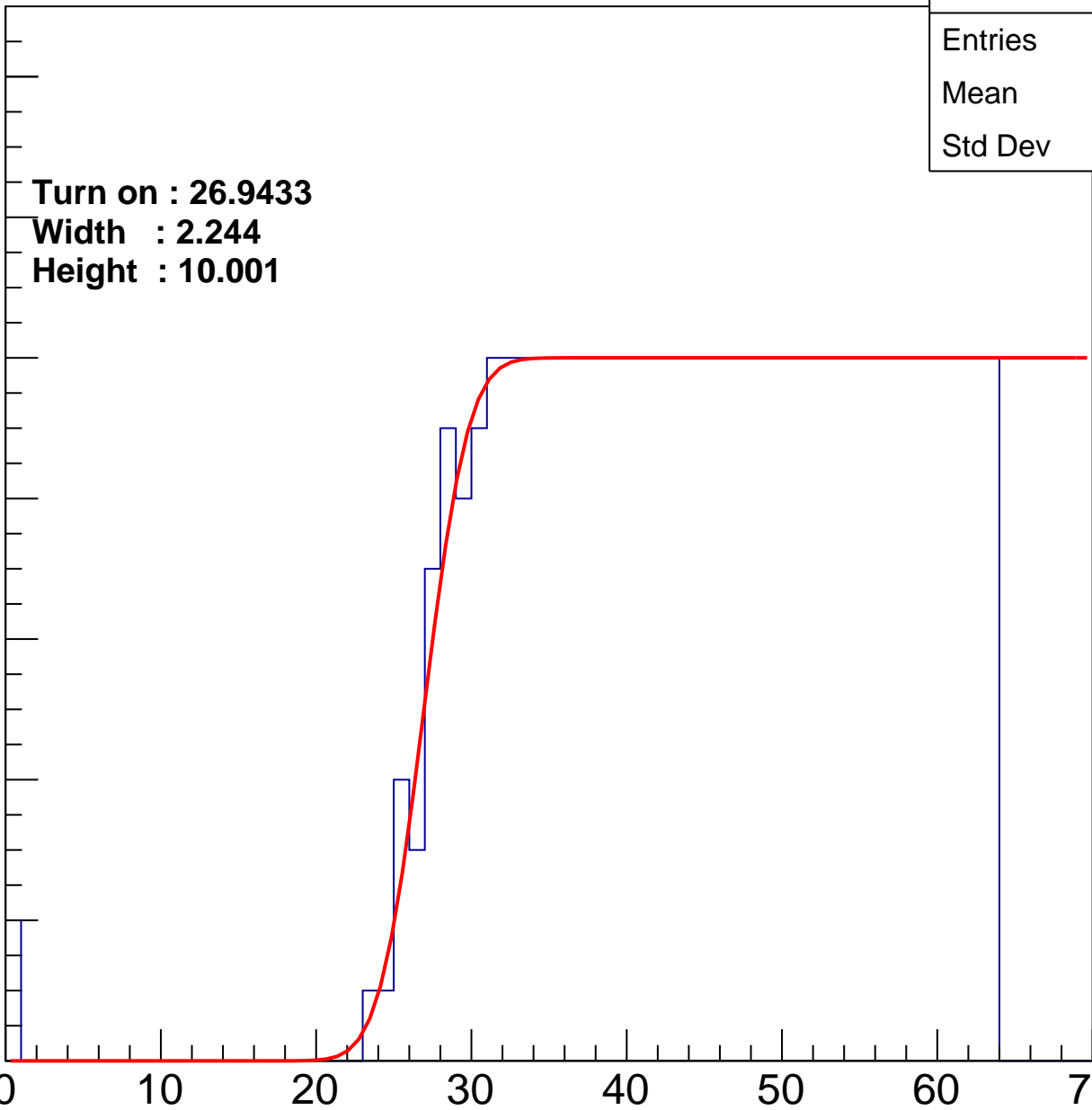
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.9433**  
**Width : 2.244**  
**Height : 10.001**

Entries	374
Mean	44.59
Std Dev	11.3

ampl



# B0L102S, U21-ch77

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	401
Mean	43.29
Std Dev	11.91

Turn on : 24.5833

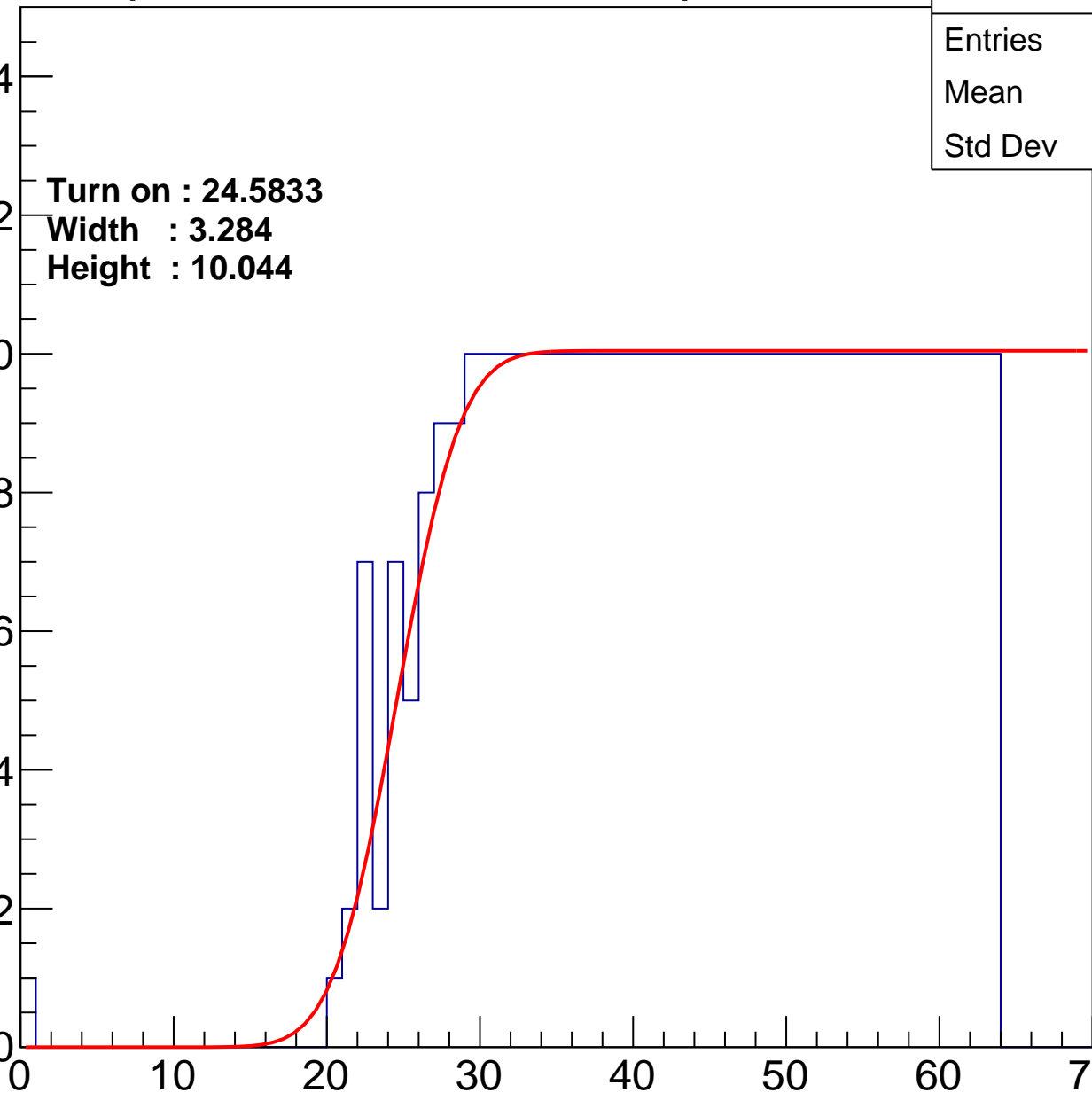
Width : 3.284

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch78

calib\_packv5\_042523\_0143.root, FC#12, port B1

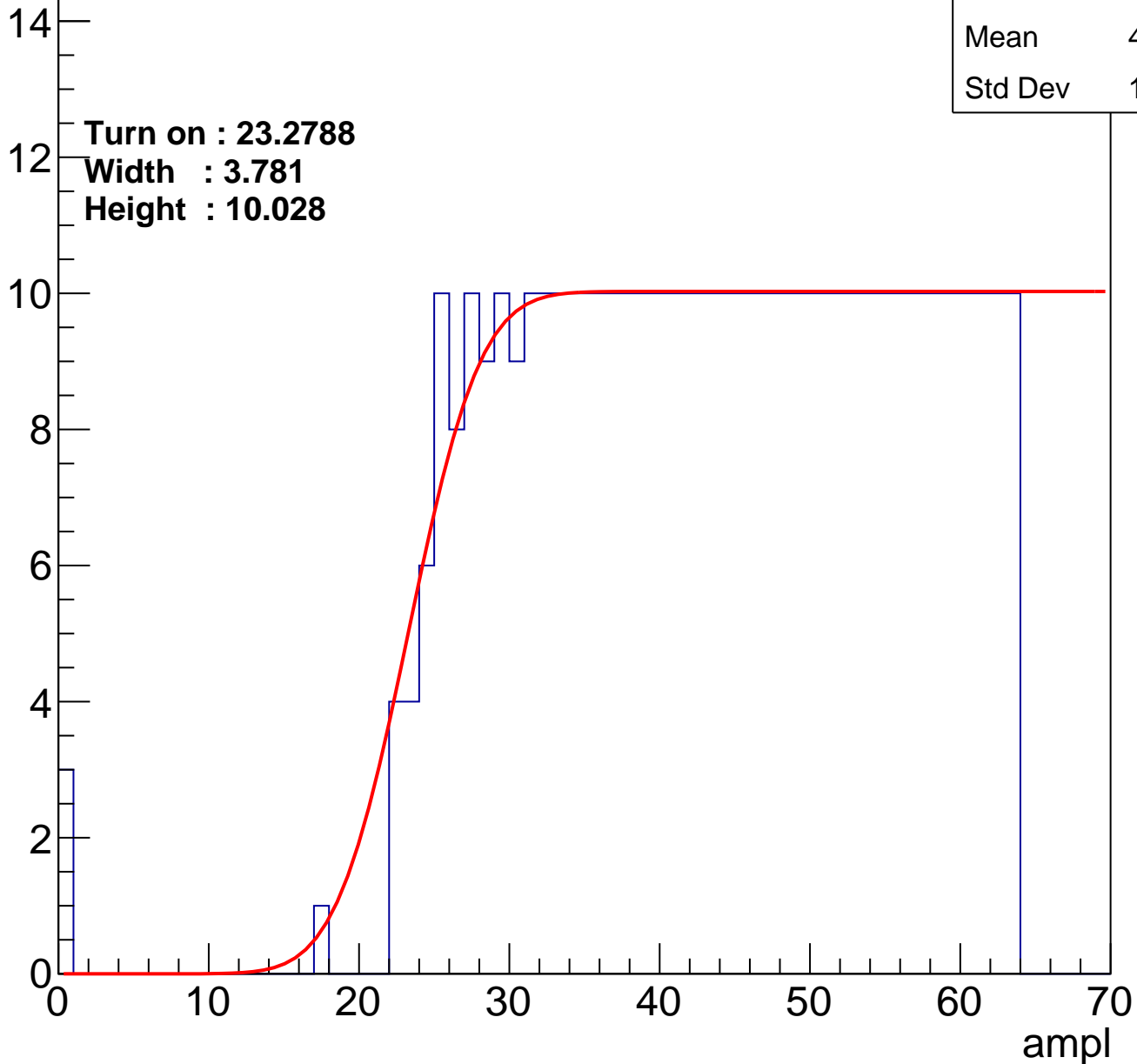
Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 23.2788

Width : 3.781

Height : 10.028

Entry





# B0L102S, U21-ch79

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.06
Std Dev	11.76

Turn on : 26.1466

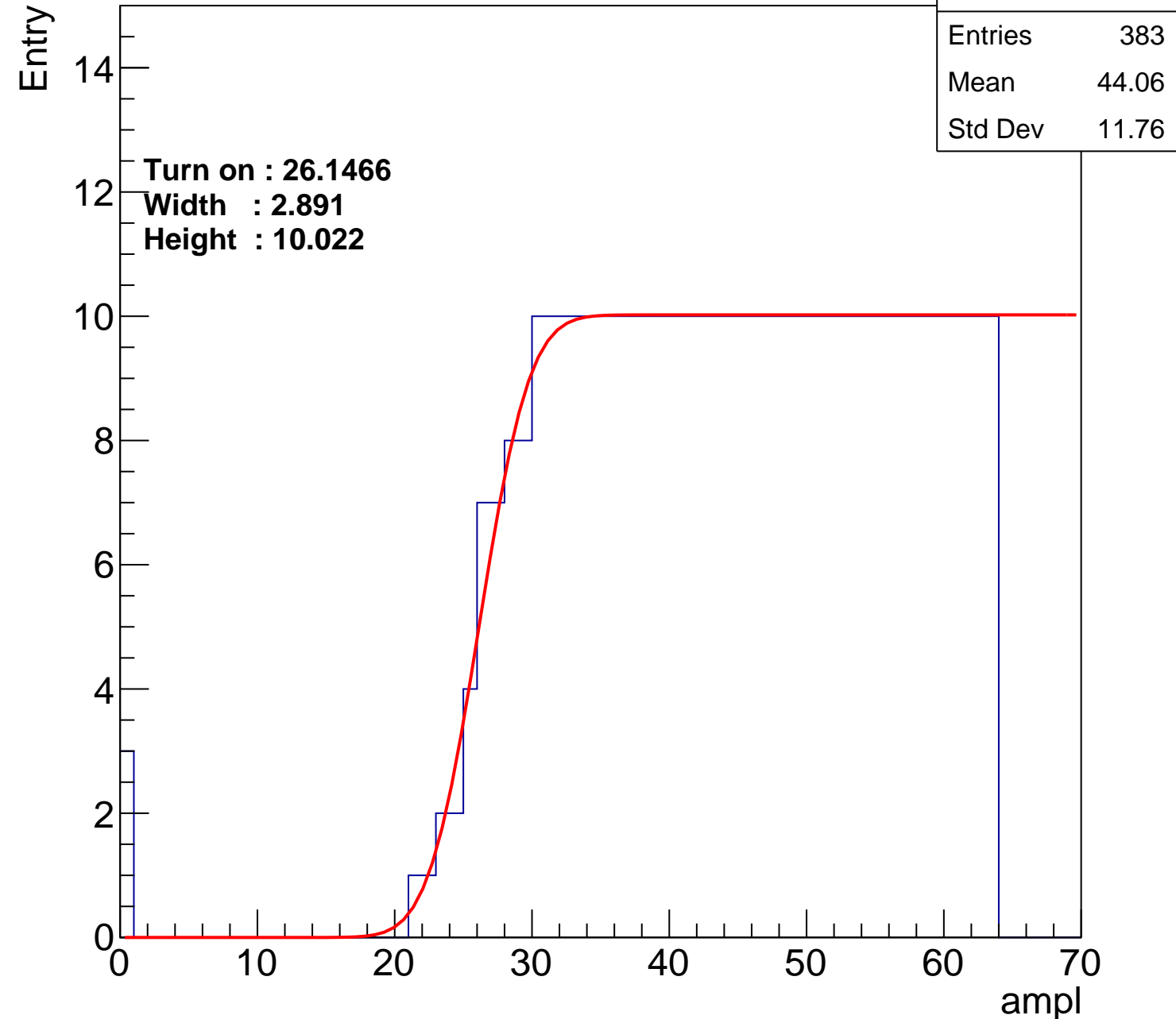
Width : 2.891

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch80

calib\_packv5\_042523\_0143.root, FC#12, port B1

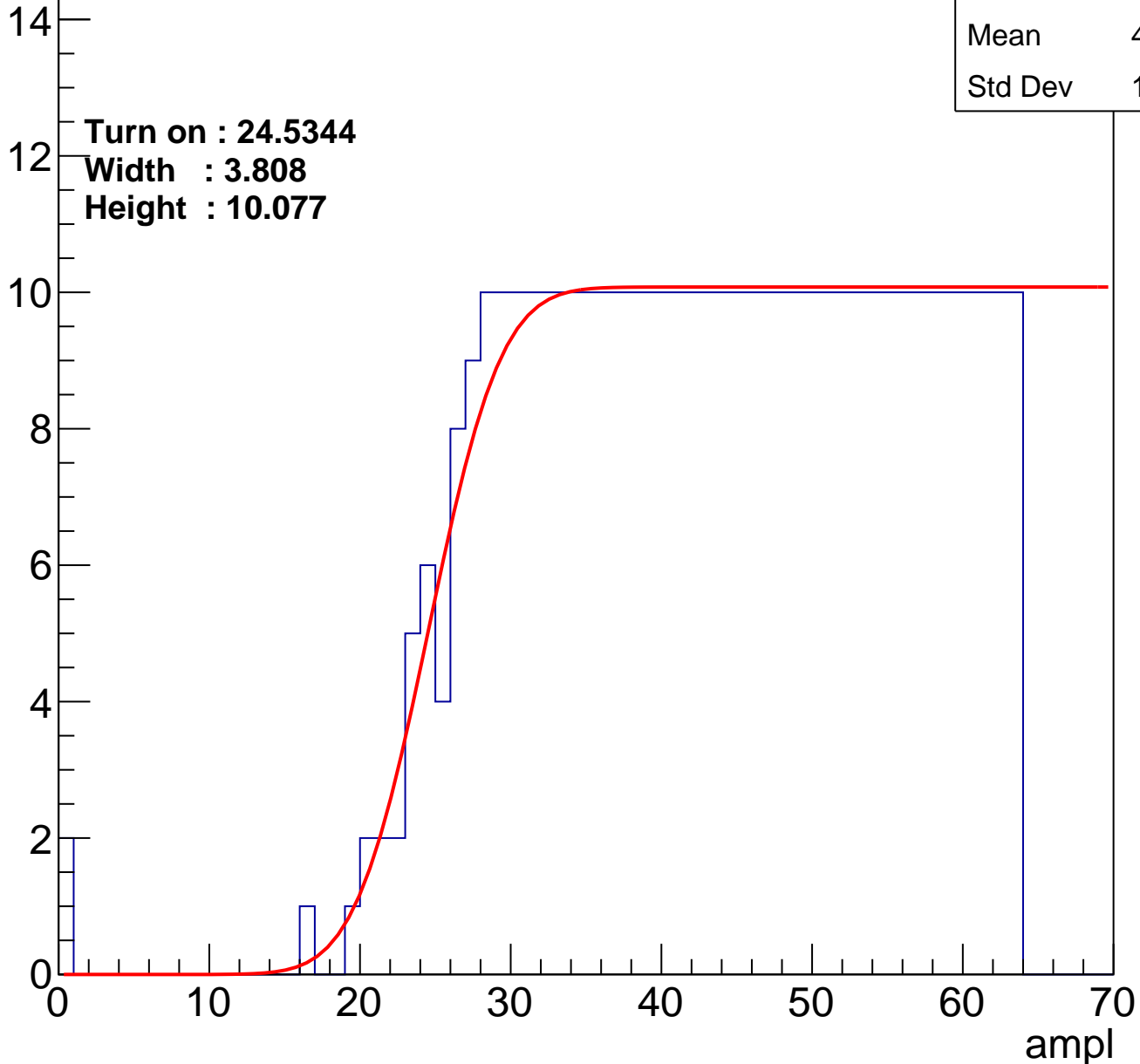
Entries	402
Mean	43.16
Std Dev	12.12

Turn on : 24.5344

Width : 3.808

Height : 10.077

Entry



# B0L102S, U21-ch81

calib\_packv5\_042523\_0143.root, FC#12, port B1

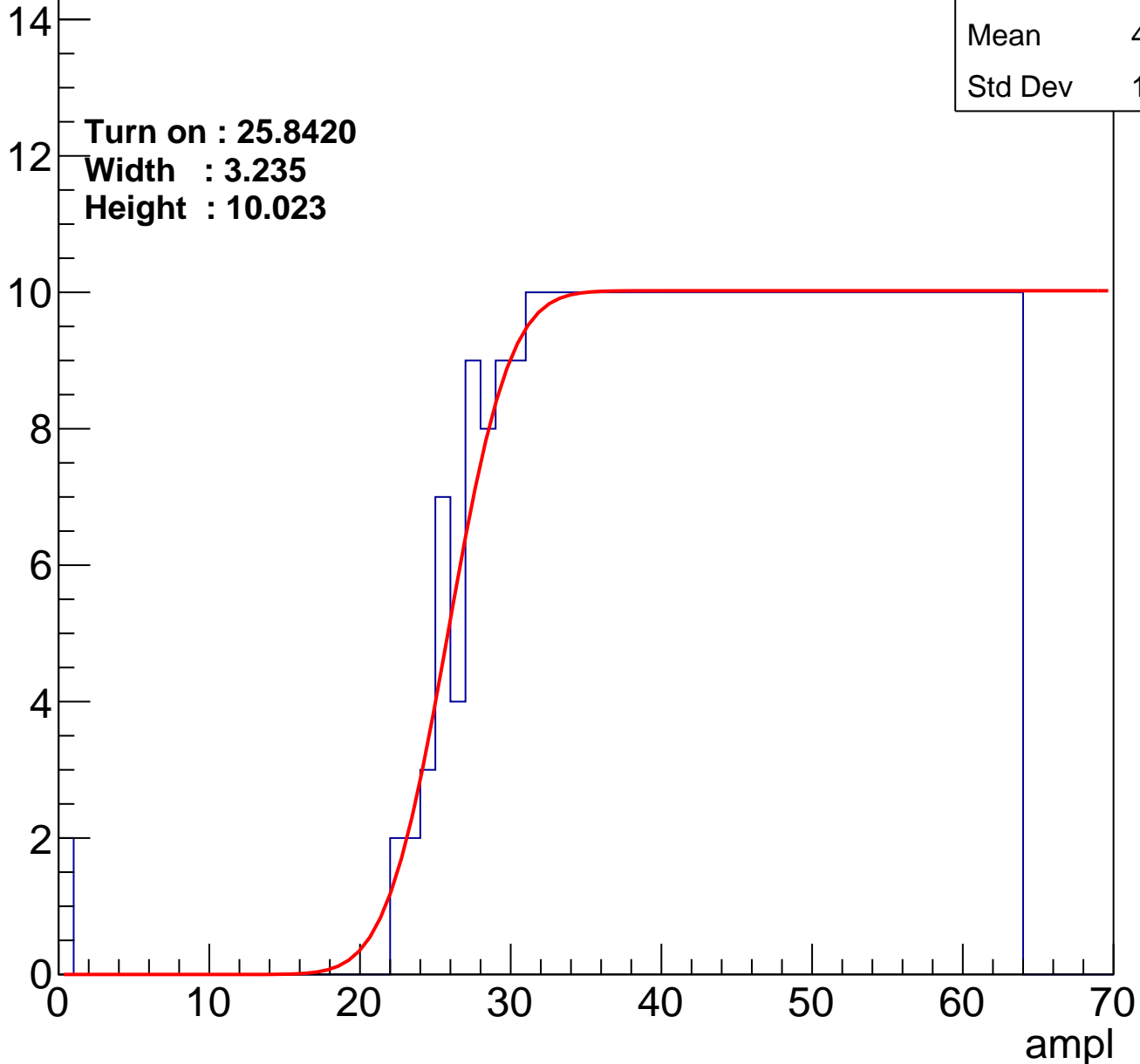
Entries	385
Mean	44.02
Std Dev	11.63

Turn on : 25.8420

Width : 3.235

Height : 10.023

Entry



# B0L102S, U21-ch82

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.04
Std Dev	11.92

Turn on : 26.8474

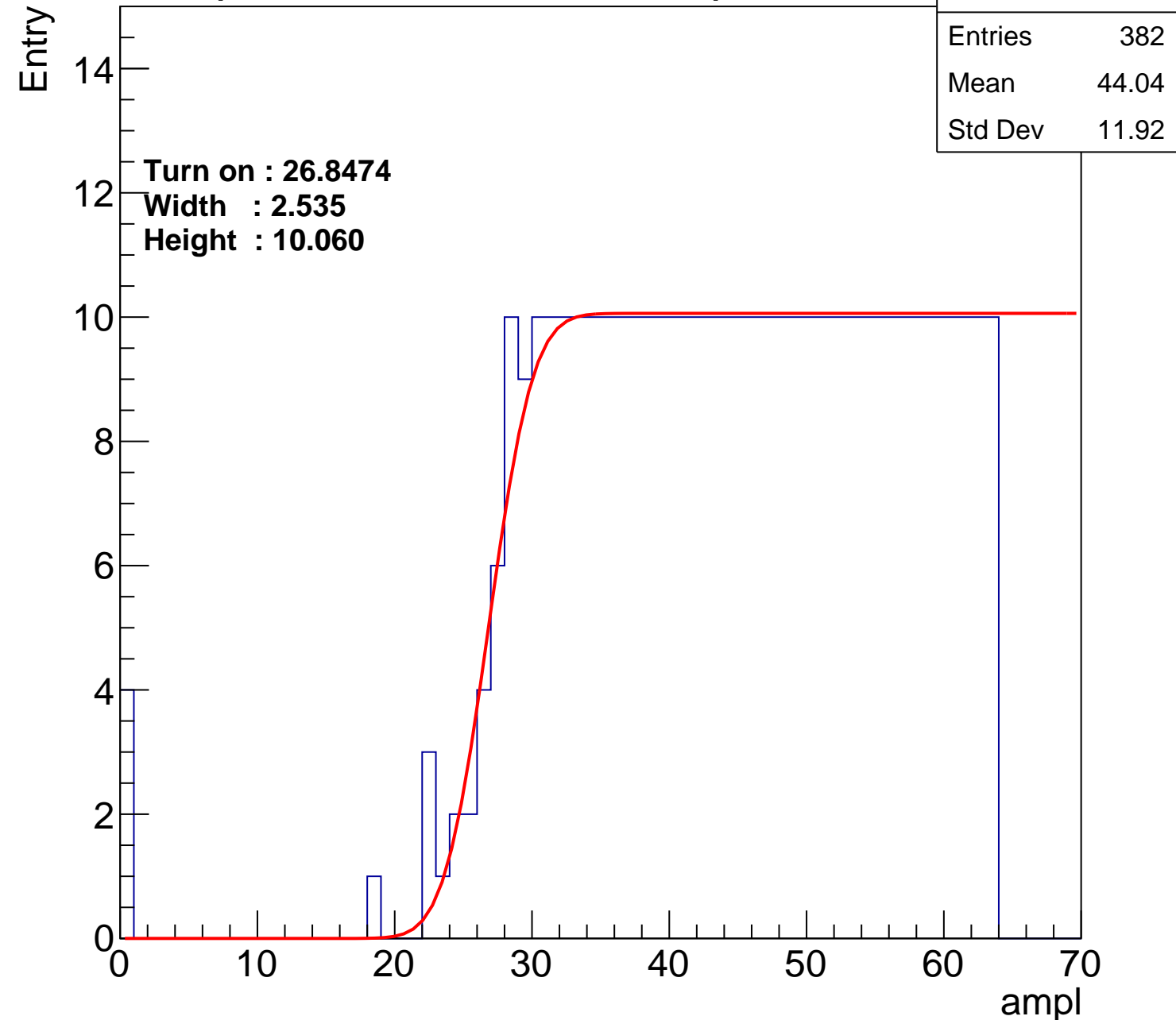
Width : 2.535

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch83

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.93
Std Dev	11.2

Turn on : 28.2348

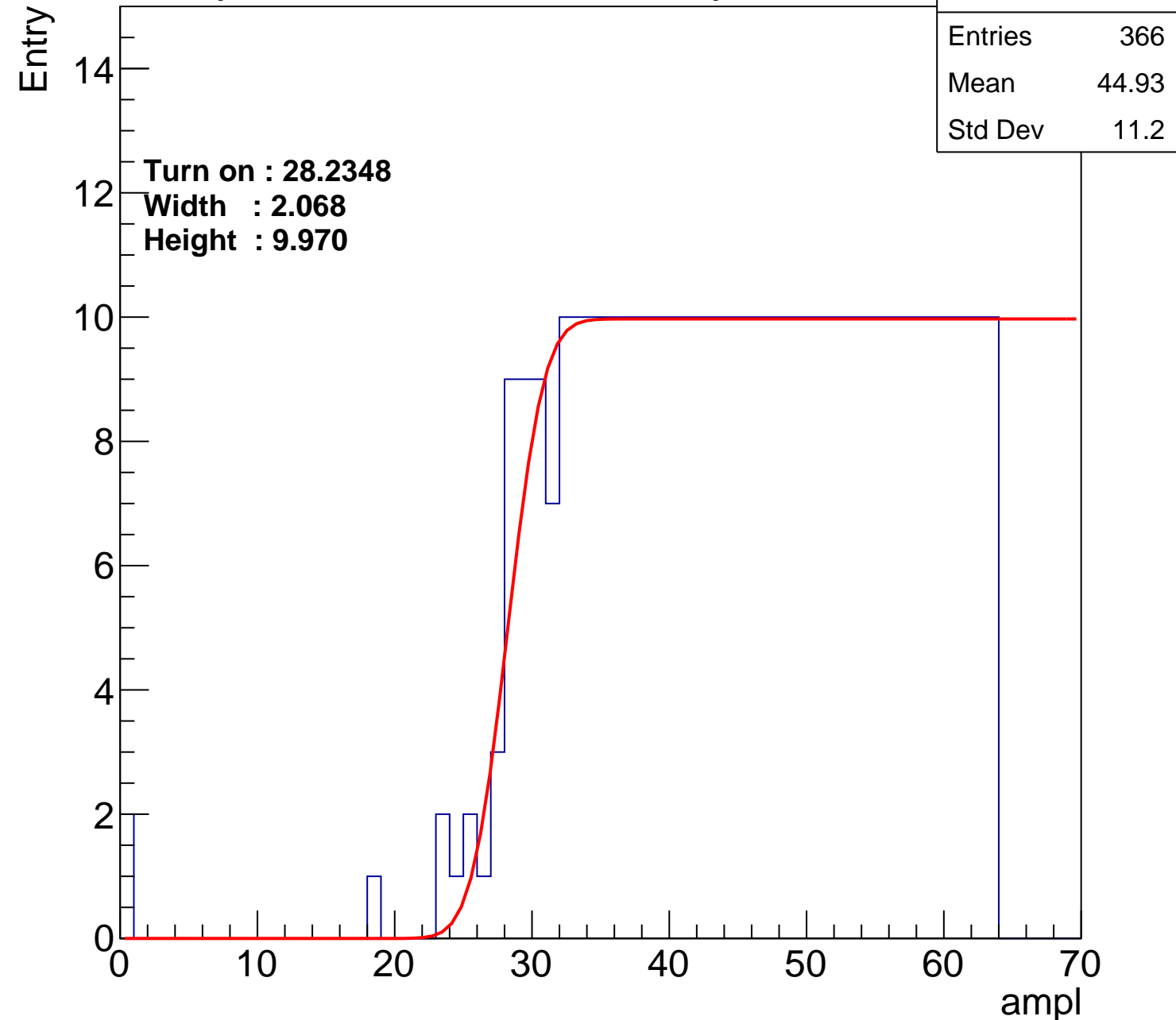
Width : 2.068

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch84

calib\_packv5\_042523\_0143.root, FC#12, port B1

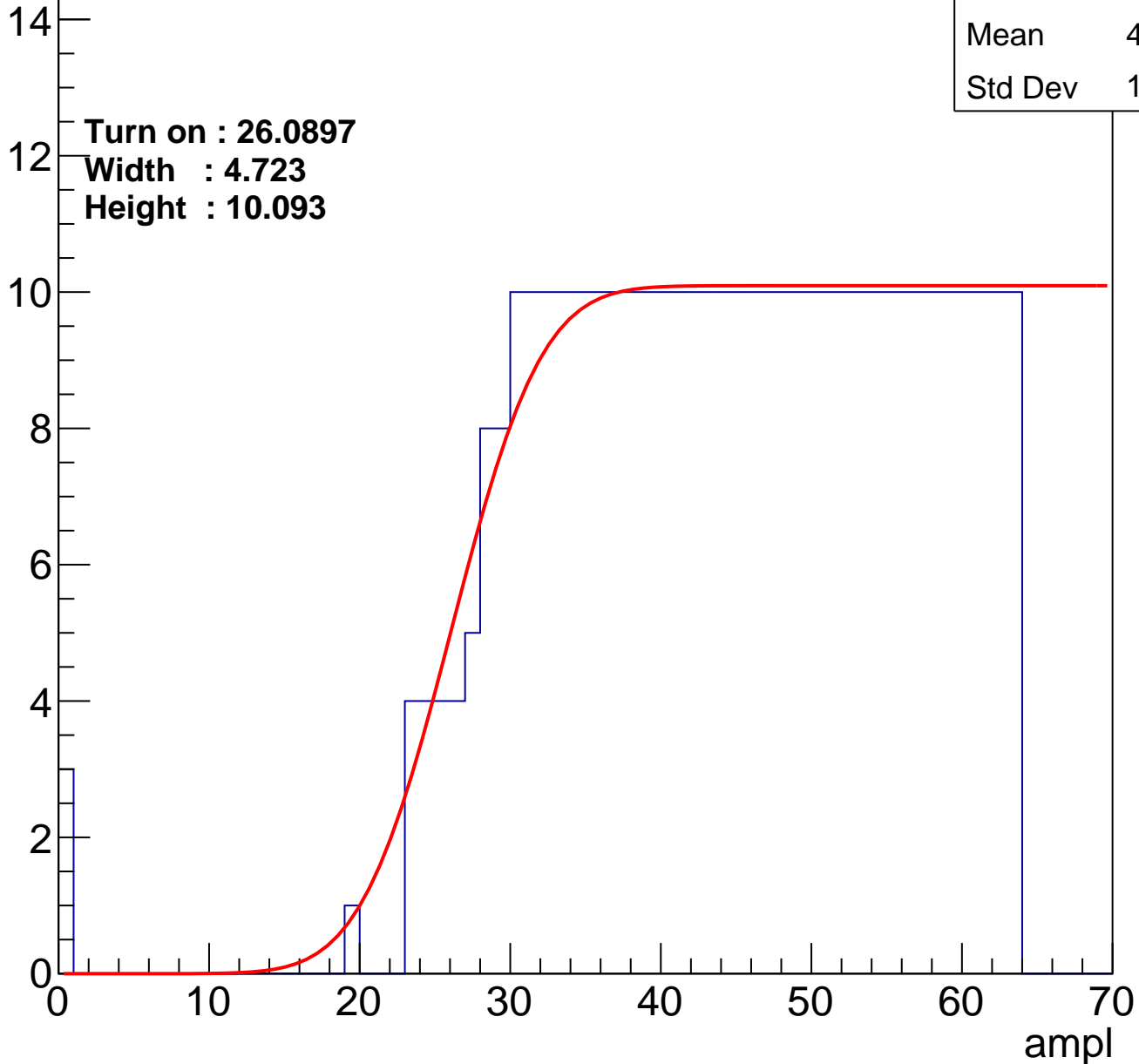
Entries	381
Mean	44.13
Std Dev	11.76

Turn on : 26.0897

Width : 4.723

Height : 10.093

Entry



# B0L102S, U21-ch85

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.58
Std Dev	11.74

Turn on : 24.7932

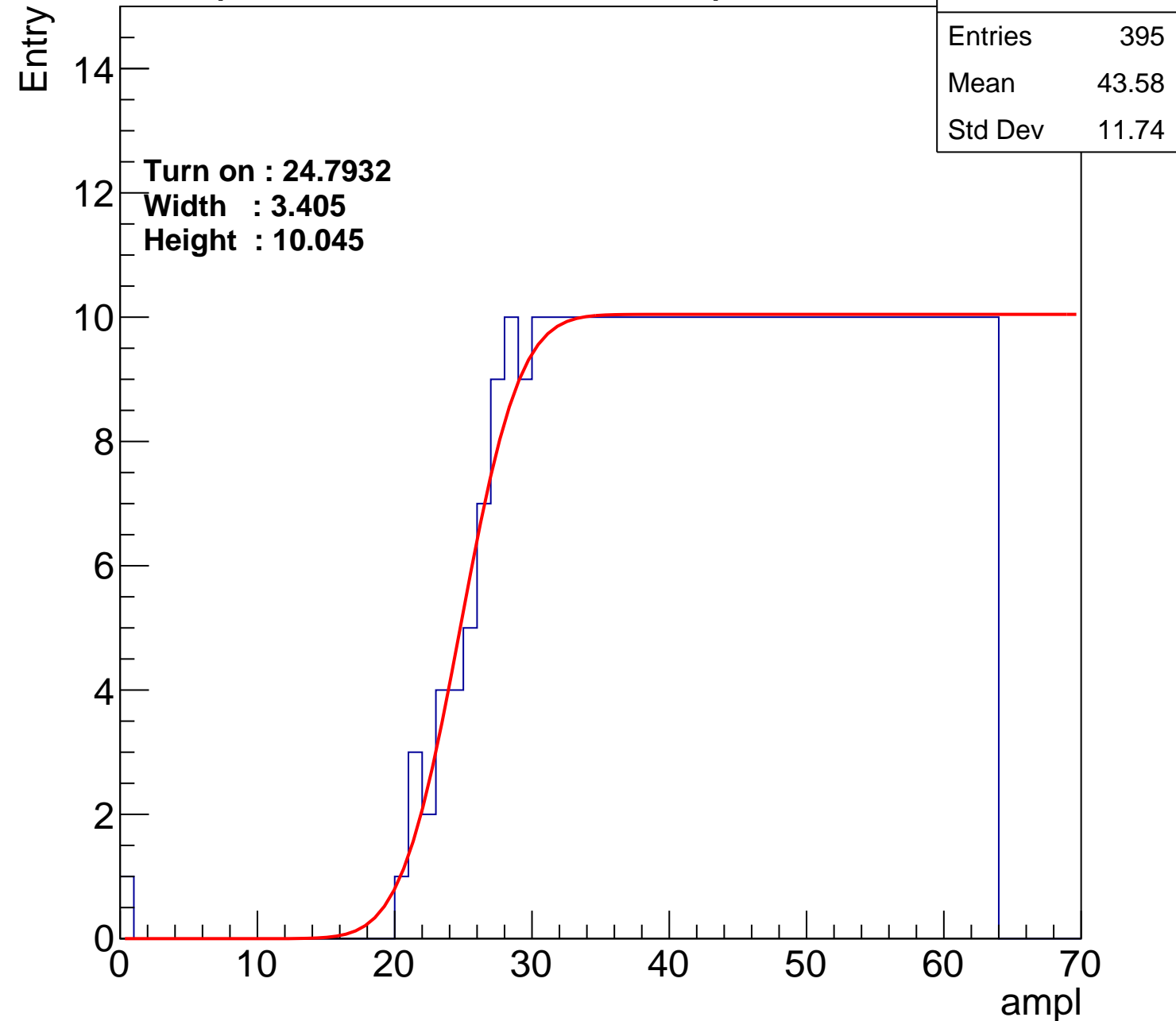
Width : 3.405

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch86

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.48
Std Dev	12.31

Turn on : 25.0439

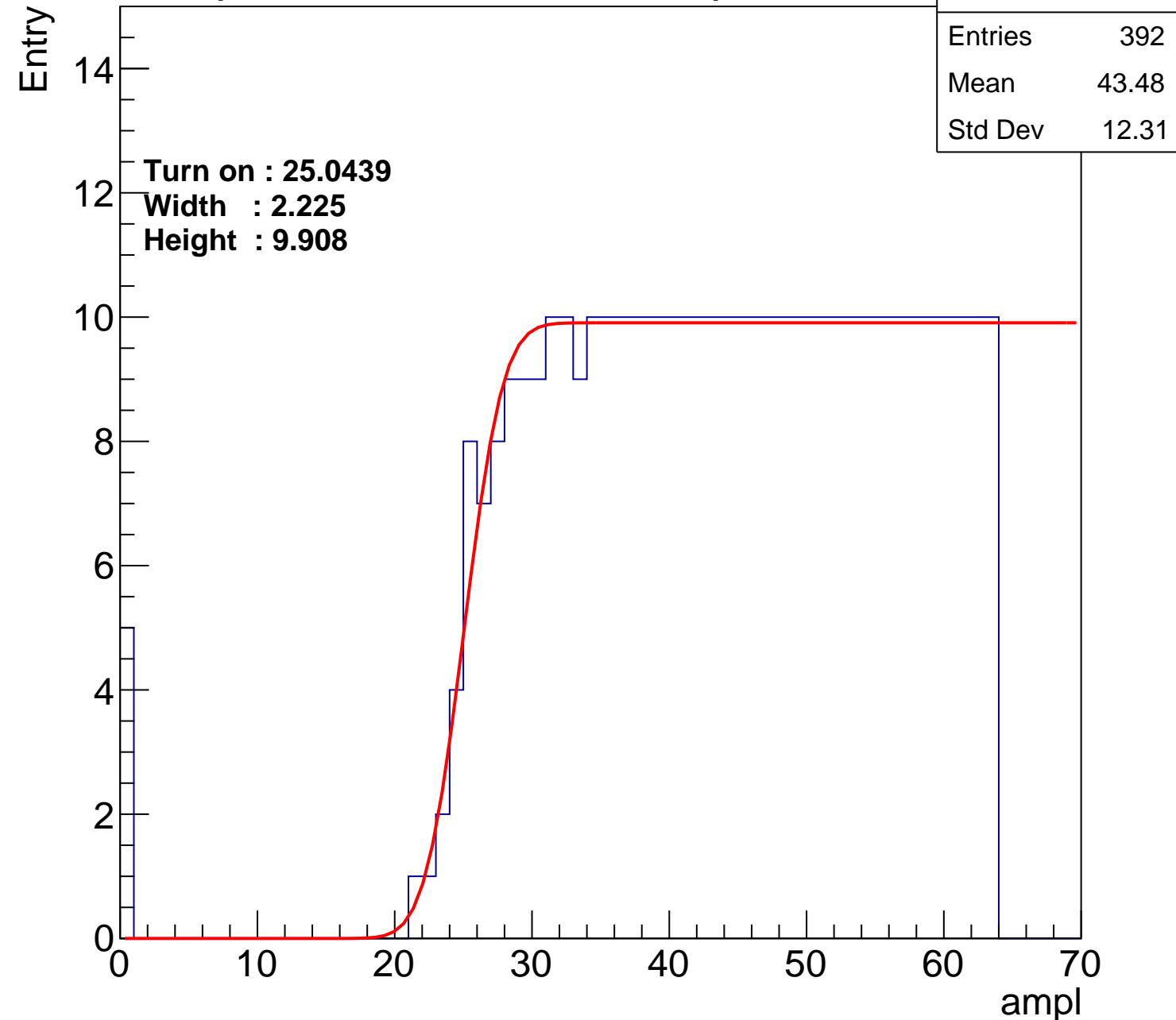
Width : 2.225

Height : 9.908

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch87

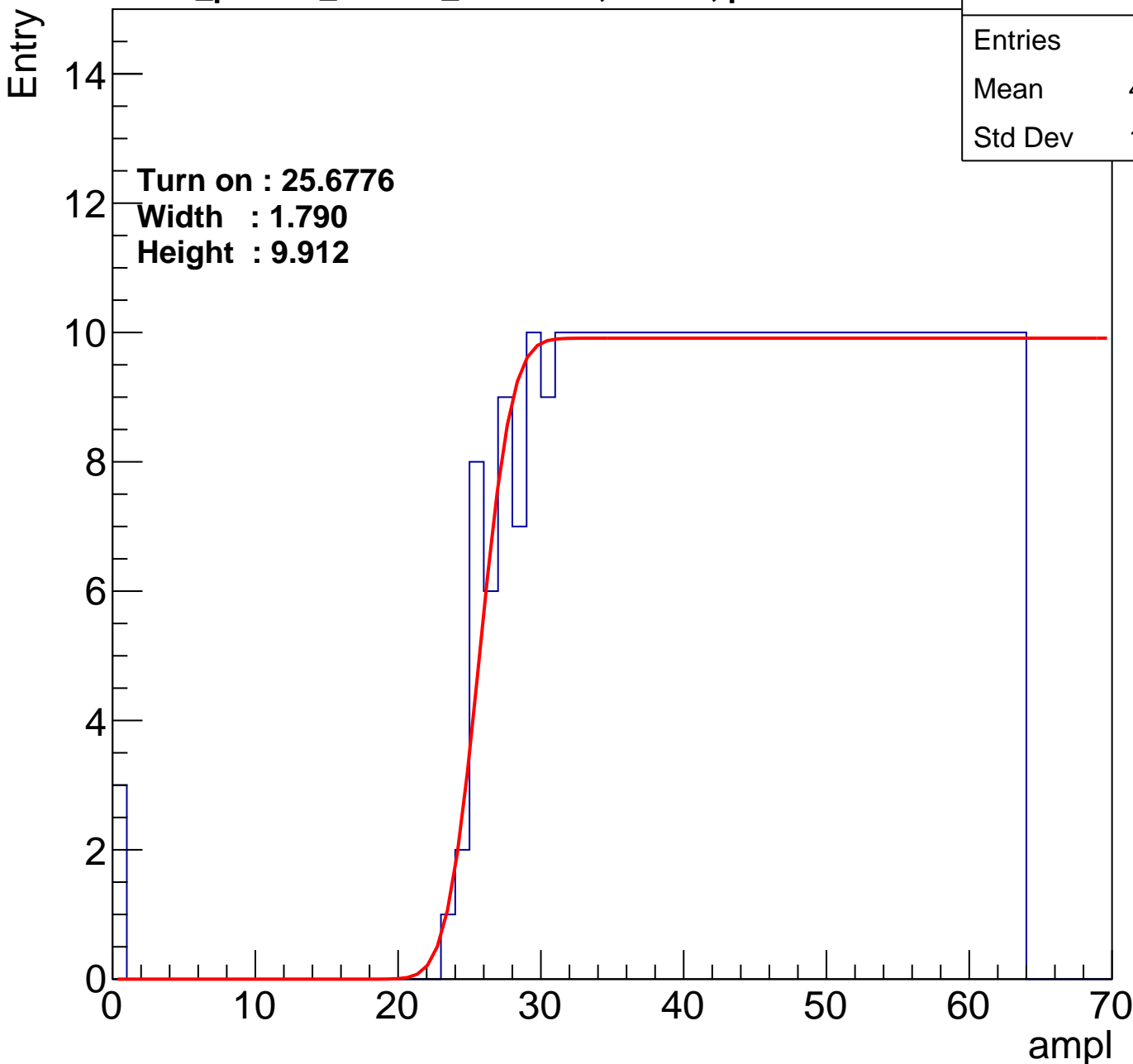
**calib\_packv5\_042523\_0143.root, FC#12, port B1**

Entries	385
Mean	43.99
Std Dev	11.75

**Turn on : 25.6776**

**Width : 1.790**

**Height : 9.912**



# B0L102S, U21-ch88

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	404
Mean	42.99
Std Dev	12.39

Turn on : 23.6671

Width : 0.918

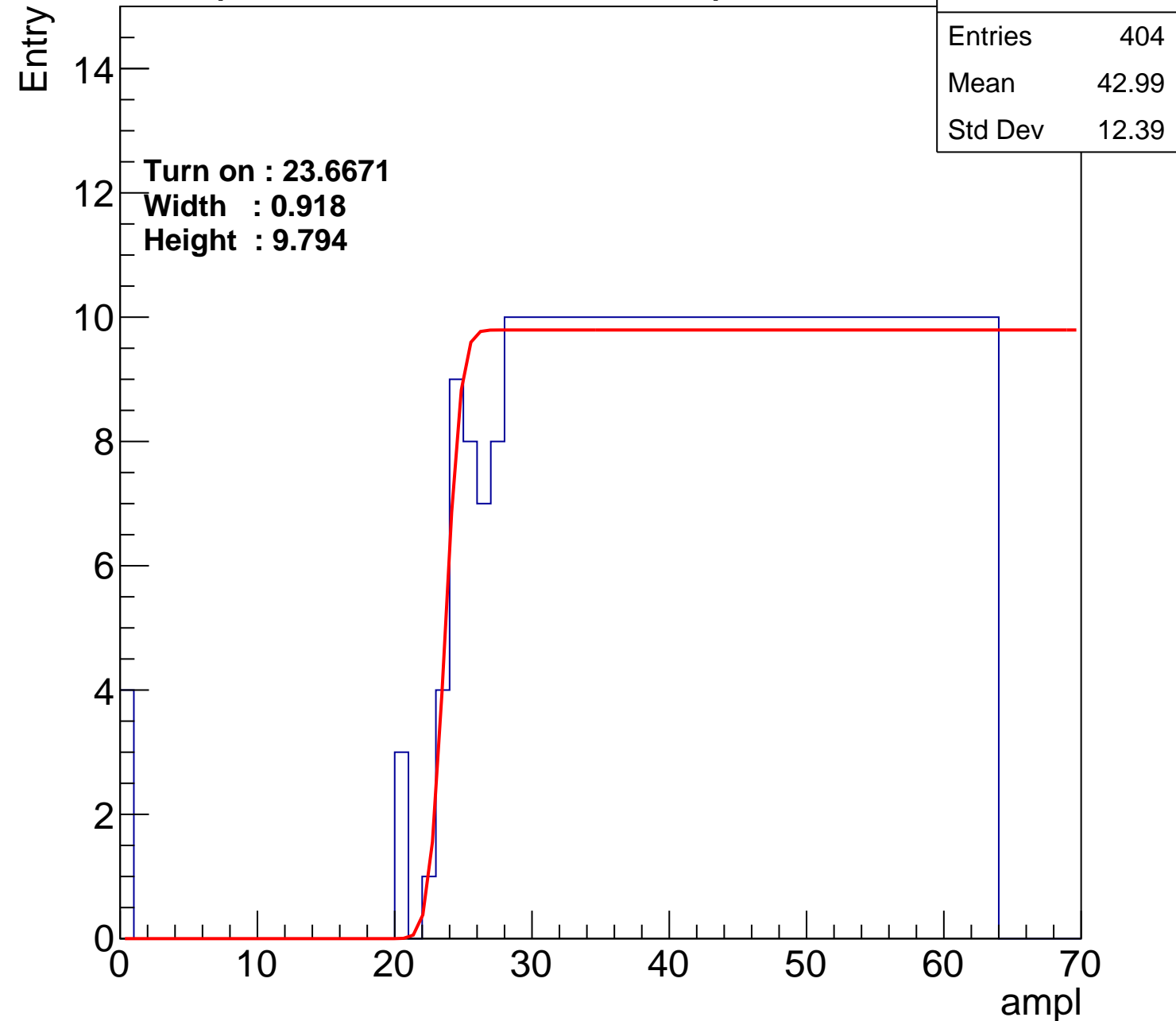
Height : 9.794

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U21-ch89

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.6
Std Dev	11.9

Turn on : 25.0003

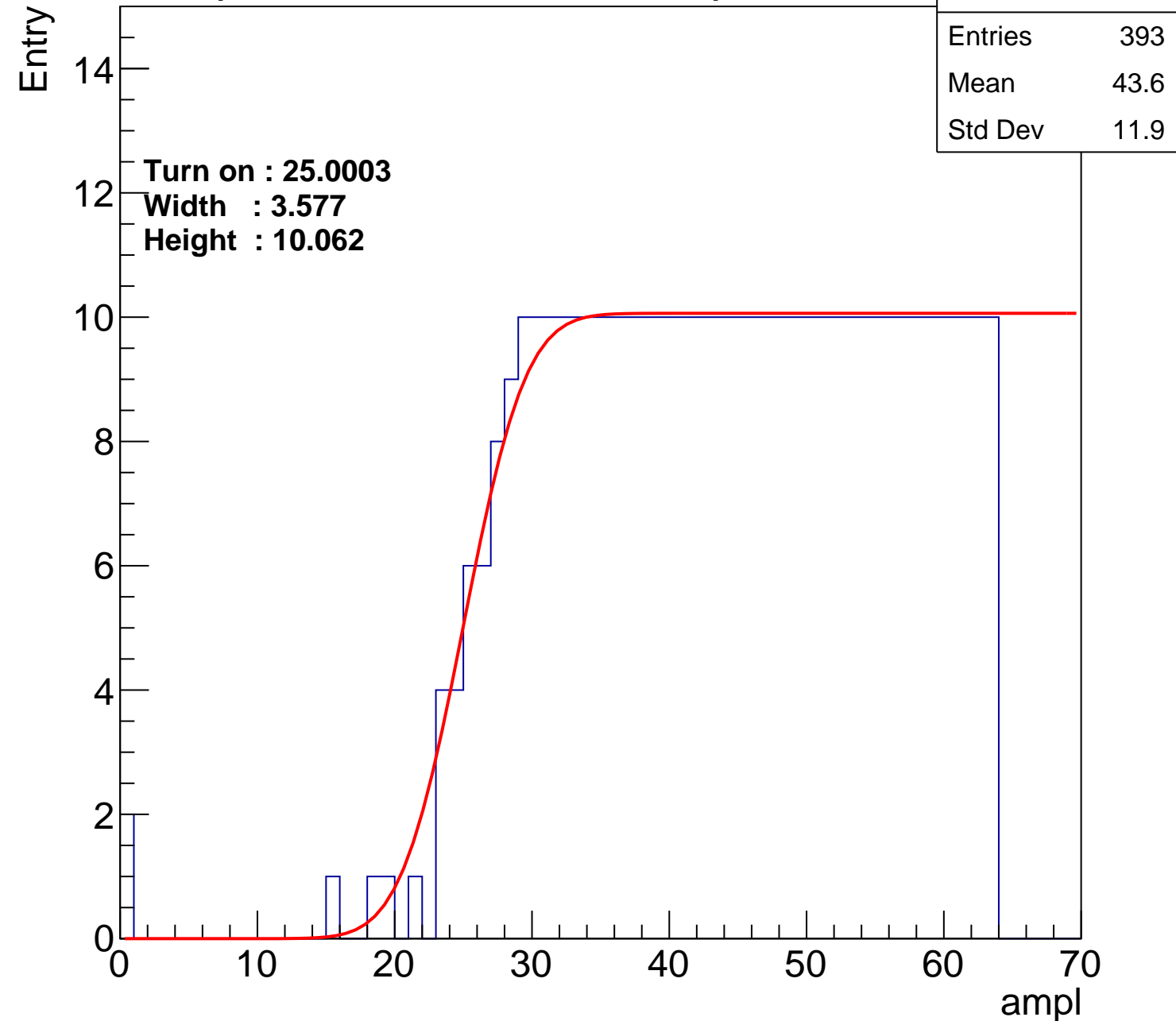
Width : 3.577

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch90

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.06
Std Dev	11.56

**Turn on : 26.6995**

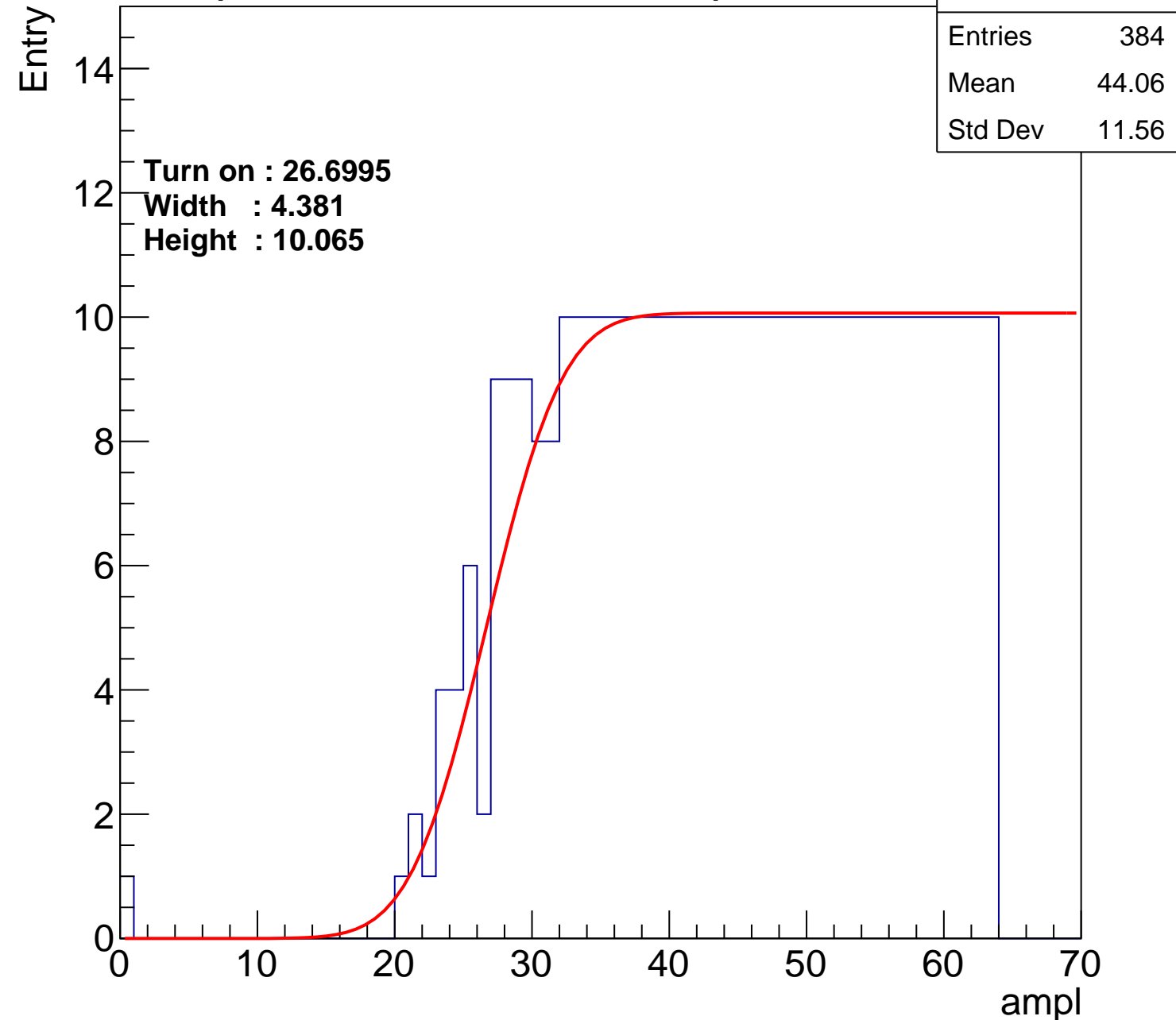
**Width : 4.381**

**Height : 10.065**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch91

calib\_packv5\_042523\_0143.root, FC#12, port B1

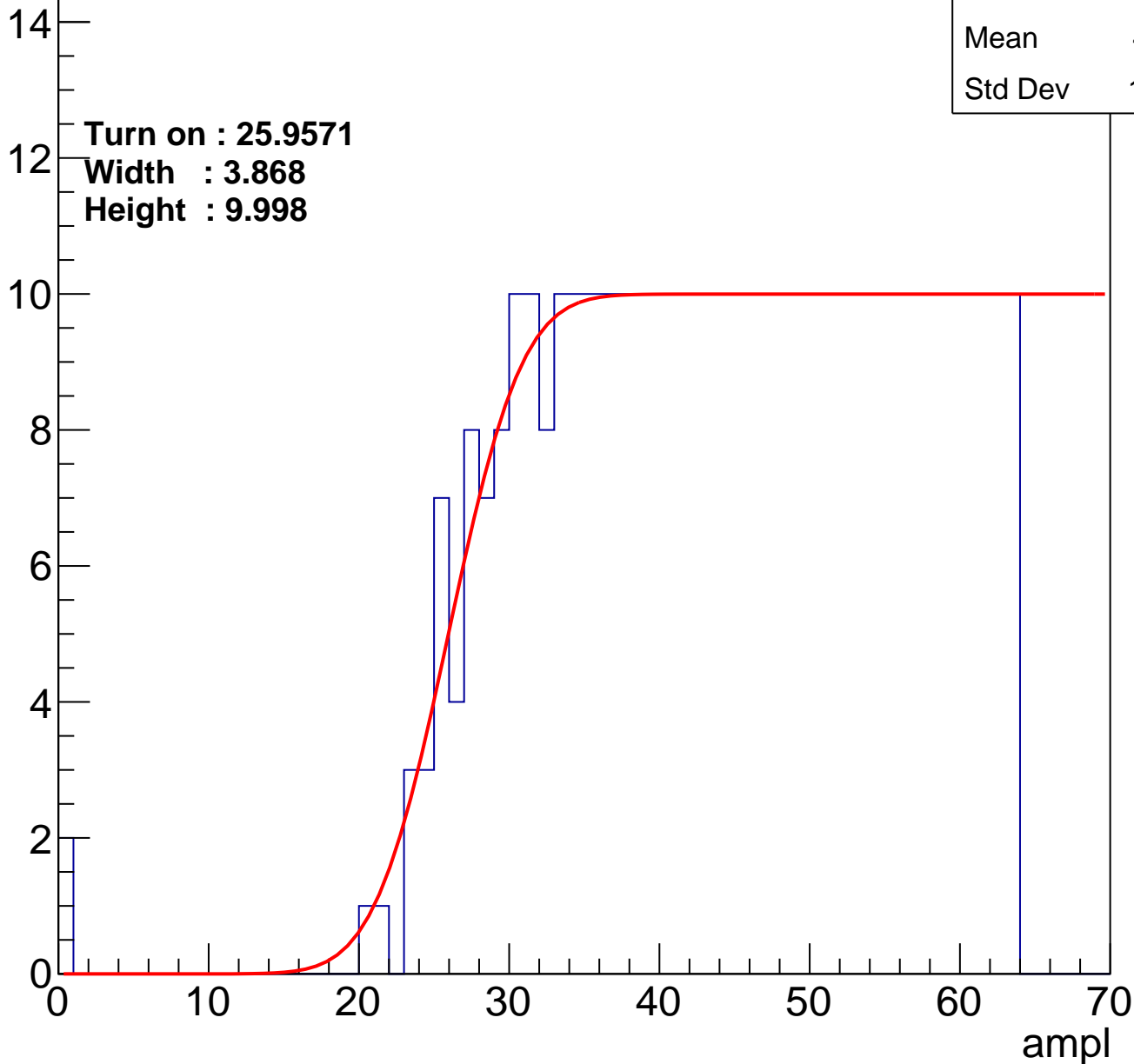
Entry

Entries	382
Mean	44.11
Std Dev	11.64

Turn on : 25.9571

Width : 3.868

Height : 9.998



# B0L102S, U21-ch92

calib\_packv5\_042523\_0143.root, FC#12, port B1

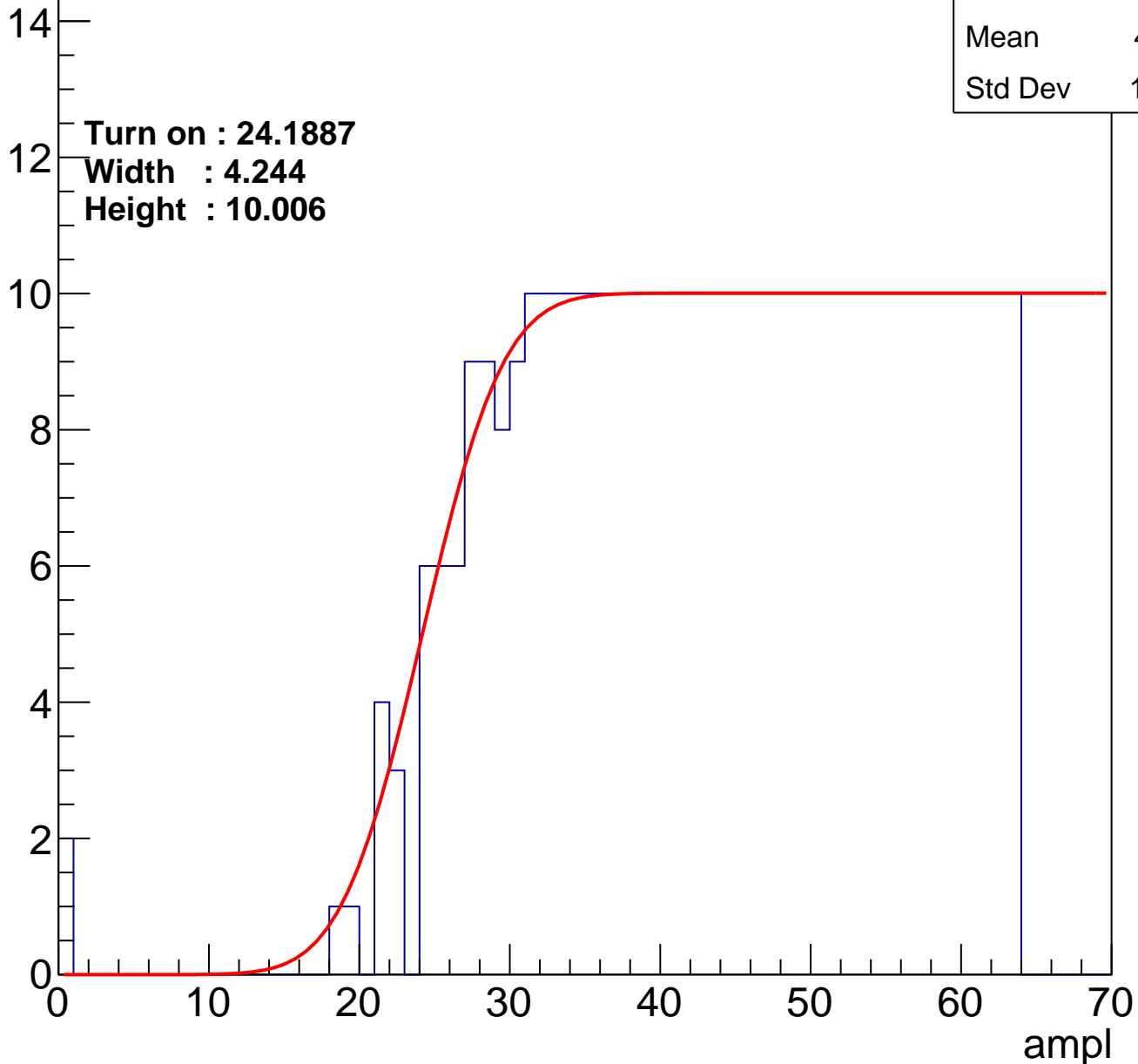
Entries	394
Mean	43.51
Std Dev	11.98

**Turn on : 24.1887**

**Width : 4.244**

**Height : 10.006**

Entry



# B0L102S, U21-ch93

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.47
Std Dev	11.95

Turn on : 25.8549

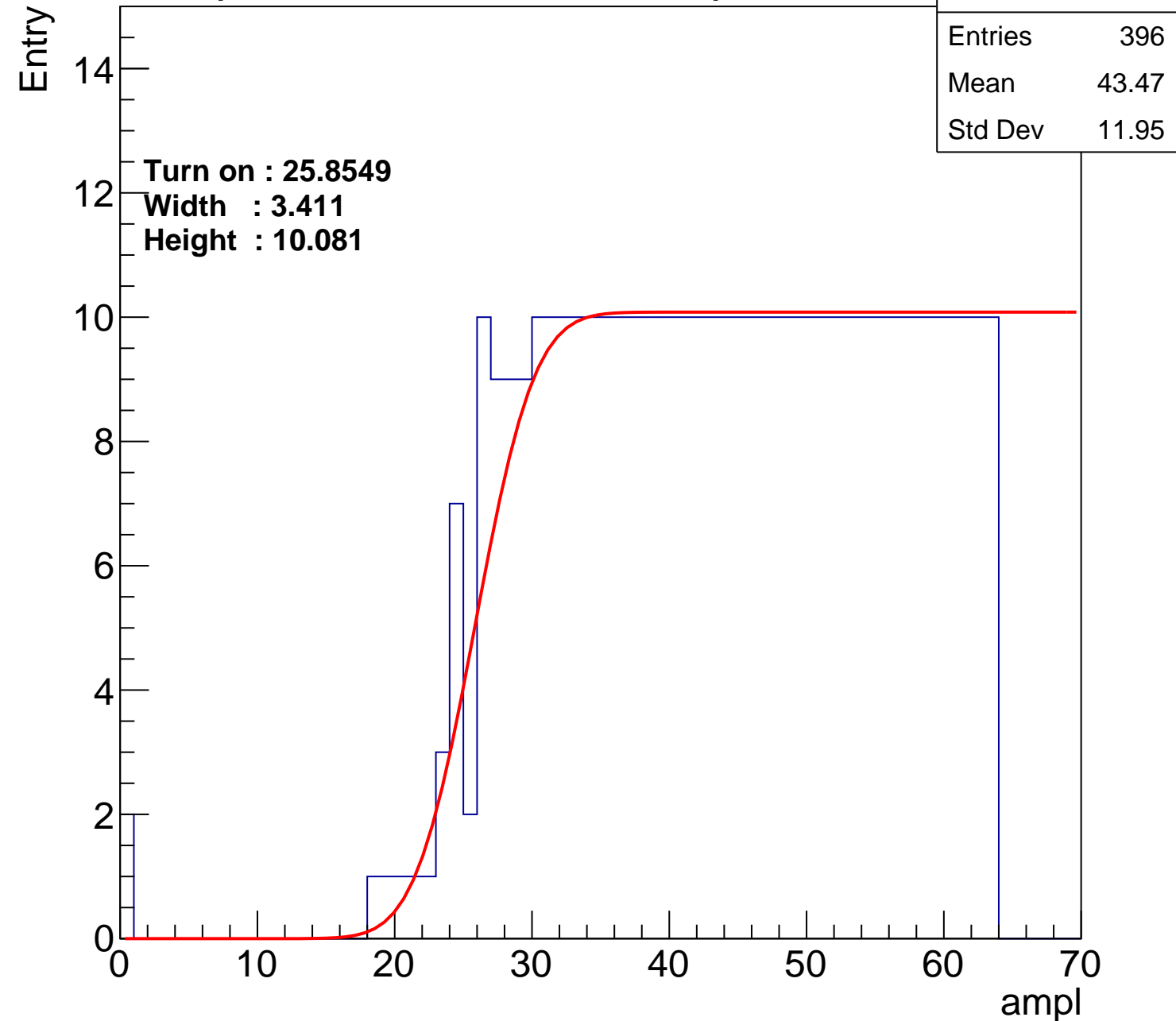
Width : 3.411

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch94

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	386
Mean	43.93
Std Dev	11.8

Turn on : 25.8708

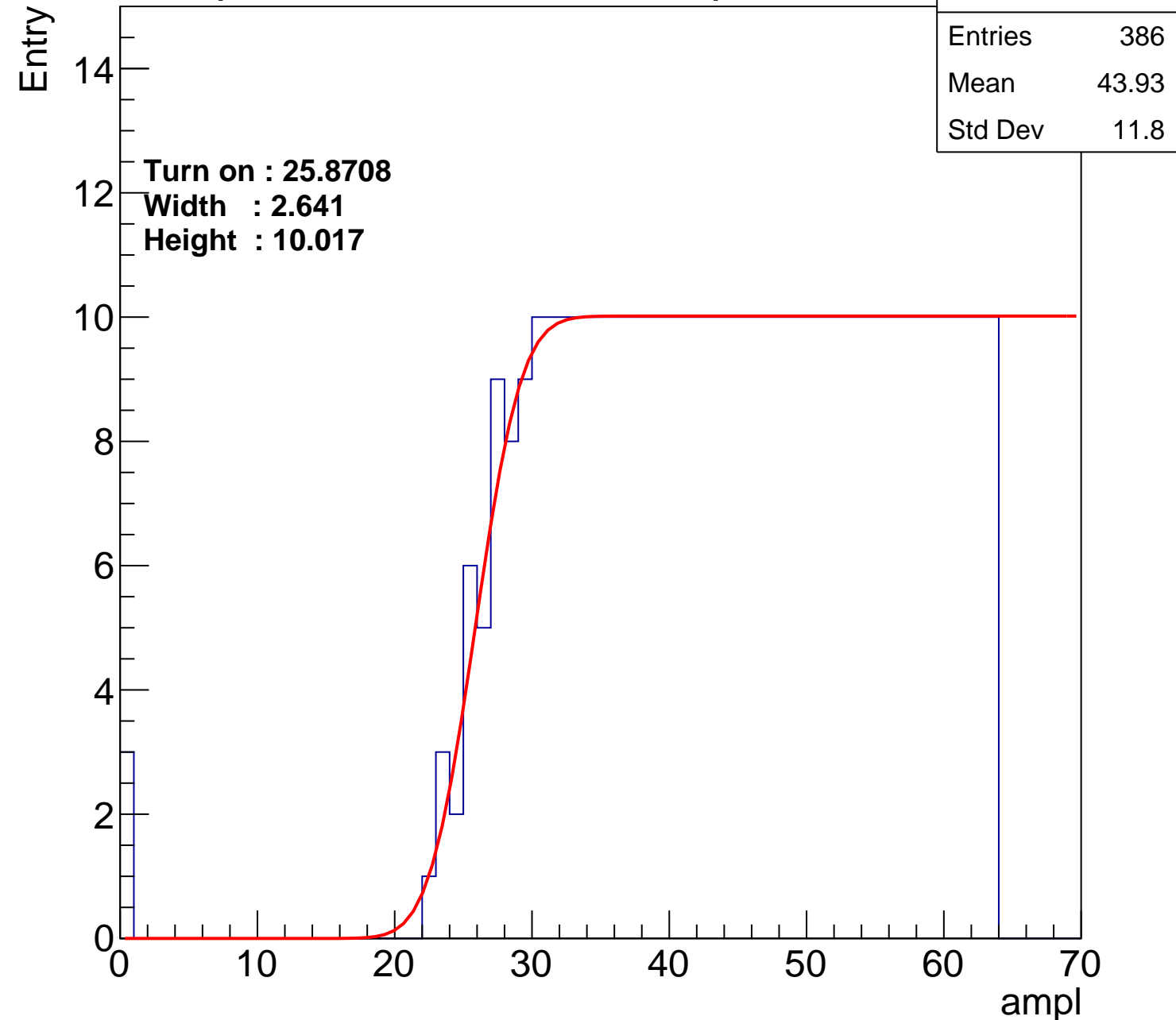
Width : 2.641

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch95

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.14
Std Dev	11.54

Turn on : 25.6816

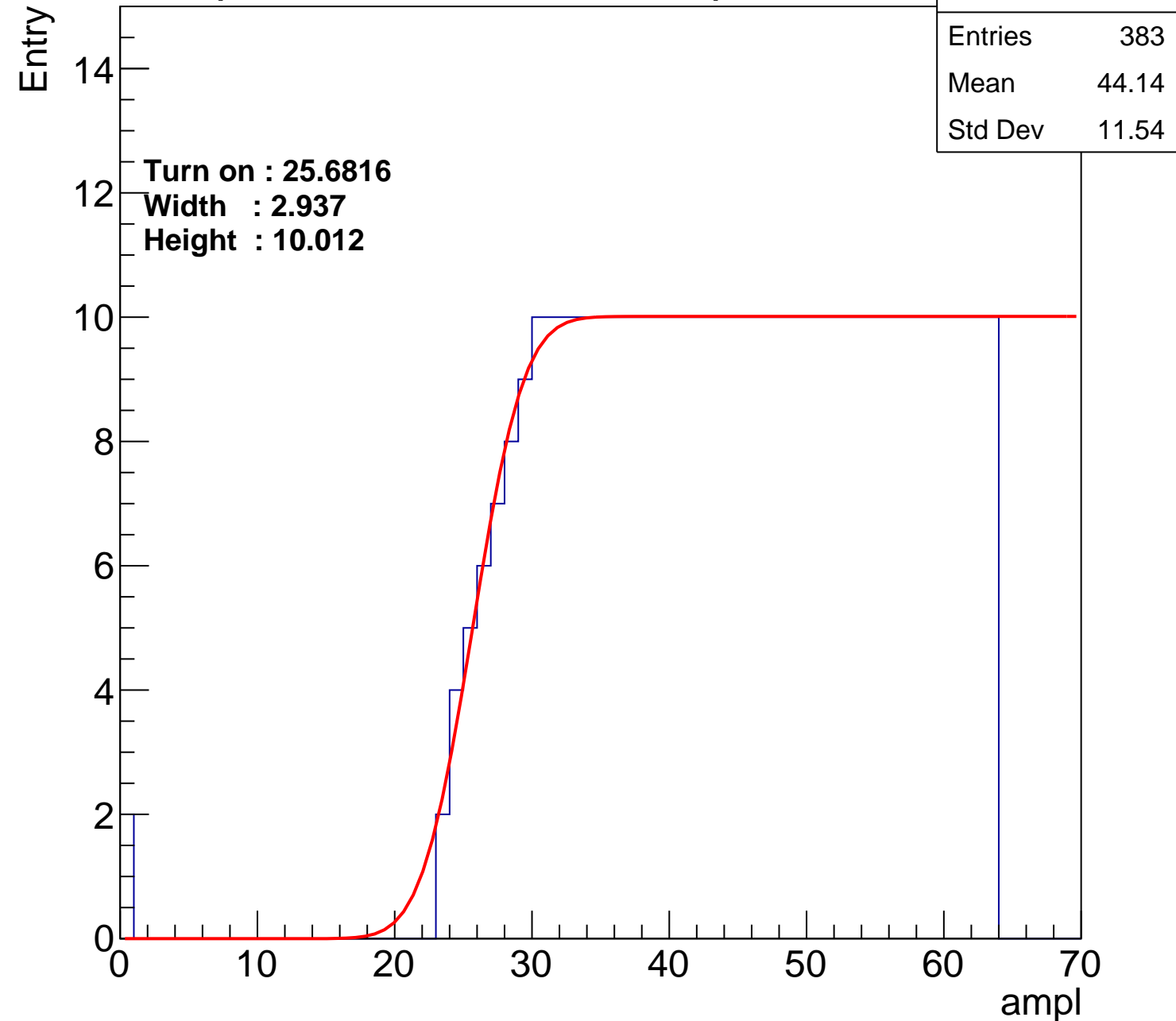
Width : 2.937

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch96

calib\_packv5\_042523\_0143.root, FC#12, port B1

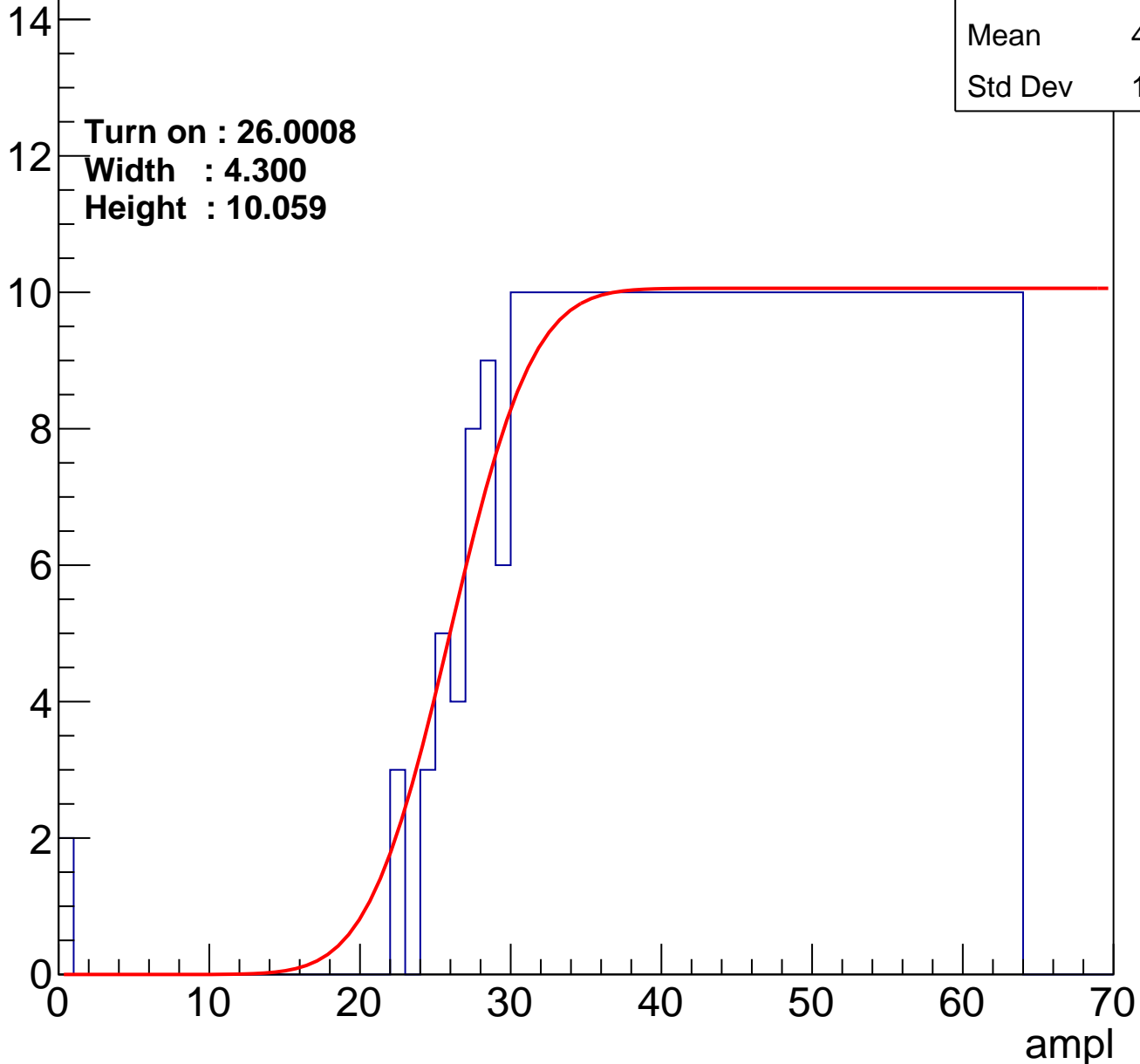
Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.0008

Width : 4.300

Height : 10.059

Entry



# B0L102S, U21-ch97

calib\_packv5\_042523\_0143.root, FC#12, port B1

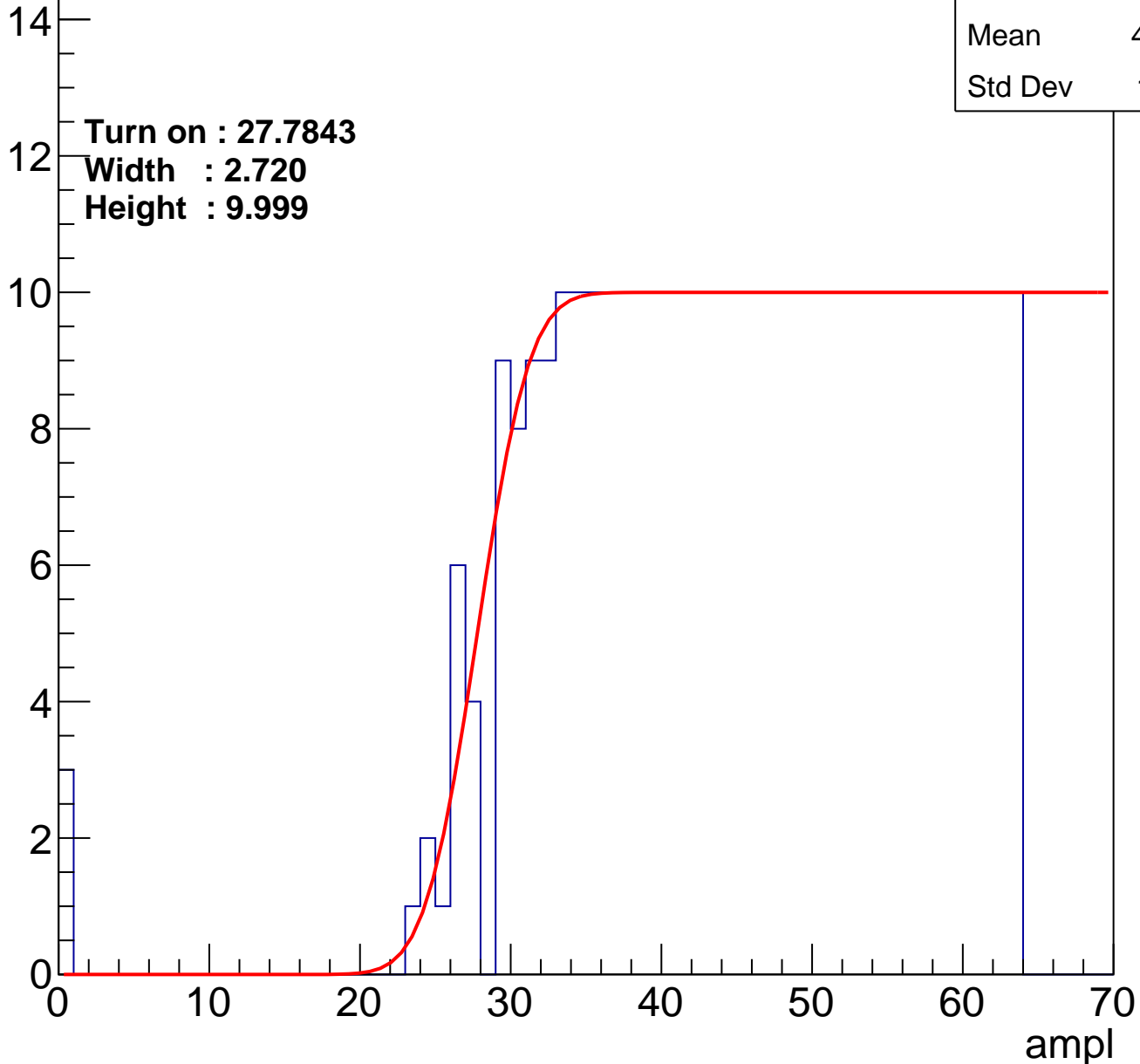
Entries	362
Mean	45.05
Std Dev	11.31

**Turn on : 27.7843**

**Width : 2.720**

**Height : 9.999**

Entry



# B0L102S, U21-ch98

calib\_packv5\_042523\_0143.root, FC#12, port B1

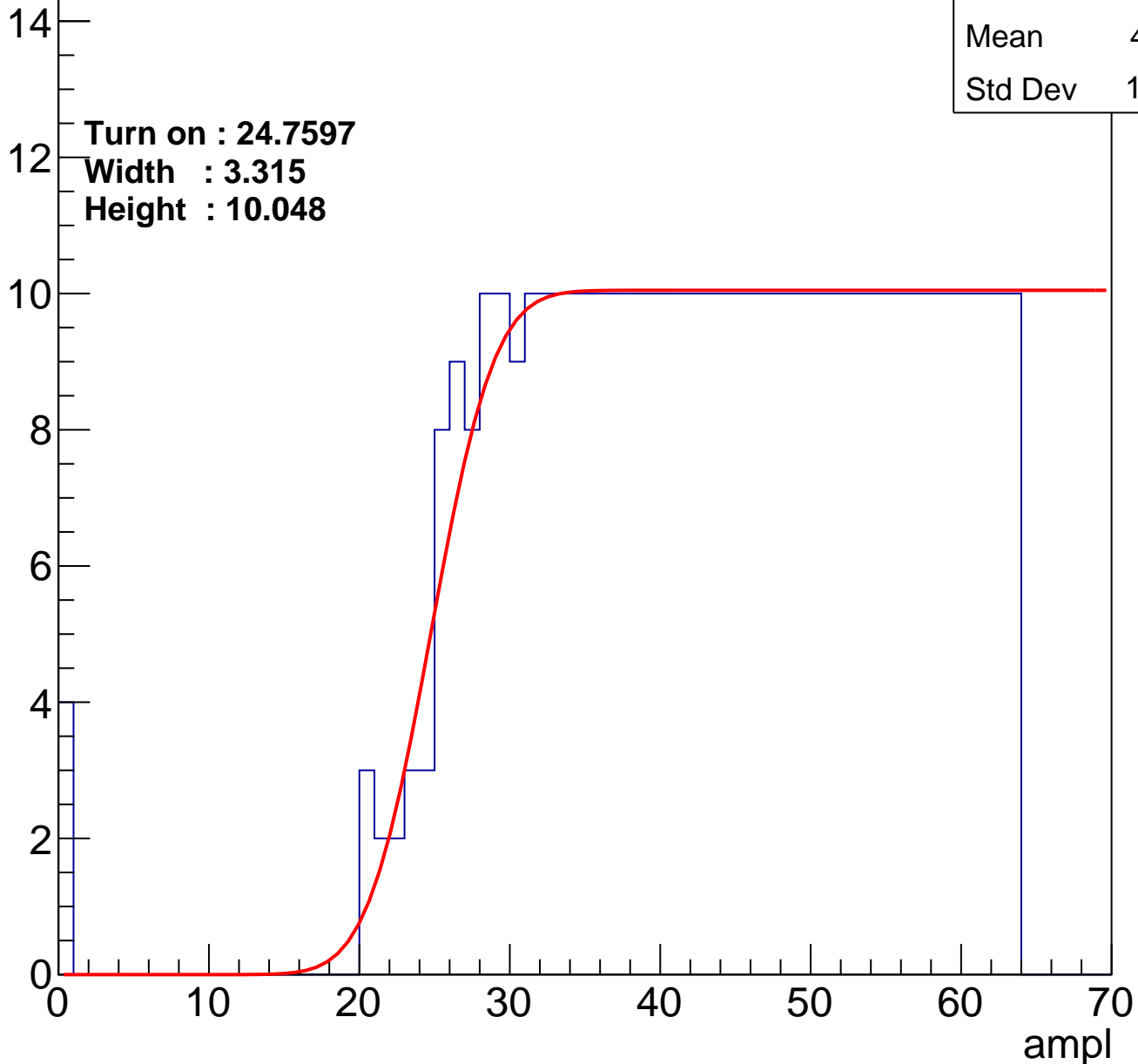
Entries	401
Mean	43.11
Std Dev	12.36

**Turn on : 24.7597**

**Width : 3.315**

**Height : 10.048**

Entry



# B0L102S, U21-ch99

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.5024

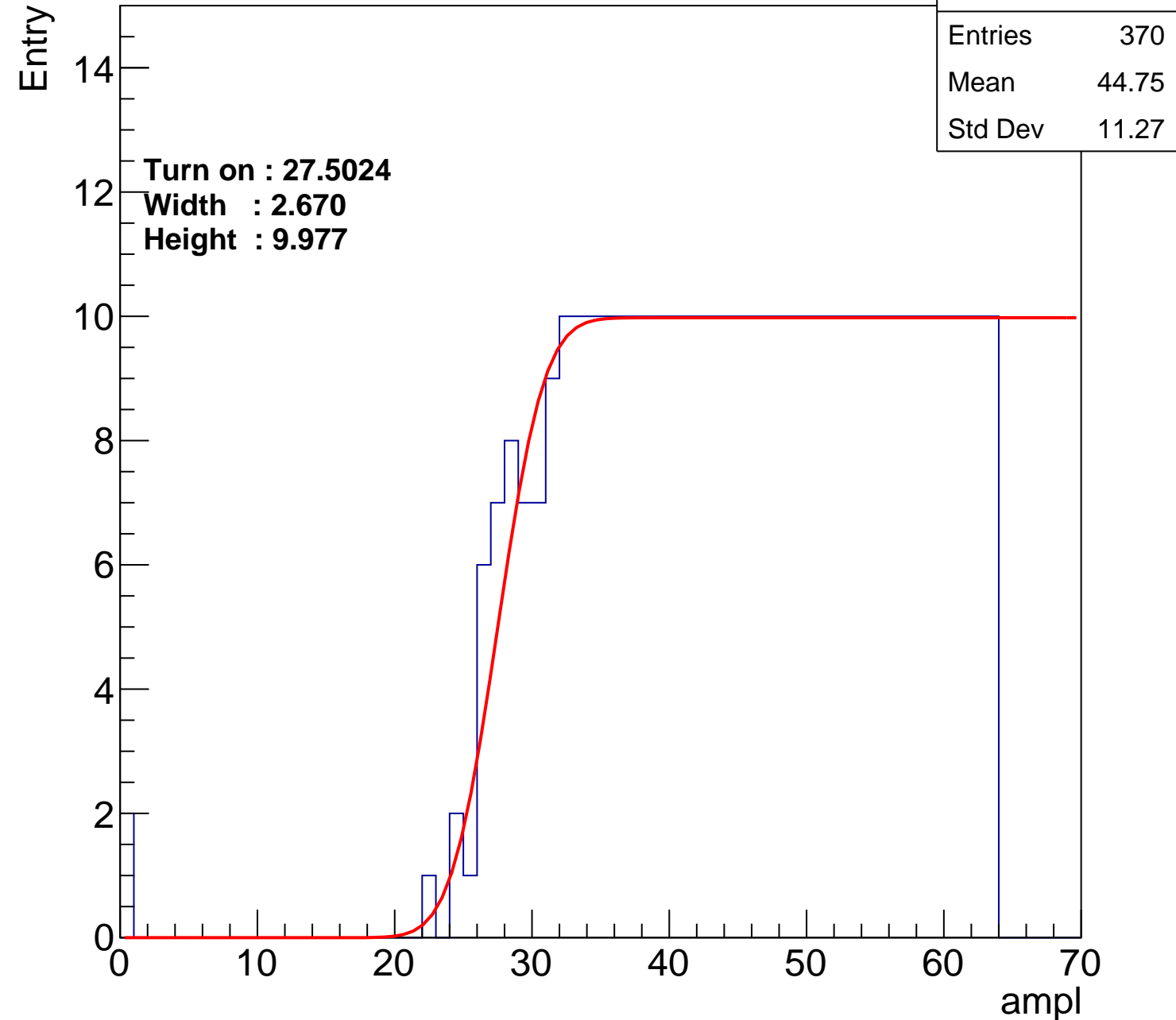
Width : 2.670

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch100

calib\_packv5\_042523\_0143.root, FC#12, port B1

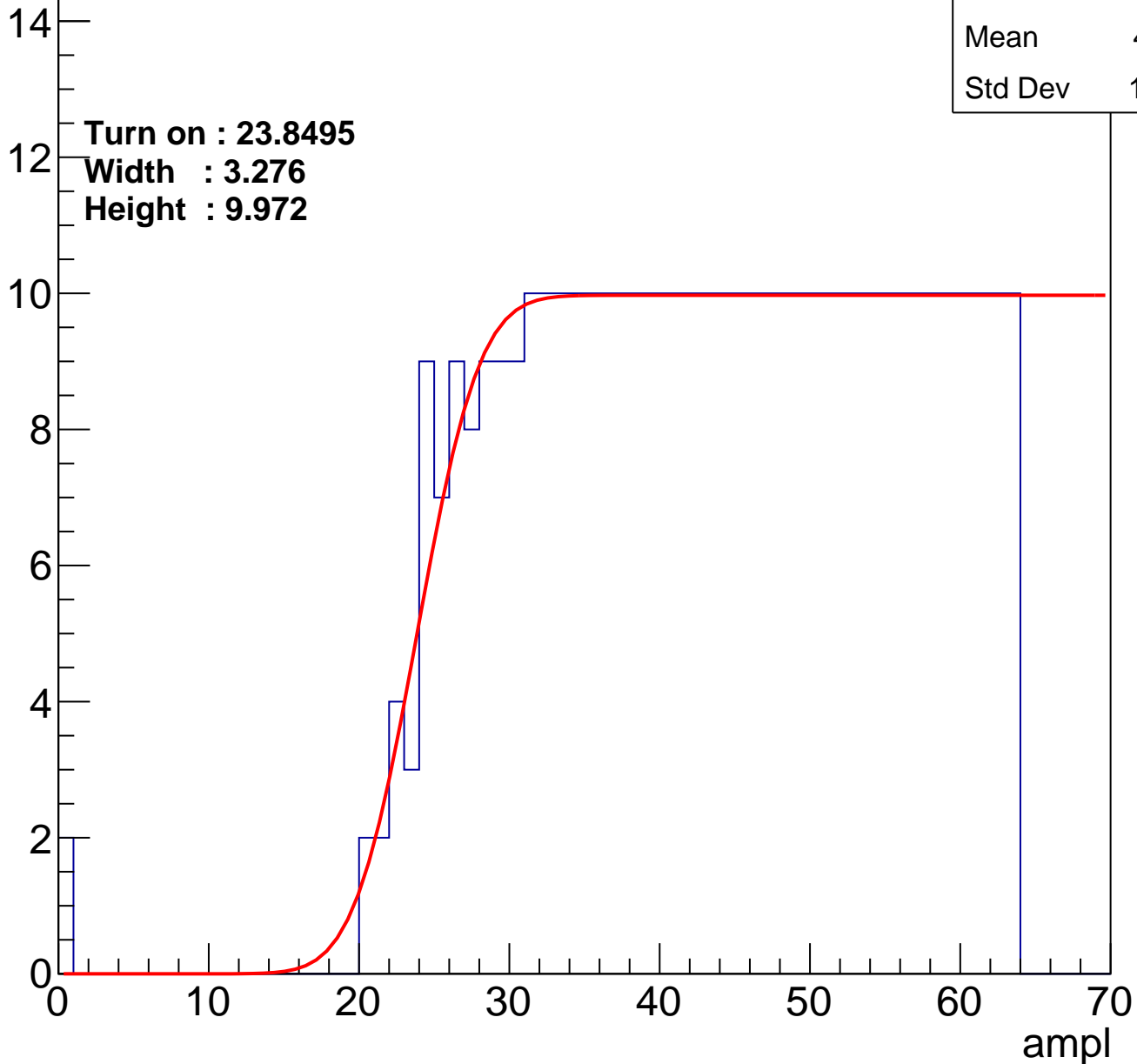
Entries	403
Mean	43.11
Std Dev	12.14

Turn on : 23.8495

Width : 3.276

Height : 9.972

Entry



# B0L102S, U21-ch101

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	45.02
Std Dev	11.25

**Turn on : 27.9299**

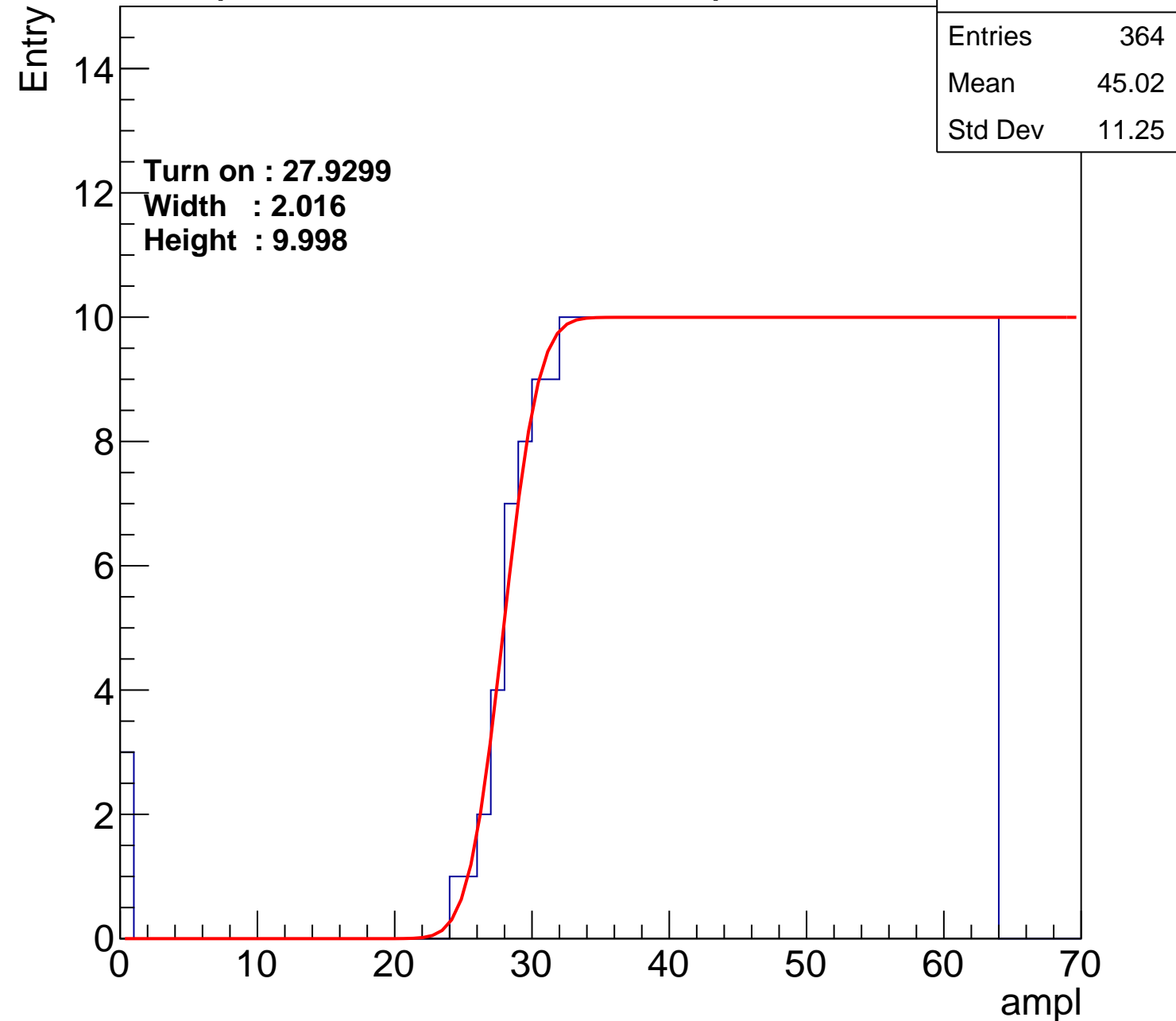
**Width : 2.016**

**Height : 9.998**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch102

calib\_packv5\_042523\_0143.root, FC#12, port B1

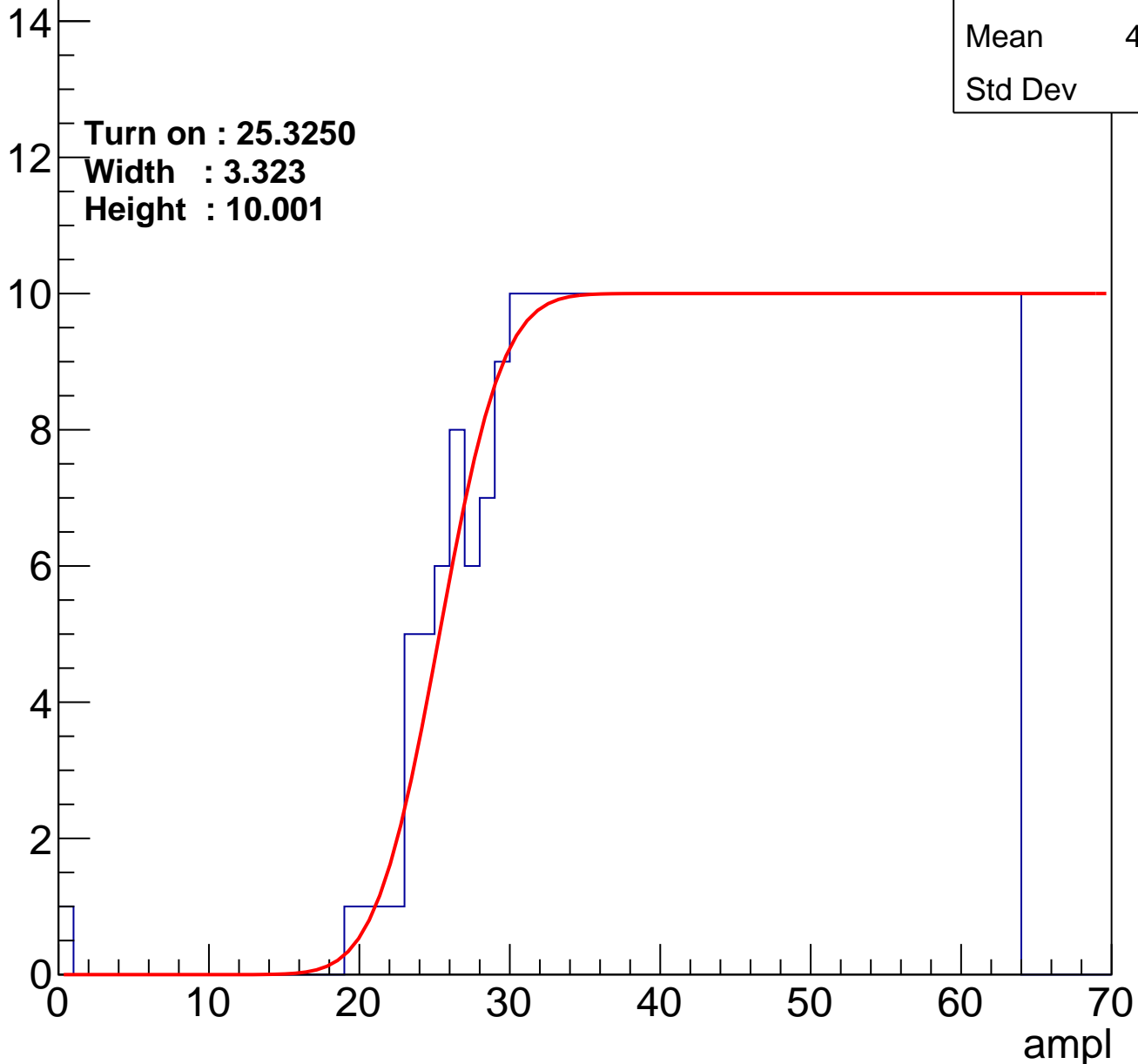
Entries	391
Mean	43.74
Std Dev	11.7

Turn on : 25.3250

Width : 3.323

Height : 10.001

Entry





# B0L102S, U21-ch103

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.8395

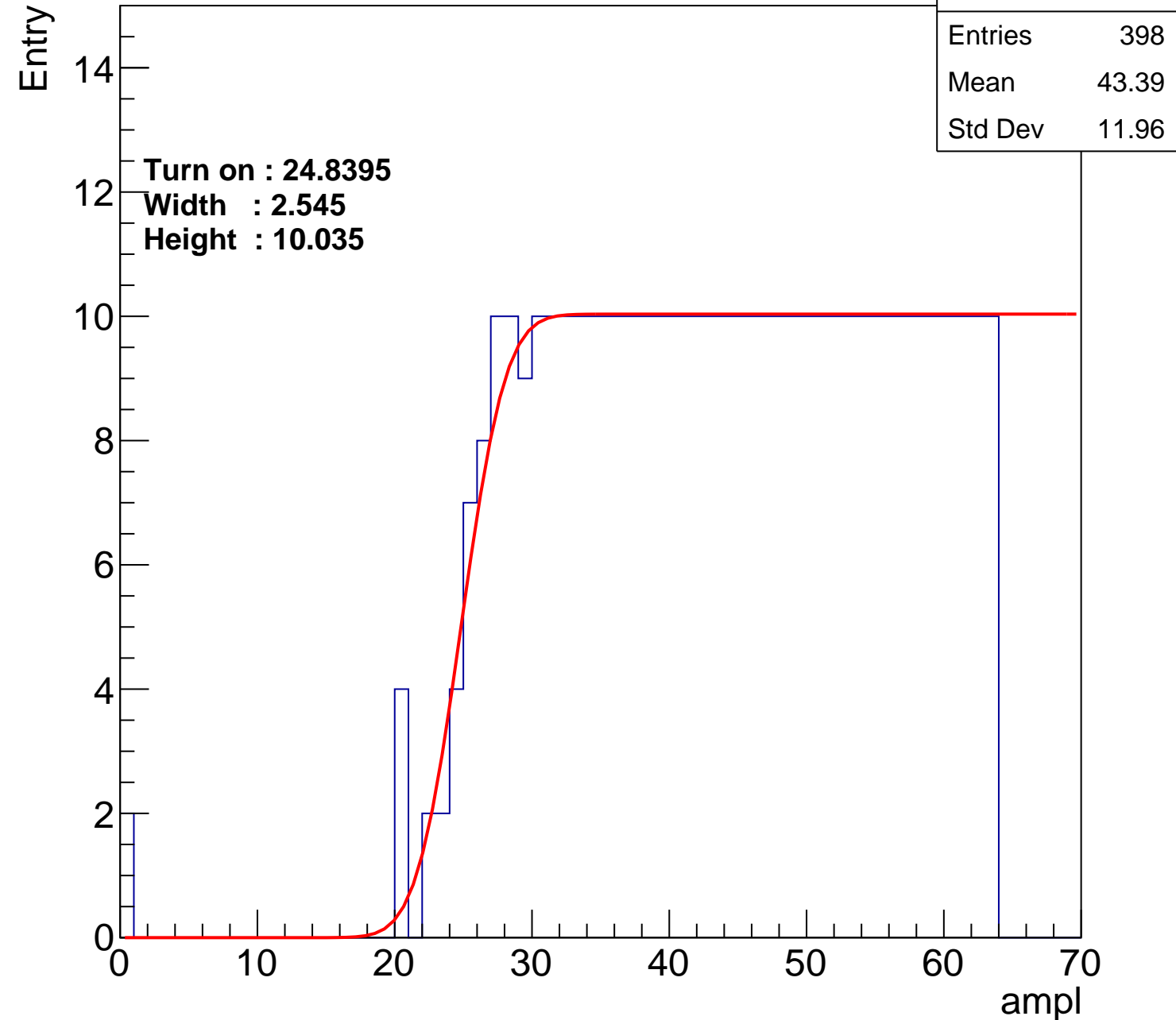
Width : 2.545

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch104

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 25.9066

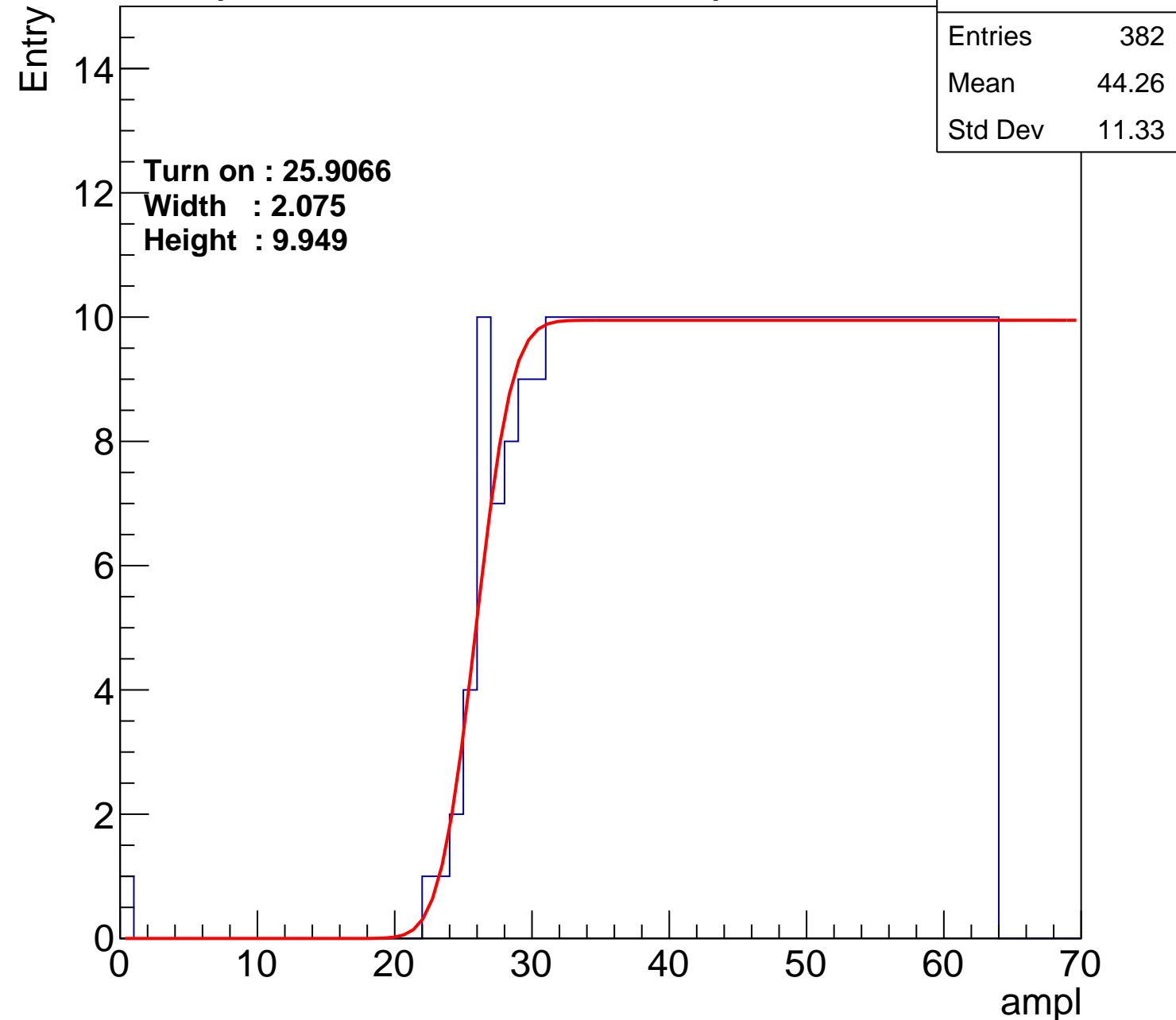
Width : 2.075

Height : 9.949

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch105

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.21
Std Dev	11.5

Turn on : 26.1009

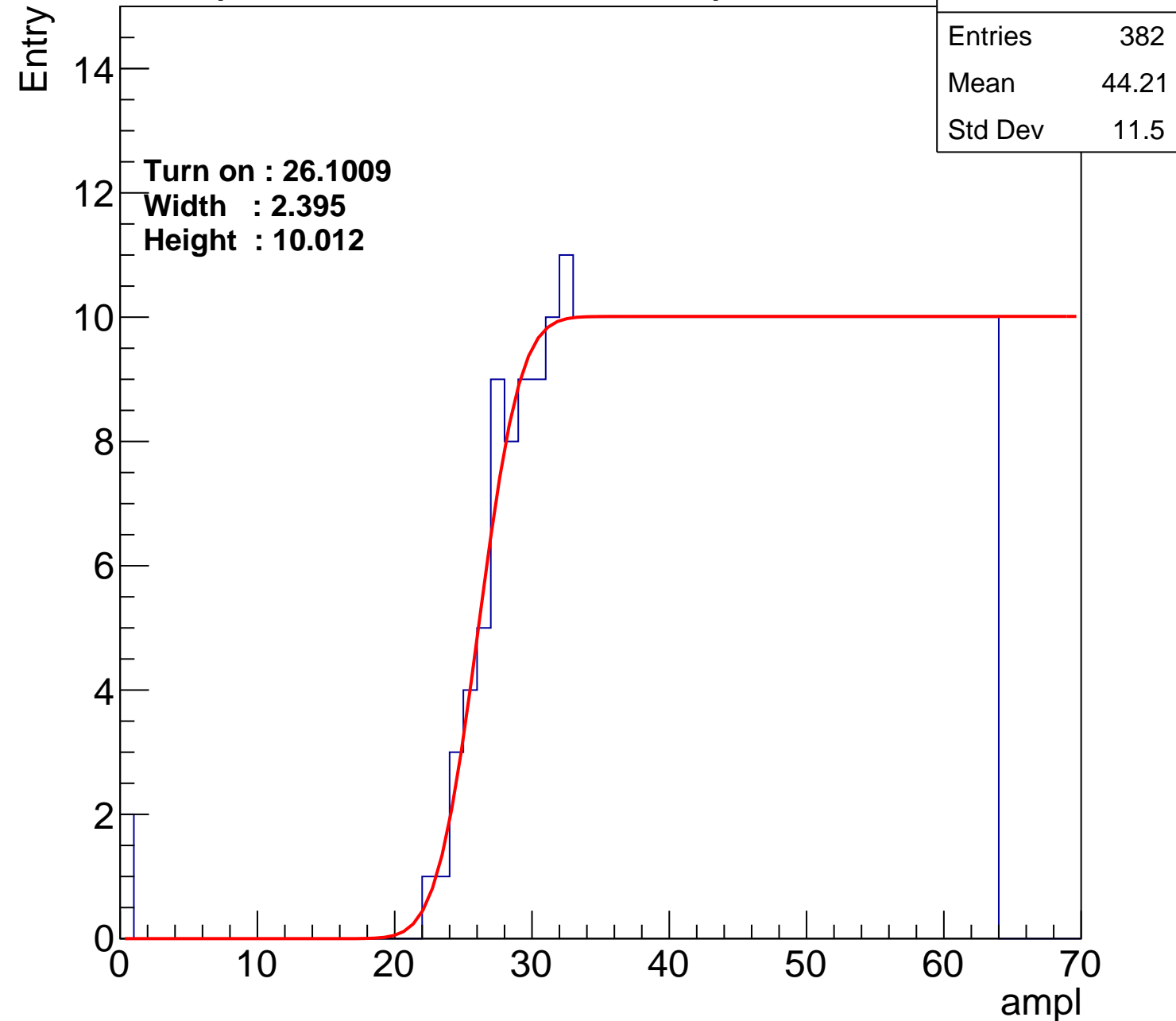
Width : 2.395

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch106

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.11
Std Dev	11.9

**Turn on : 26.4763**

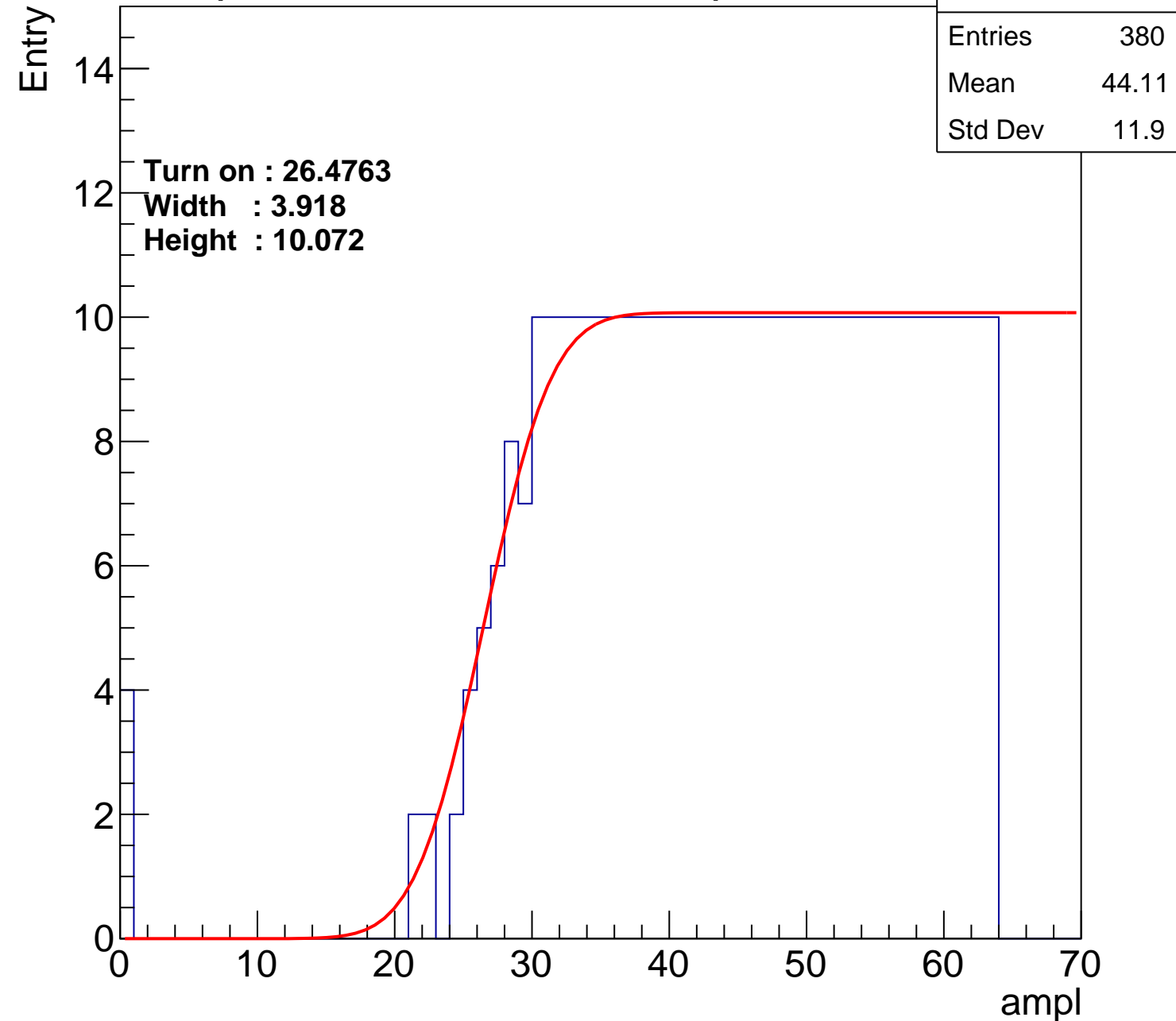
**Width : 3.918**

**Height : 10.072**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch107

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.57
Std Dev	11.34

**Turn on : 26.8979**

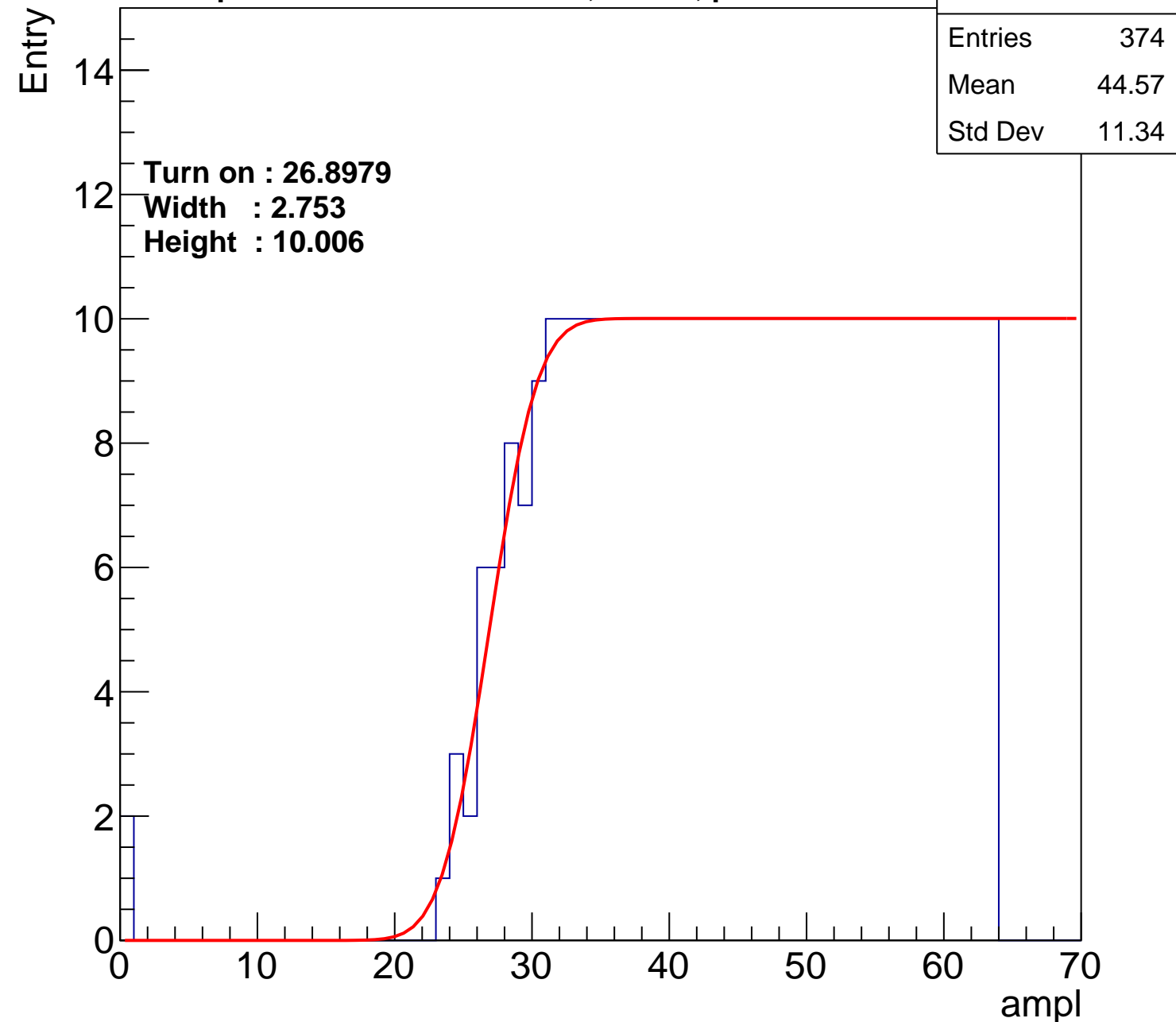
**Width : 2.753**

**Height : 10.006**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch108

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.87
Std Dev	11.59

Turn on : 25.6133

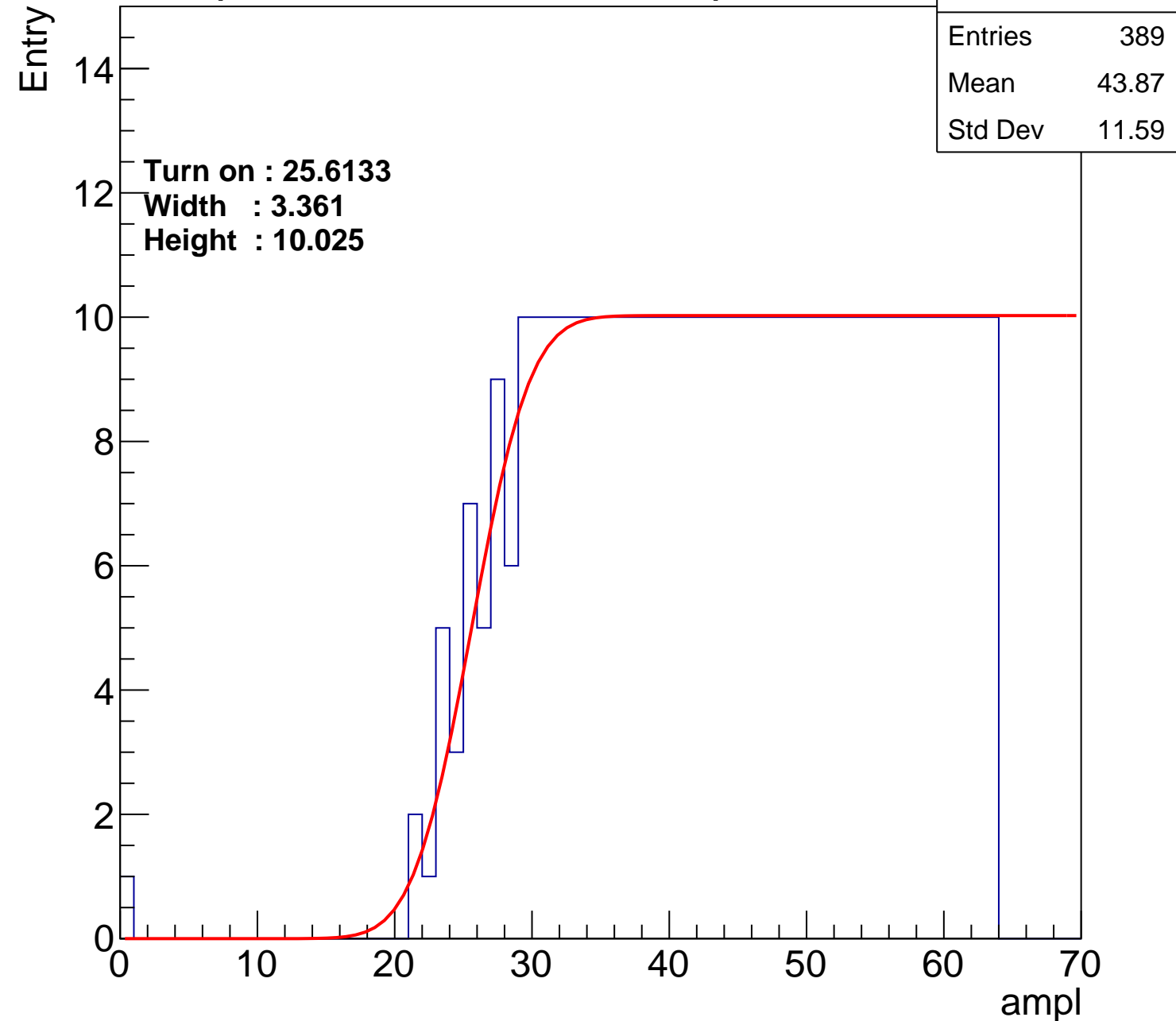
Width : 3.361

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch109

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.9
Std Dev	11.73

Turn on : 25.8410

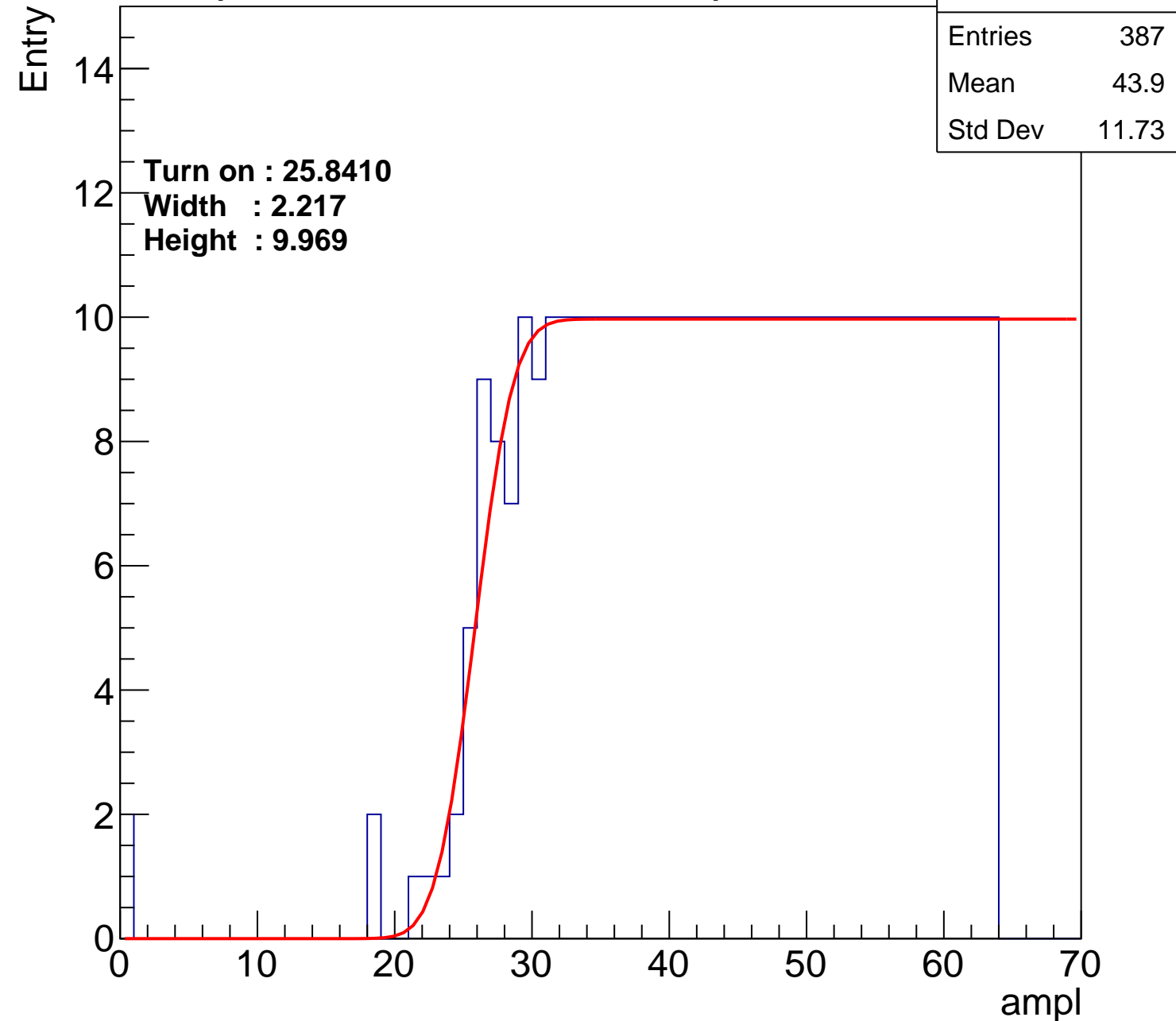
Width : 2.217

Height : 9.969

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch110

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.52
Std Dev	11.8

Turn on : 24.9947

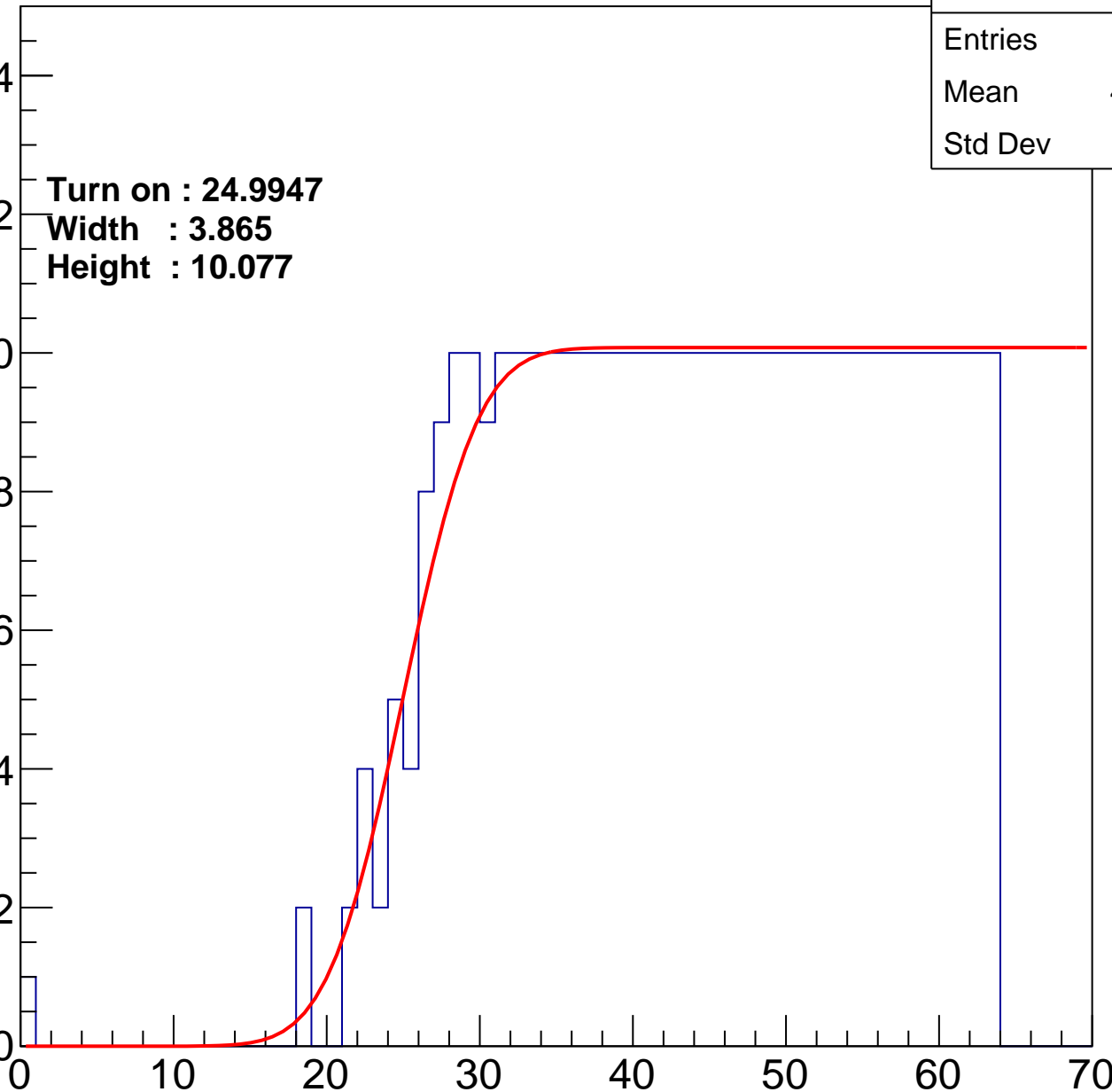
Width : 3.865

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch111

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.2013

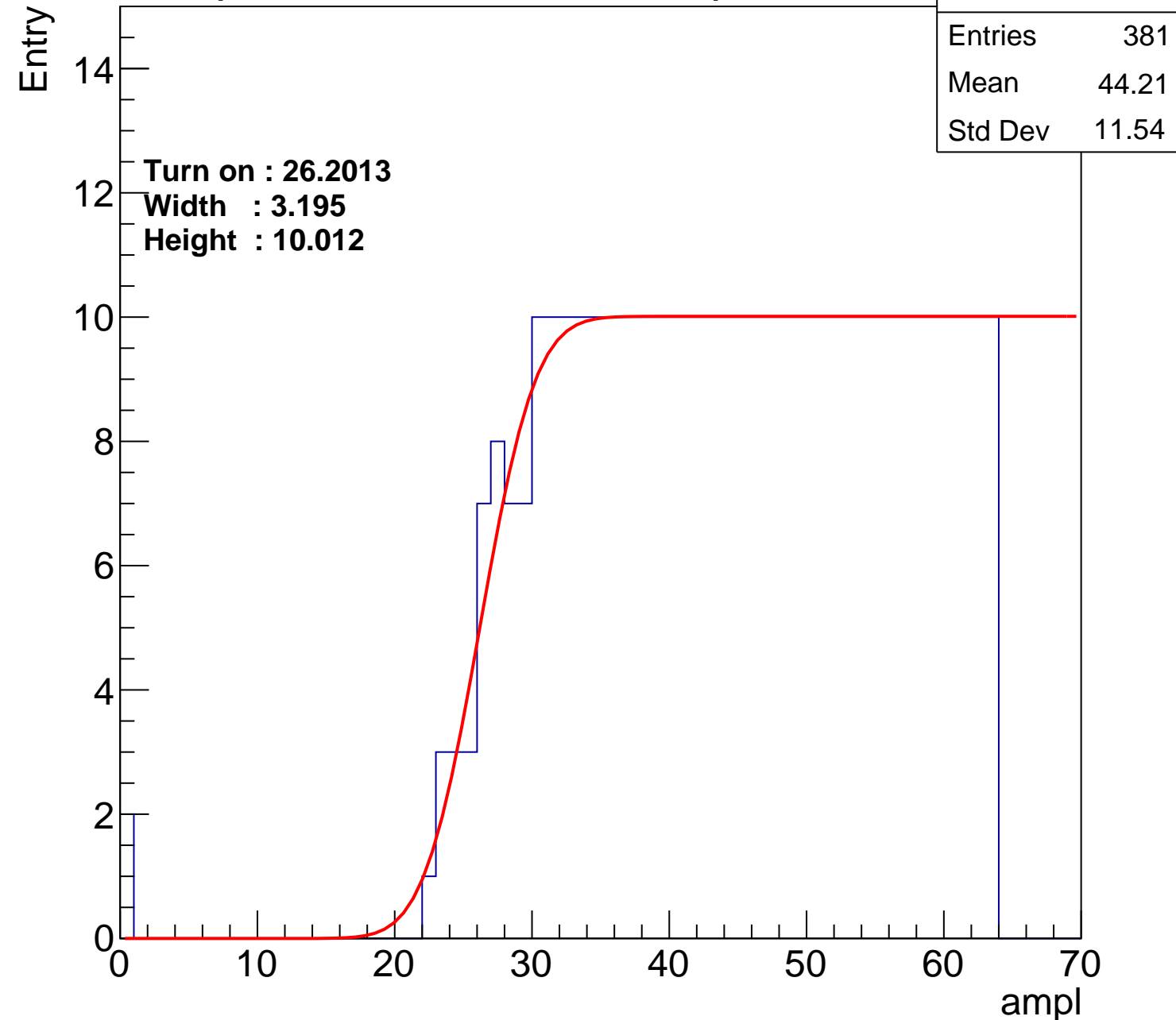
Width : 3.195

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch112

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.34
Std Dev	11.37

Turn on : 26.8435

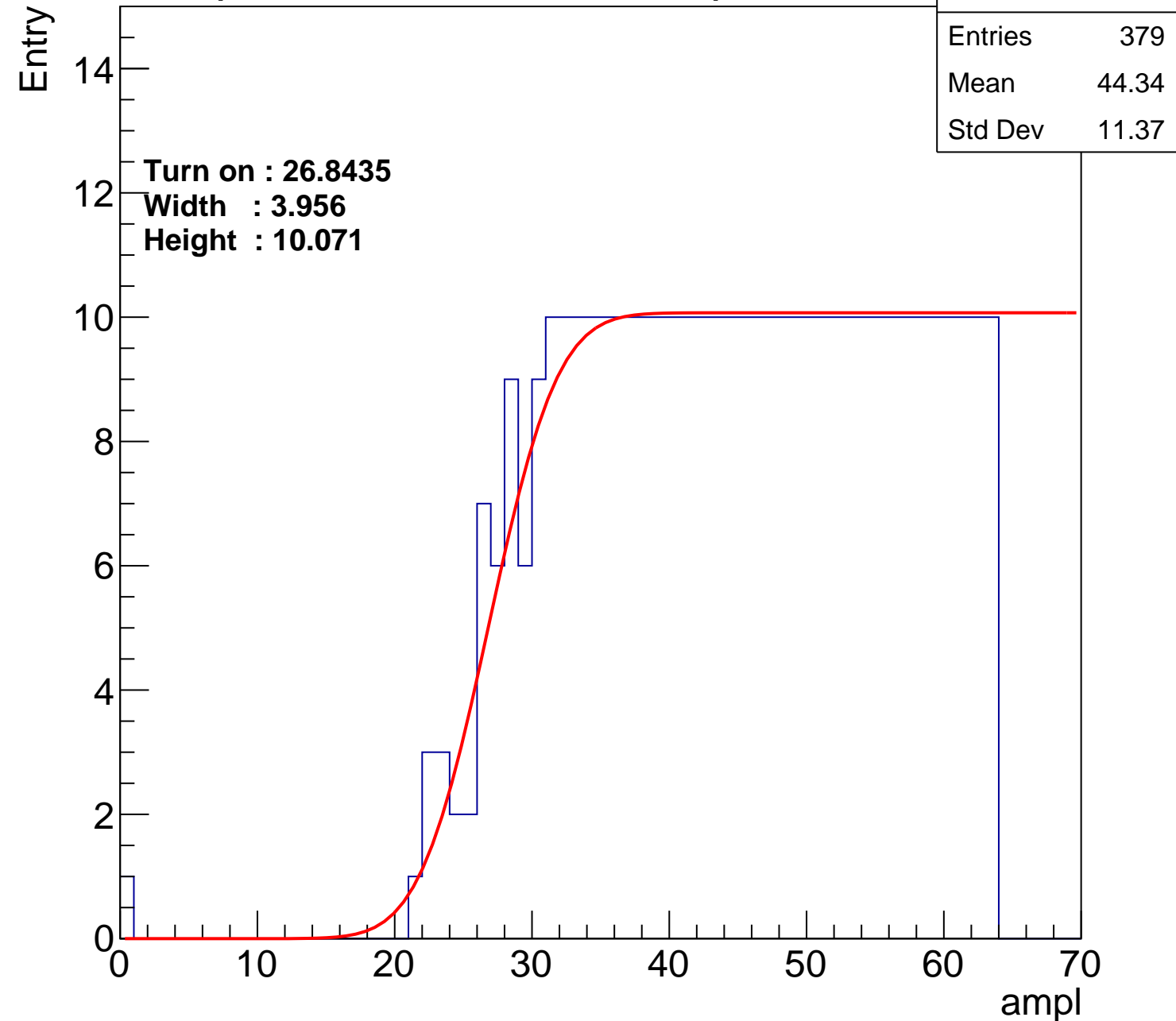
Width : 3.956

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch113

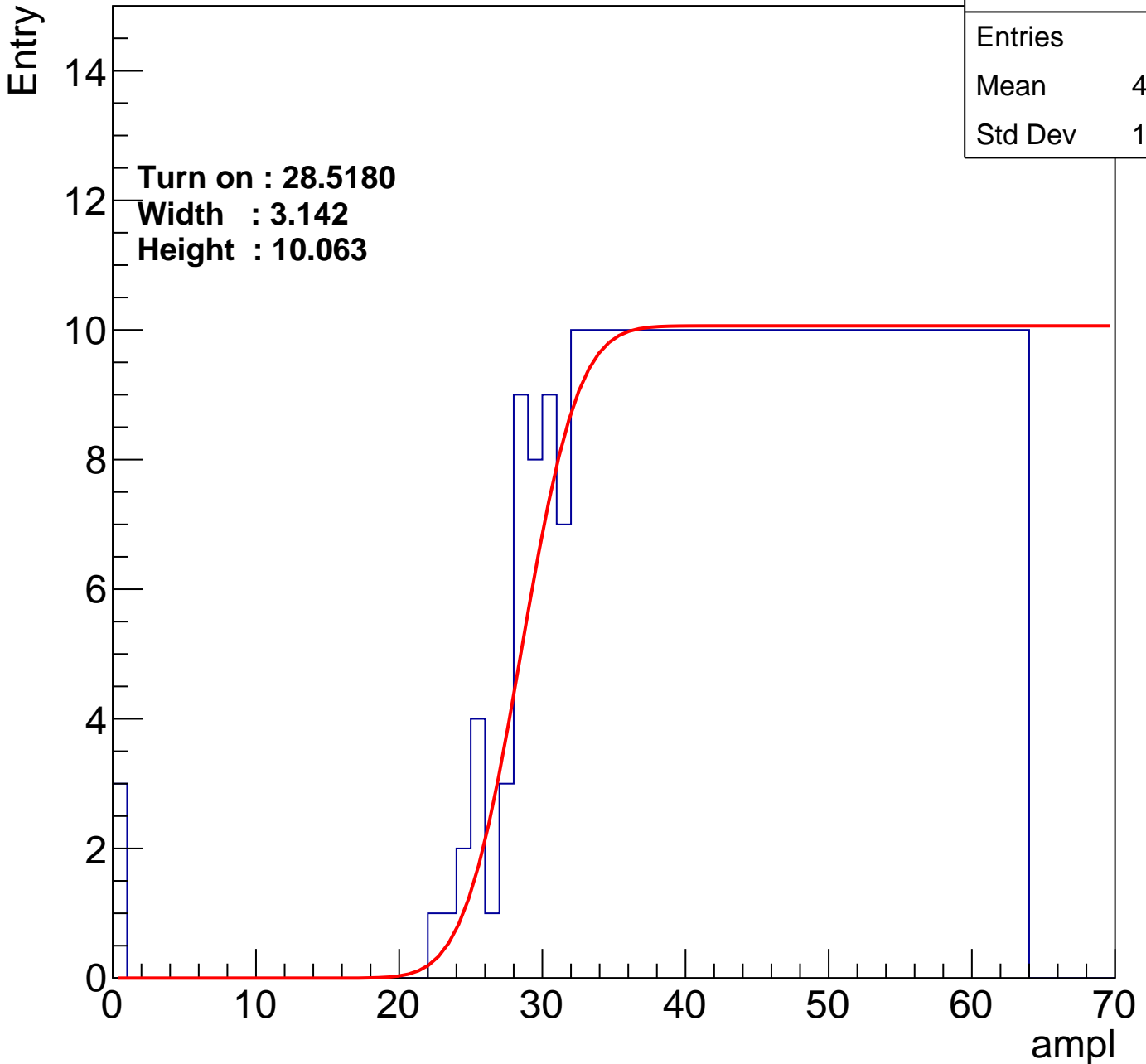
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.76
Std Dev	11.45

Turn on : 28.5180

Width : 3.142

Height : 10.063



# B0L102S, U21-ch114

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.39
Std Dev	11.3

Turn on : 26.9038

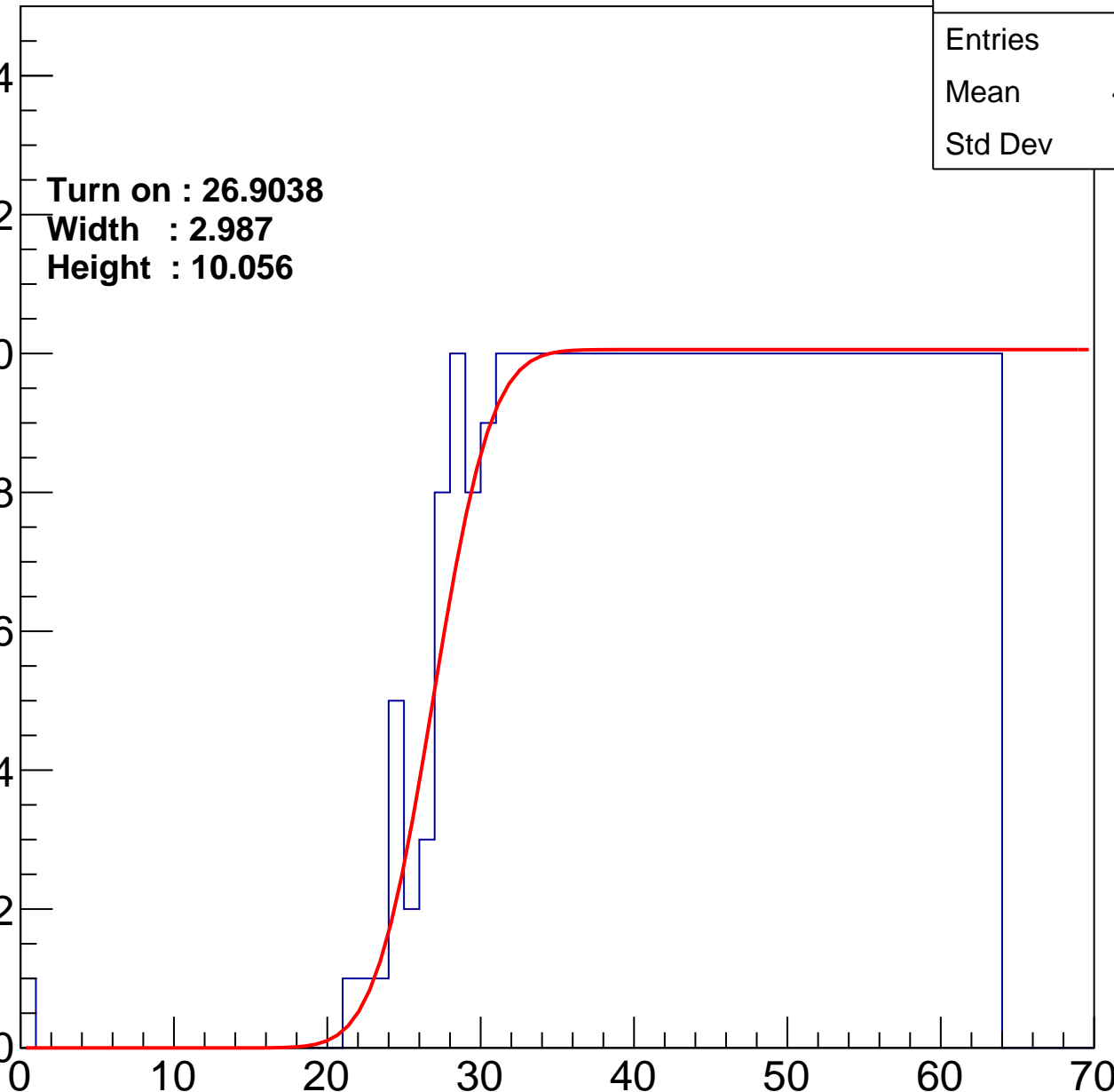
Width : 2.987

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch115

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 25.1952

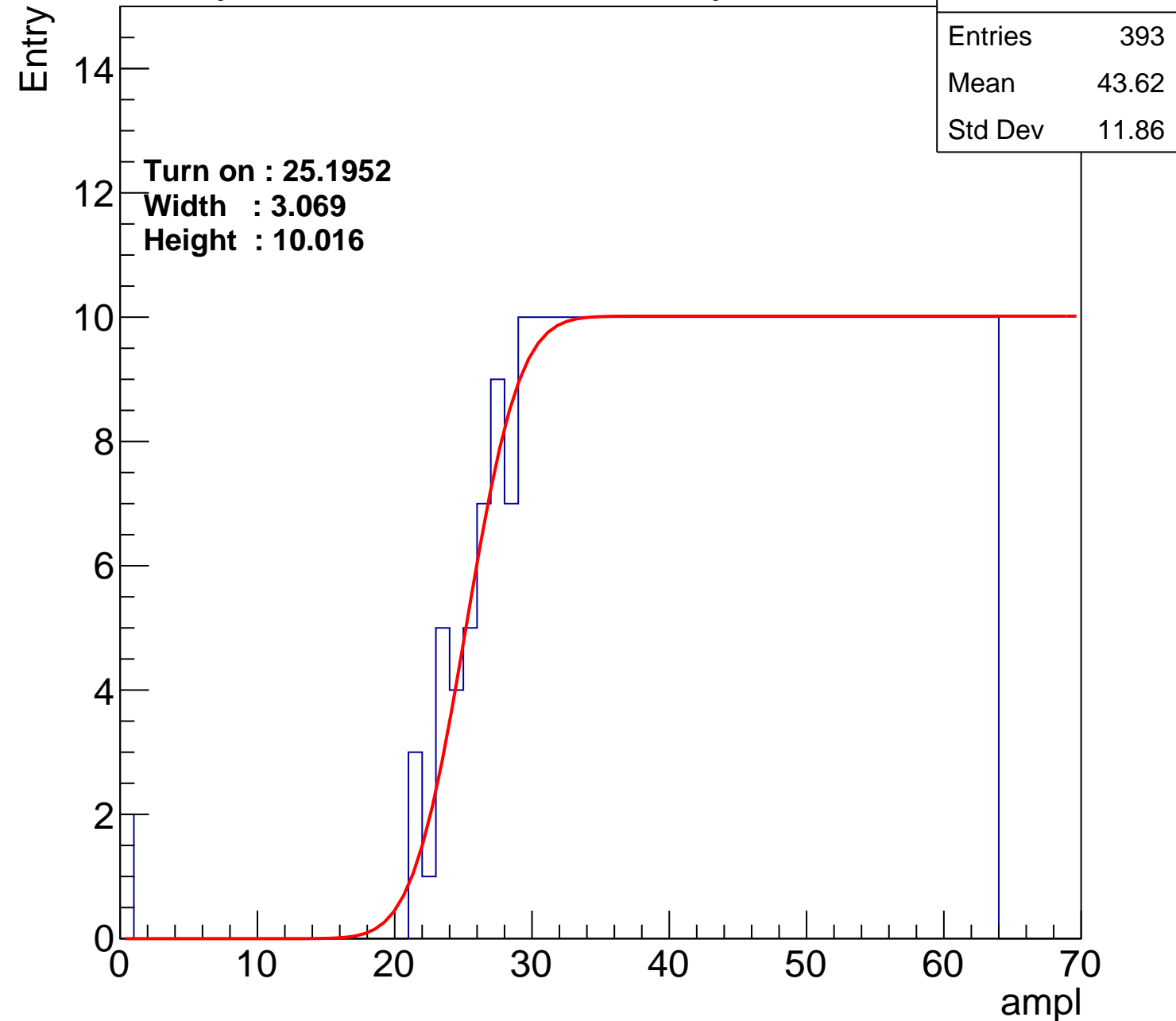
Width : 3.069

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch116

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	416
Mean	42.53
Std Dev	12.34

Turn on : 22.9343

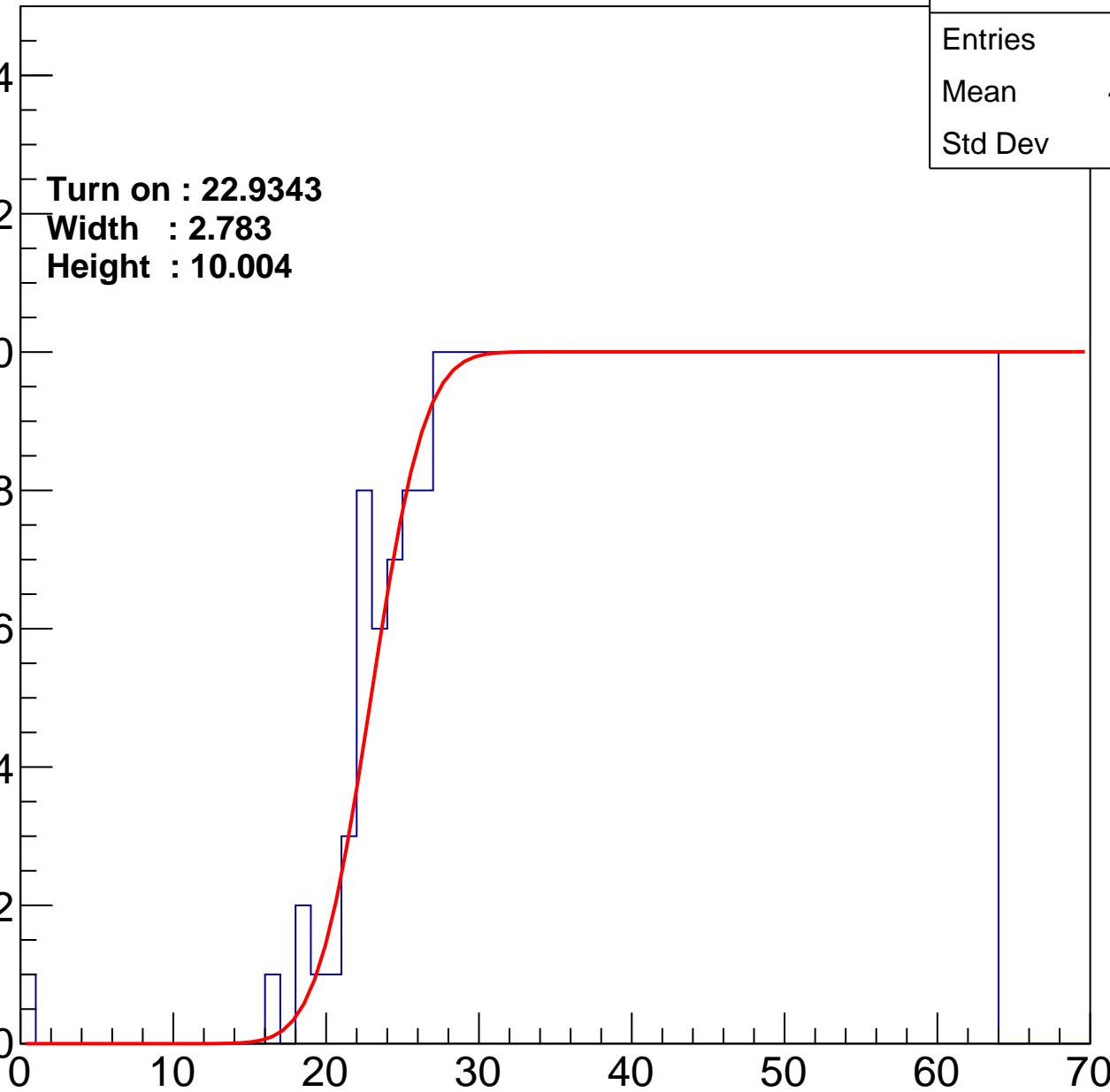
Width : 2.783

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch117

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.72
Std Dev	11.28

**Turn on : 27.5567**

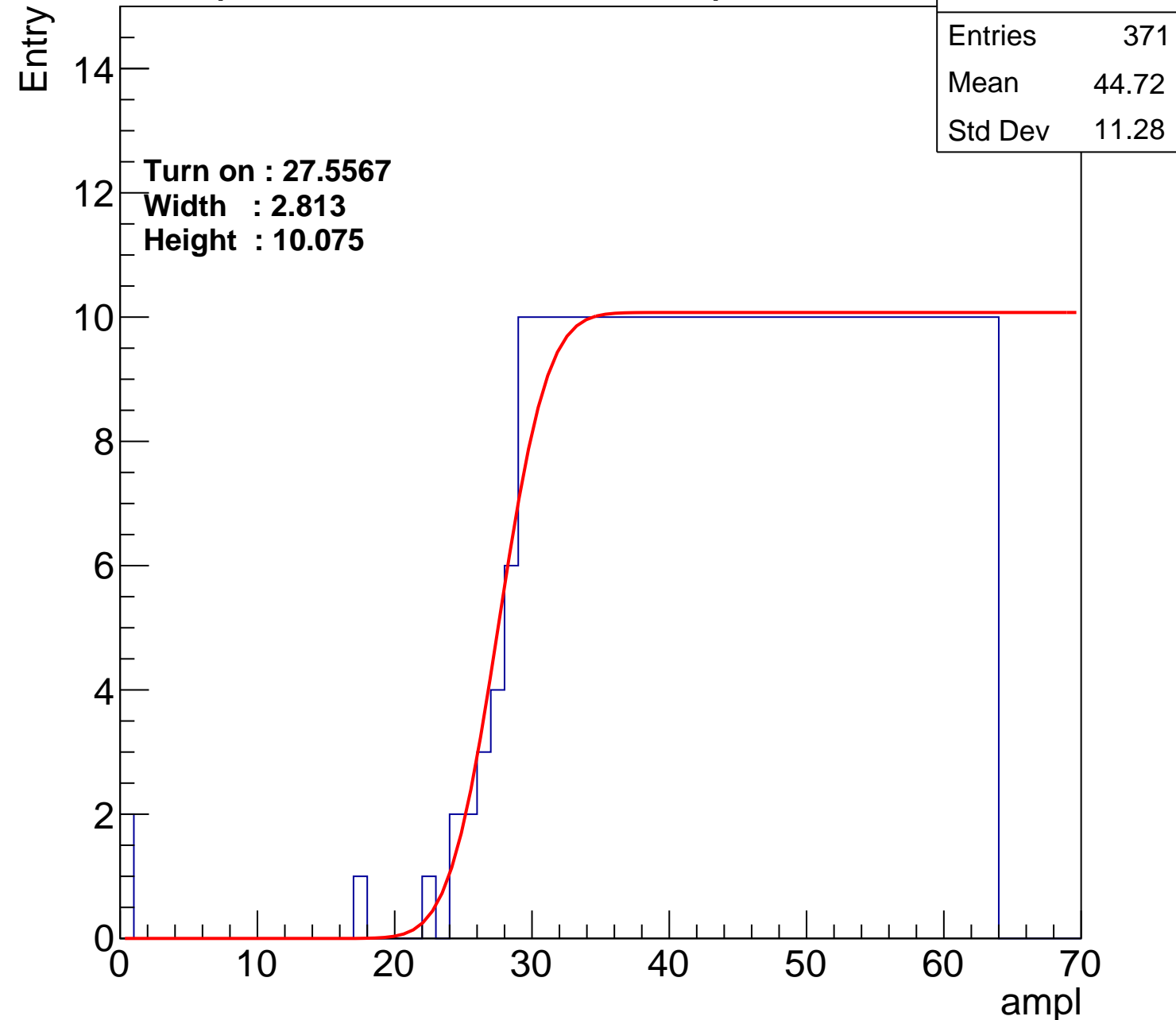
**Width : 2.813**

**Height : 10.075**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch118

calib\_packv5\_042523\_0143.root, FC#12, port B1

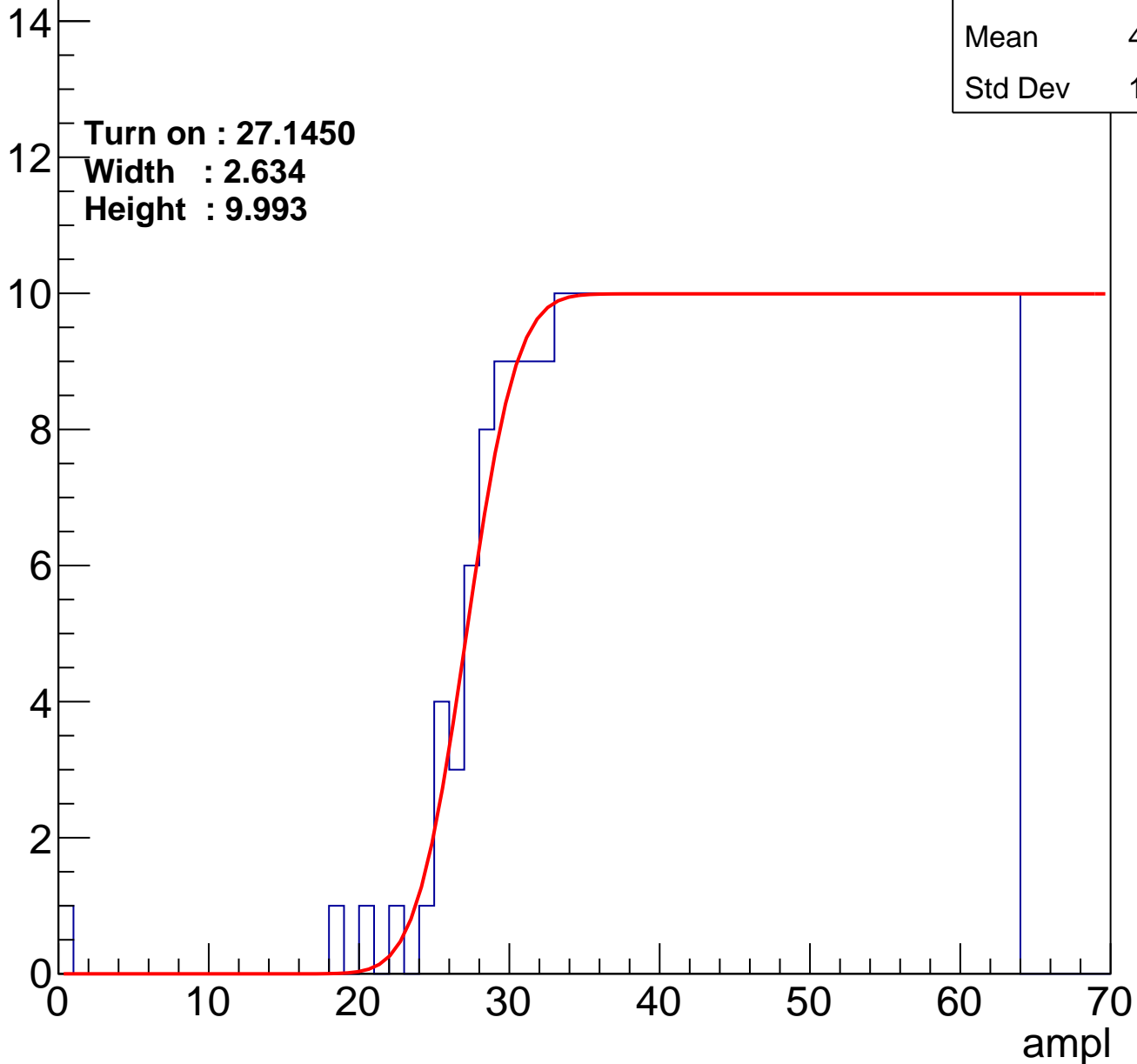
Entries	372
Mean	44.69
Std Dev	11.18

Turn on : 27.1450

Width : 2.634

Height : 9.993

Entry





# B0L102S, U21-ch119

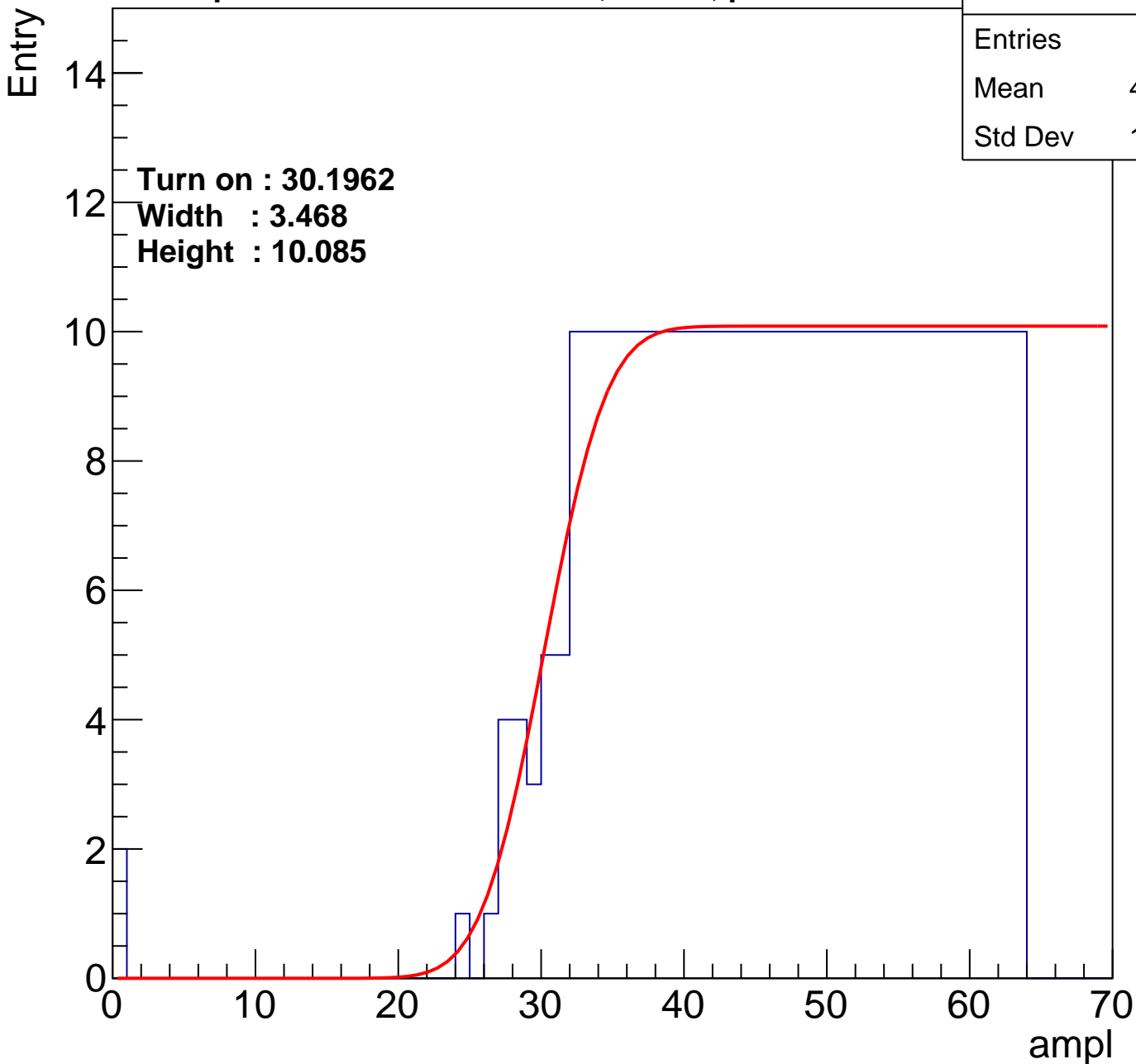
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	345
Mean	45.98
Std Dev	10.65

Turn on : 30.1962

Width : 3.468

Height : 10.085



# B0L102S, U21-ch120

calib\_packv5\_042523\_0143.root, FC#12, port B1

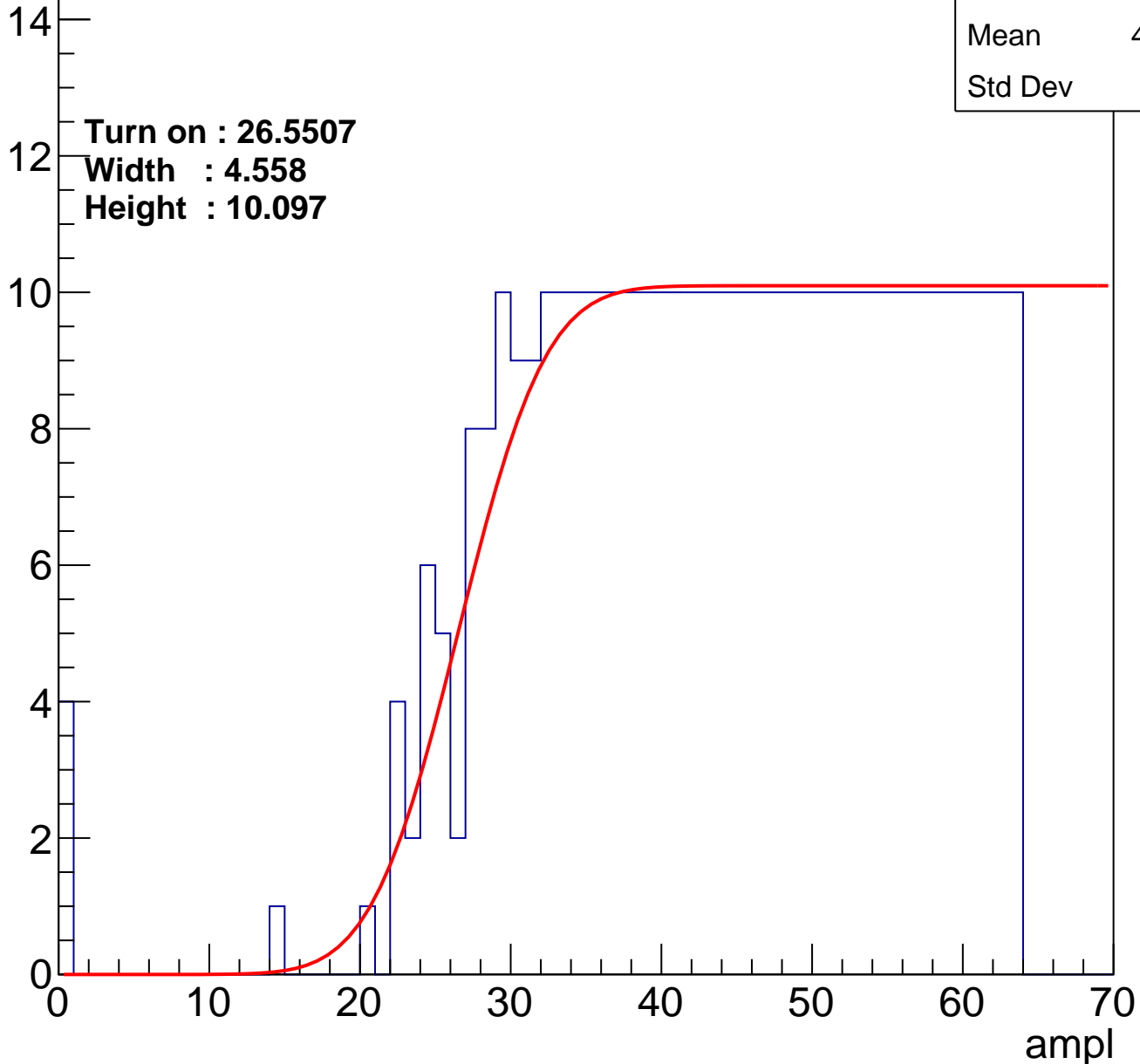
Entries	389
Mean	43.62
Std Dev	12.2

Turn on : 26.5507

Width : 4.558

Height : 10.097

Entry



# B0L102S, U21-ch121

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.78
Std Dev	11.21

**Turn on : 27.5455**

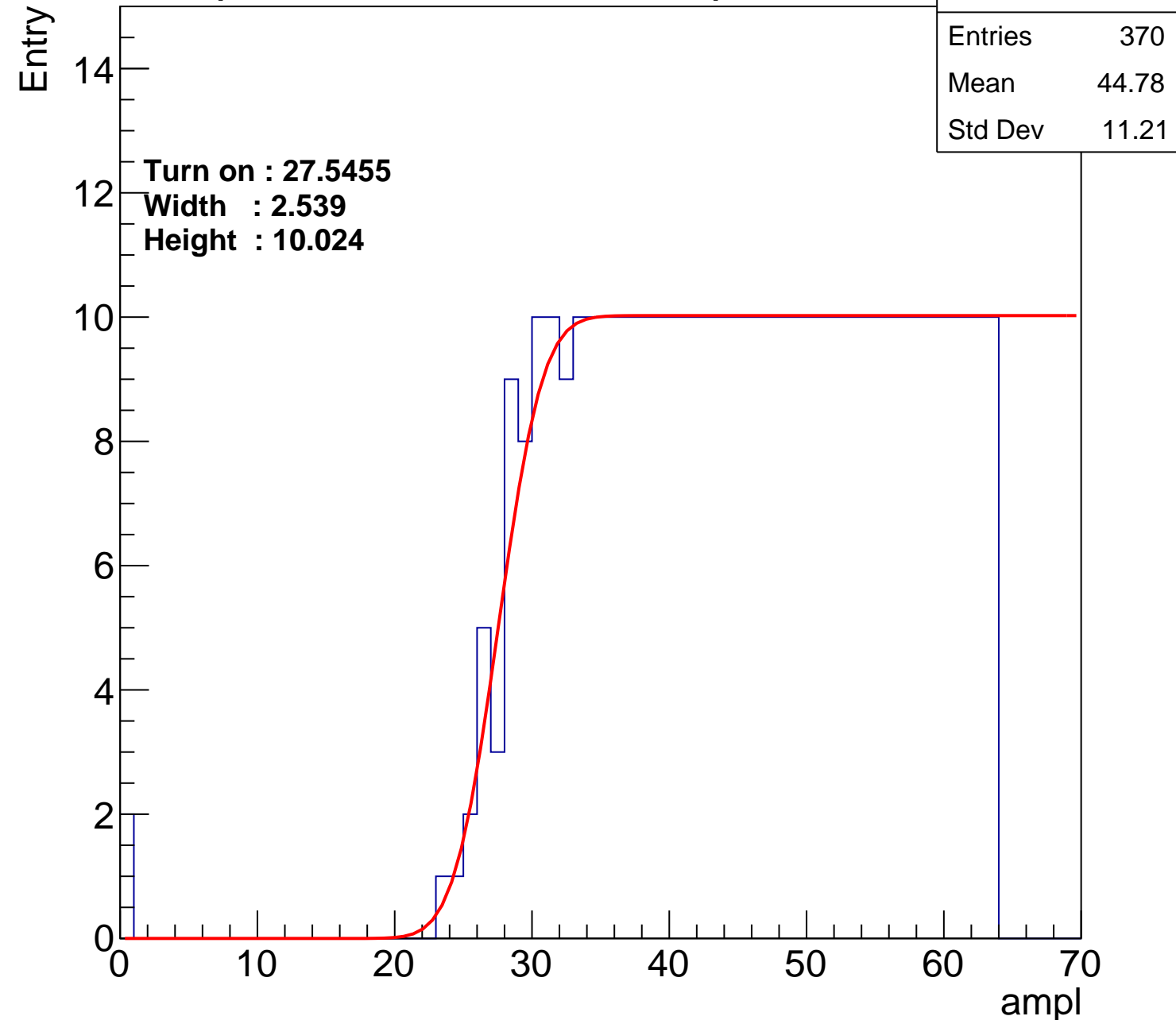
**Width : 2.539**

**Height : 10.024**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch122

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.3
Std Dev	12.32

Turn on : 25.3302

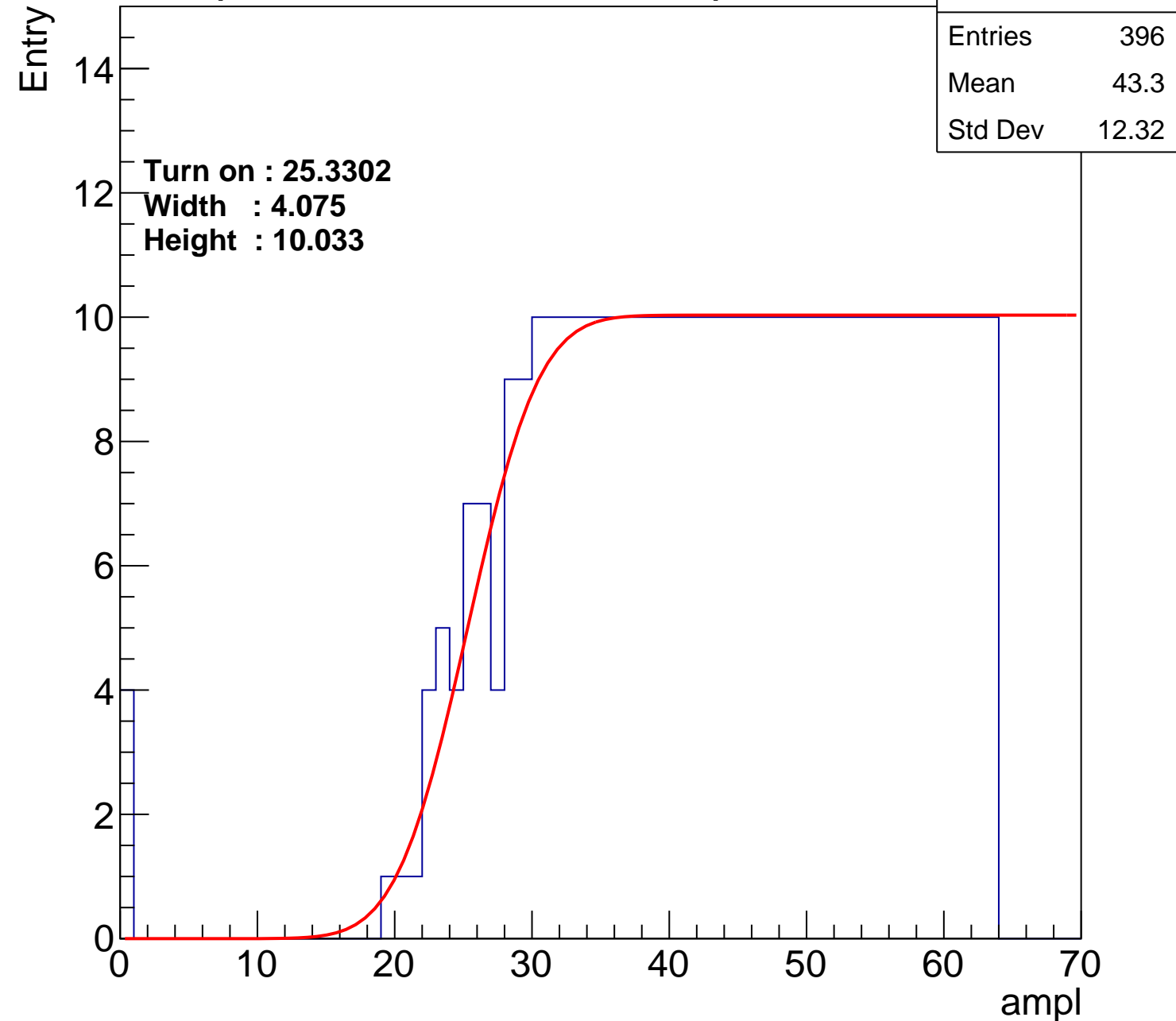
Width : 4.075

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch123

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	44.95
Std Dev	11.37

Turn on : 28.0169

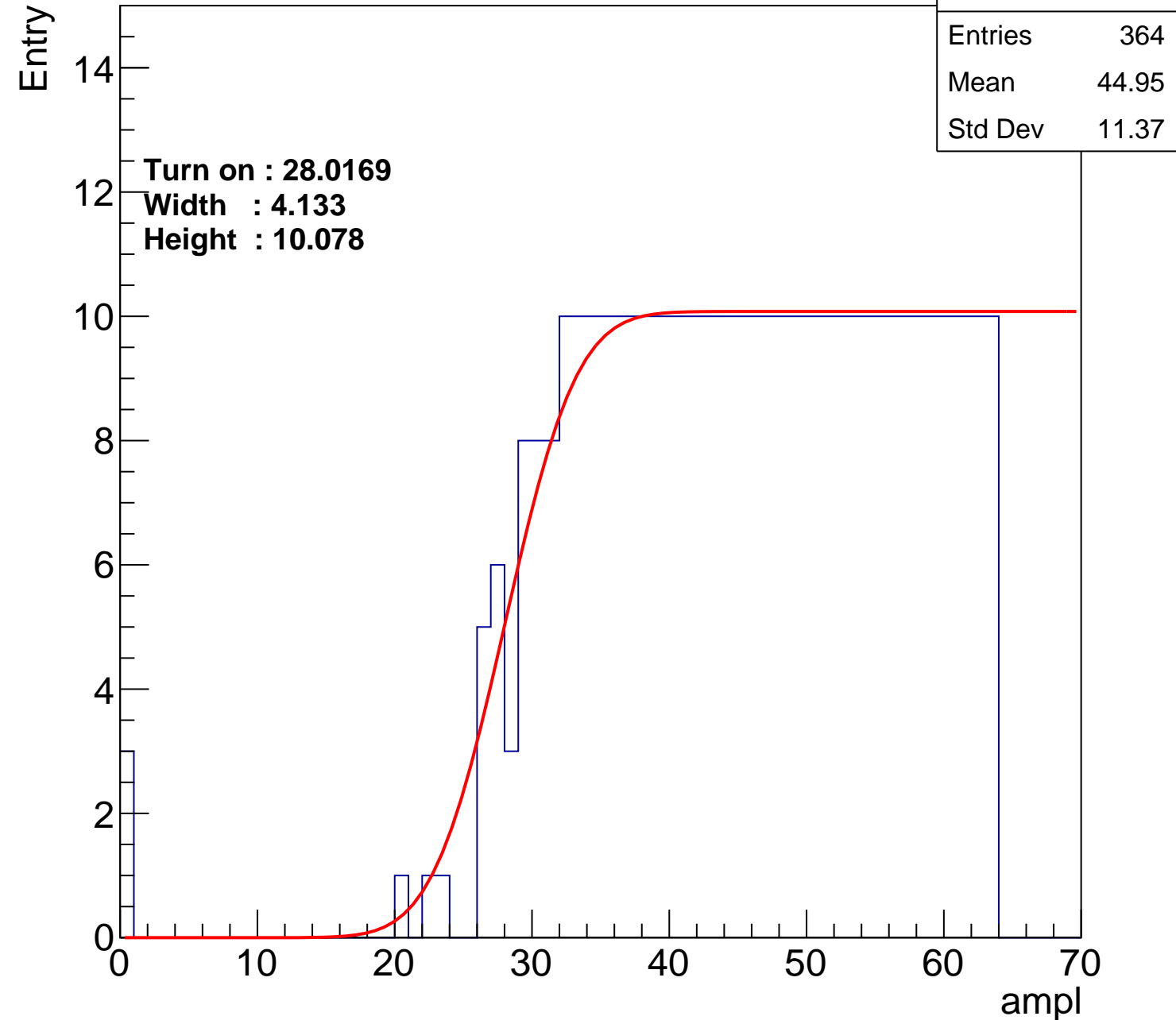
Width : 4.133

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch124

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.48
Std Dev	12.23

Turn on : 25.9675

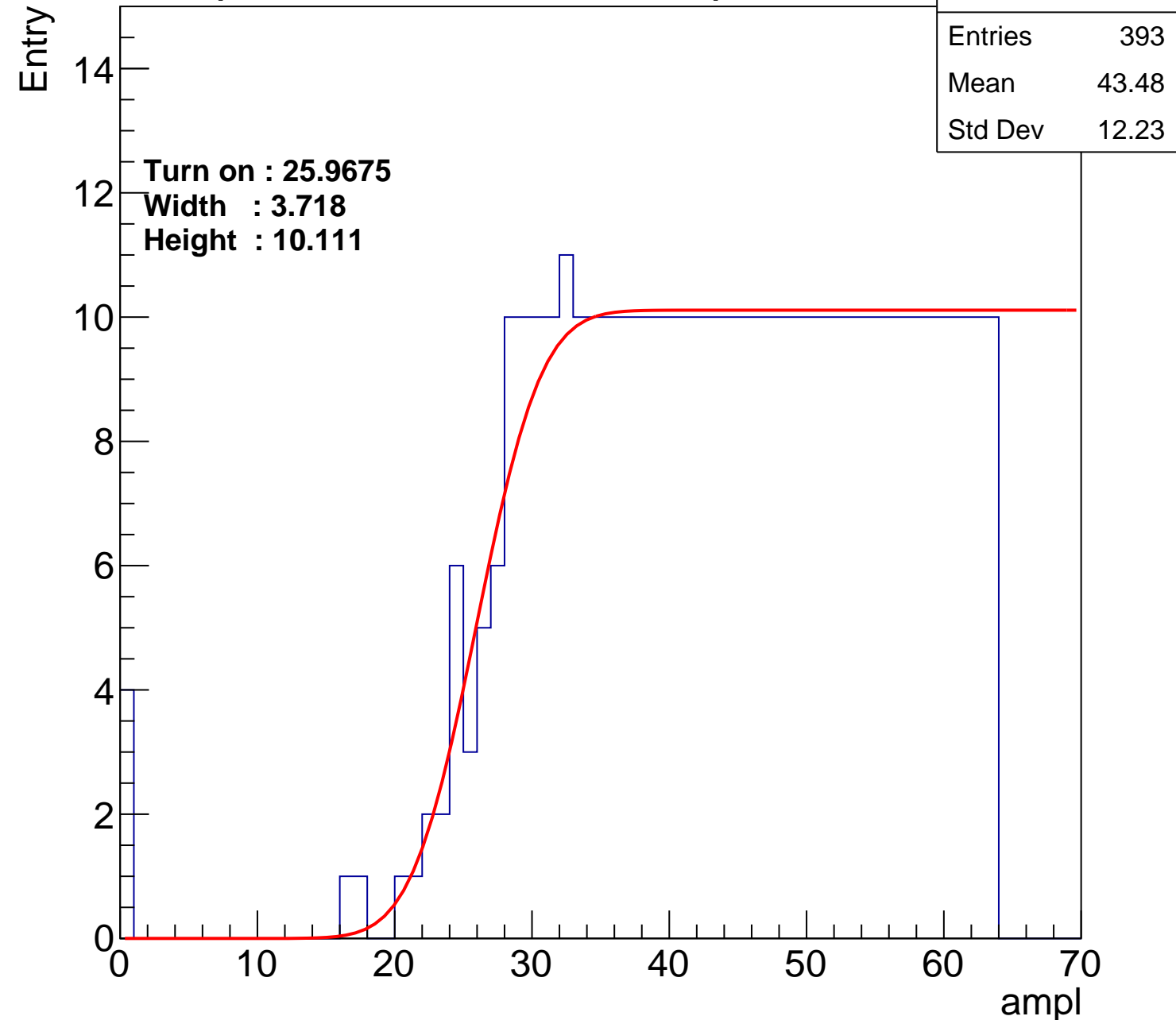
Width : 3.718

Height : 10.111

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch125

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	420
Mean	42.77
Std Dev	12.87

Turn on : 23.4944

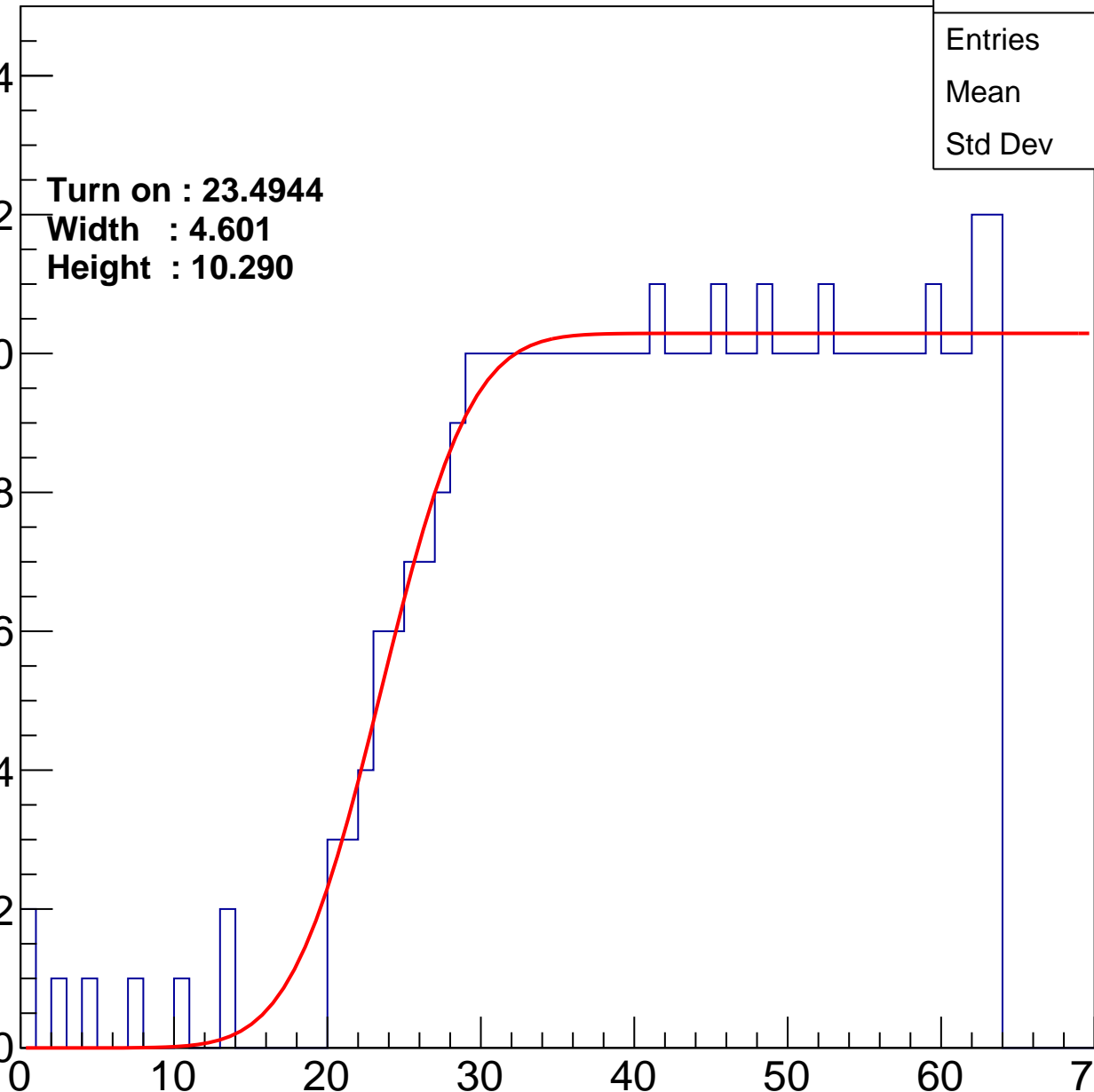
Width : 4.601

Height : 10.290

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch126

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	416
Mean	42.43
Std Dev	12.55

Turn on : 23.4664

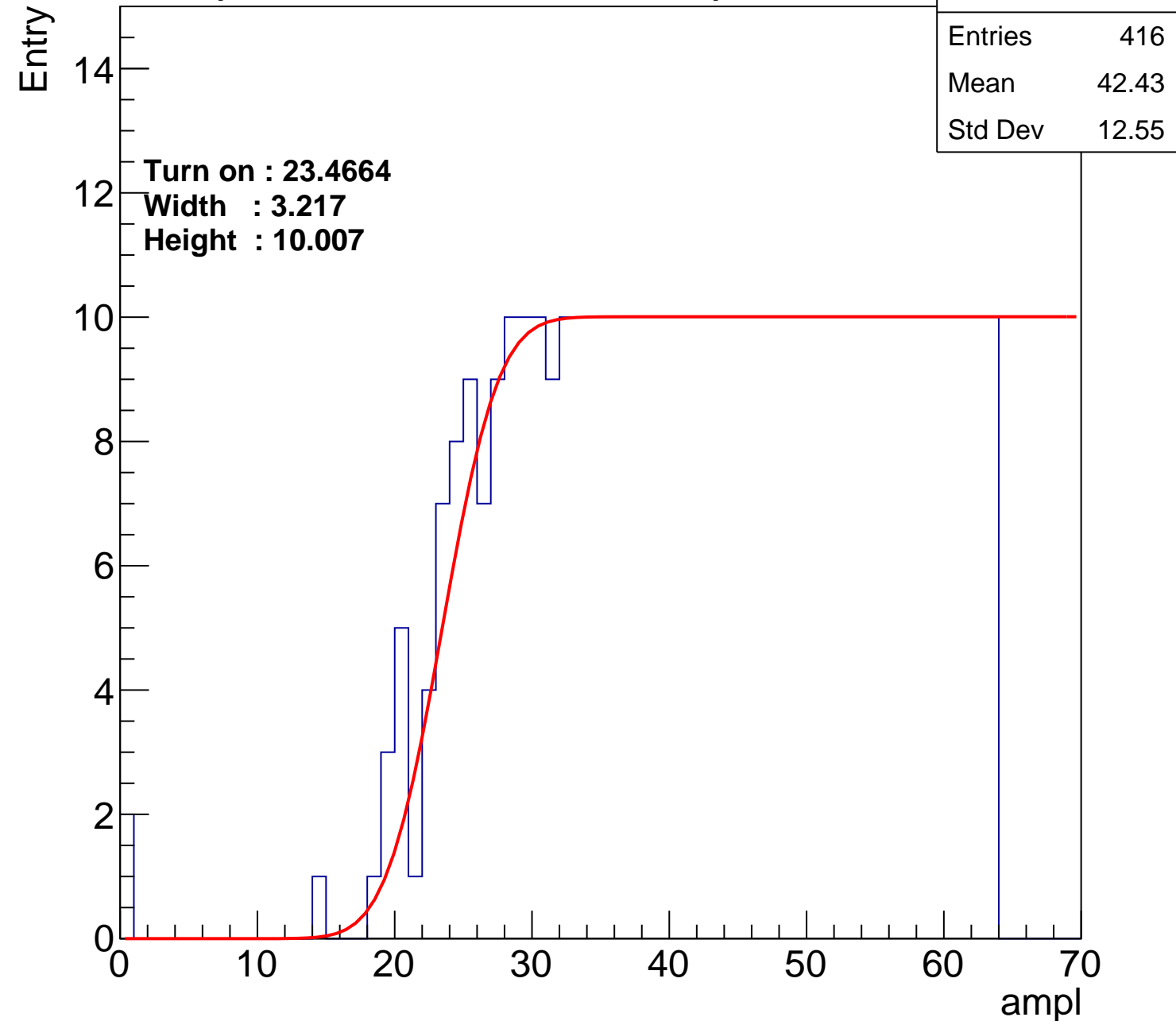
Width : 3.217

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.45
Std Dev	12.08

Turn on : 24.8578

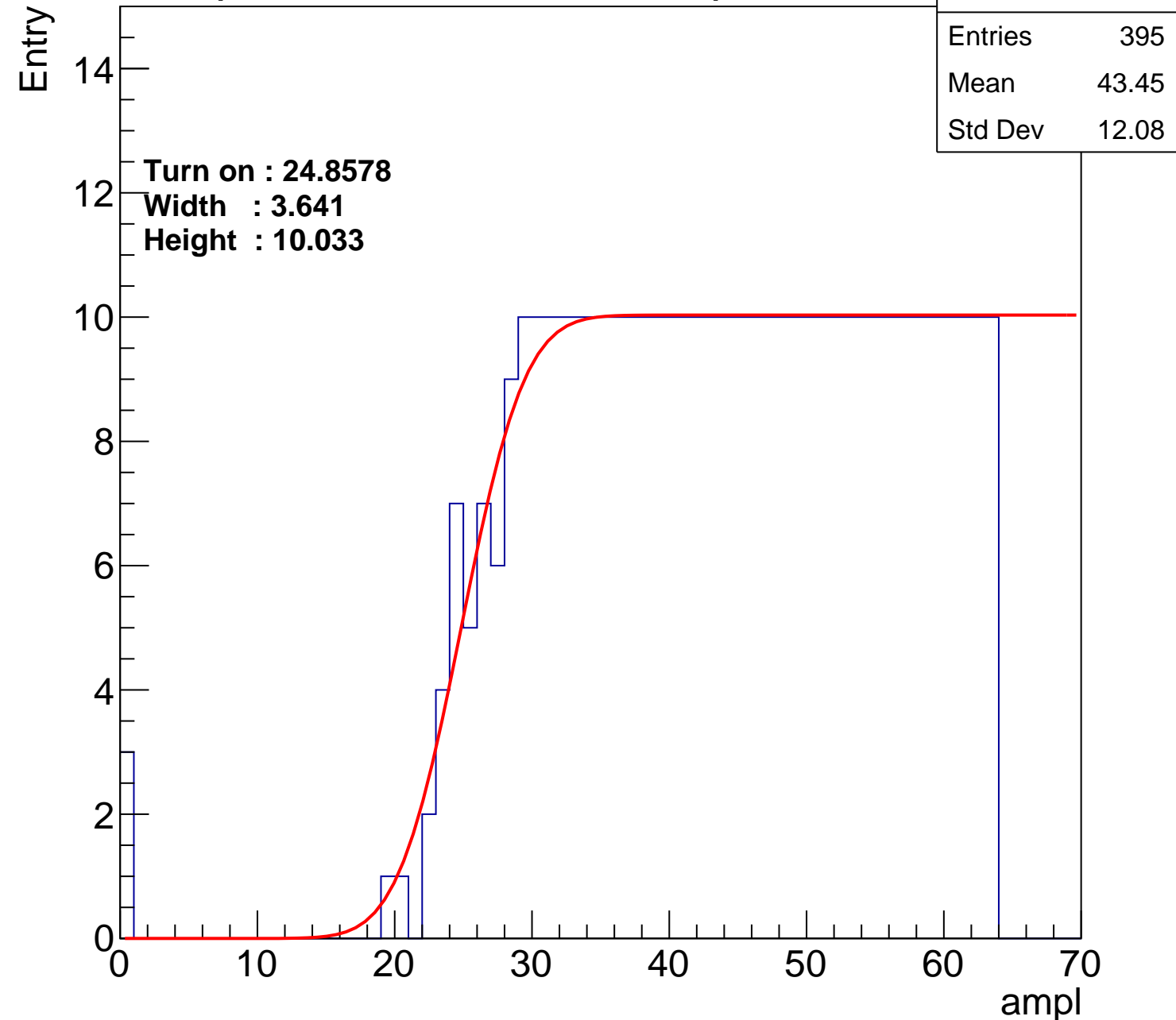
Width : 3.641

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.45
Std Dev	12.08

Turn on : 24.8578

Width : 3.641

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

