

B1L002S, U17-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.1245

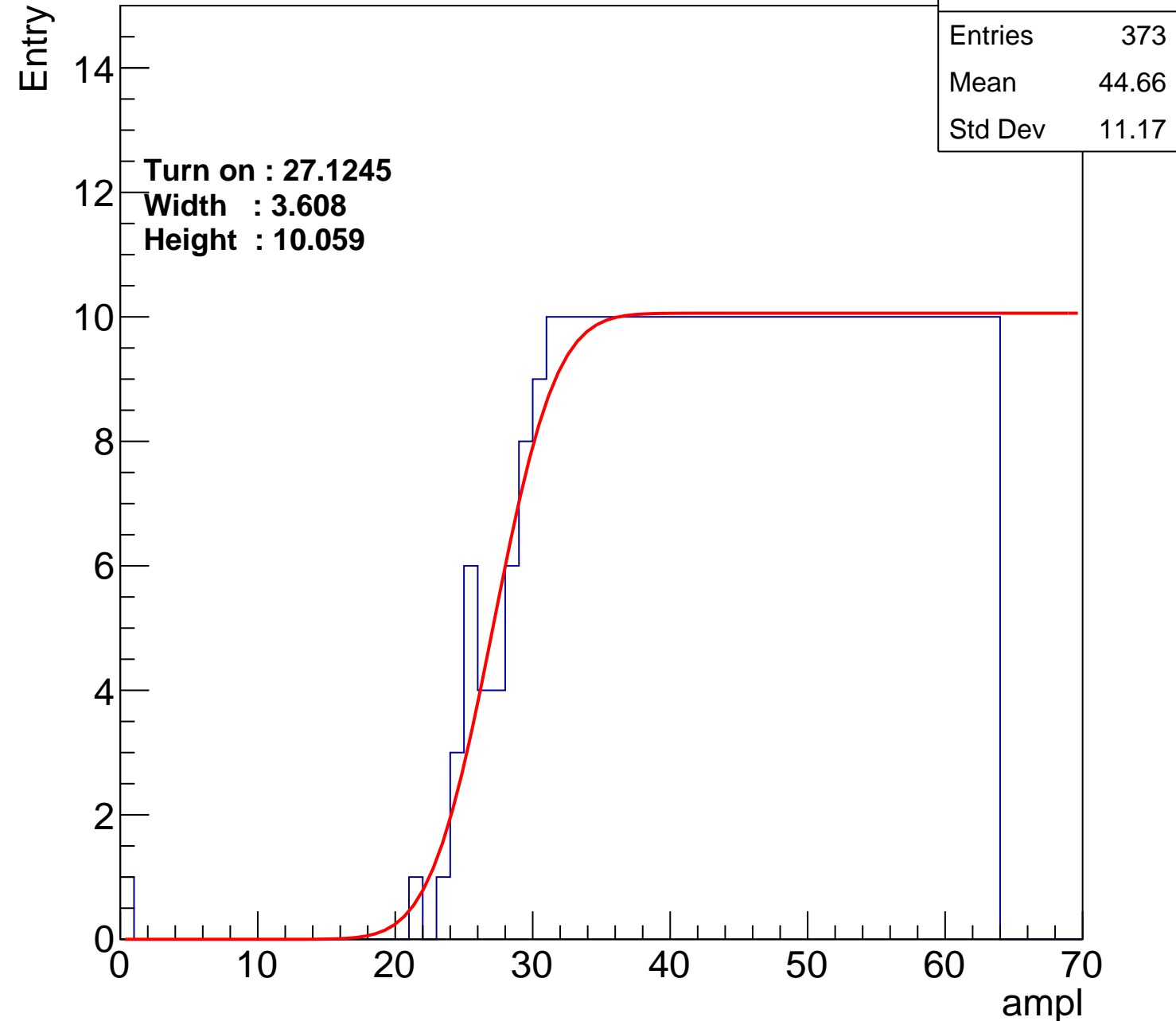
Width : 3.608

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.4696

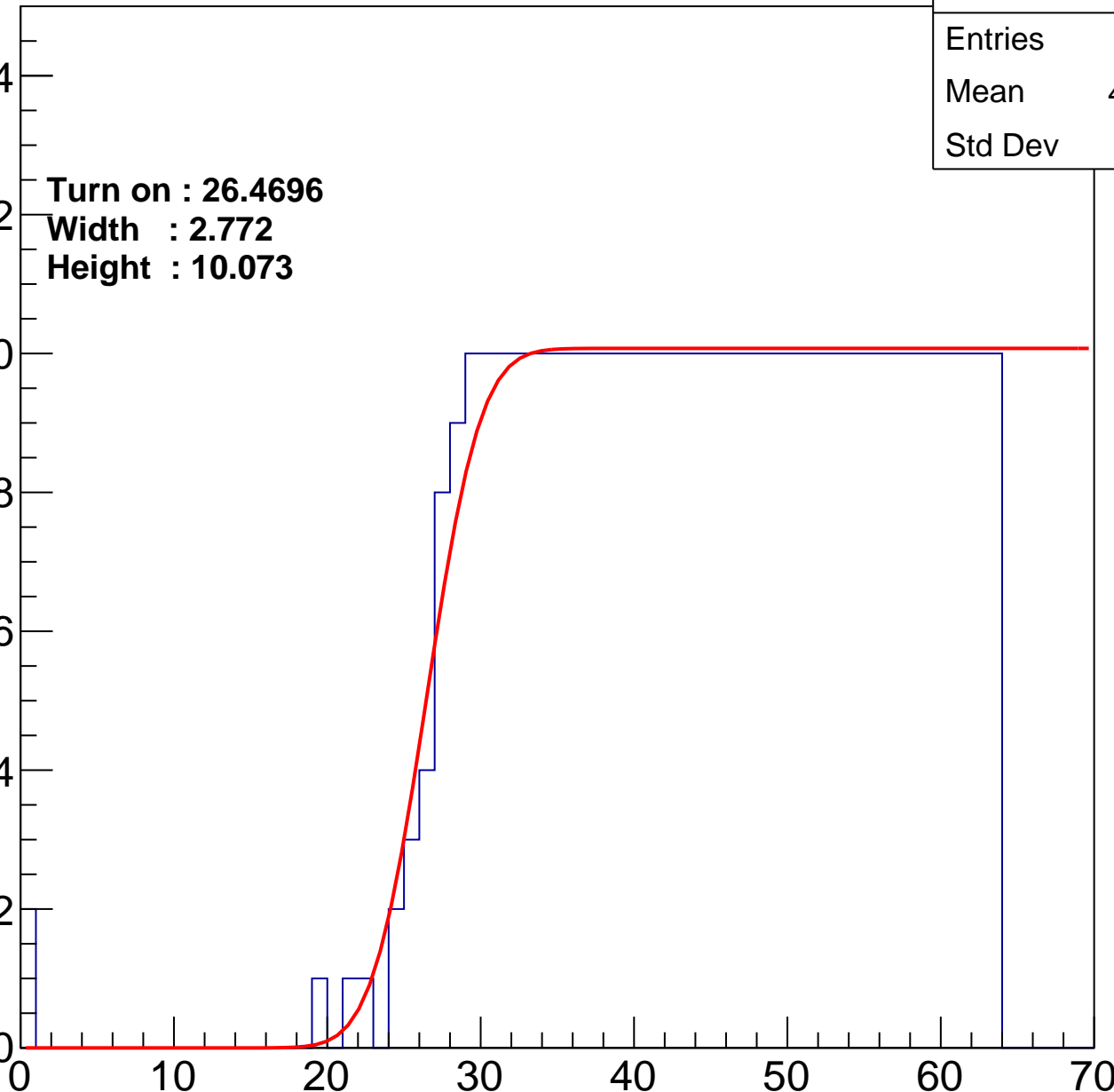
Width : 2.772

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 25.6780

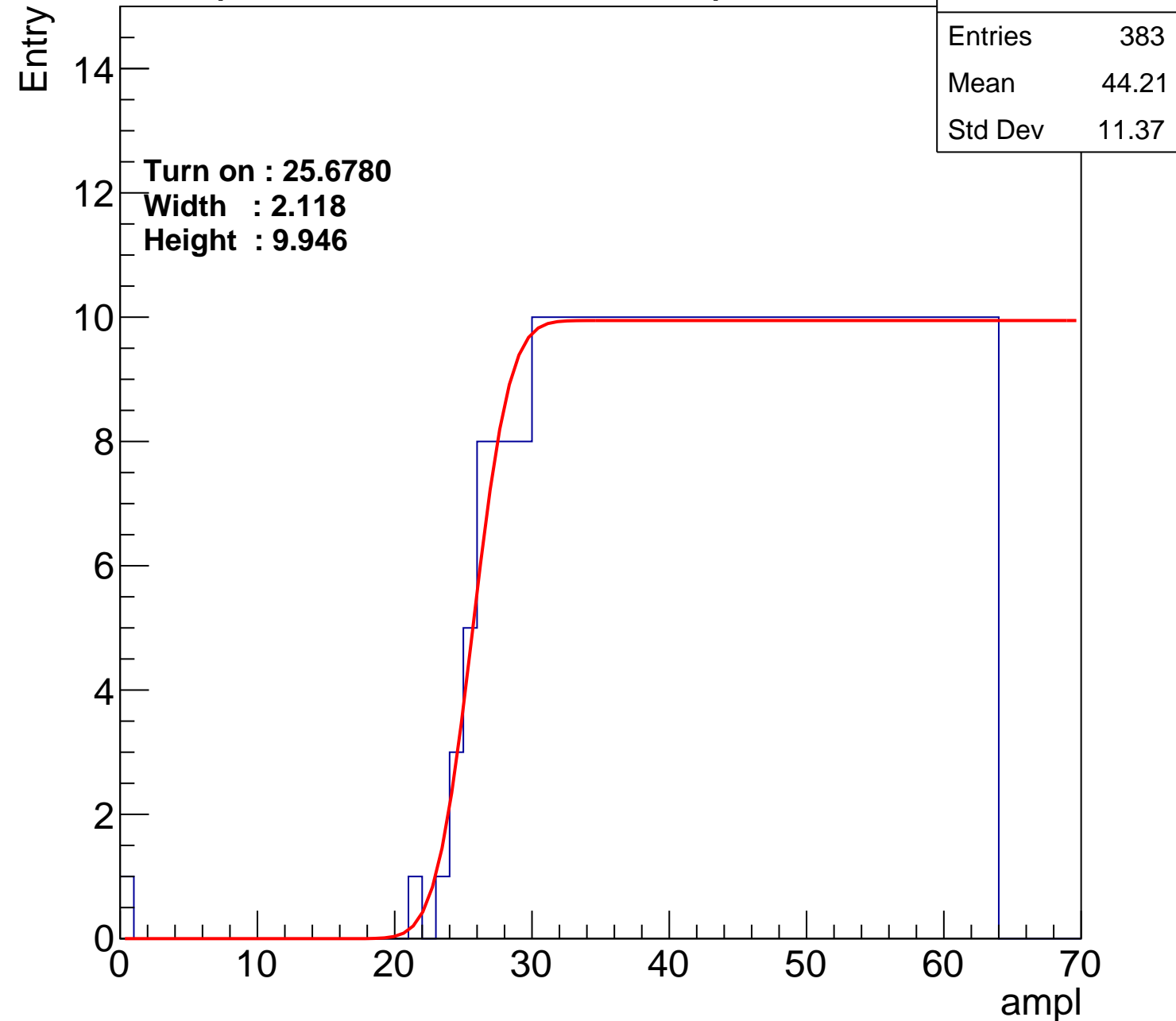
Width : 2.118

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.12
Std Dev	12.18

Turn on : 24.4154

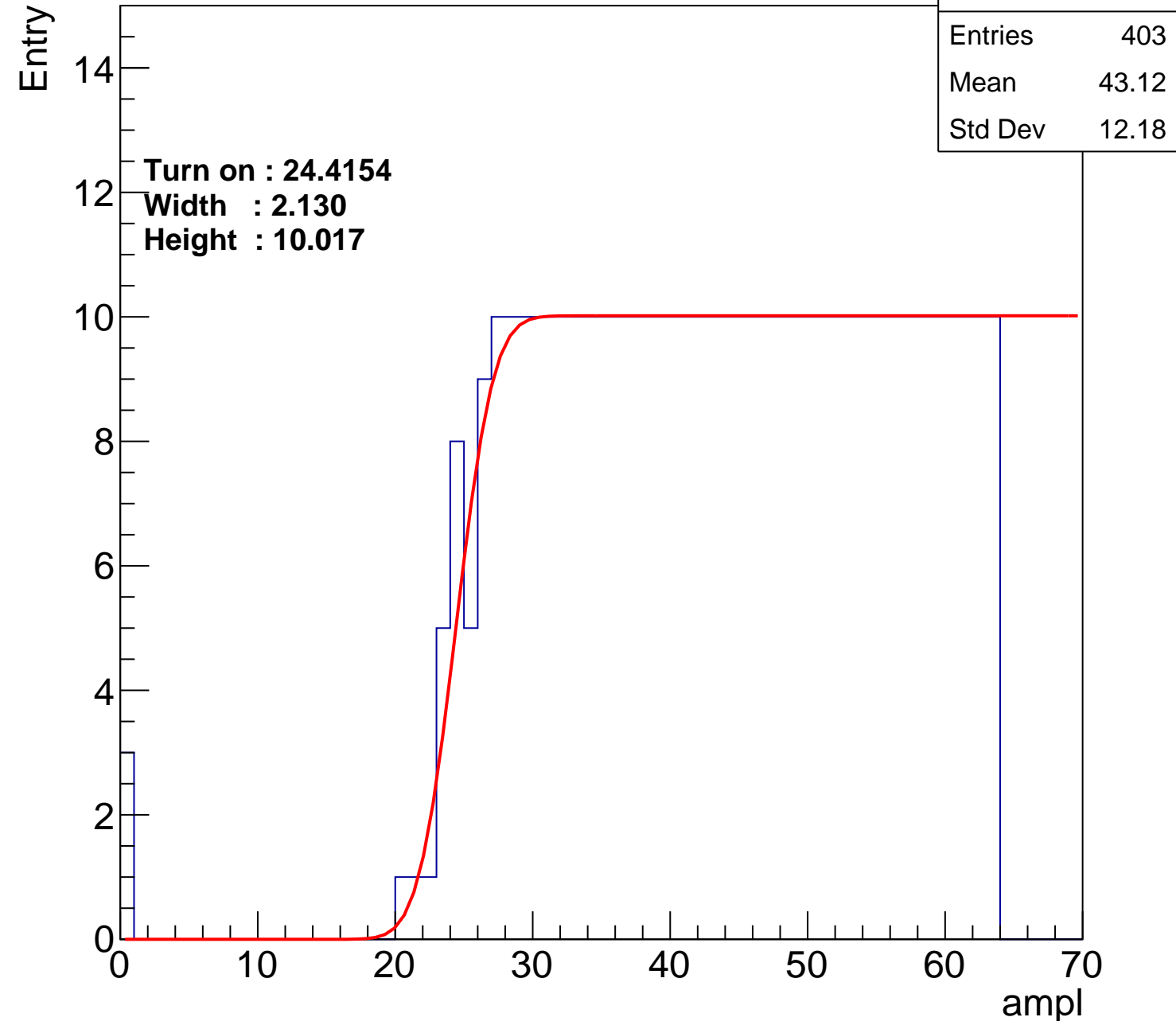
Width : 2.130

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.24
Std Dev	11.81

Turn on : 26.2840

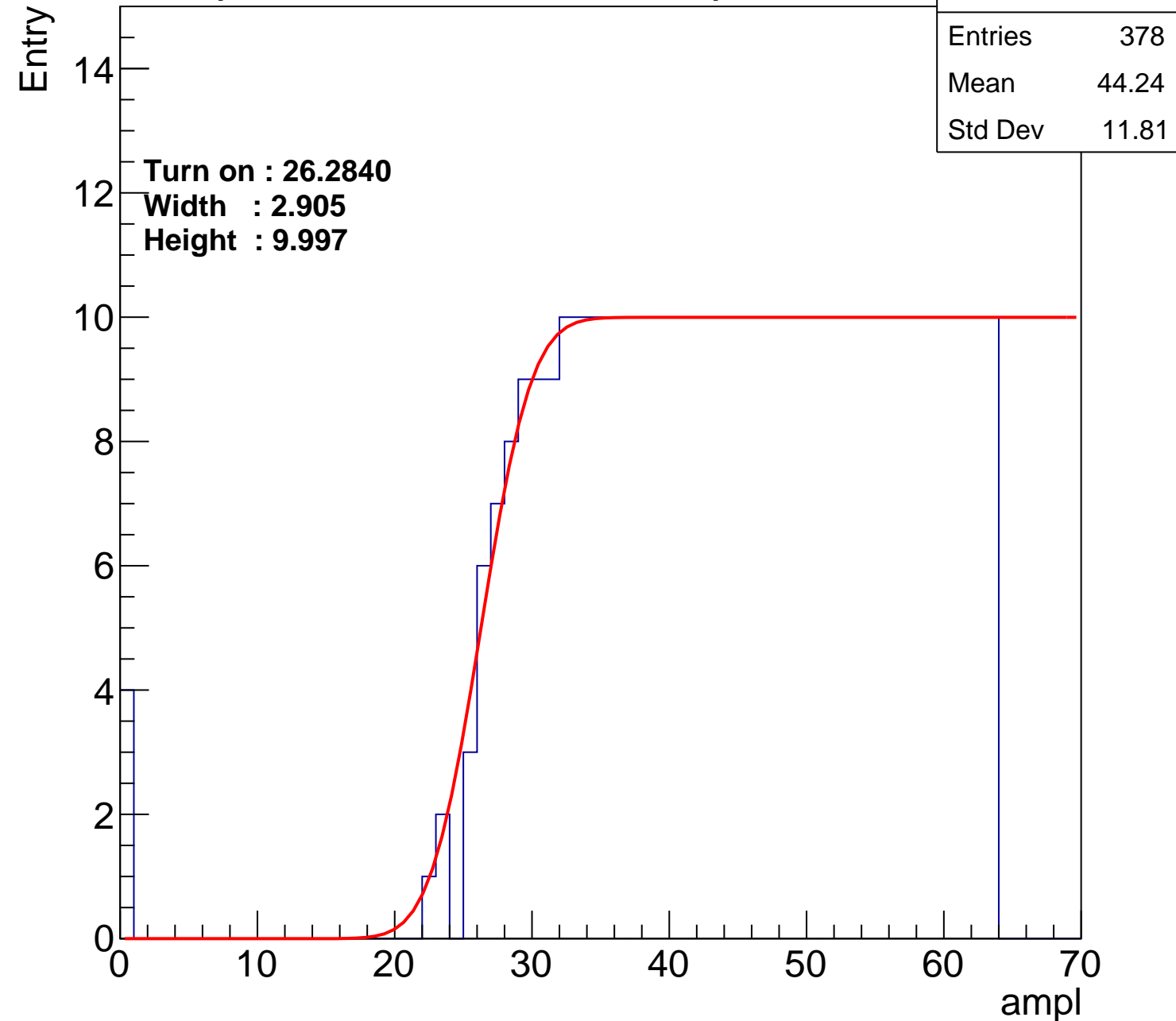
Width : 2.905

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch5

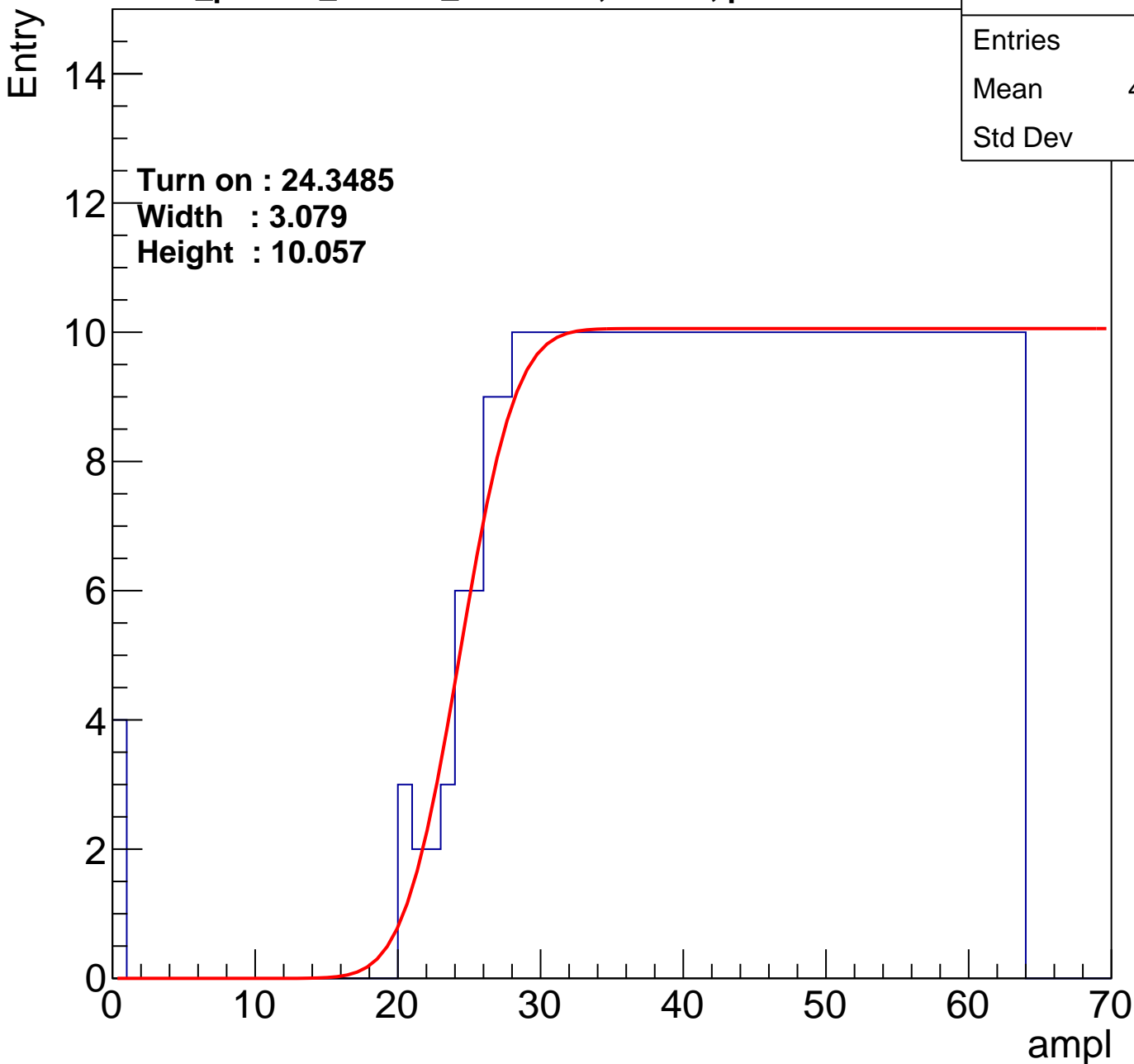
calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	42.99
Std Dev	12.4

Turn on : 24.3485

Width : 3.079

Height : 10.057



B1L002S, U17-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	419
Mean	42.31
Std Dev	12.64

Turn on : 22.8135

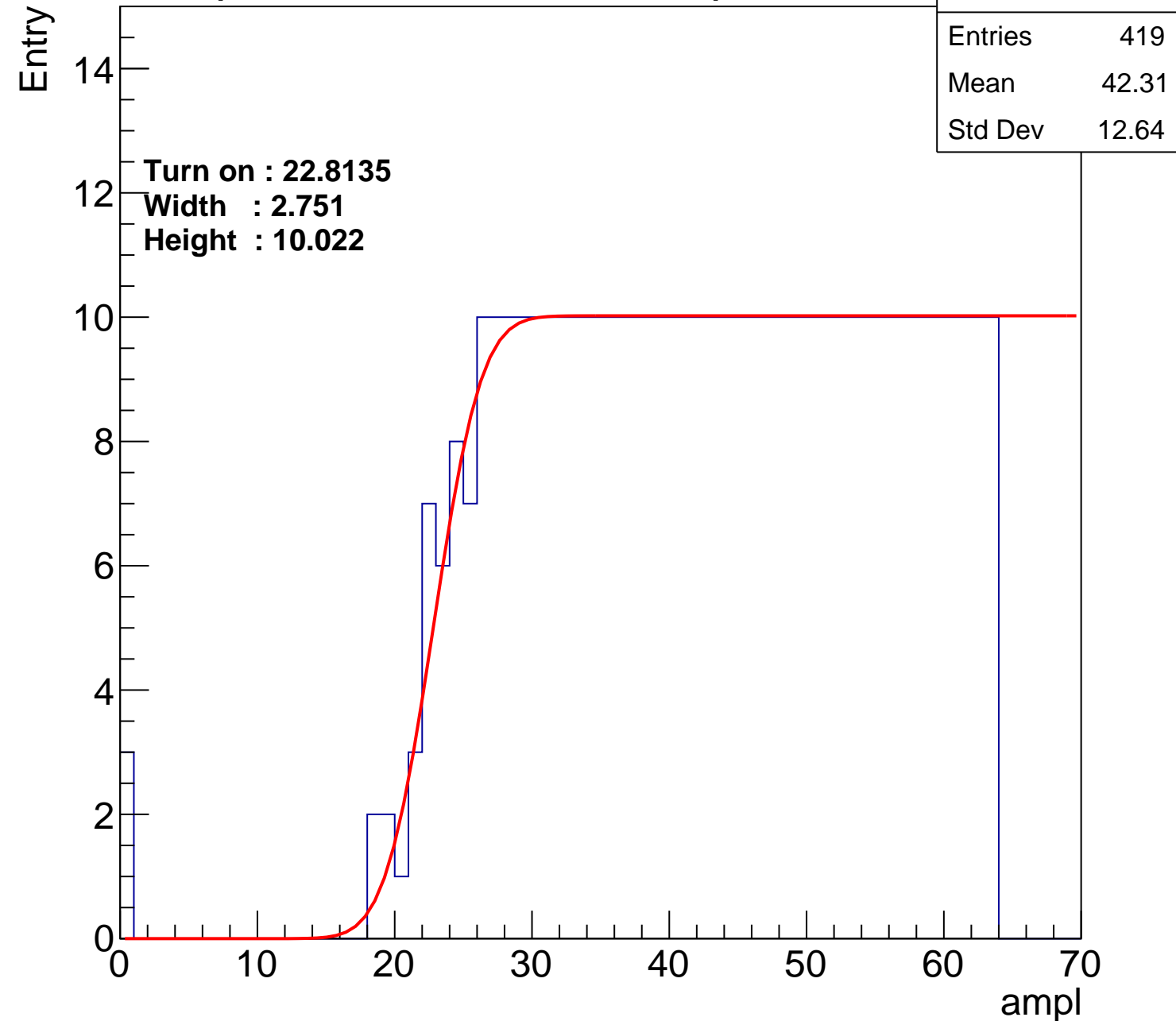
Width : 2.751

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.14
Std Dev	11.68

Turn on : 26.1336

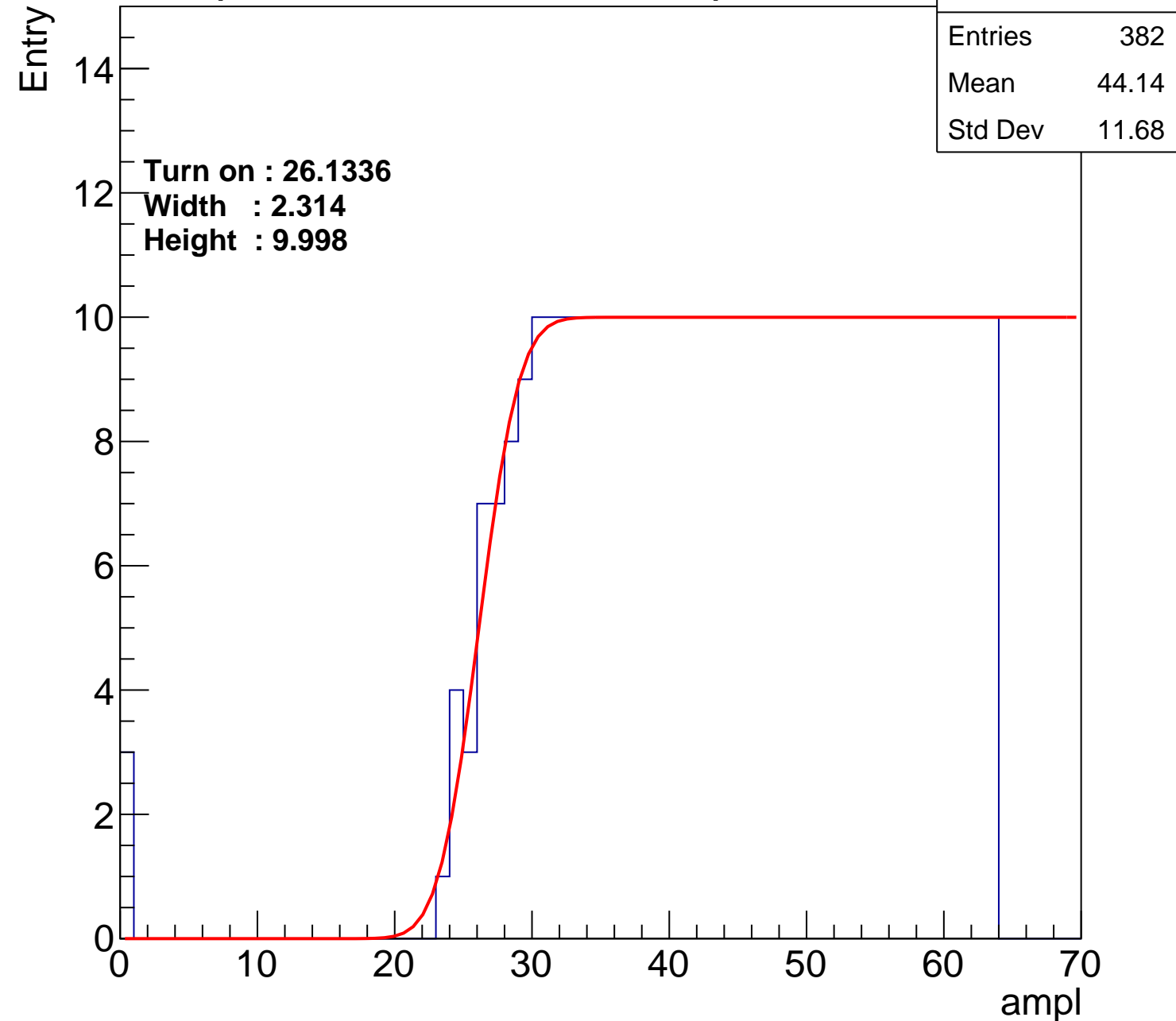
Width : 2.314

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch8

calib_packv5_042523_0143.root, FC#10, port B3

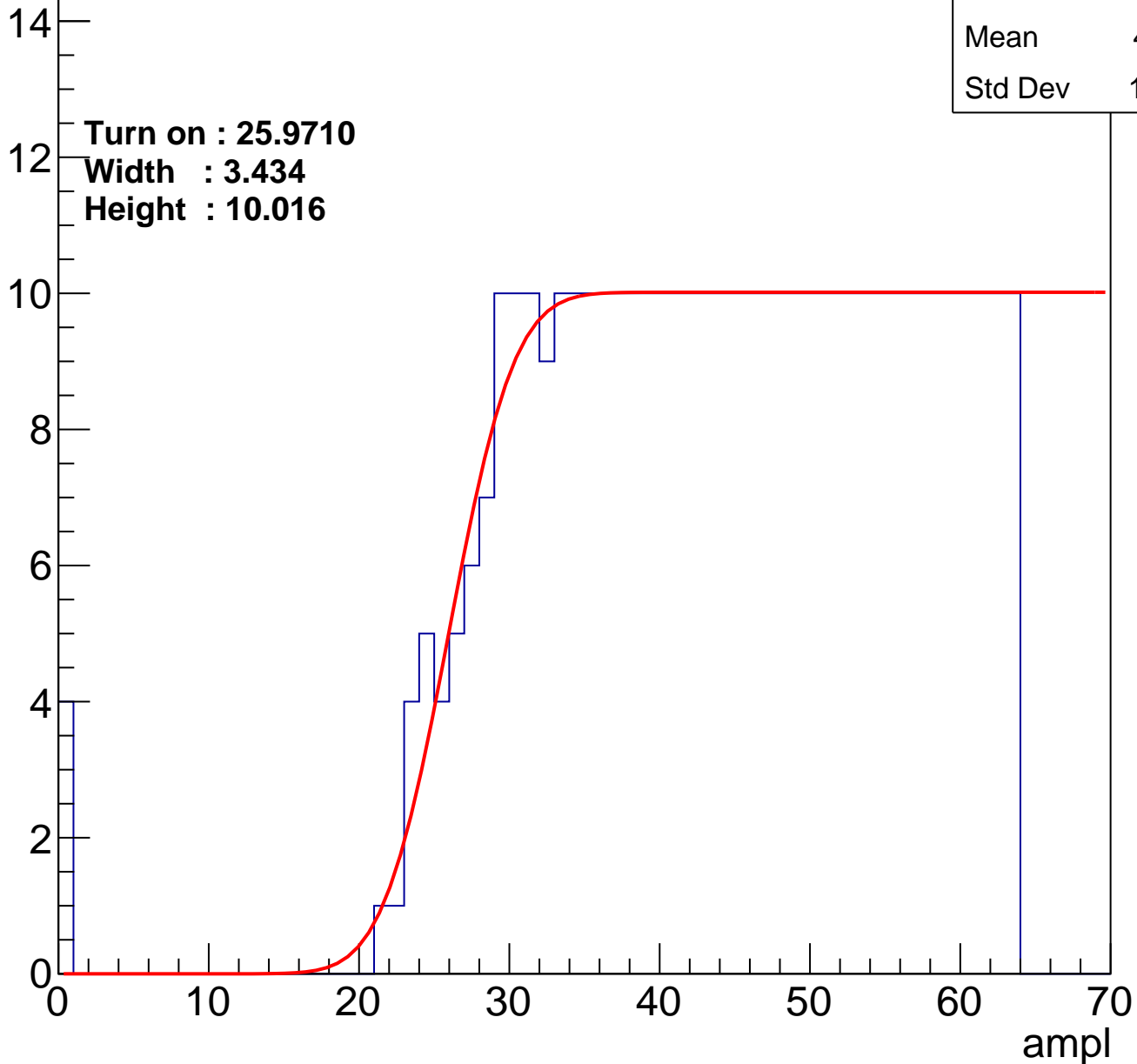
Entries	386
Mean	43.81
Std Dev	12.05

Turn on : 25.9710

Width : 3.434

Height : 10.016

Entry



B1L002S, U17-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.45
Std Dev	11.91

Turn on : 24.3966

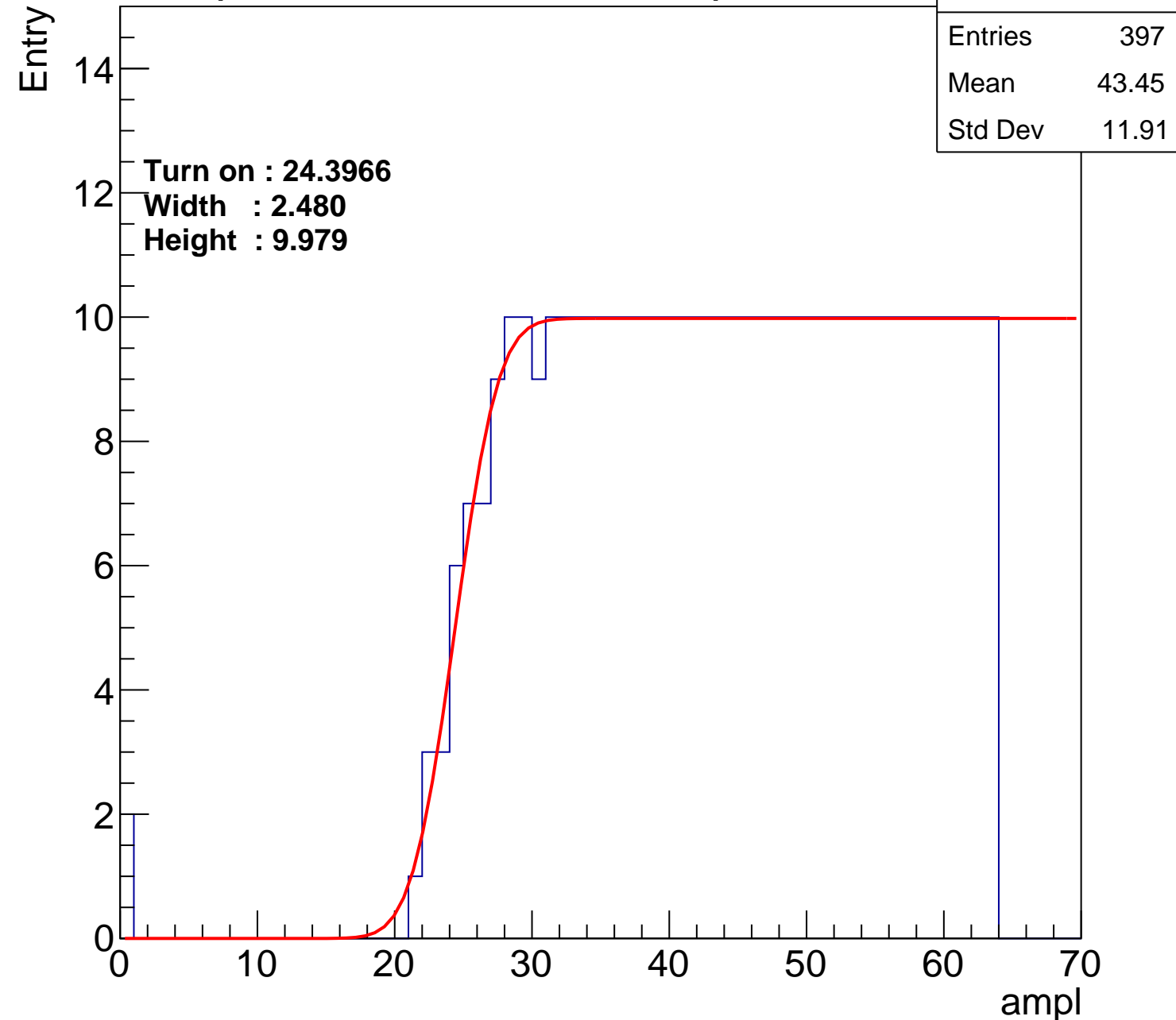
Width : 2.480

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch10

calib_packv5_042523_0143.root, FC#10, port B3

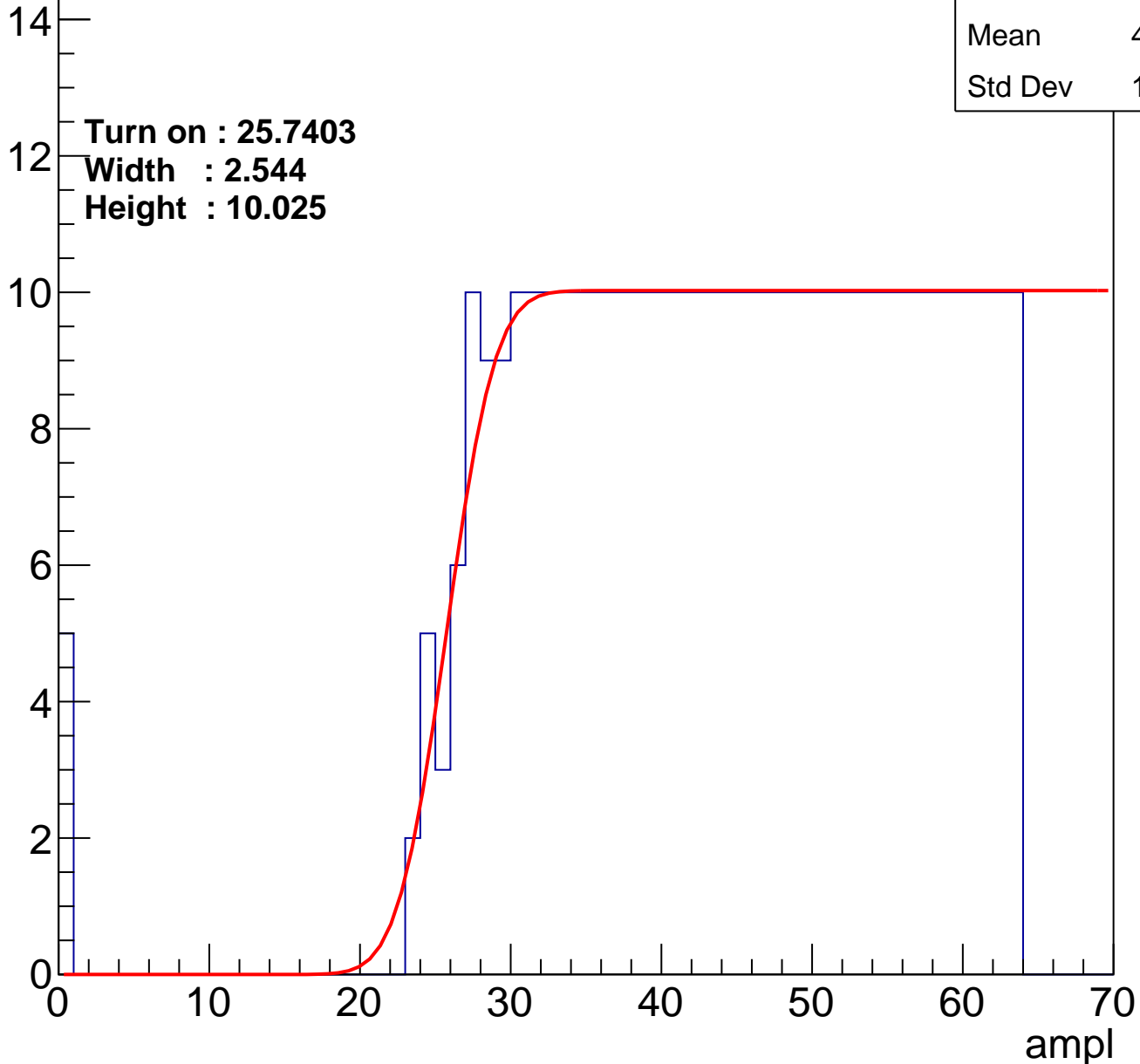
Entries	389
Mean	43.68
Std Dev	12.17

Turn on : 25.7403

Width : 2.544

Height : 10.025

Entry



B1L002S, U17-ch11

calib_packv5_042523_0143.root, FC#10, port B3

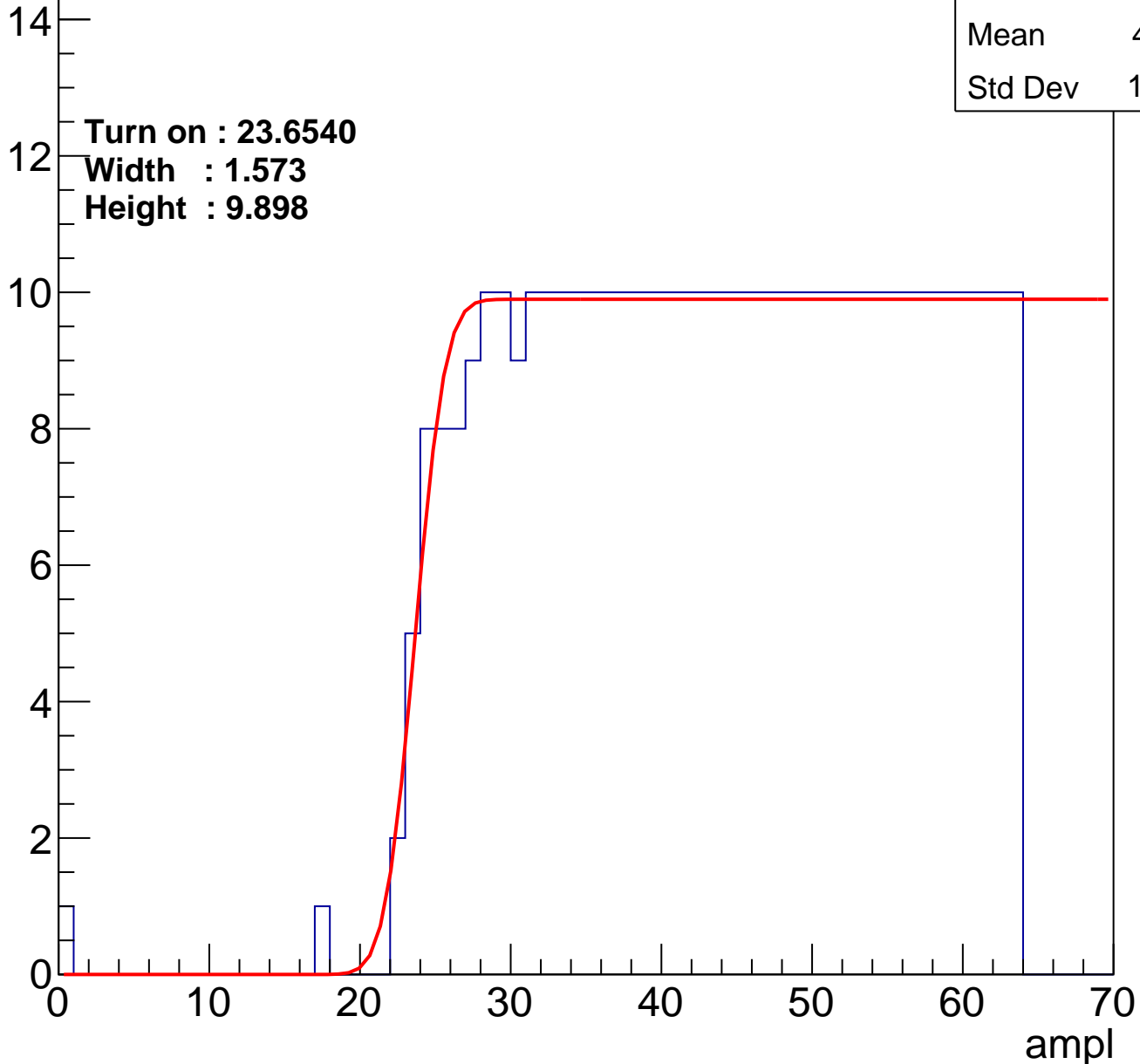
Entries	401
Mean	43.31
Std Dev	11.86

Turn on : 23.6540

Width : 1.573

Height : 9.898

Entry



B1L002S, U17-ch12

calib_packv5_042523_0143.root, FC#10, port B3

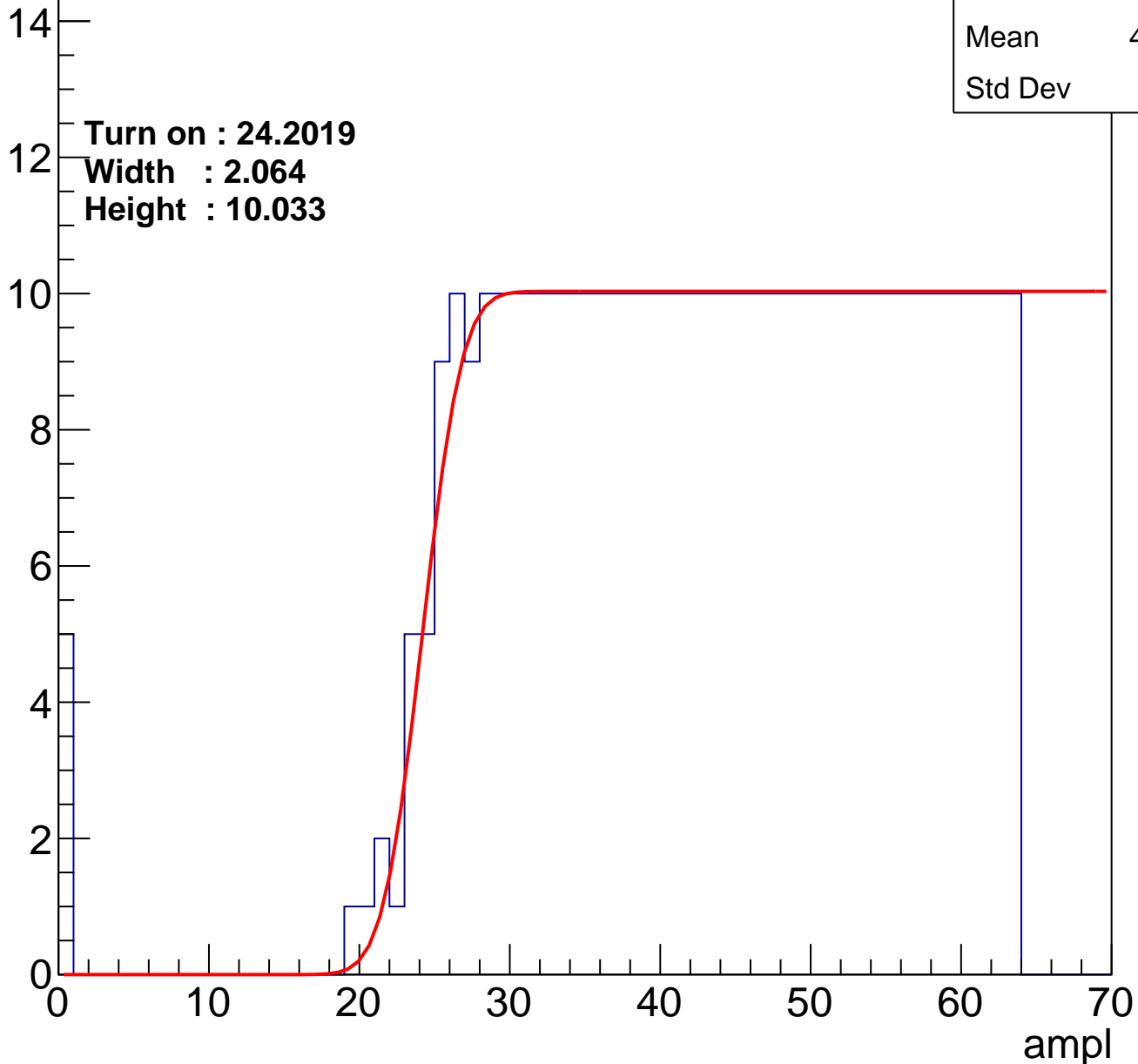
Entries	408
Mean	42.76
Std Dev	12.6

Turn on : 24.2019

Width : 2.064

Height : 10.033

Entry



B1L002S, U17-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 26.9283

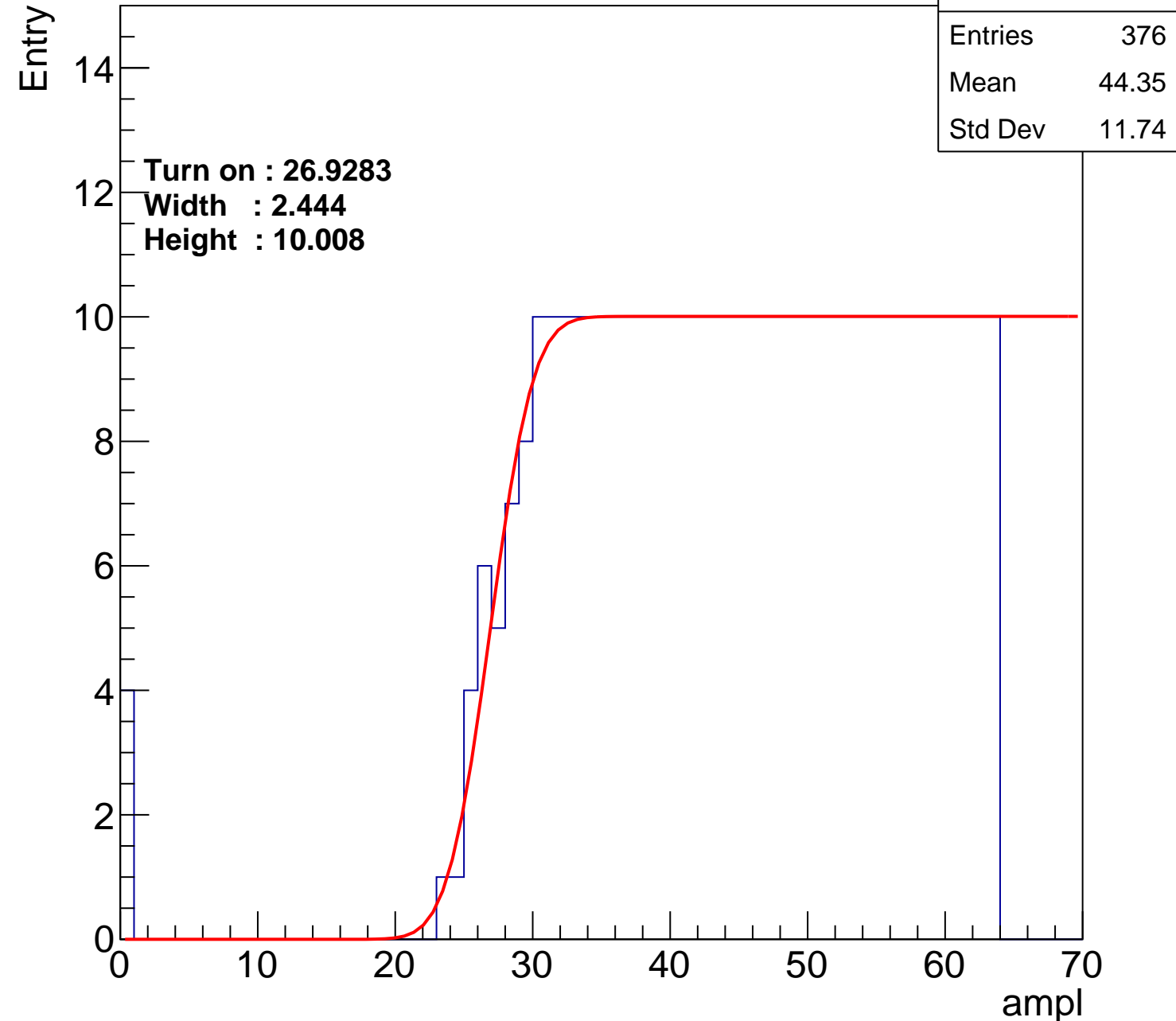
Width : 2.444

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch14

calib_packv5_042523_0143.root, FC#10, port B3

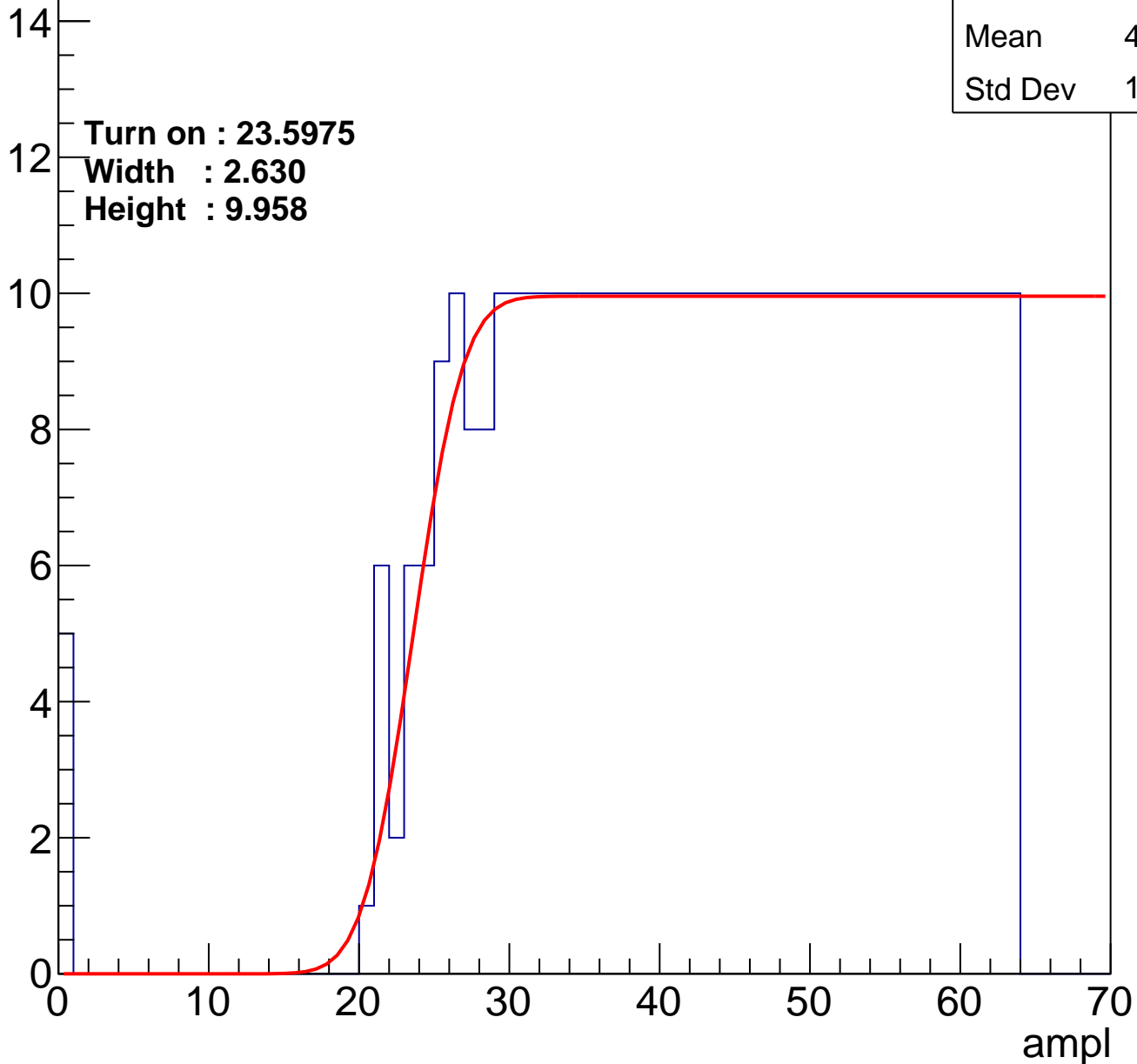
Entries	411
Mean	42.57
Std Dev	12.73

Turn on : 23.5975

Width : 2.630

Height : 9.958

Entry



B1L002S, U17-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.7
Std Dev	12.08

Turn on : 25.8995

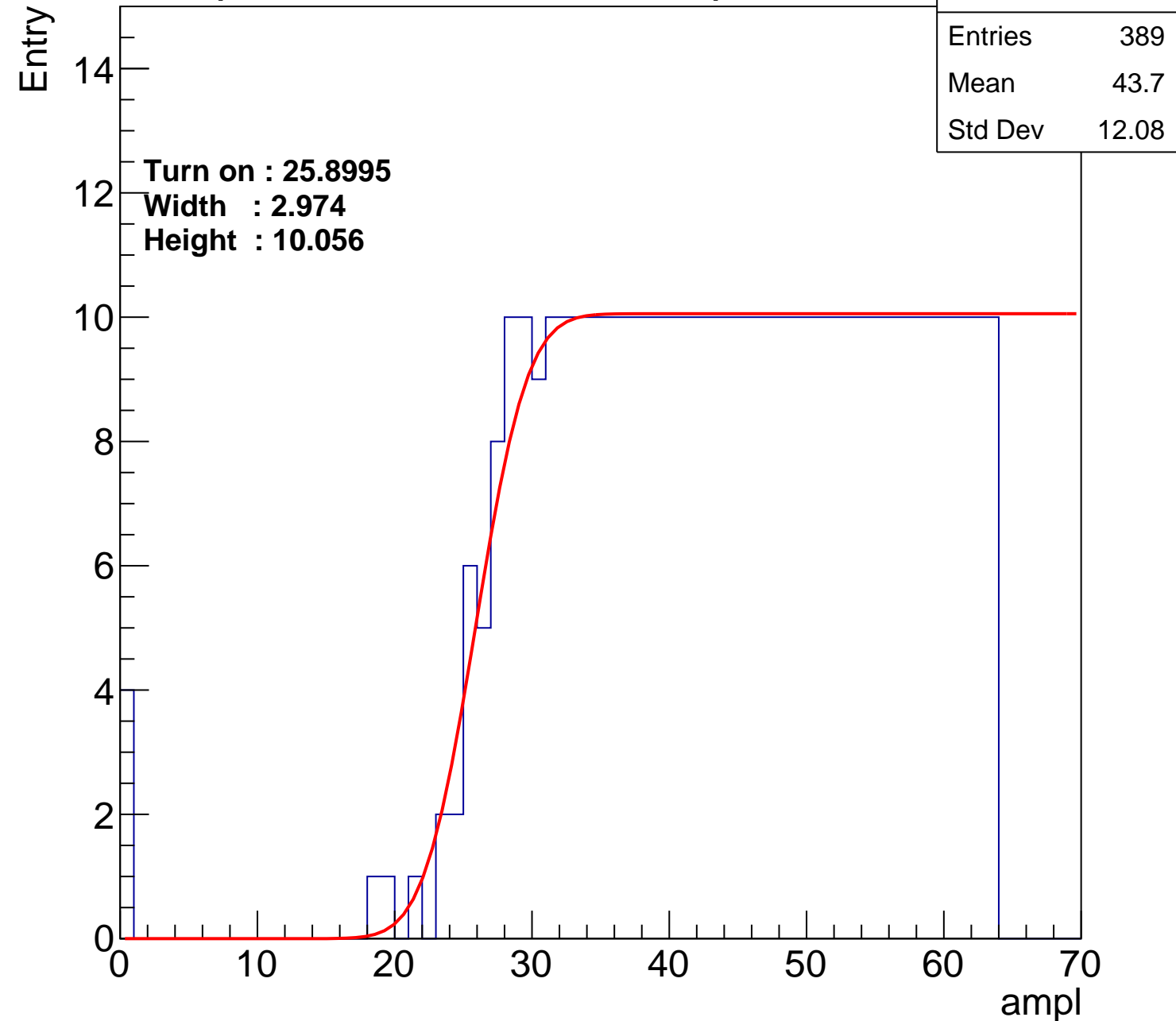
Width : 2.974

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.31
Std Dev	12.37

Turn on : 24.7114

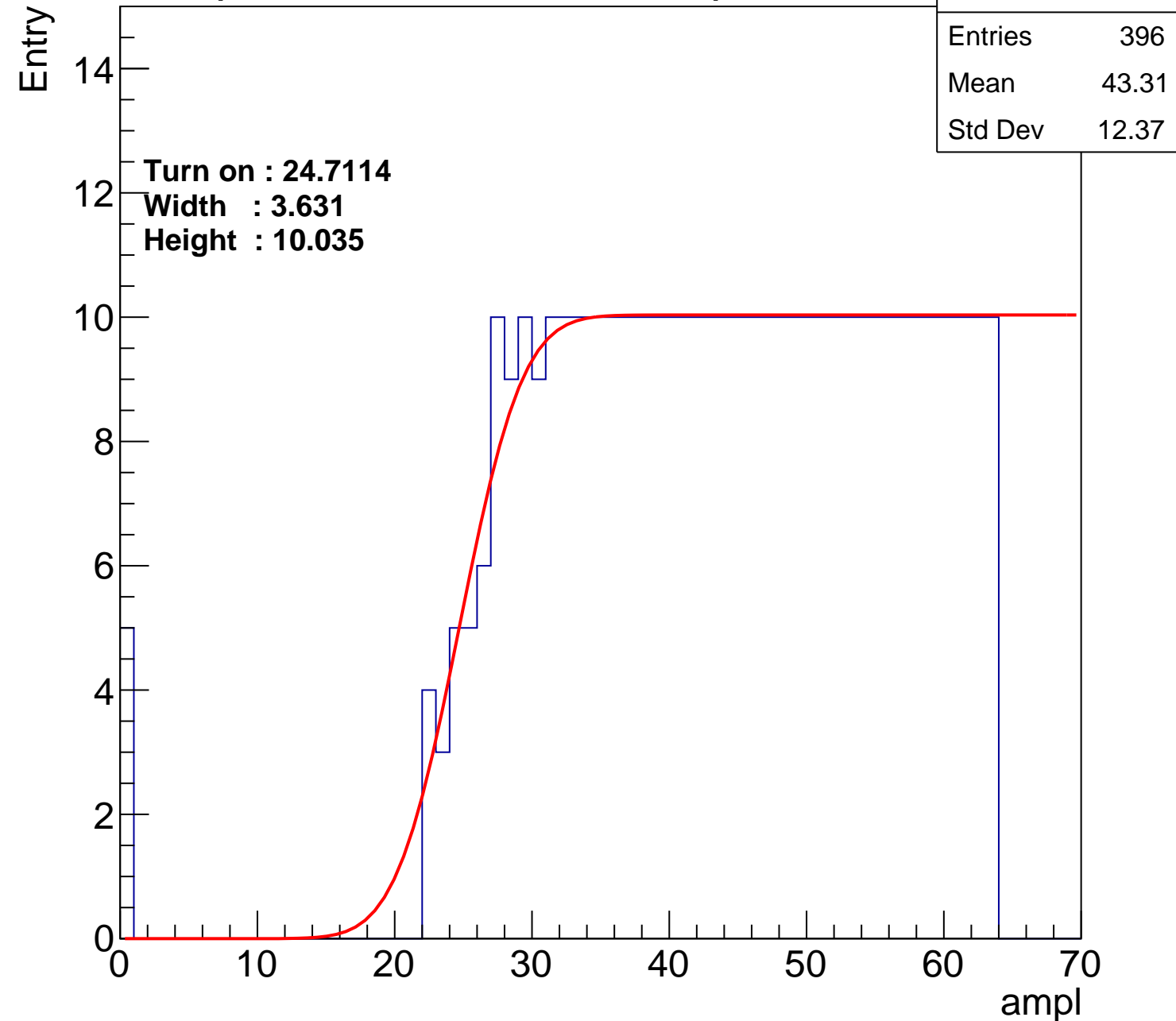
Width : 3.631

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.3
Std Dev	11.65

Turn on : 26.6865

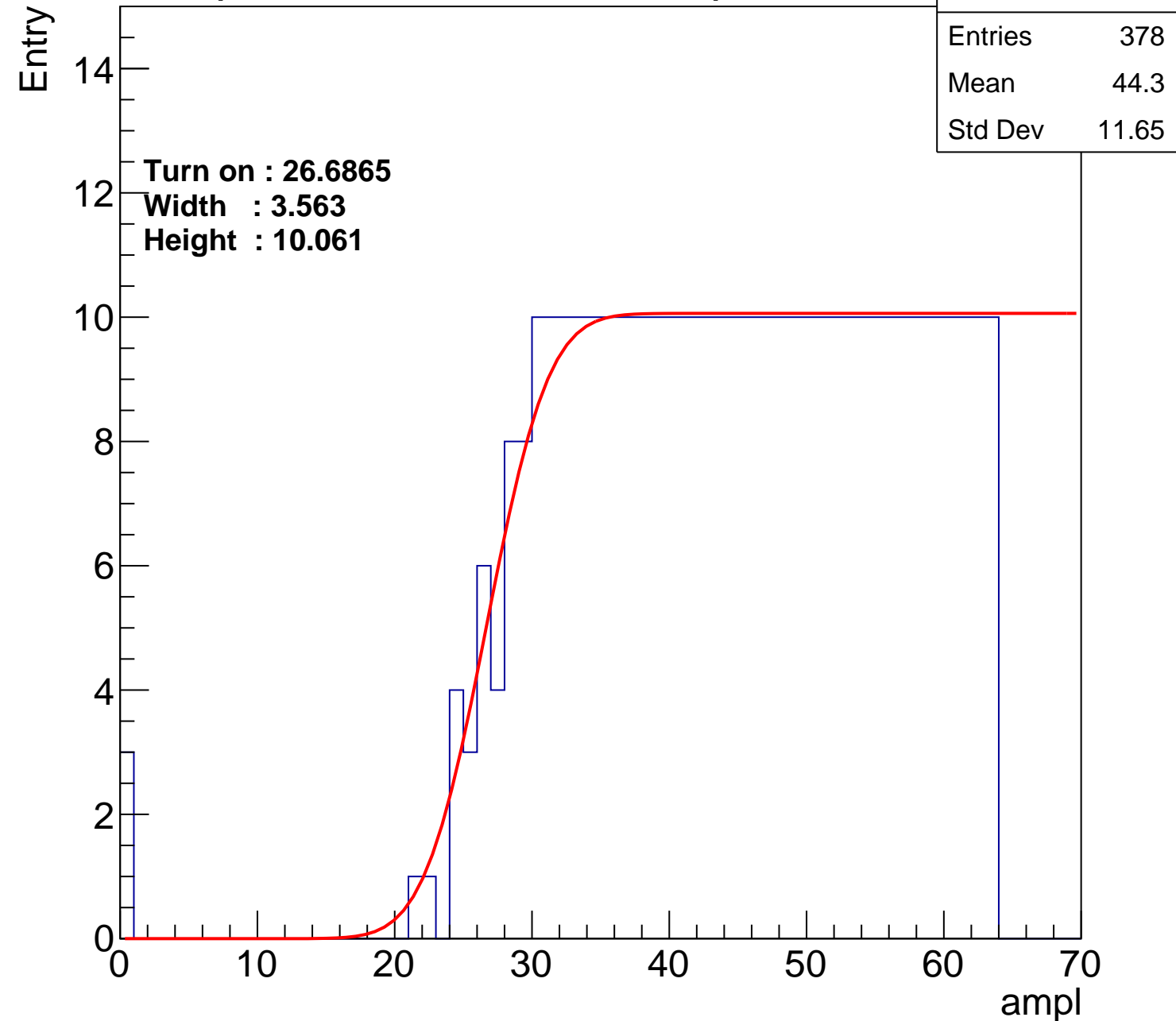
Width : 3.563

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.97
Std Dev	12.27

Turn on : 23.6159

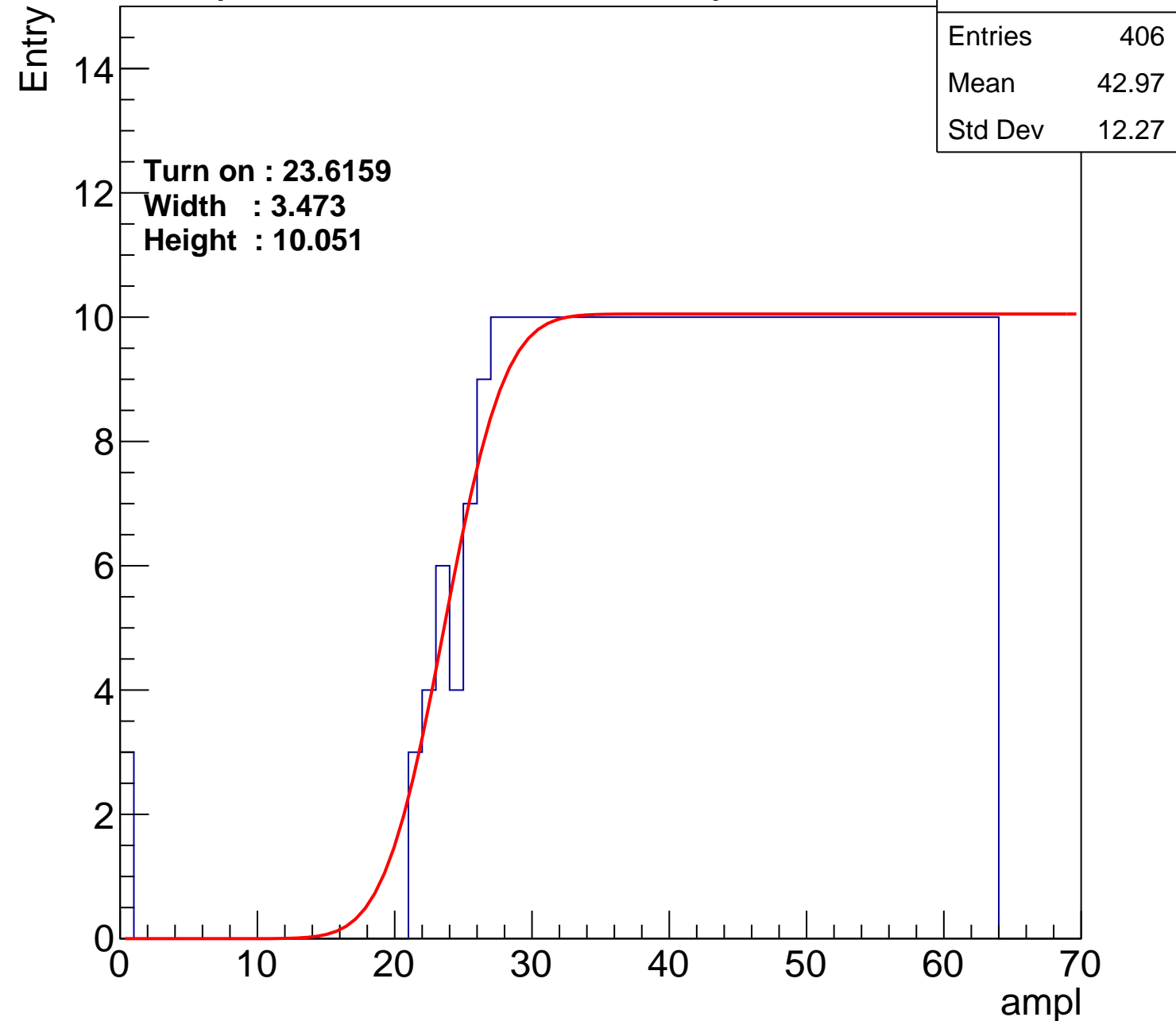
Width : 3.473

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.68
Std Dev	12.1

Turn on : 26.1238

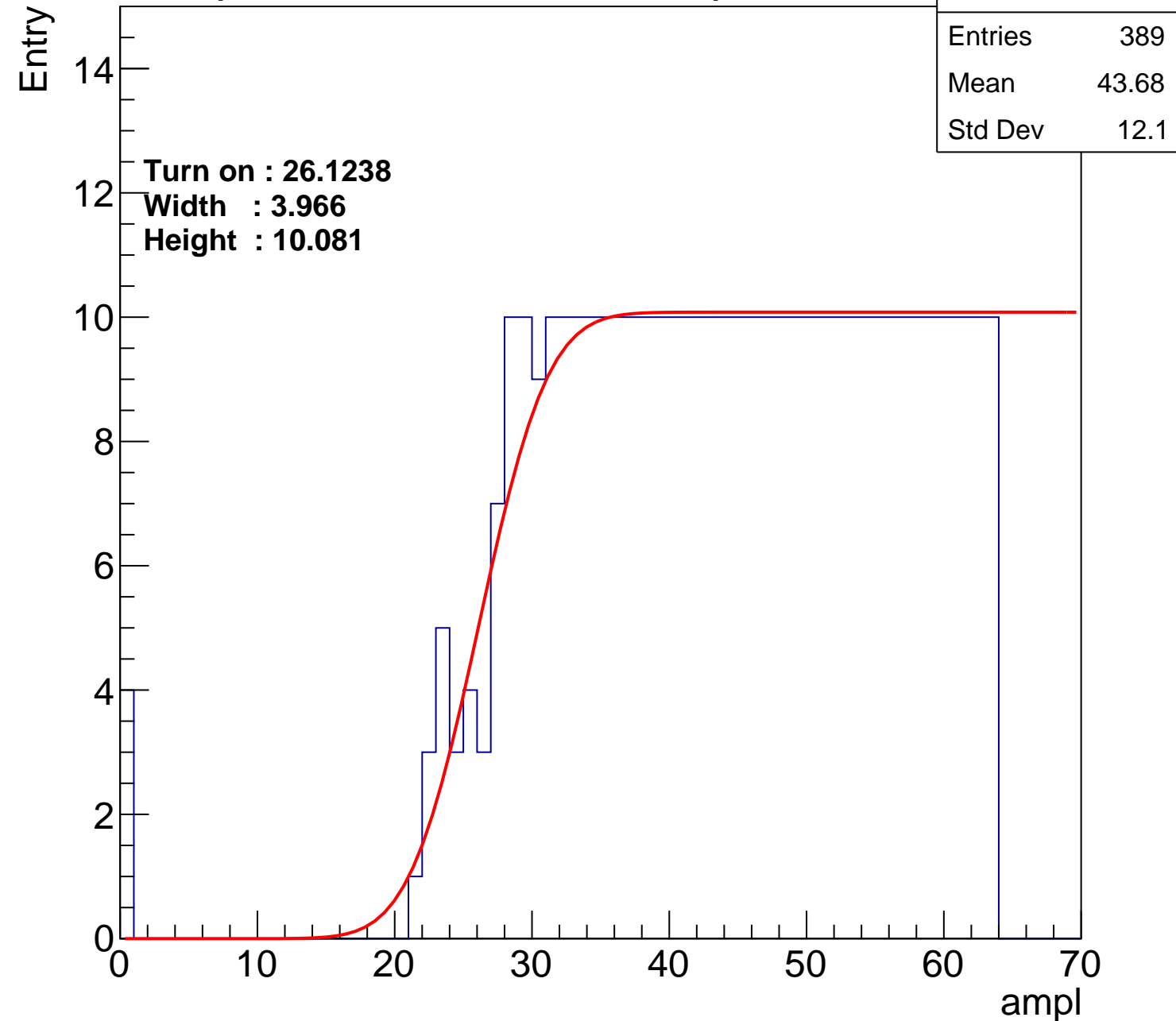
Width : 3.966

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.81

Turn on : 25.5657

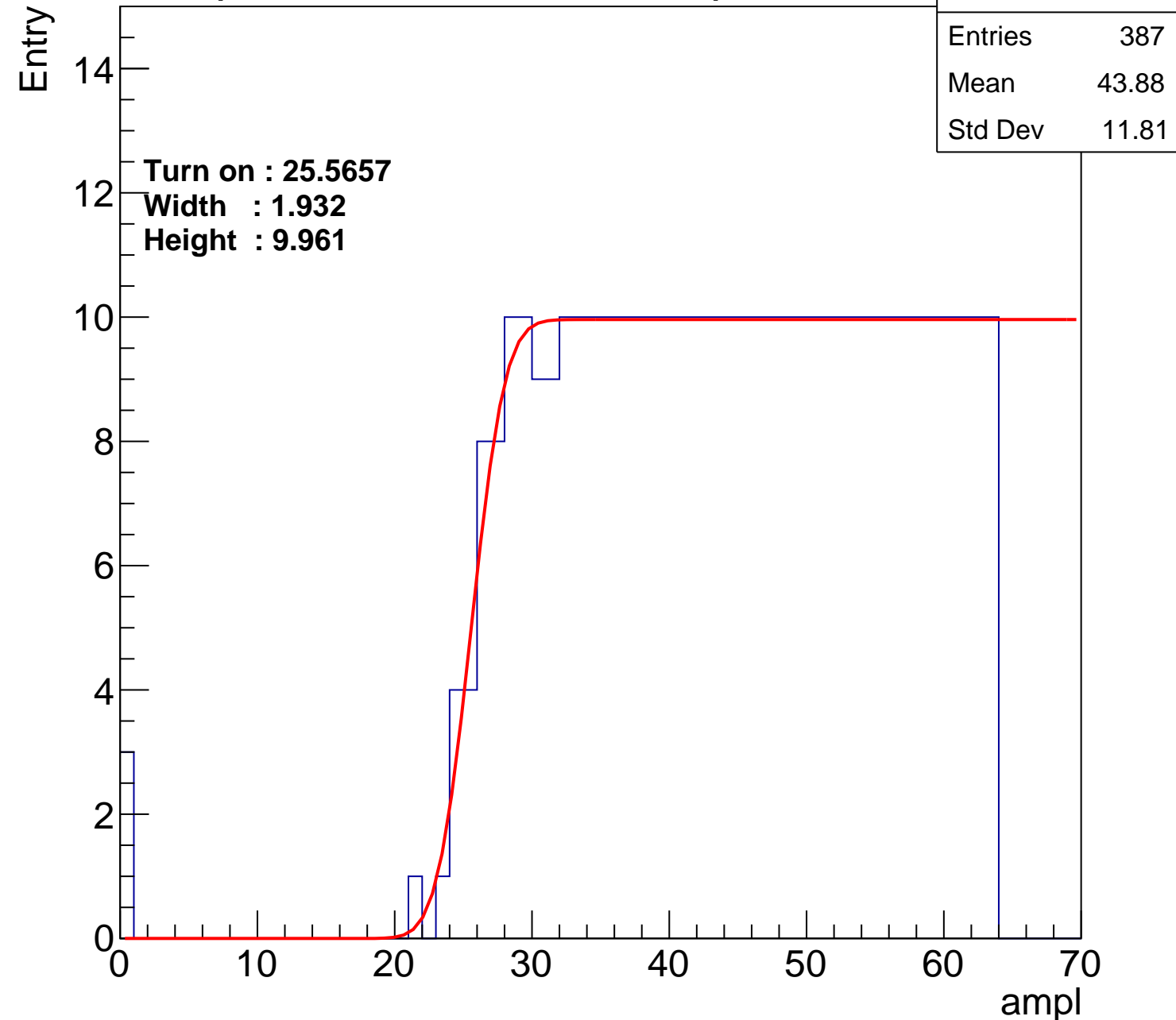
Width : 1.932

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.7109

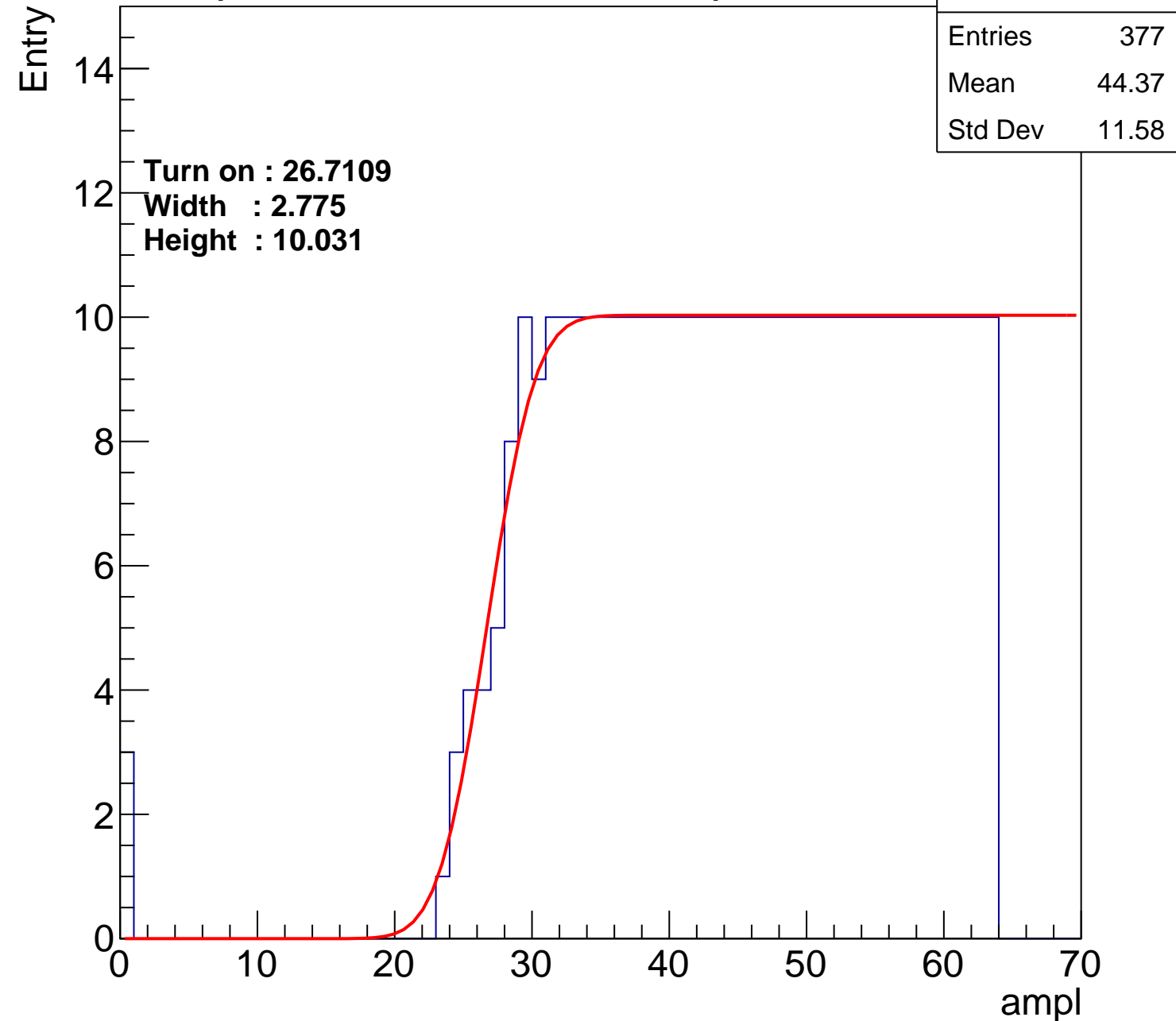
Width : 2.775

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.41
Std Dev	12.32

Turn on : 25.5537

Width : 2.698

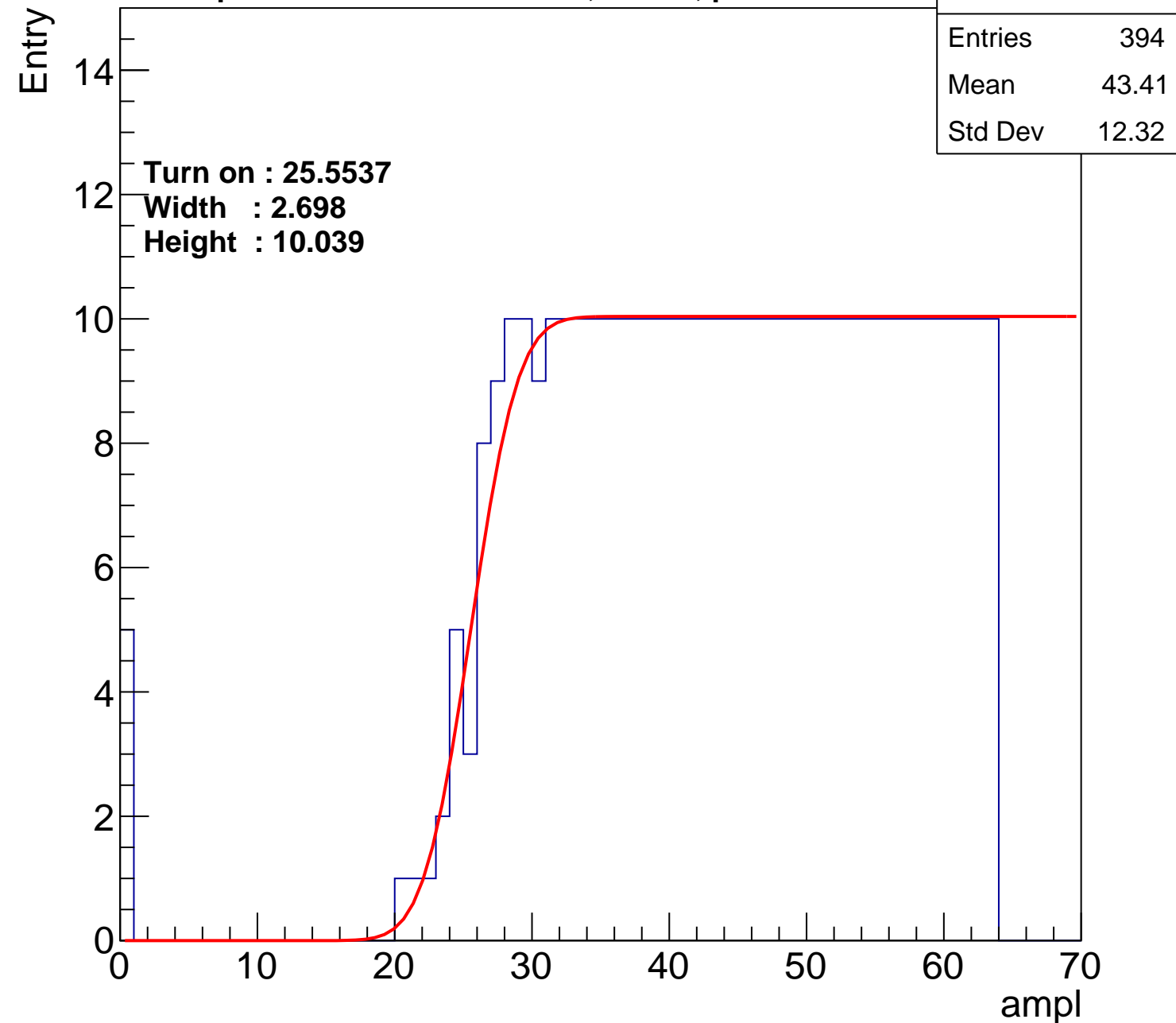
Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U17-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.21
Std Dev	12.04

Turn on : 24.1969

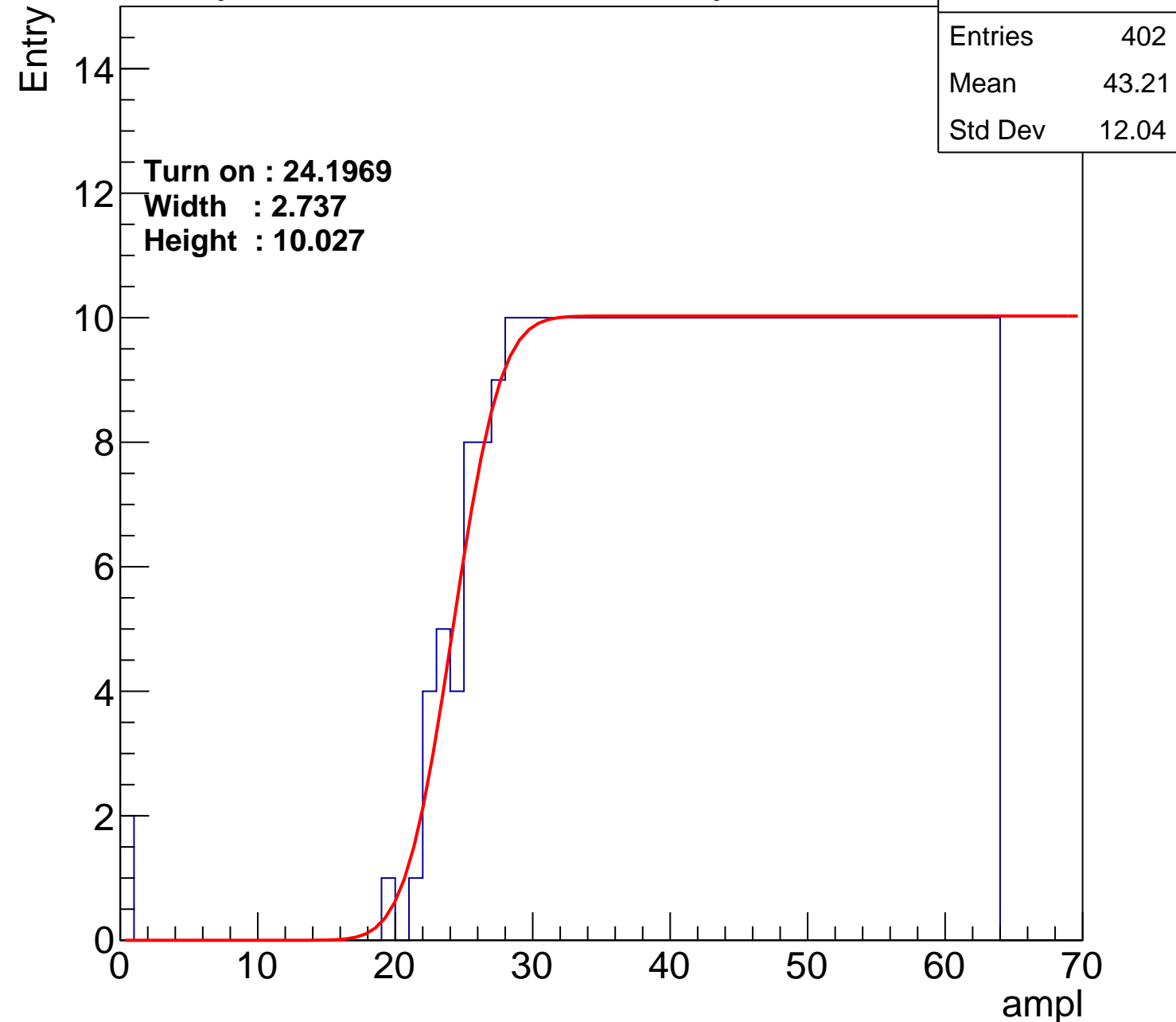
Width : 2.737

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.42
Std Dev	11.96

Turn on : 24.7019

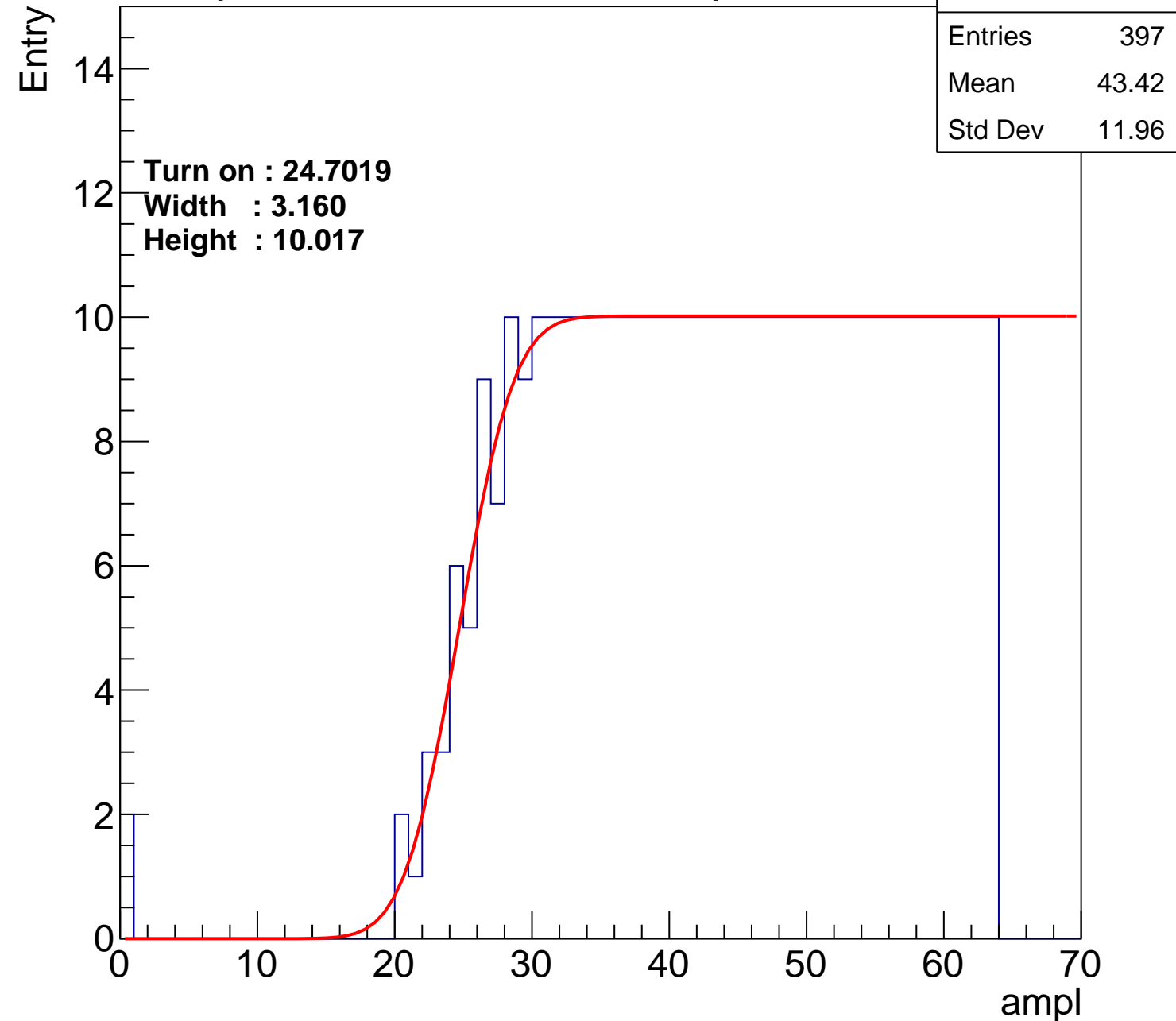
Width : 3.160

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch25

calib_packv5_042523_0143.root, FC#10, port B3

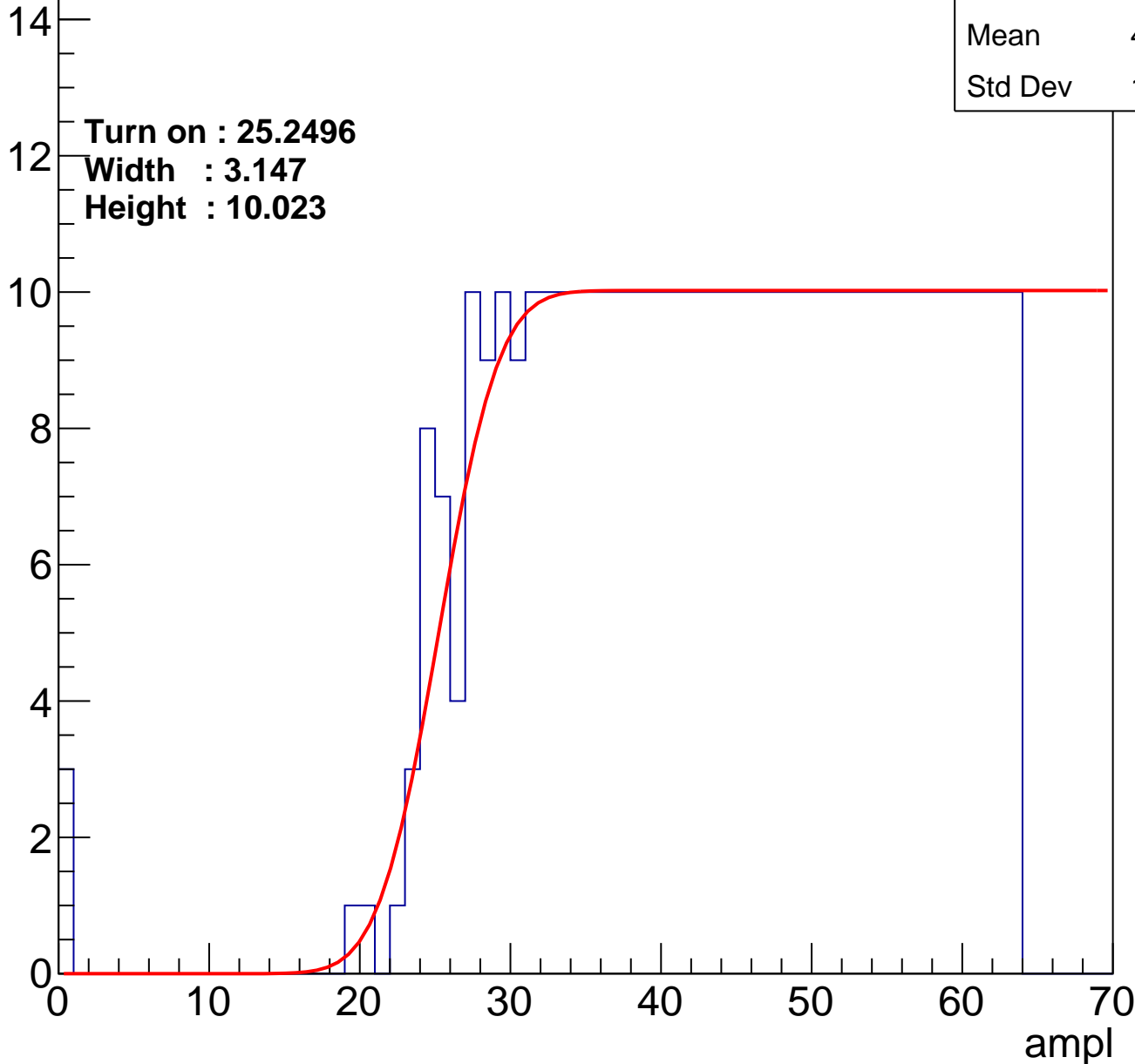
Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 25.2496

Width : 3.147

Height : 10.023

Entry



B1L002S, U17-ch26

calib_packv5_042523_0143.root, FC#10, port B3

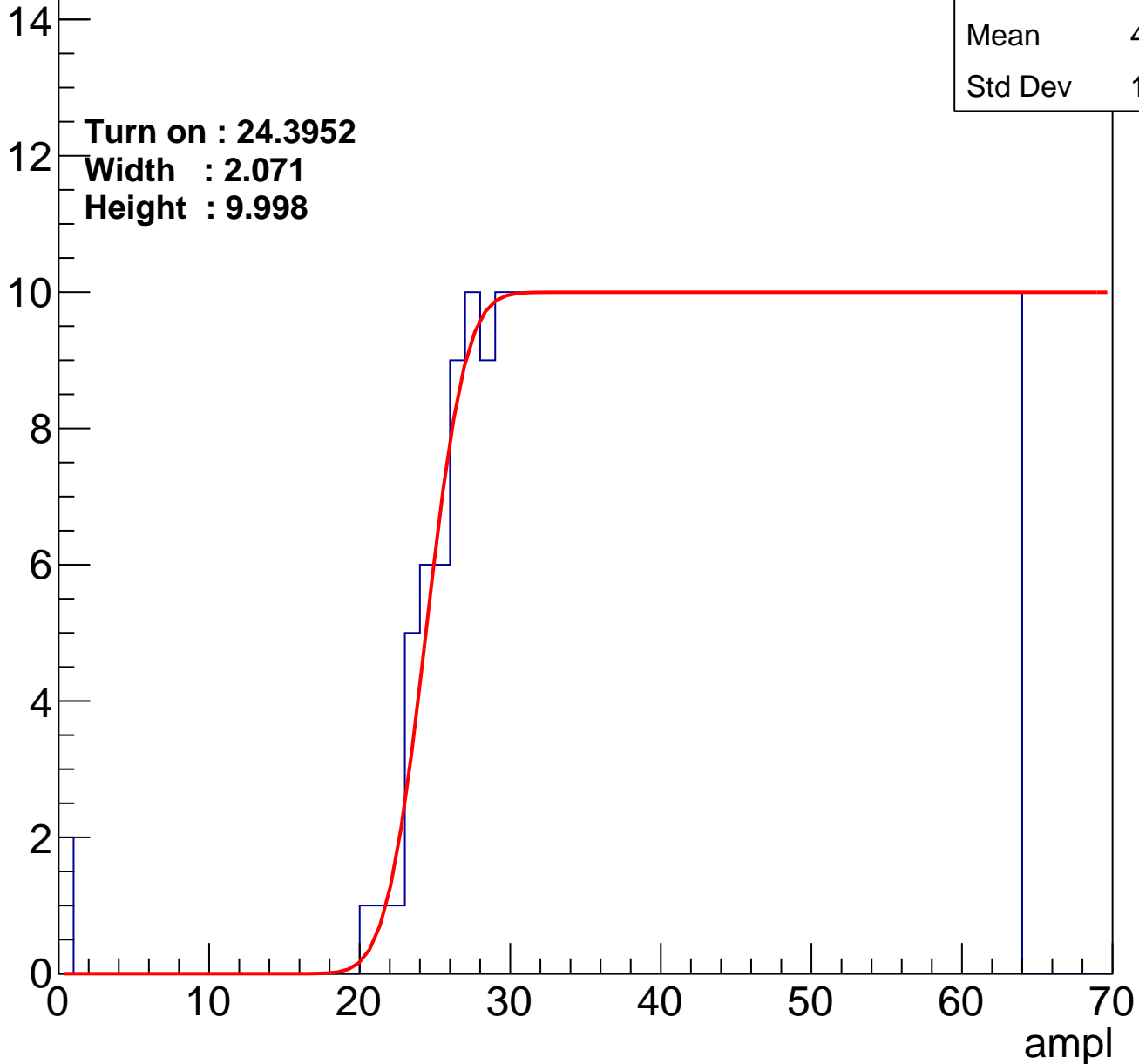
Entries	400
Mean	43.32
Std Dev	11.96

Turn on : 24.3952

Width : 2.071

Height : 9.998

Entry



B1L002S, U17-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.82
Std Dev	11.99

Turn on : 25.8968

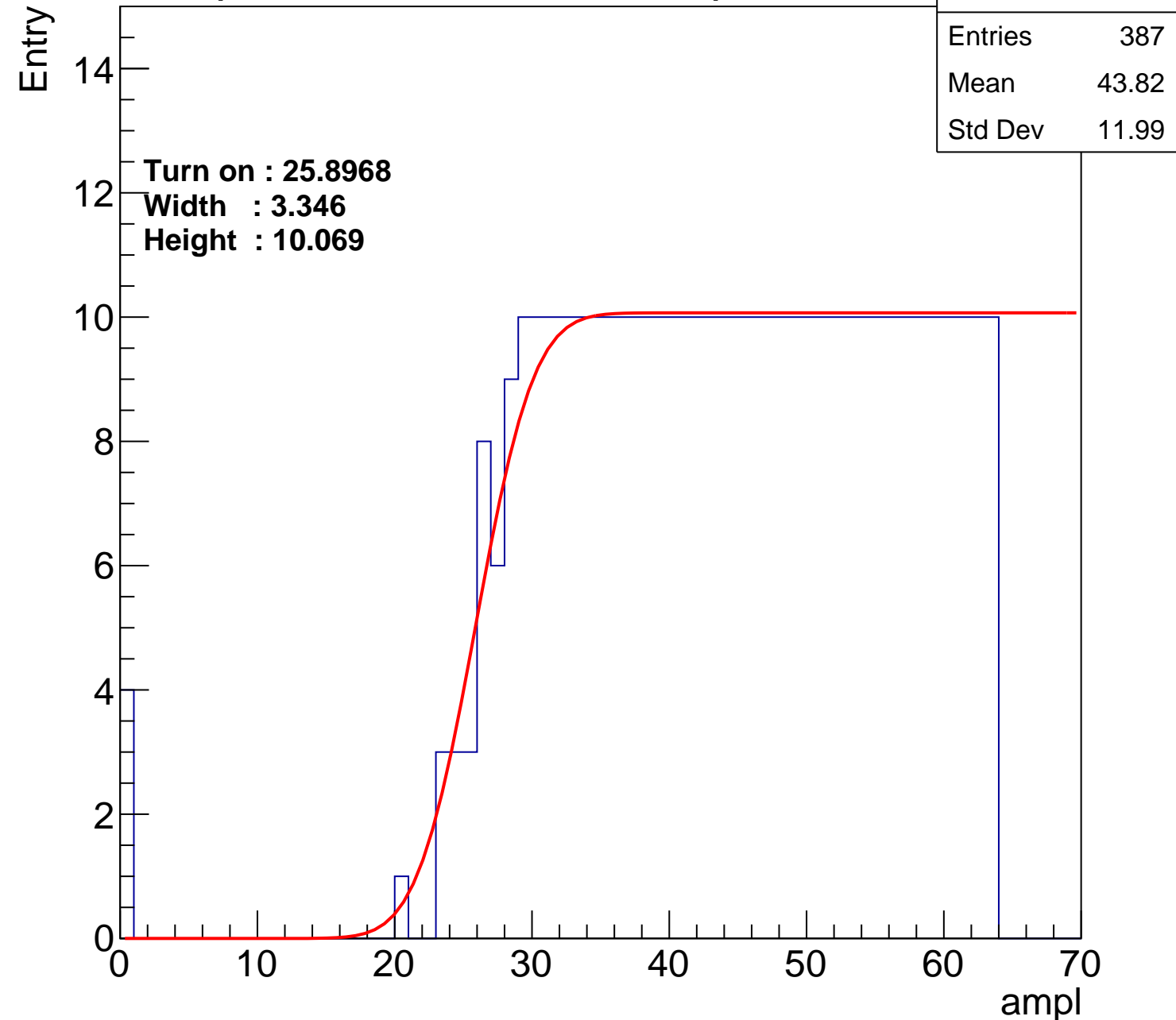
Width : 3.346

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch28

calib_packv5_042523_0143.root, FC#10, port B3

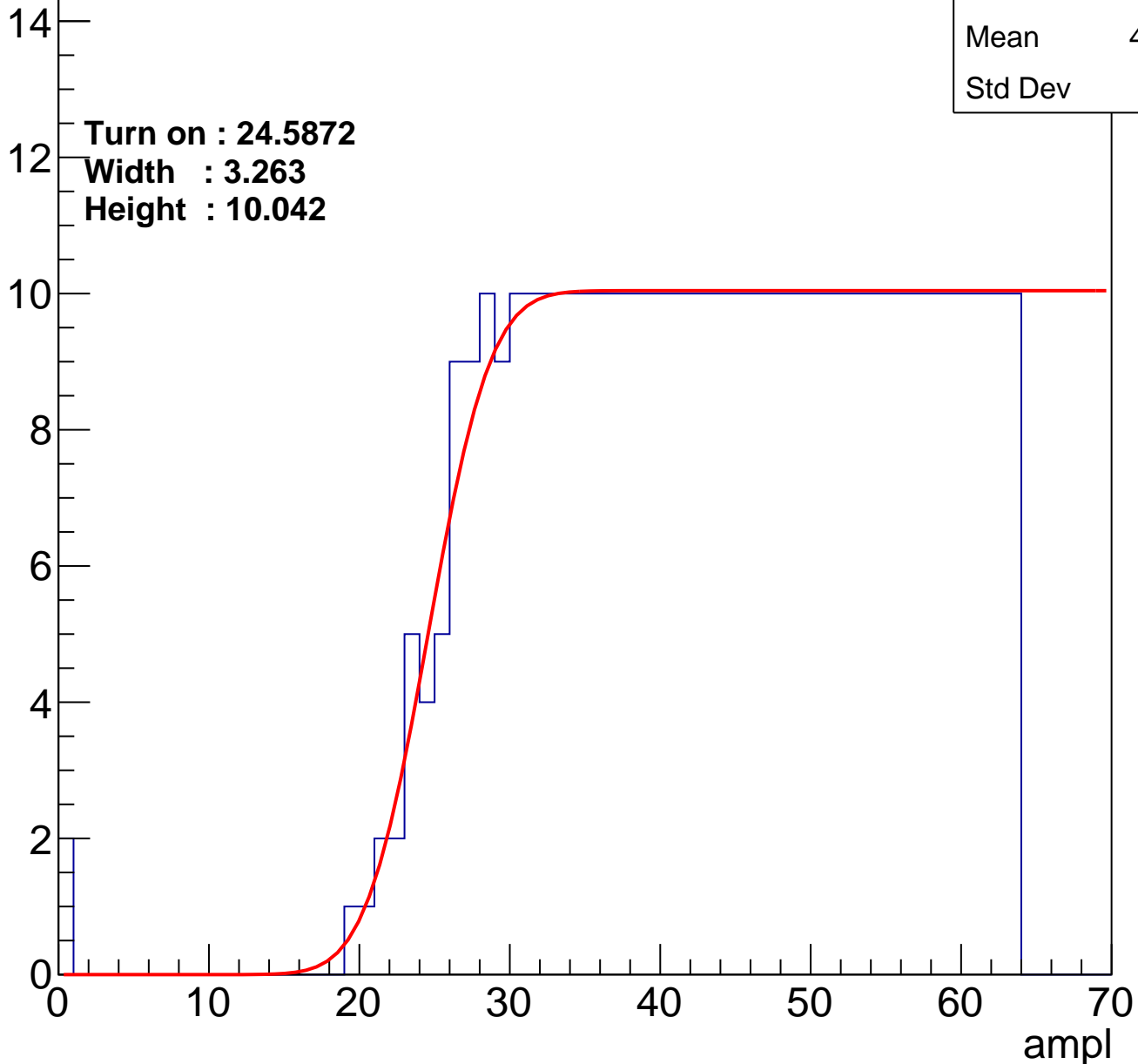
Entries	399
Mean	43.33
Std Dev	12

Turn on : 24.5872

Width : 3.263

Height : 10.042

Entry



B1L002S, U17-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.08
Std Dev	12.35

Turn on : 24.2476

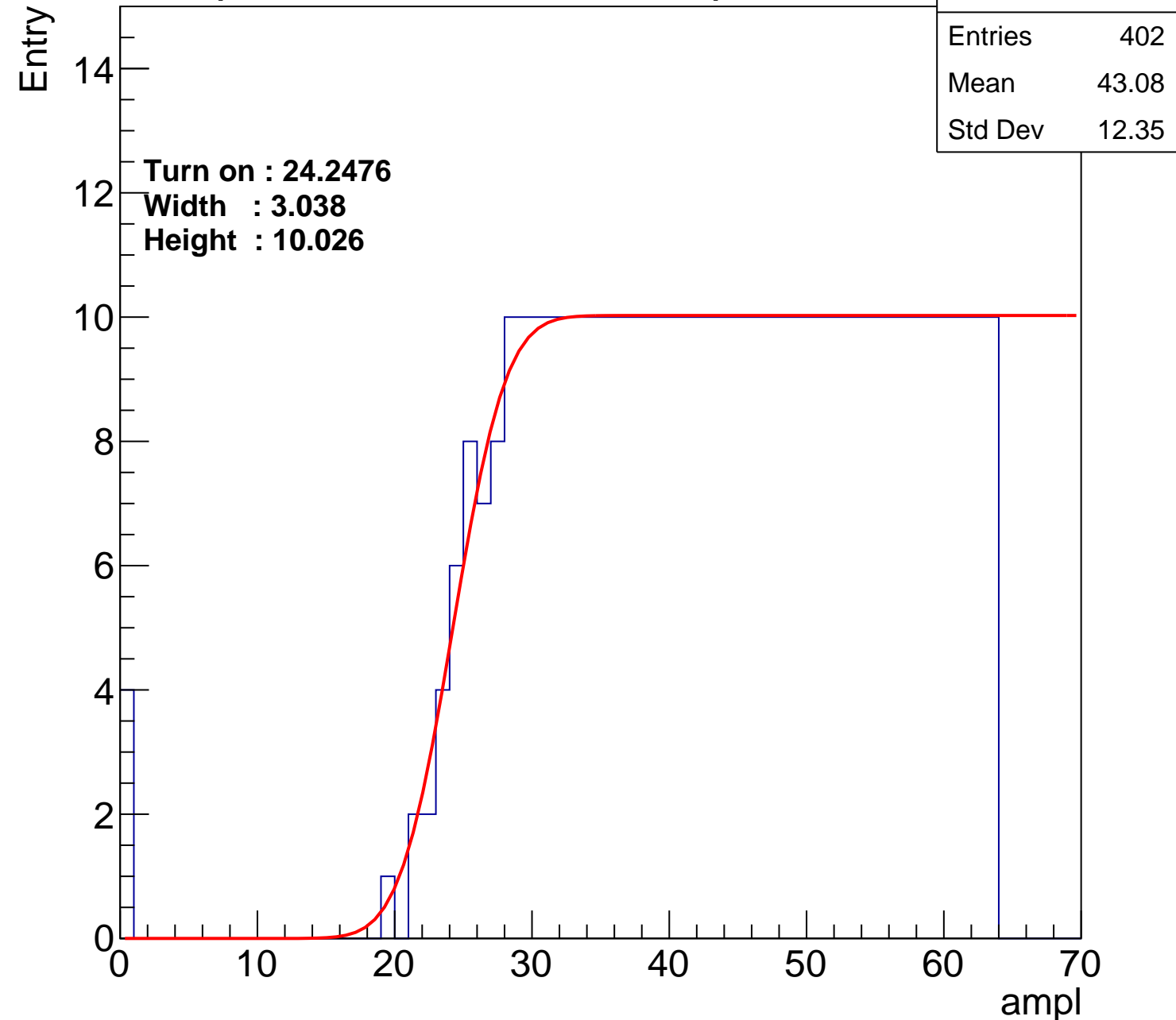
Width : 3.038

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch30

calib_packv5_042523_0143.root, FC#10, port B3

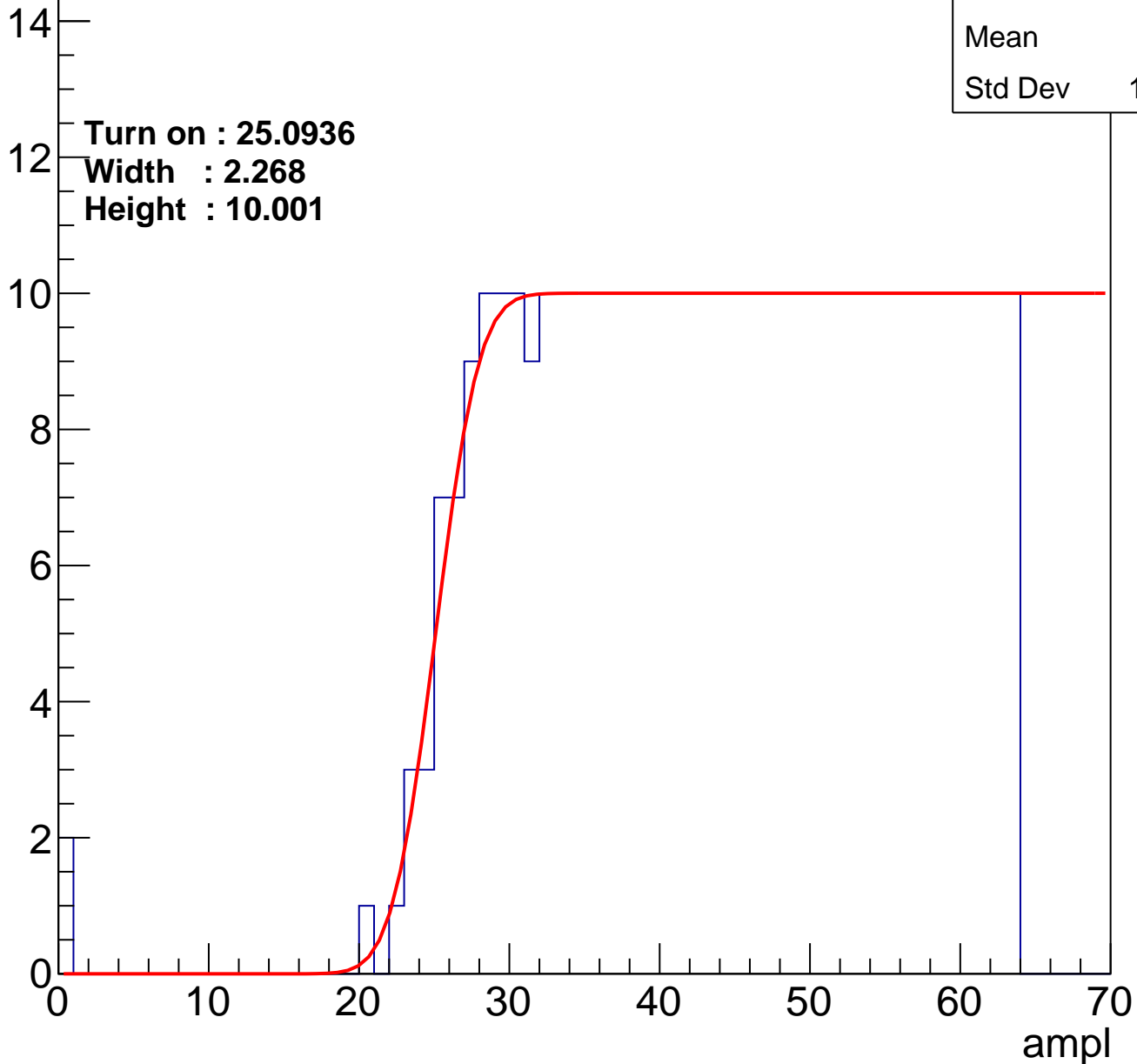
Entries	392
Mean	43.7
Std Dev	11.77

Turn on : 25.0936

Width : 2.268

Height : 10.001

Entry



B1L002S, U17-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 24.9837

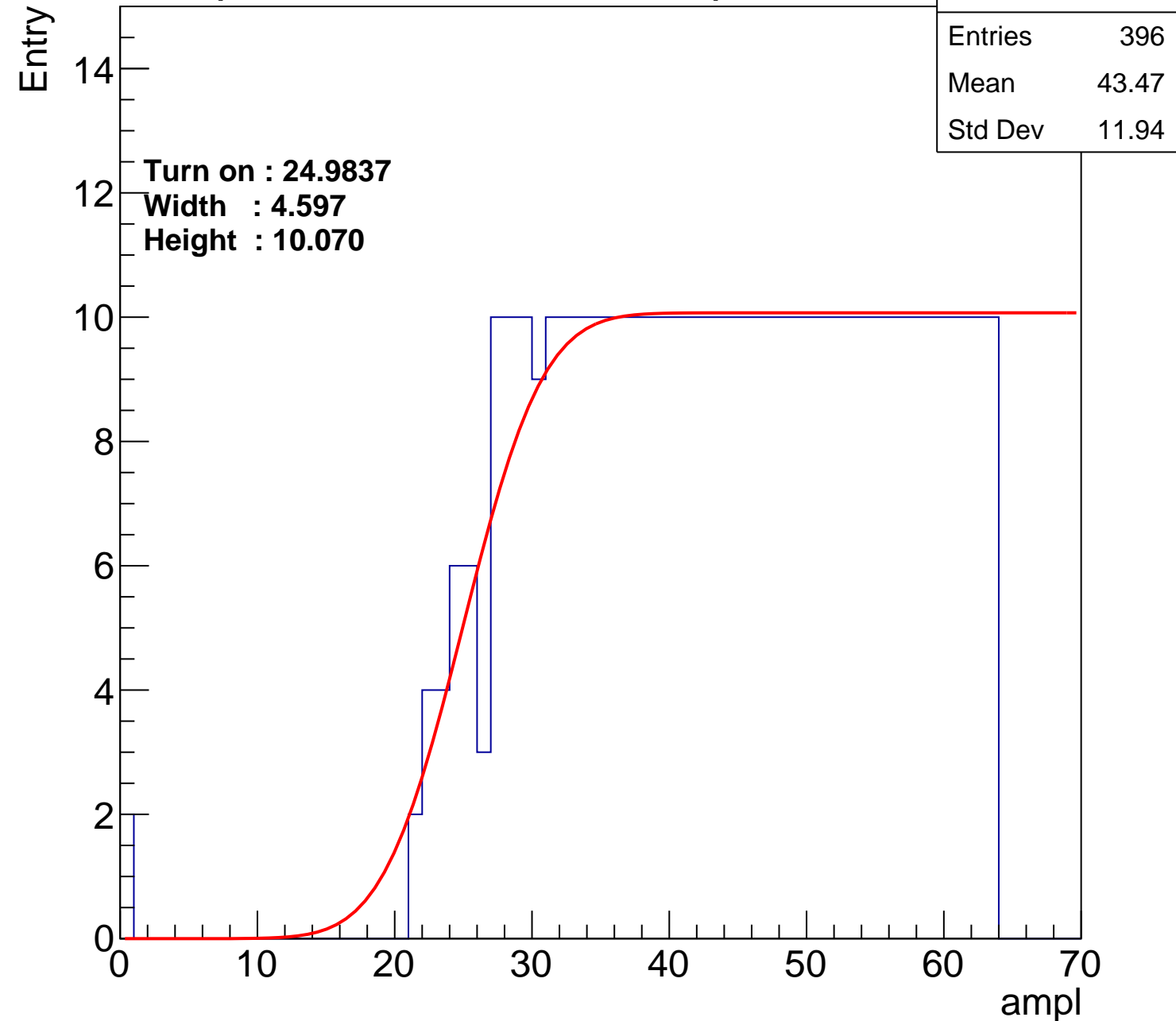
Width : 4.597

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.1230

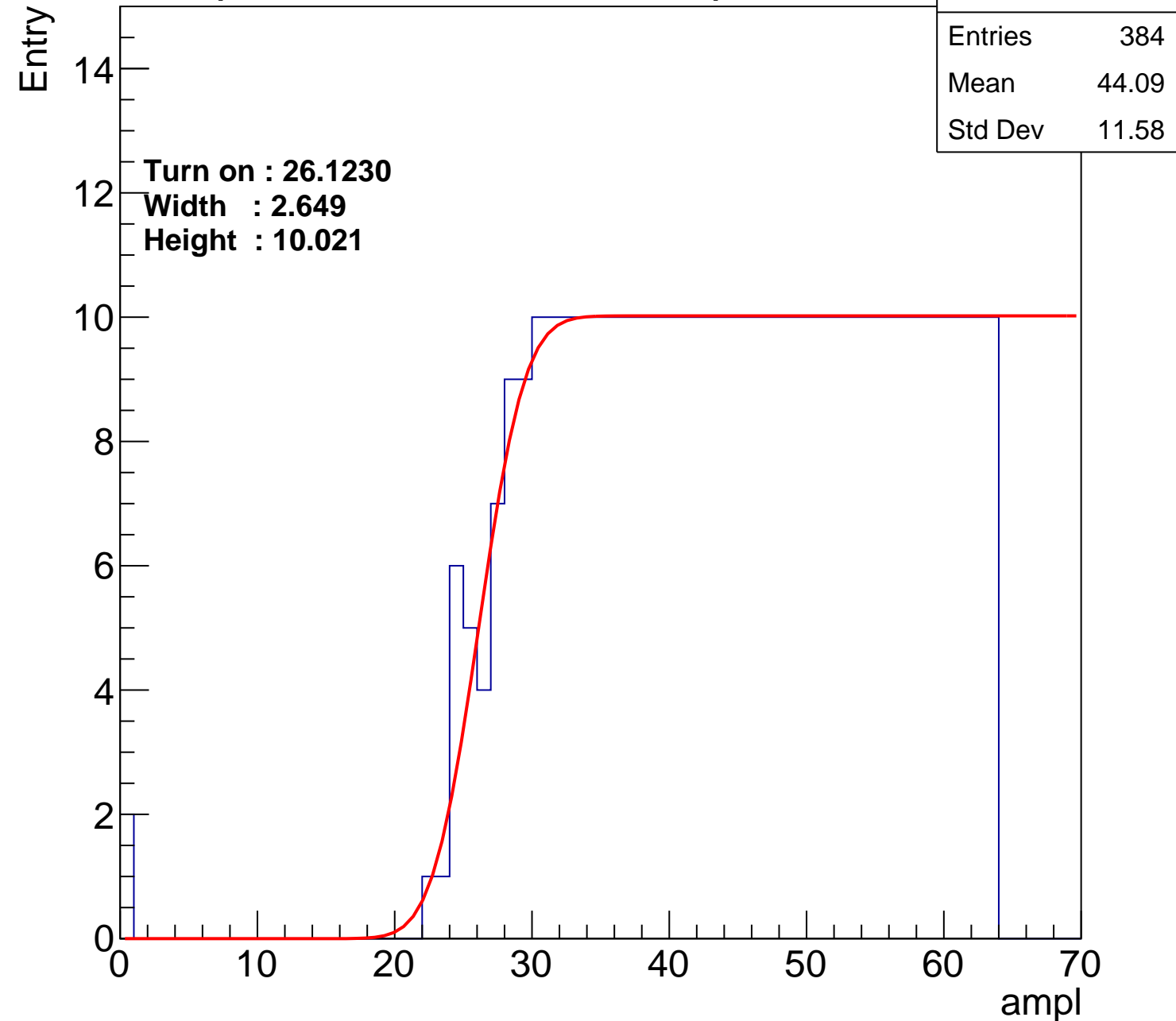
Width : 2.649

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch33

calib_packv5_042523_0143.root, FC#10, port B3

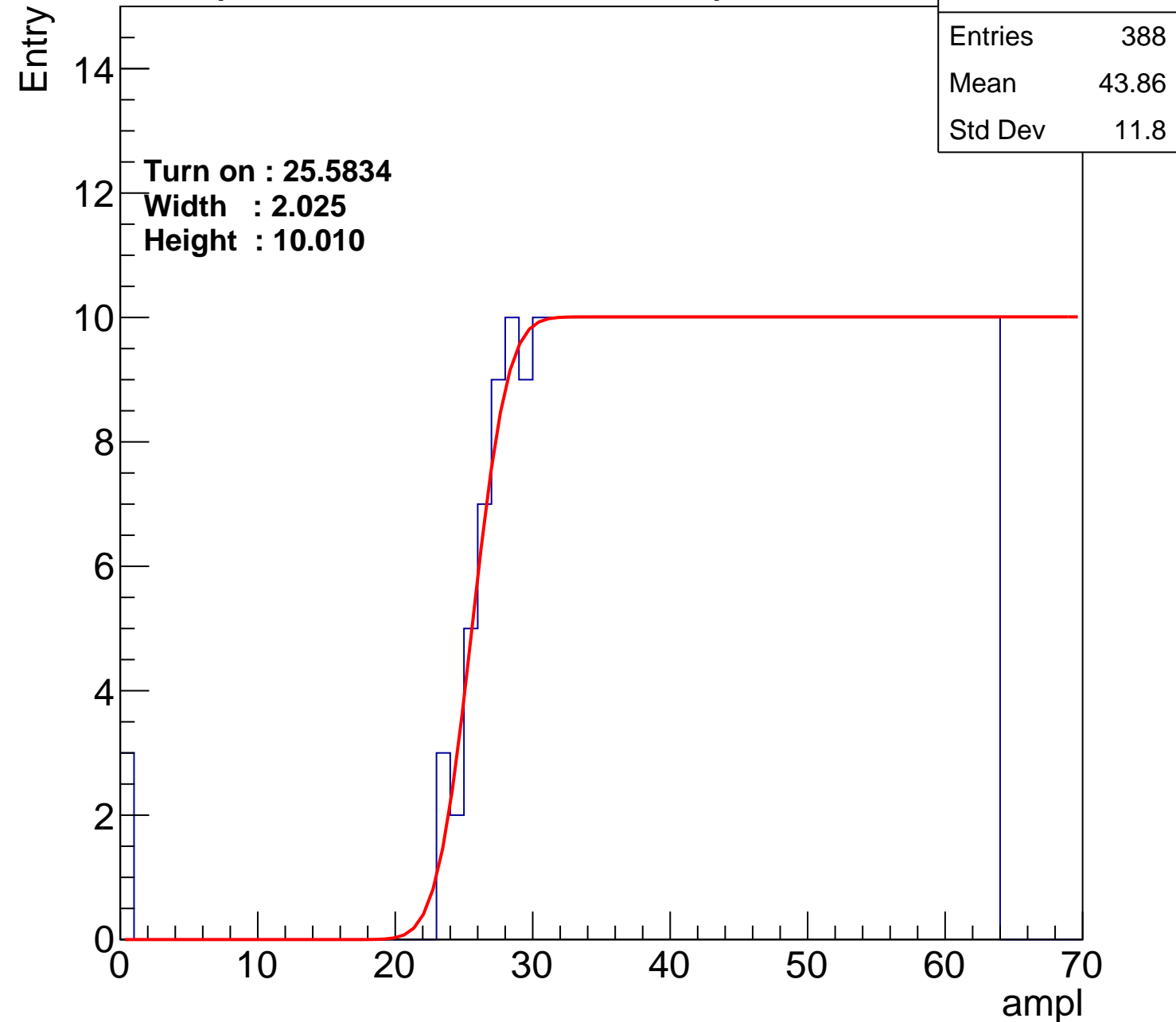
Entry

14
12
10
8
6
4
2
0

Turn on : 25.5834
Width : 2.025
Height : 10.010

Entries	388
Mean	43.86
Std Dev	11.8

ampl



B1L002S, U17-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	425
Mean	42.03
Std Dev	12.77

Turn on : 22.2357

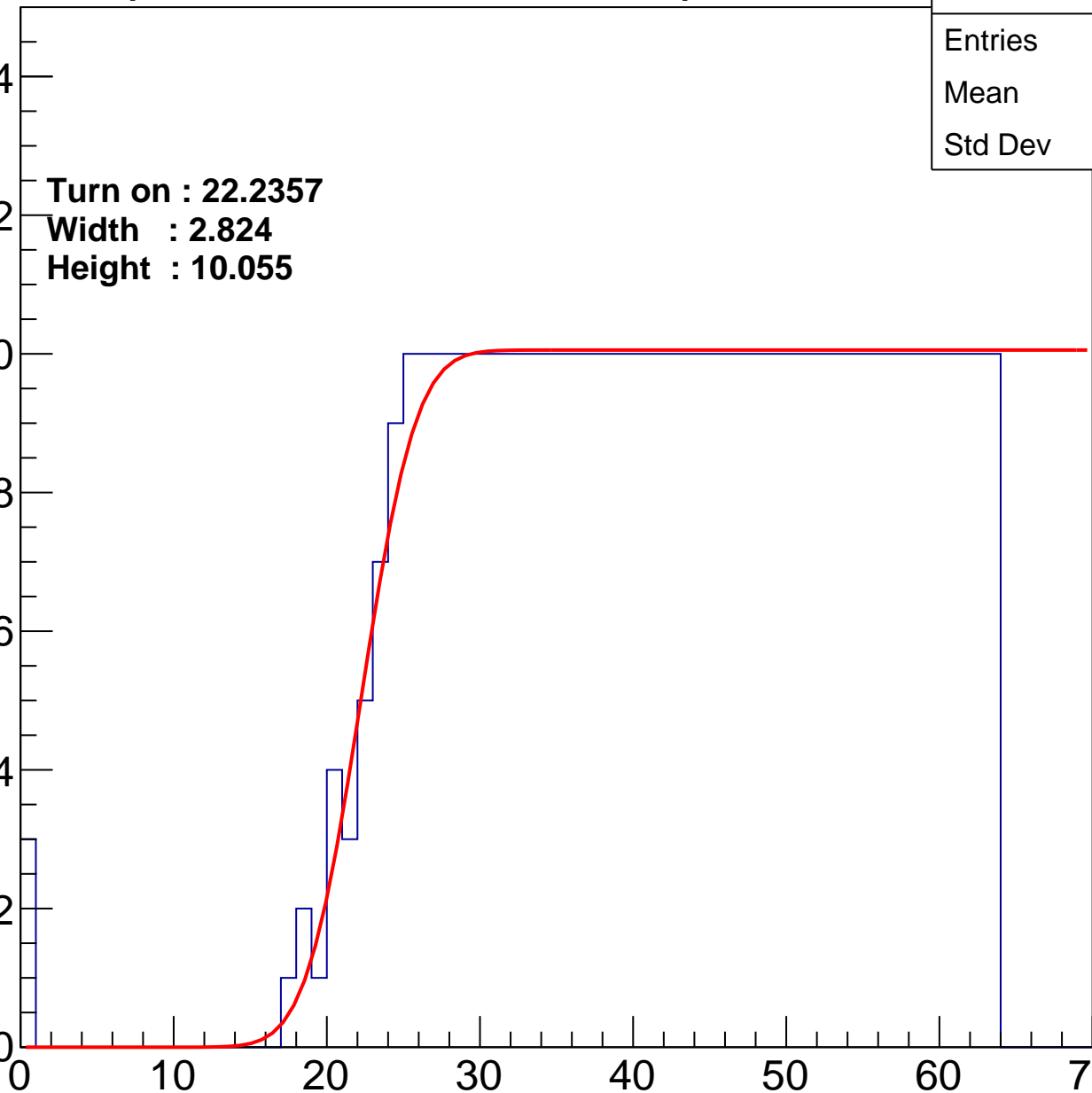
Width : 2.824

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.45
Std Dev	12.03

Turn on : 25.1018

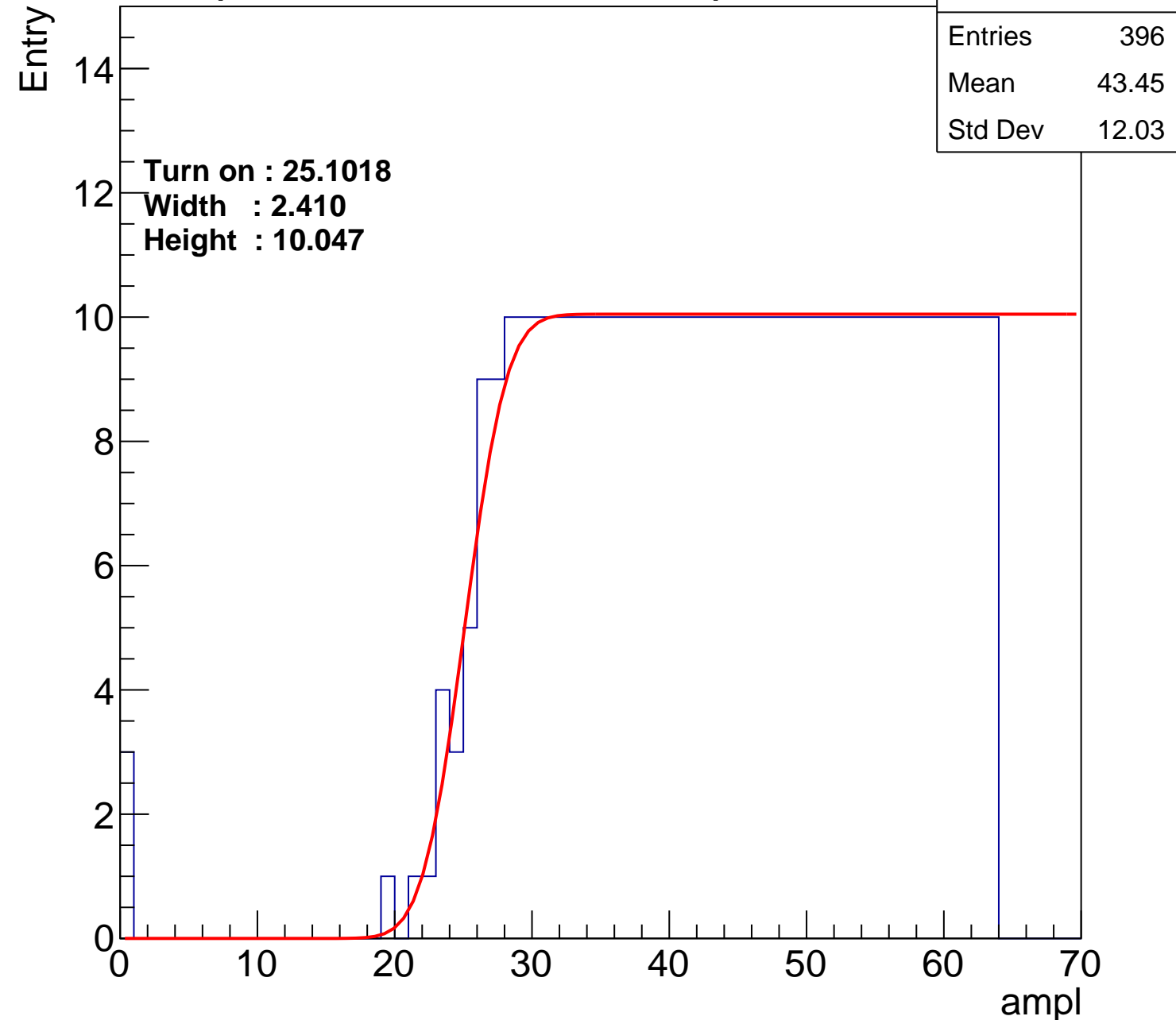
Width : 2.410

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch36

calib_packv5_042523_0143.root, FC#10, port B3

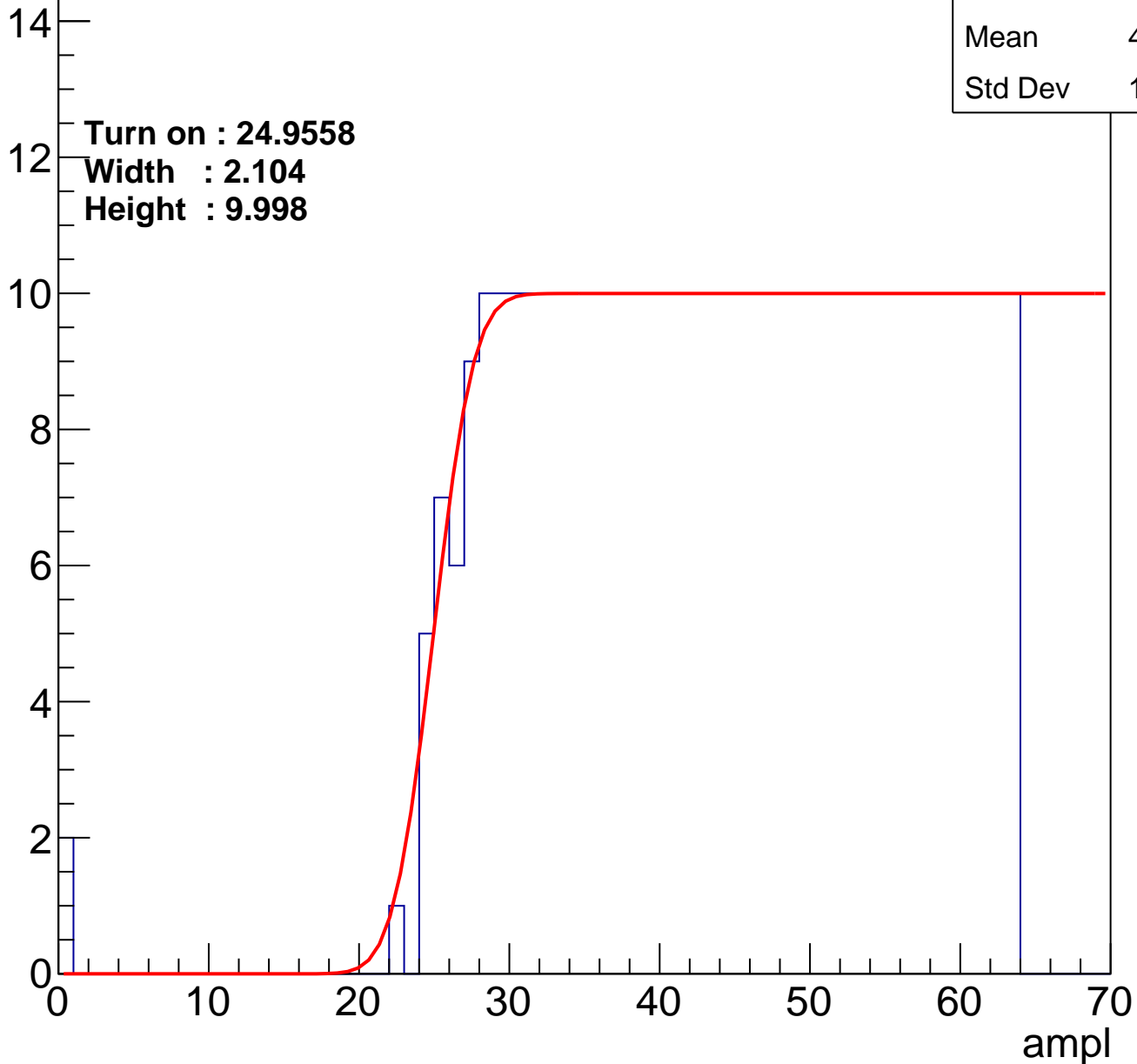
Entries	390
Mean	43.84
Std Dev	11.67

Turn on : 24.9558

Width : 2.104

Height : 9.998

Entry



B1L002S, U17-ch37

calib_packv5_042523_0143.root, FC#10, port B3

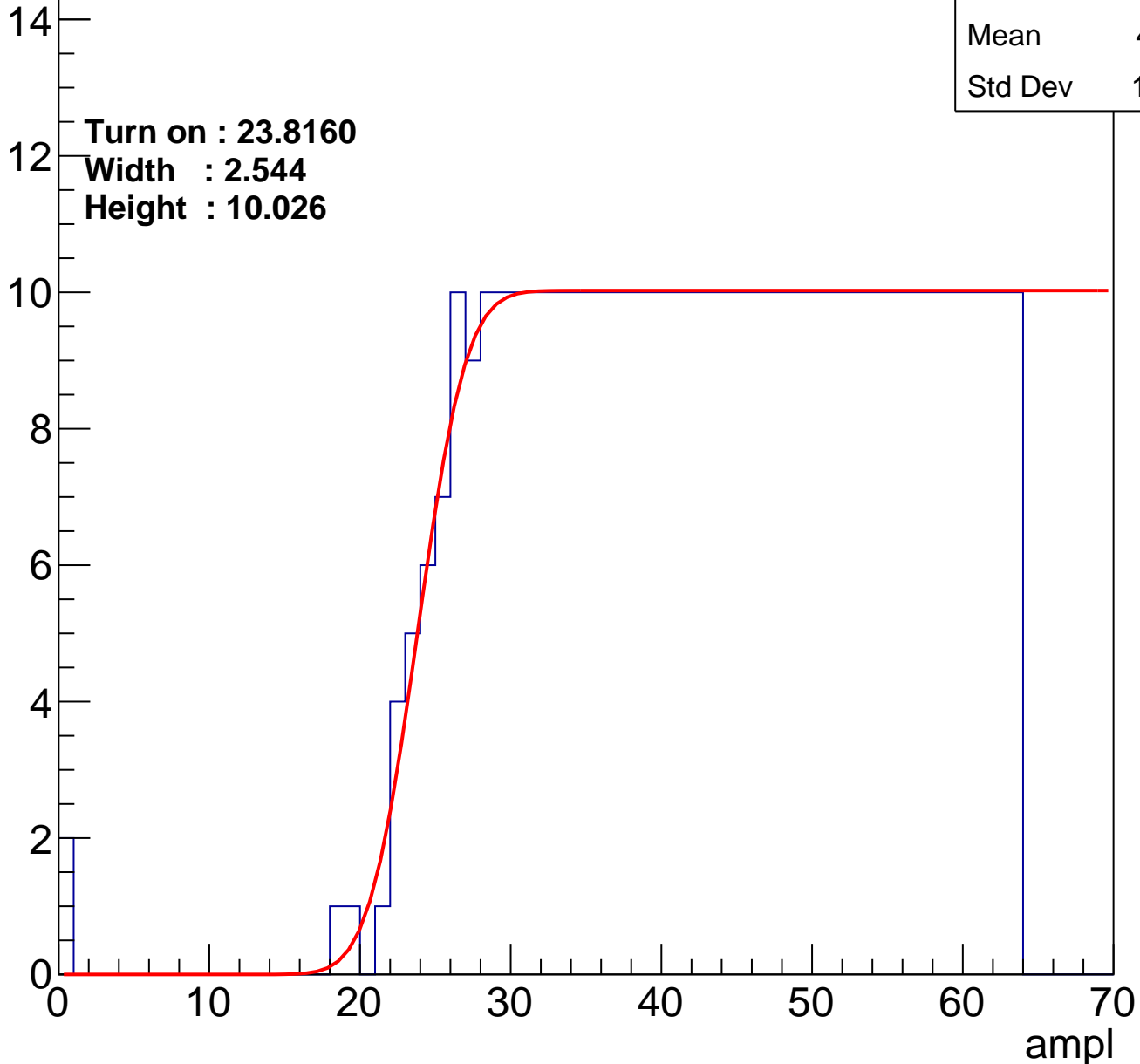
Entries	406
Mean	43.01
Std Dev	12.14

Turn on : 23.8160

Width : 2.544

Height : 10.026

Entry



B1L002S, U17-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.95
Std Dev	12.34

Turn on : 24.2488

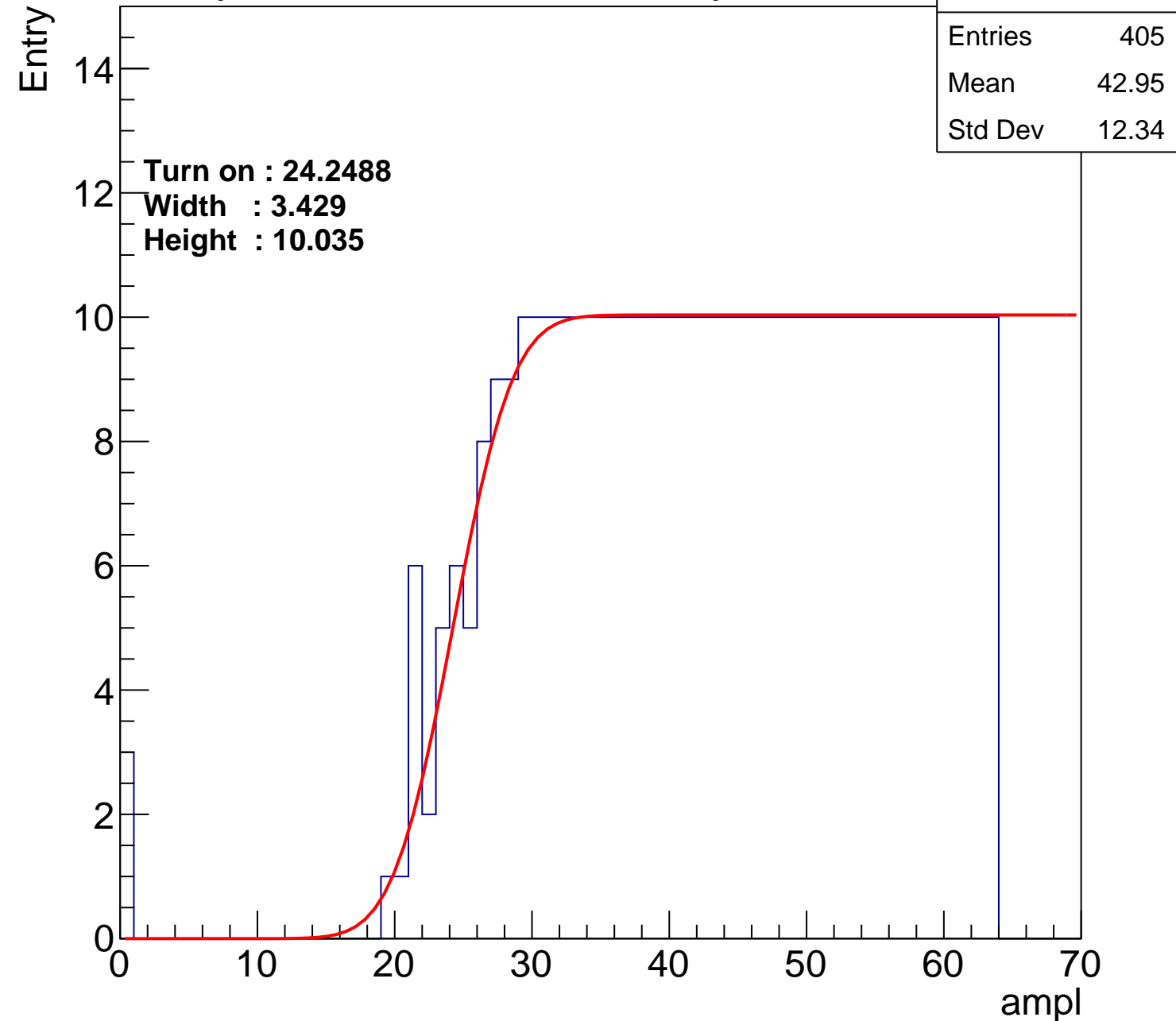
Width : 3.429

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch39

calib_packv5_042523_0143.root, FC#10, port B3

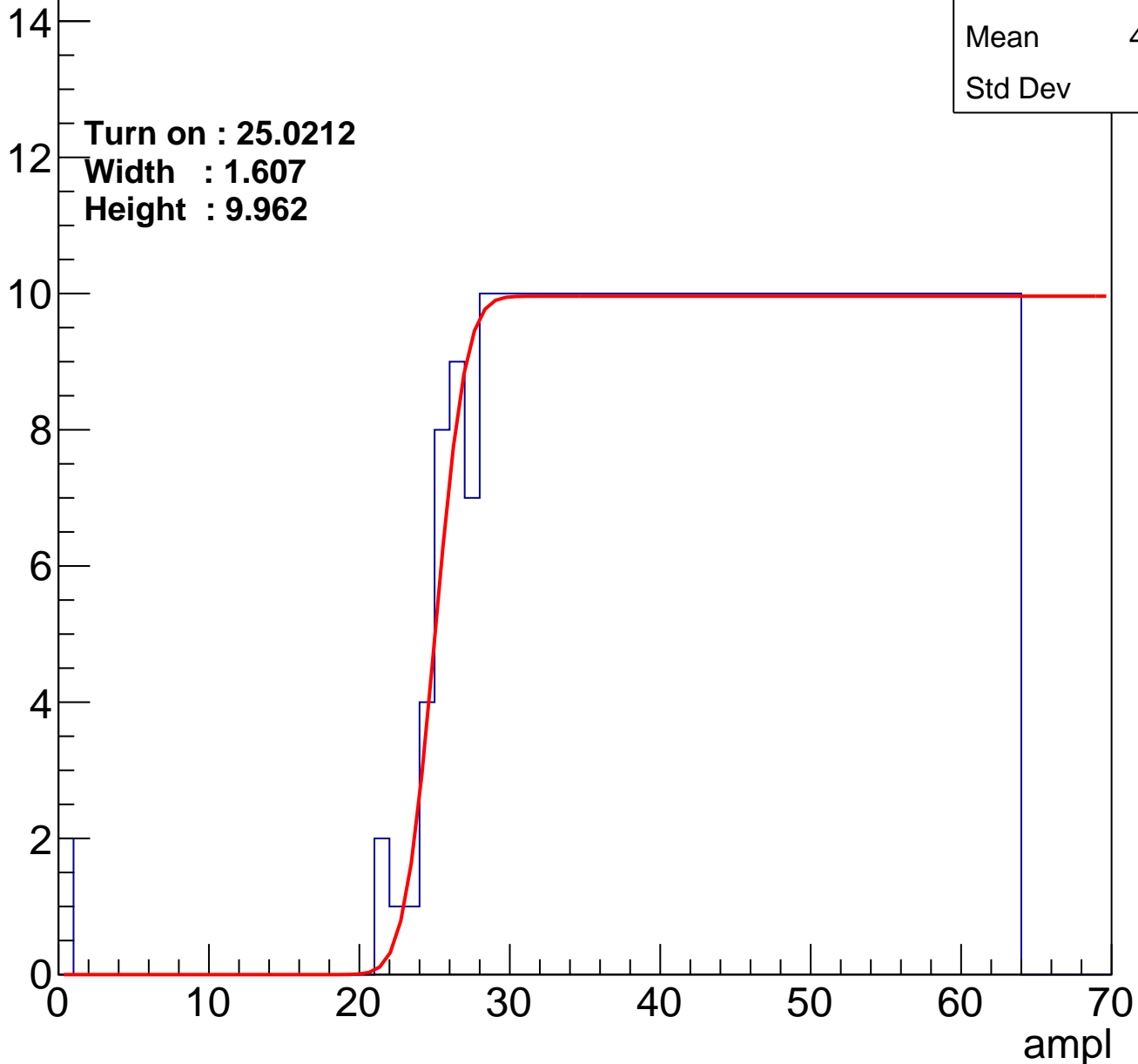
Entries	394
Mean	43.62
Std Dev	11.8

Turn on : 25.0212

Width : 1.607

Height : 9.962

Entry



B1L002S, U17-ch40

calib_packv5_042523_0143.root, FC#10, port B3

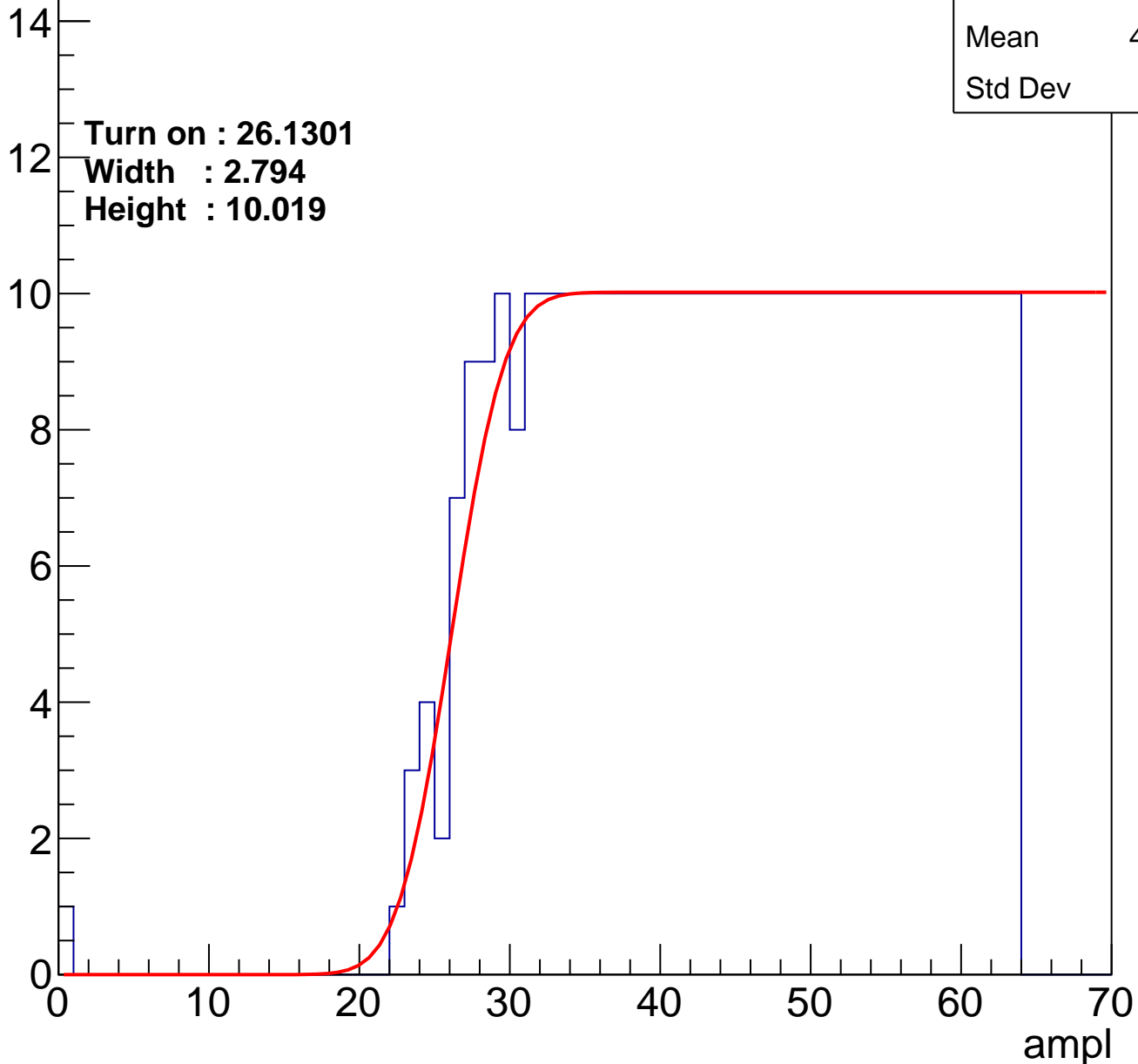
Entries	384
Mean	44.15
Std Dev	11.4

Turn on : 26.1301

Width : 2.794

Height : 10.019

Entry



B1L002S, U17-ch41

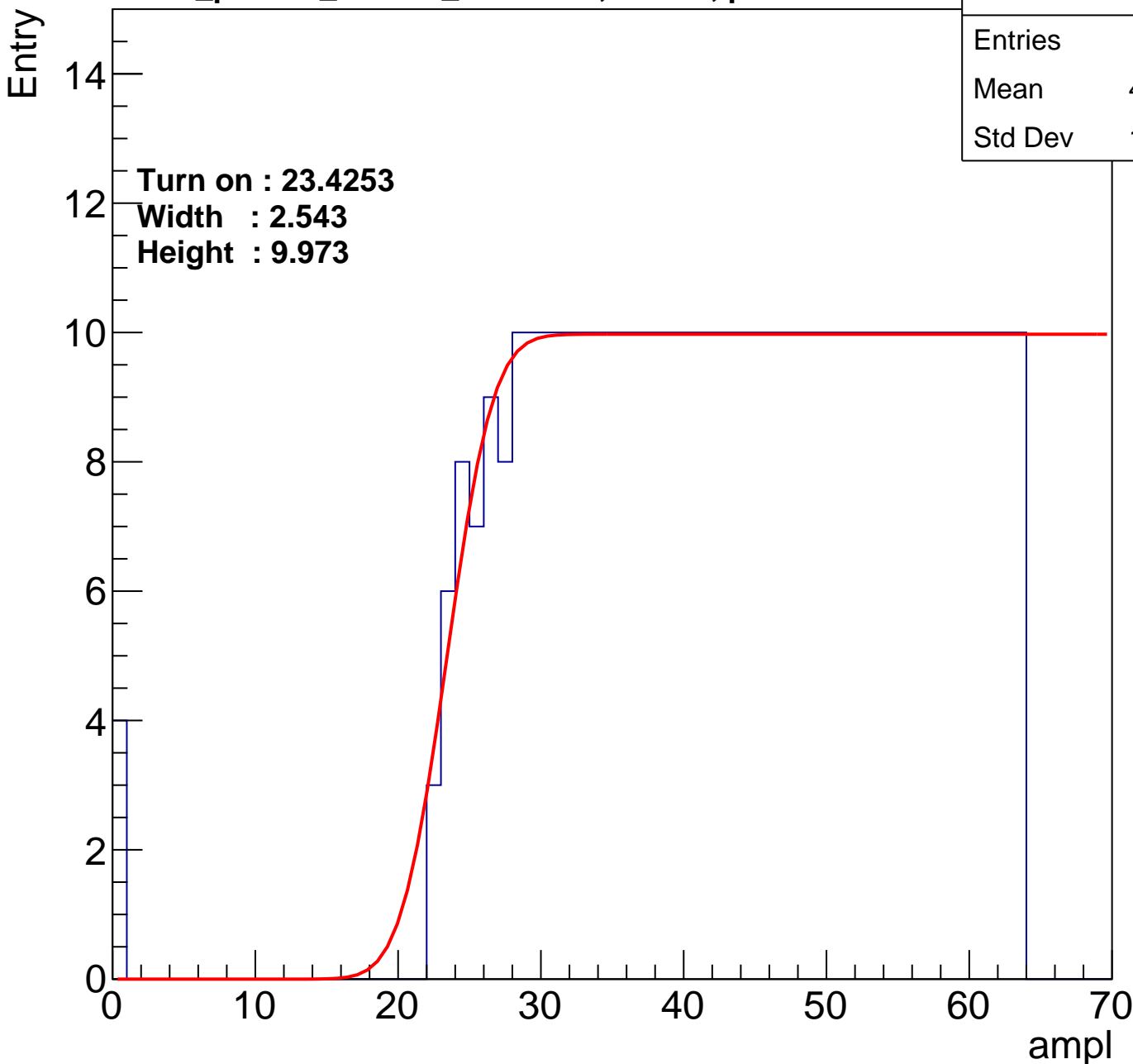
calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.97
Std Dev	12.38

Turn on : 23.4253

Width : 2.543

Height : 9.973



B1L002S, U17-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.3
Std Dev	11.76

Turn on : 27.3839

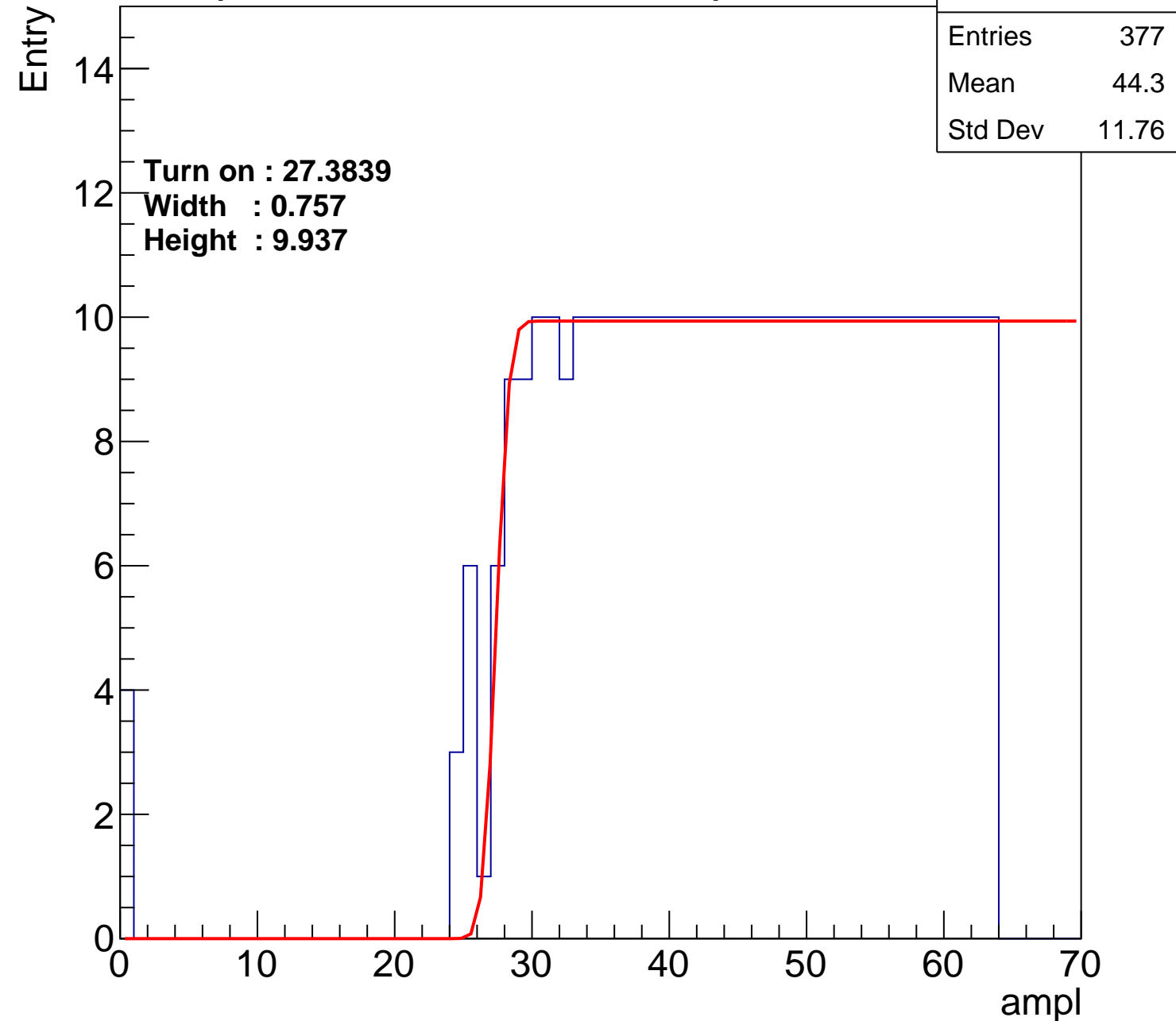
Width : 0.757

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.62
Std Dev	11.97

Turn on : 25.4882

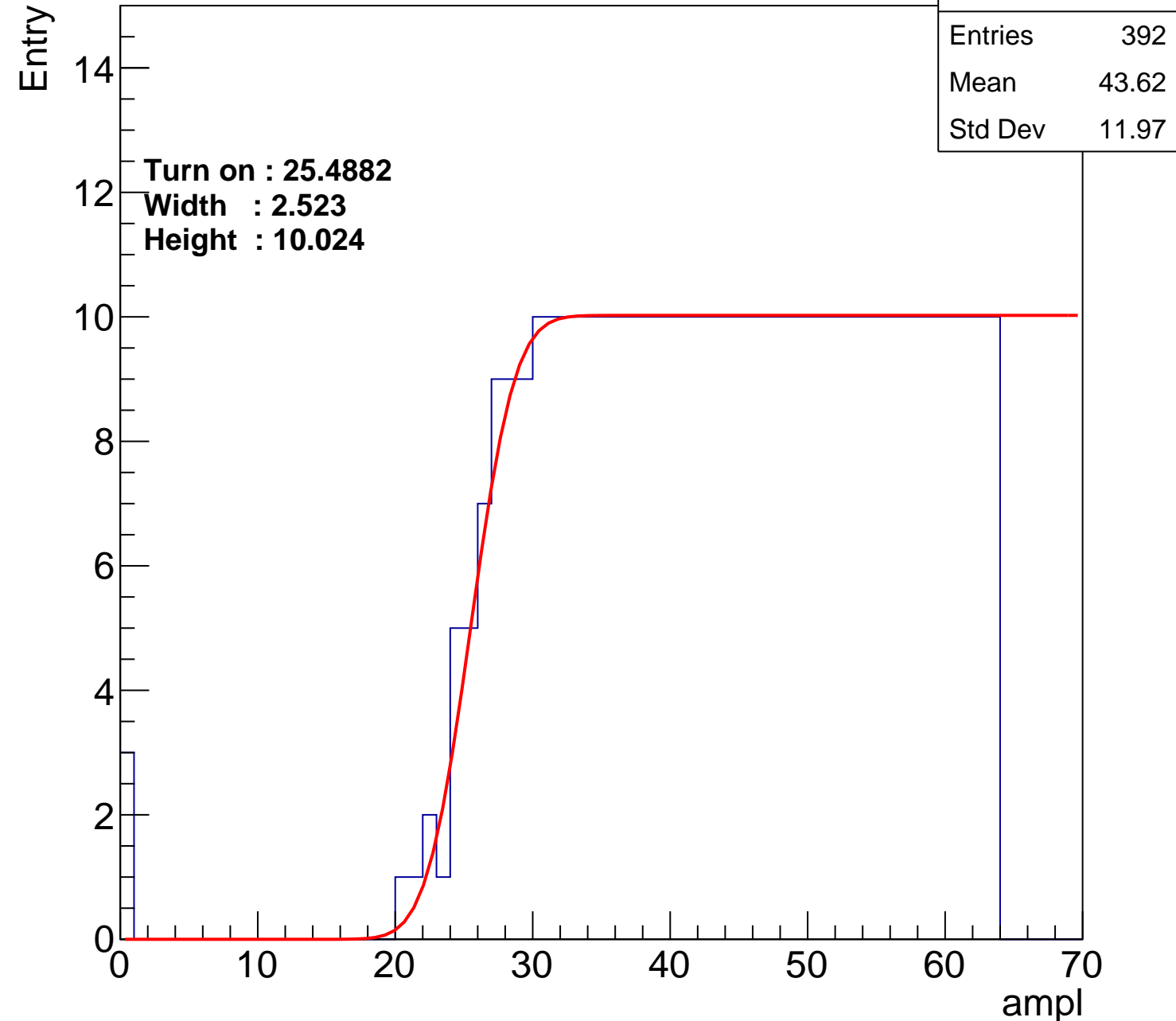
Width : 2.523

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.41
Std Dev	12.22

Turn on : 25.6961

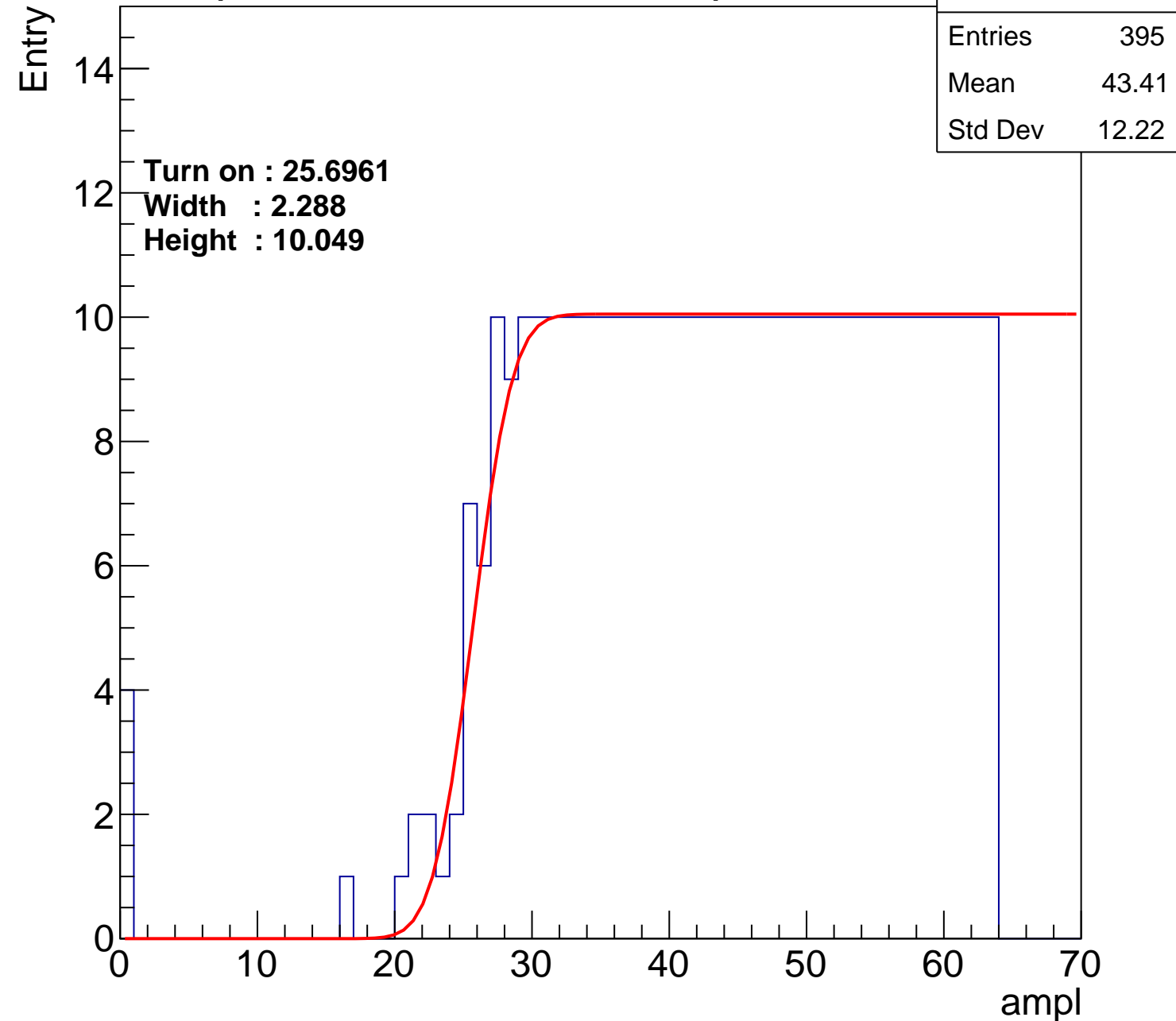
Width : 2.288

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch45

calib_packv5_042523_0143.root, FC#10, port B3

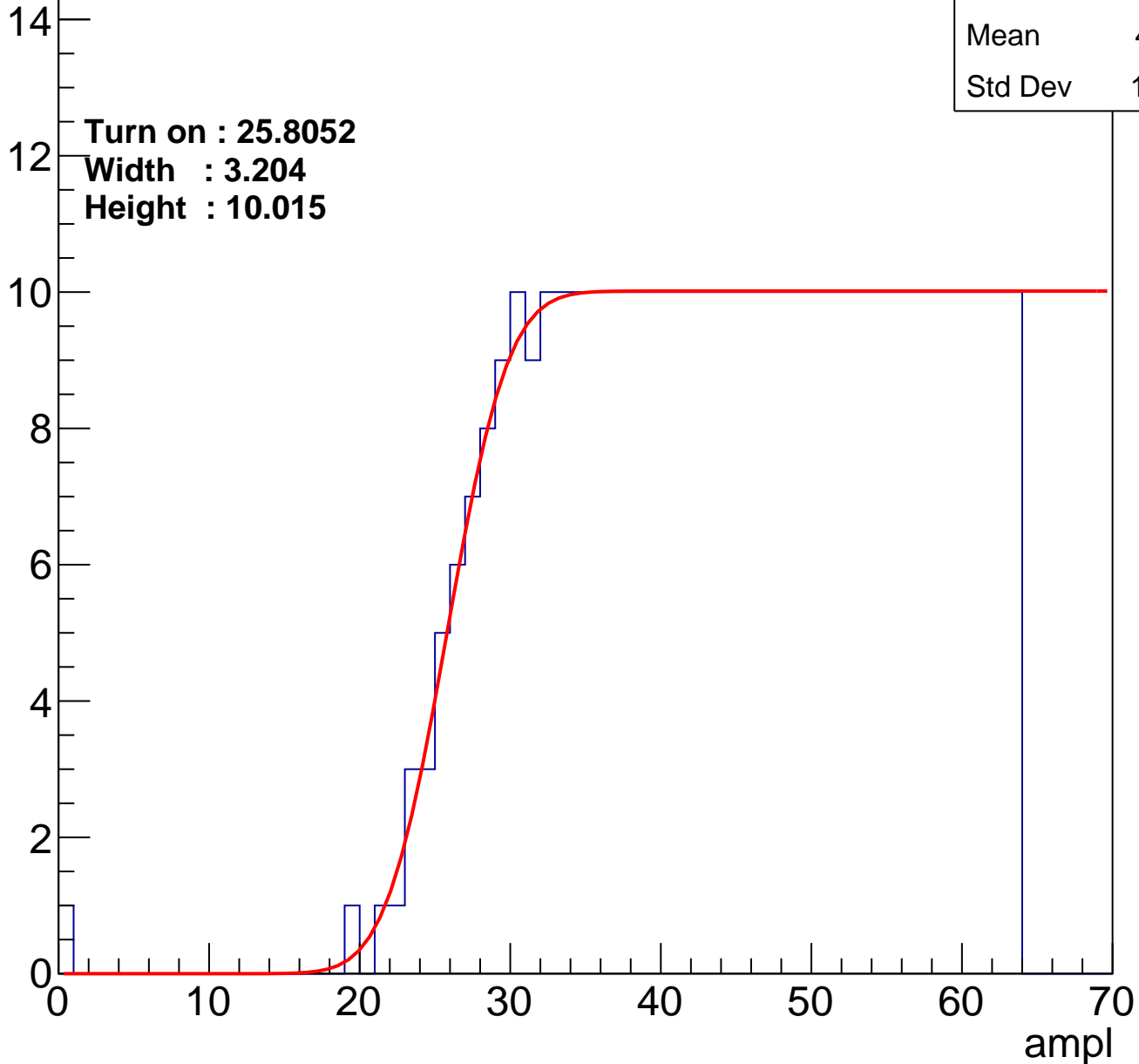
Entries	384
Mean	44.11
Std Dev	11.48

Turn on : 25.8052

Width : 3.204

Height : 10.015

Entry



B1L002S, U17-ch46

calib_packv5_042523_0143.root, FC#10, port B3

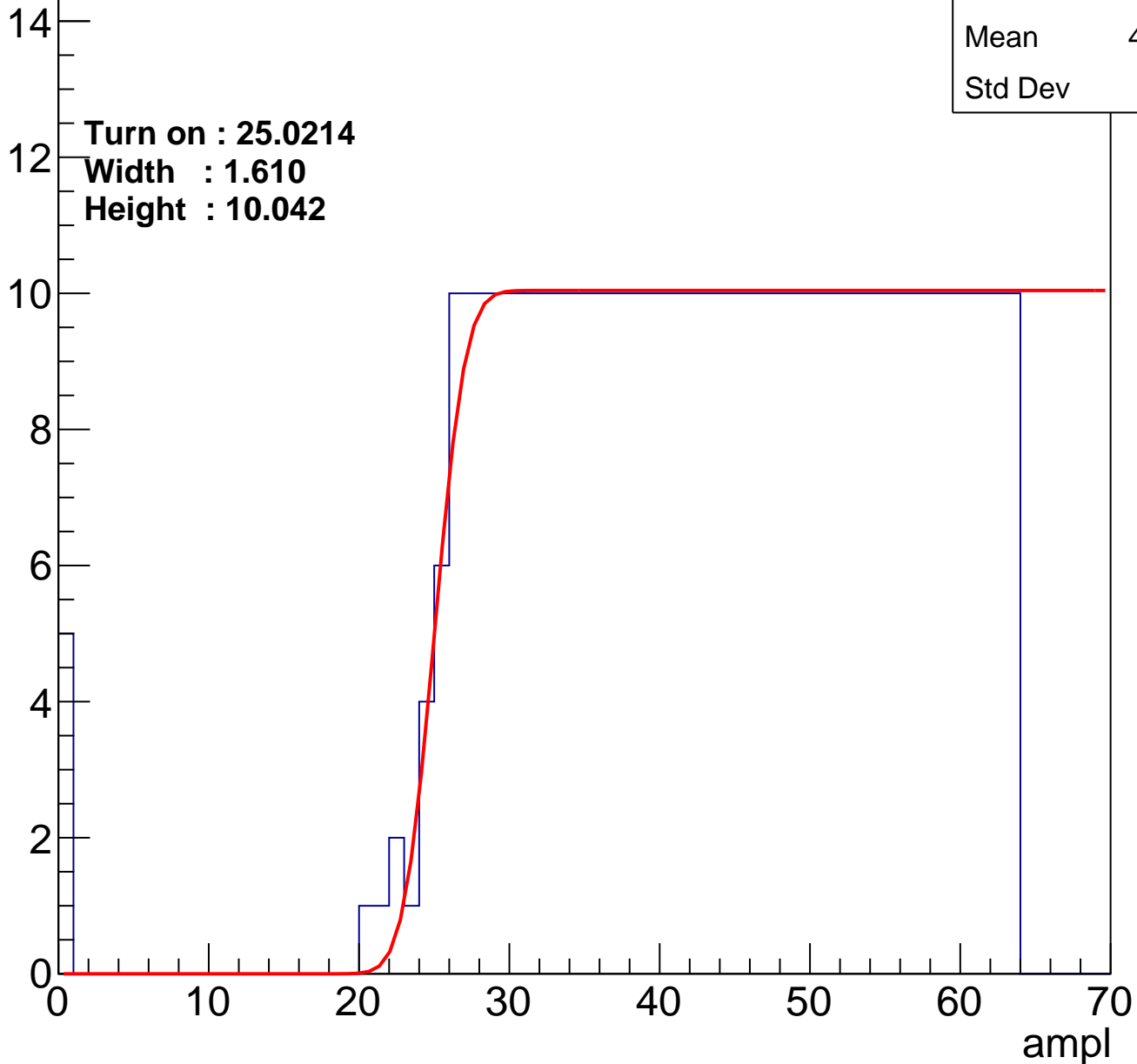
Entries	400
Mean	43.16
Std Dev	12.4

Turn on : 25.0214

Width : 1.610

Height : 10.042

Entry



B1L002S, U17-ch47

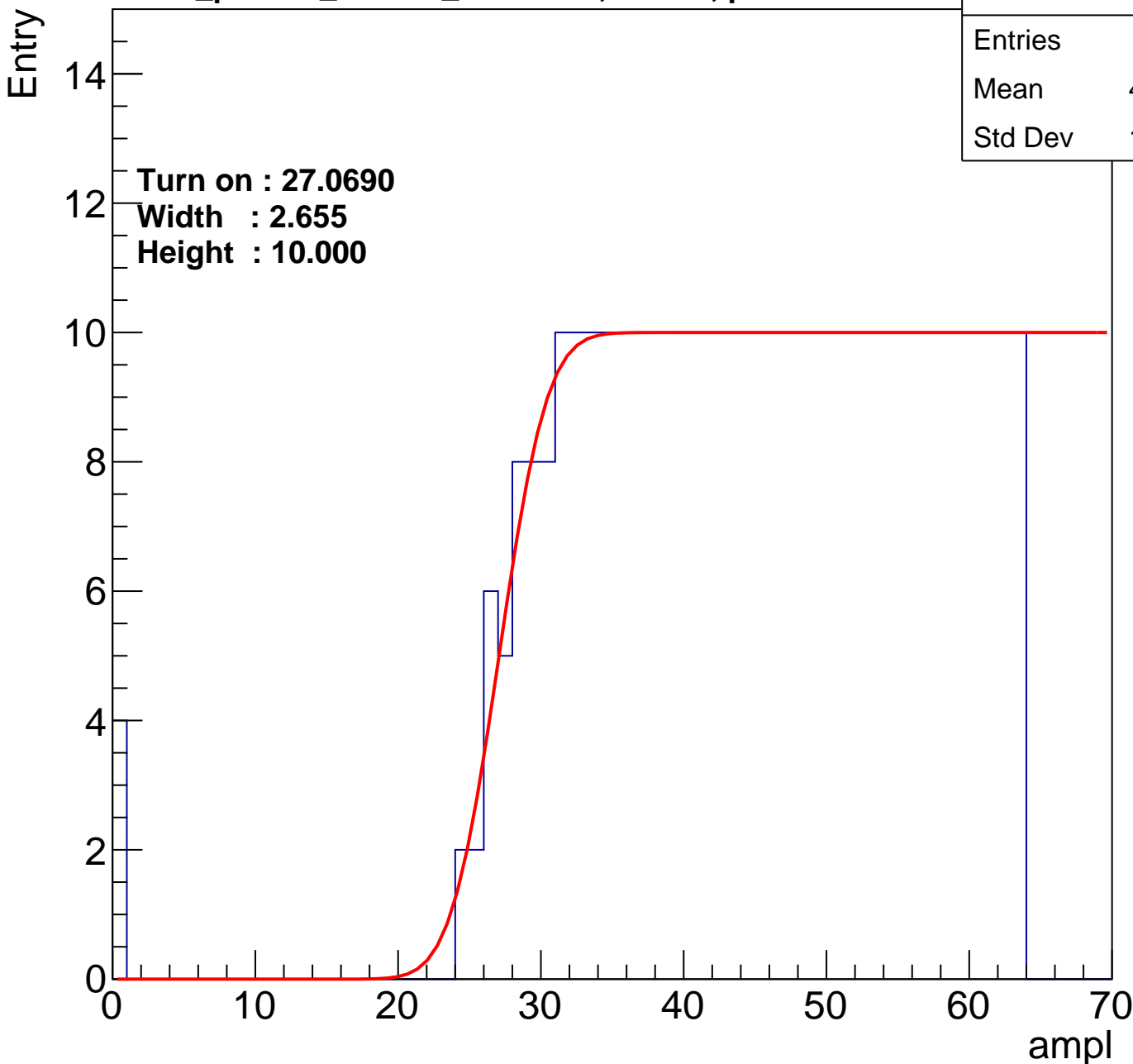
calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.49
Std Dev	11.68

Turn on : 27.0690

Width : 2.655

Height : 10.000



B1L002S, U17-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.6718

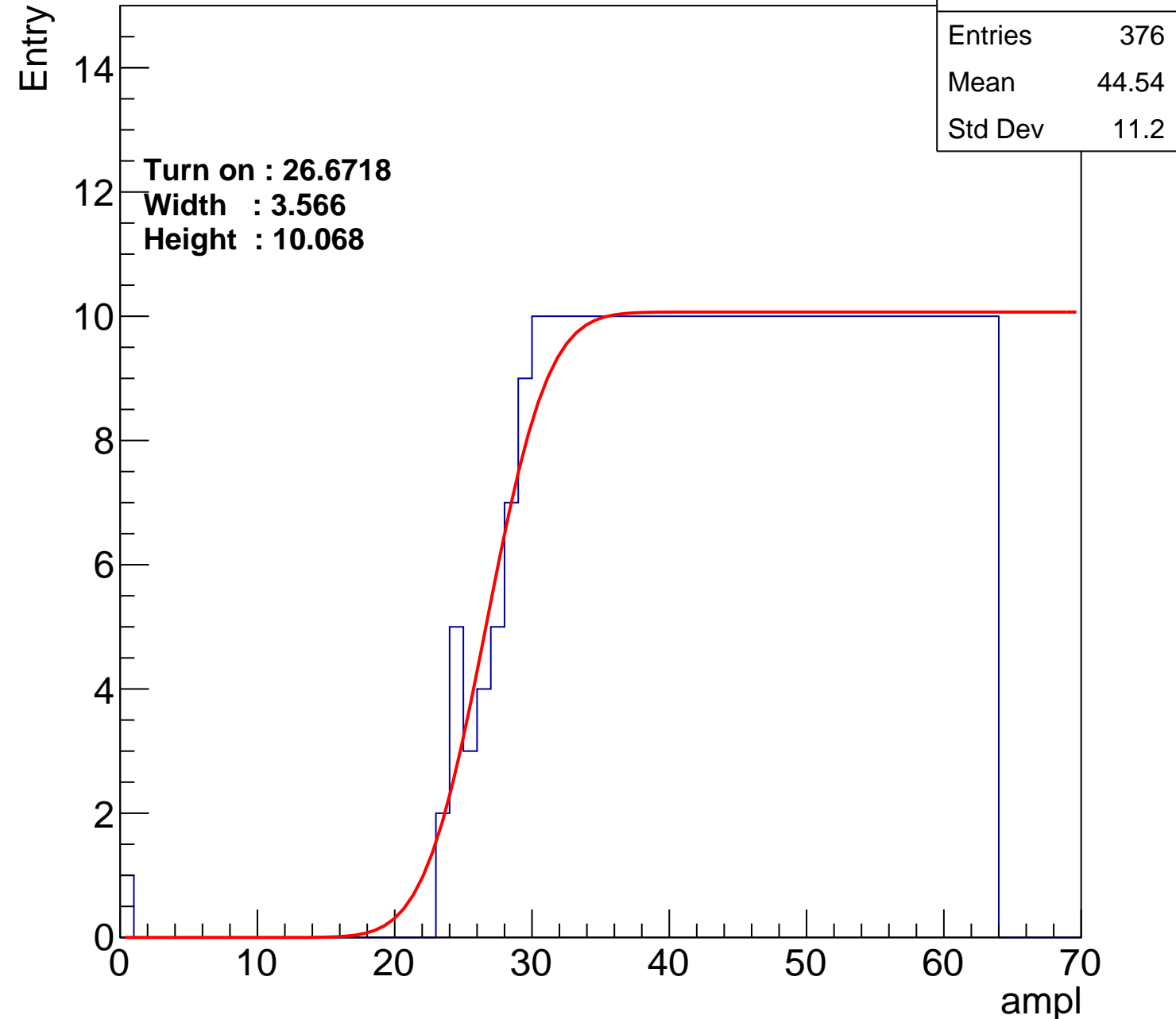
Width : 3.566

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.74
Std Dev	11.64

Turn on : 25.0511

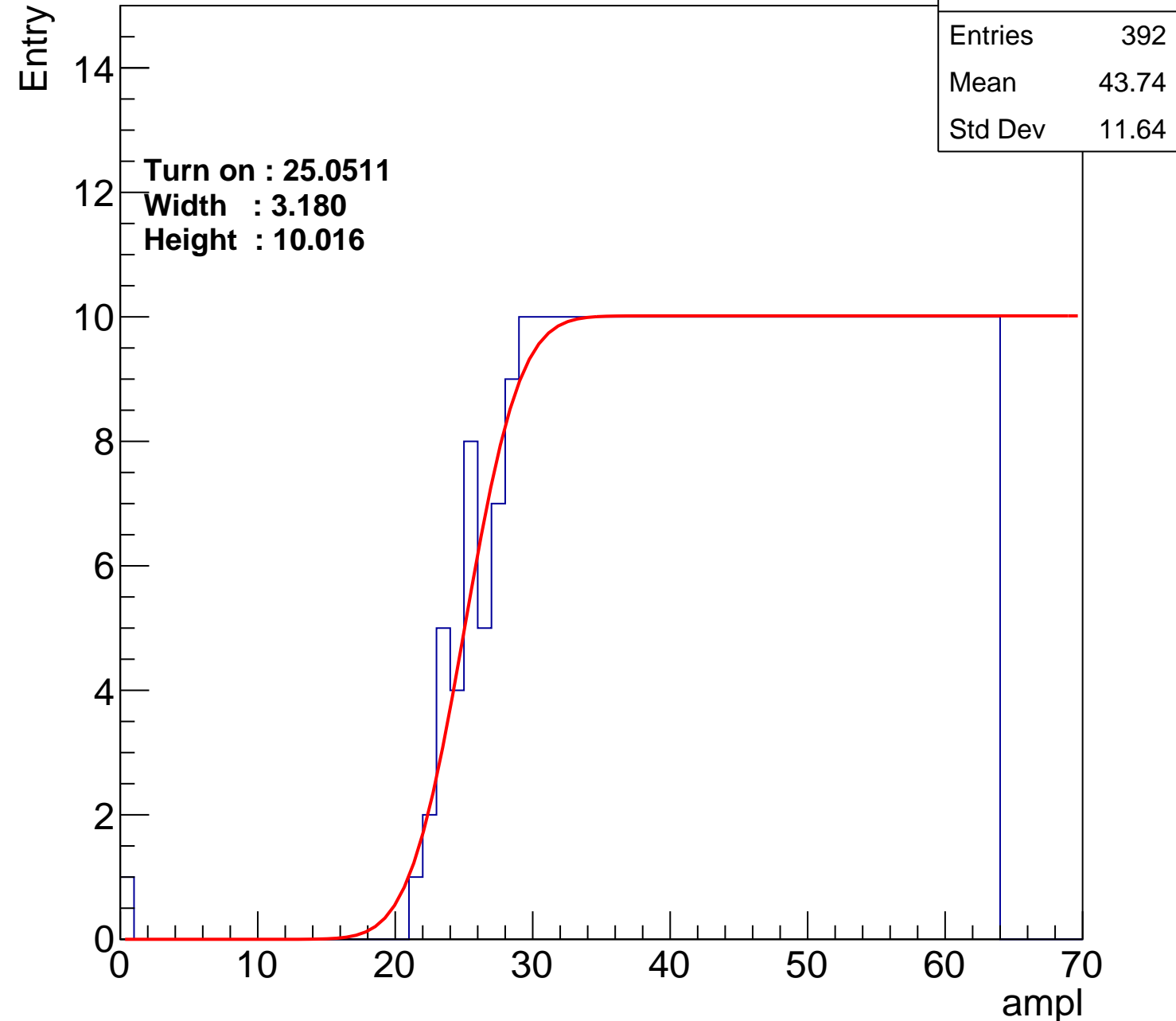
Width : 3.180

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch50

calib_packv5_042523_0143.root, FC#10, port B3

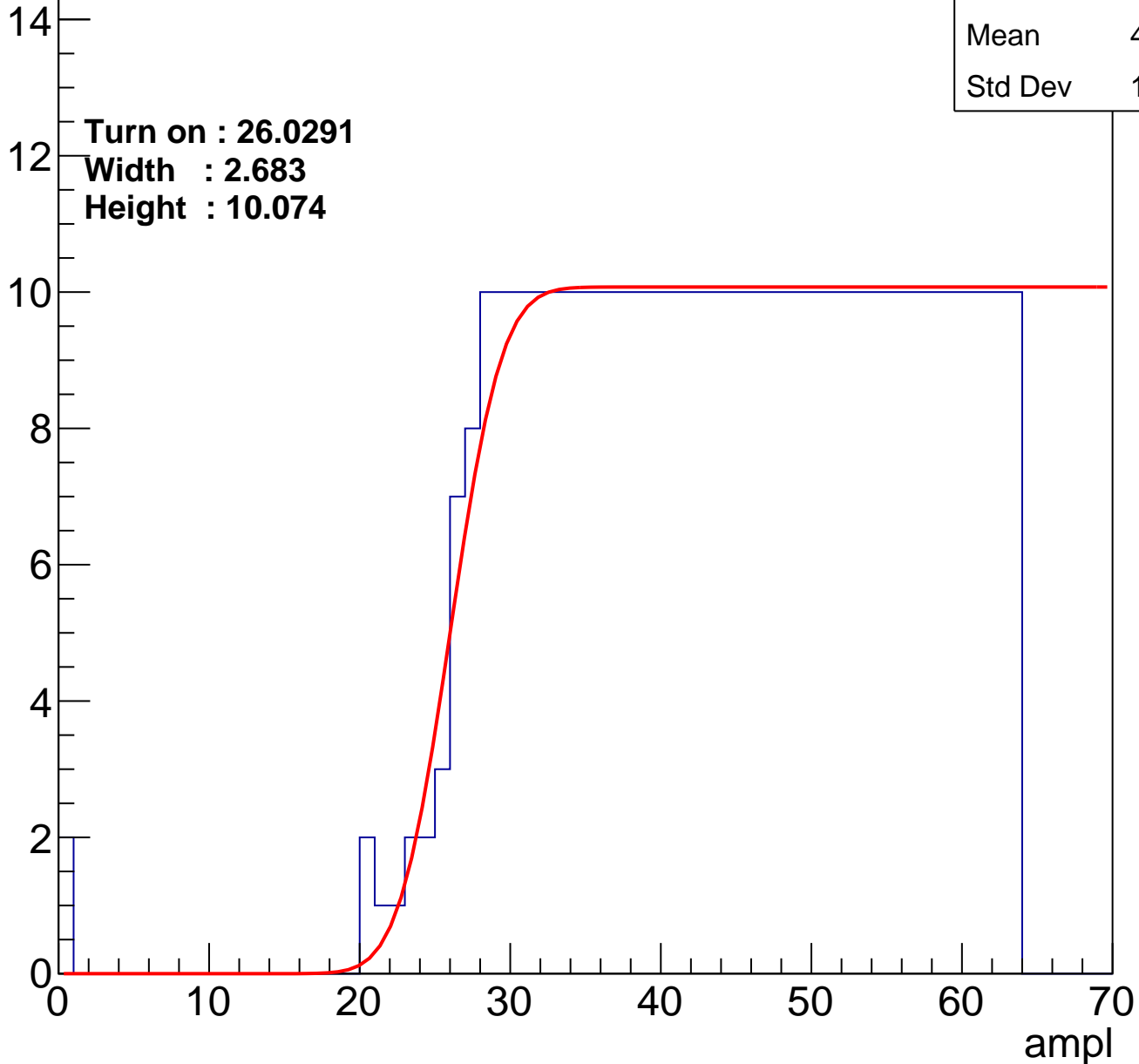
Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 26.0291

Width : 2.683

Height : 10.074

Entry



B1L002S, U17-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.63
Std Dev	12.64

Turn on : 25.6932

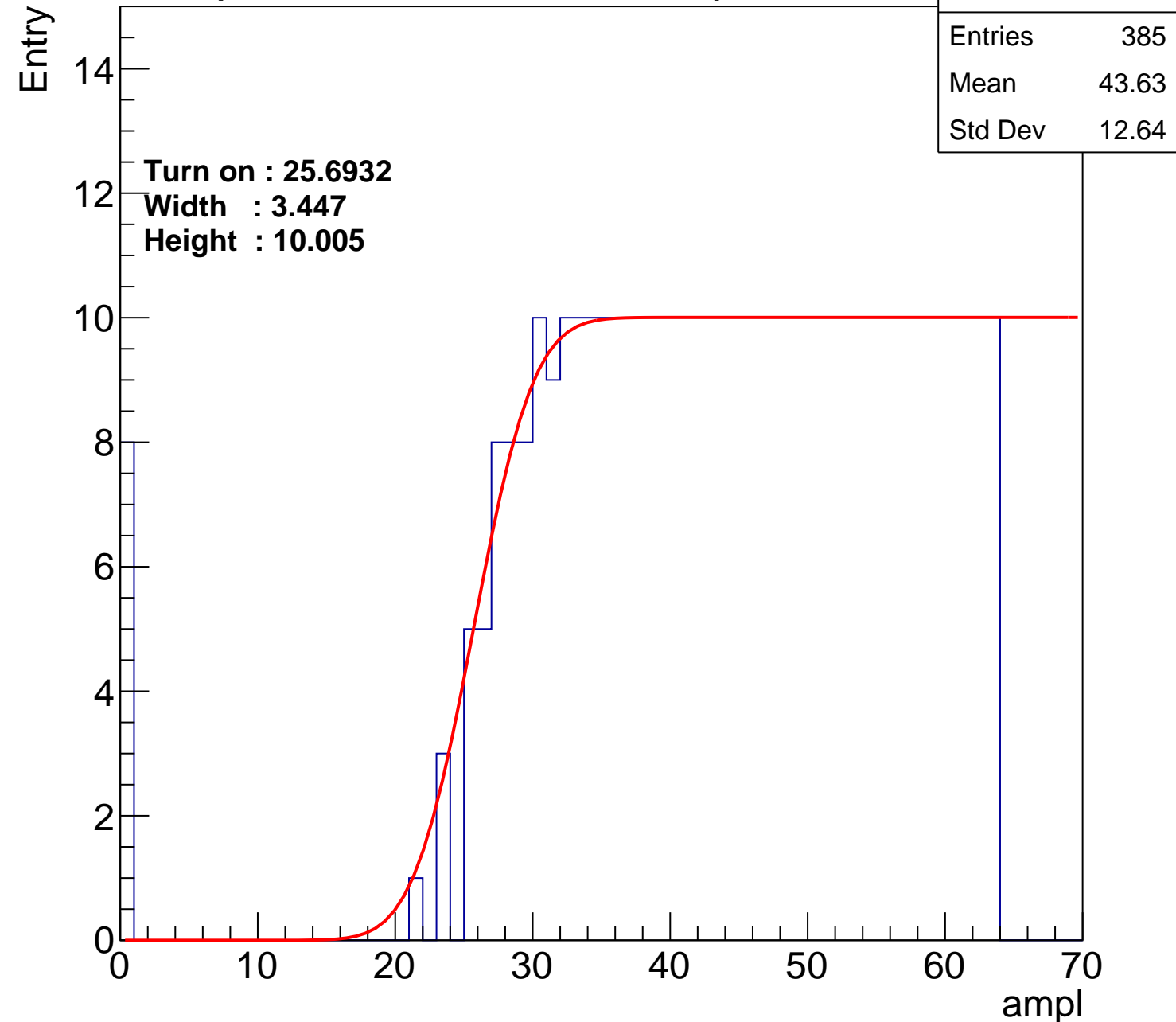
Width : 3.447

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 26.1892

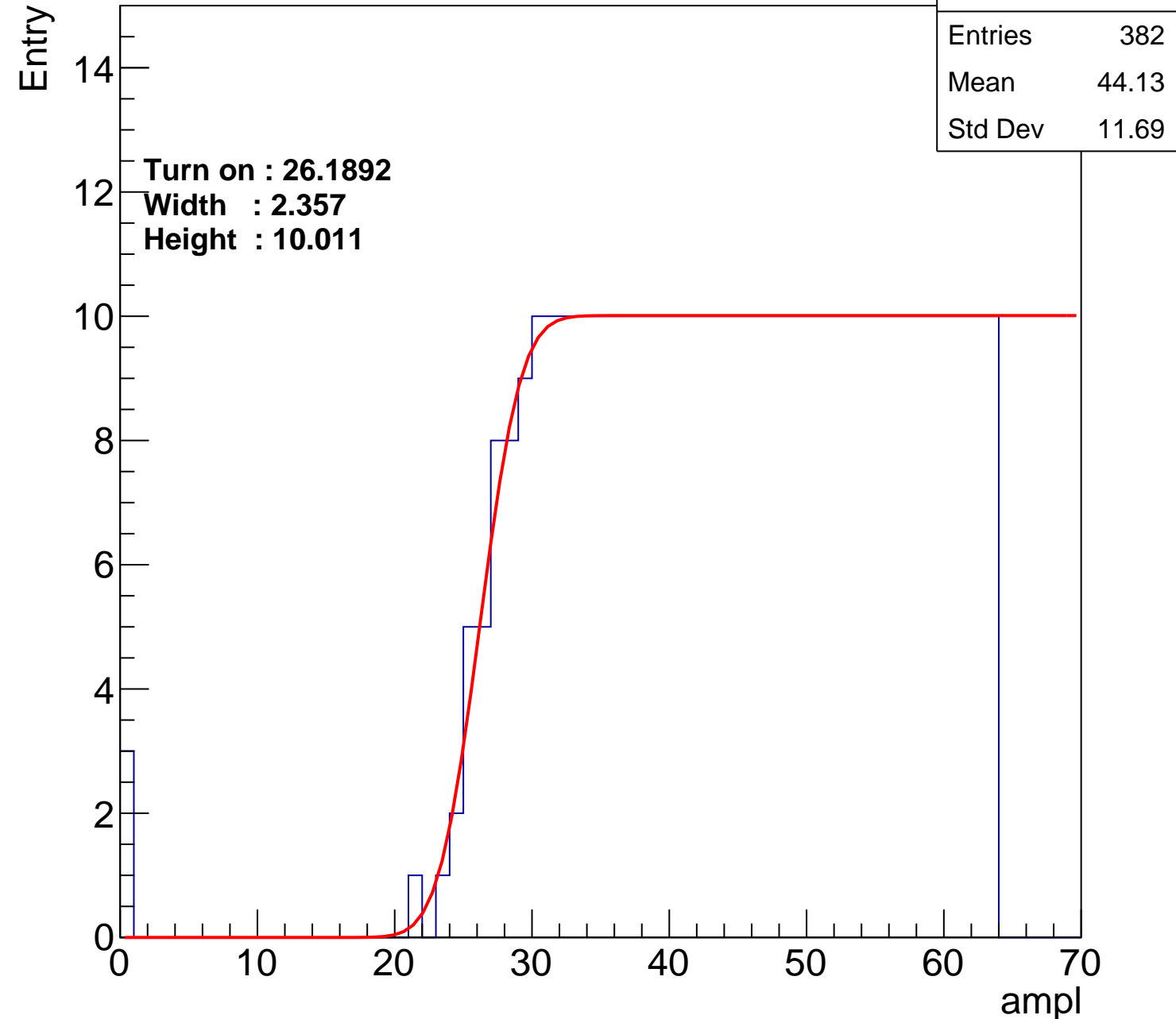
Width : 2.357

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 26.0346

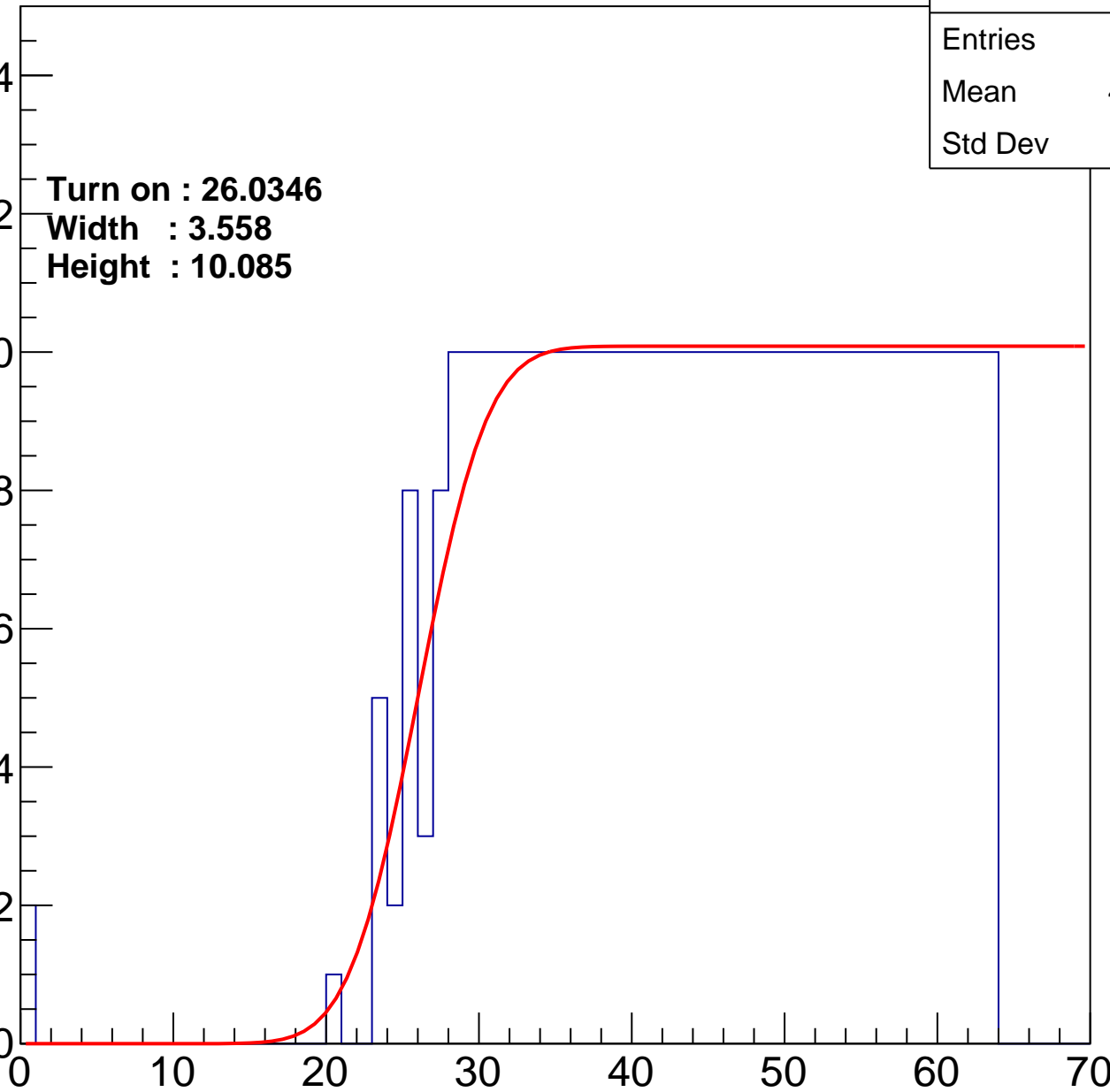
Width : 3.558

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch54

calib_packv5_042523_0143.root, FC#10, port B3

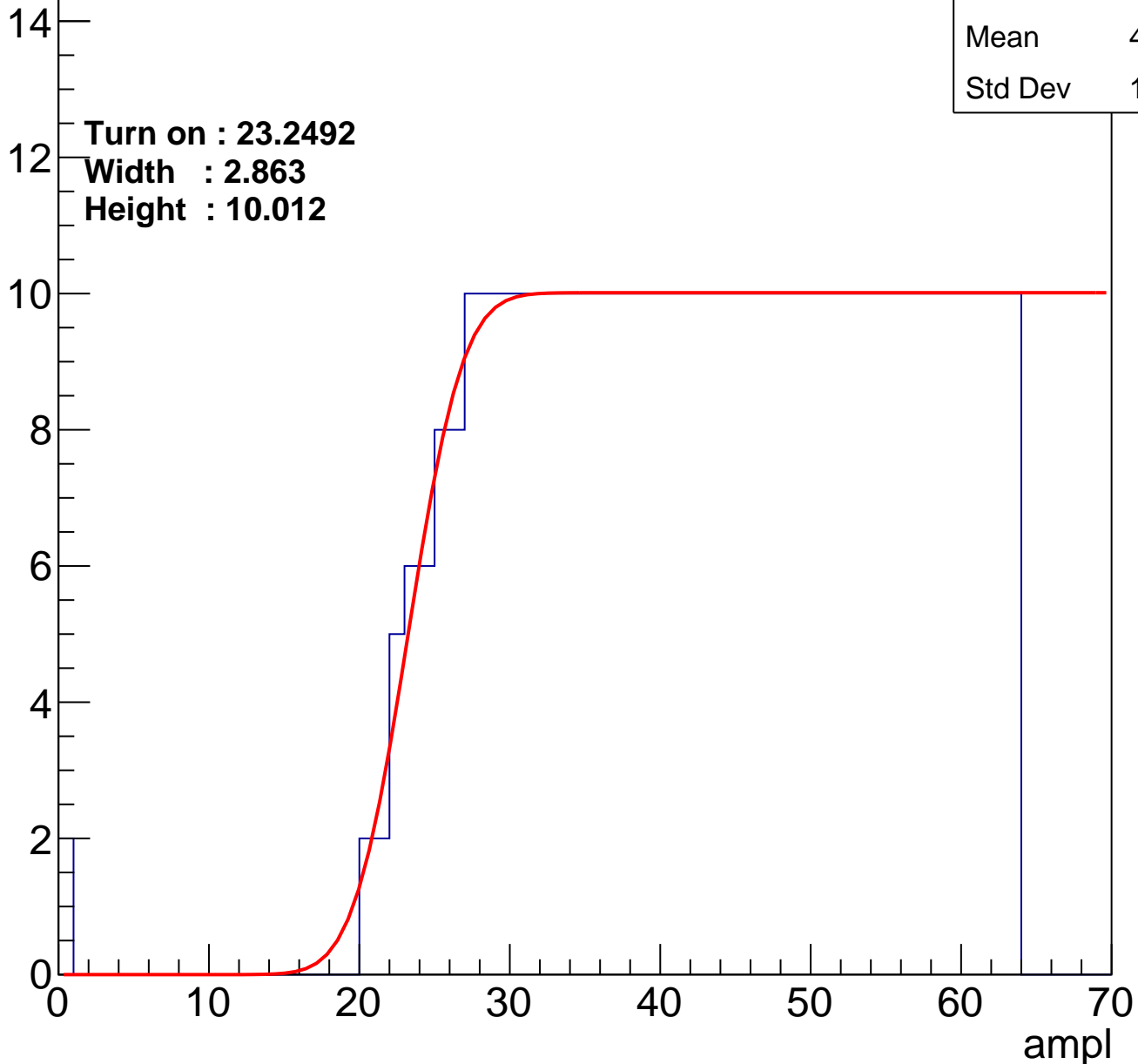
Entries	409
Mean	42.87
Std Dev	12.22

Turn on : 23.2492

Width : 2.863

Height : 10.012

Entry



B1L002S, U17-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.16
Std Dev	12.34

Turn on : 24.8875

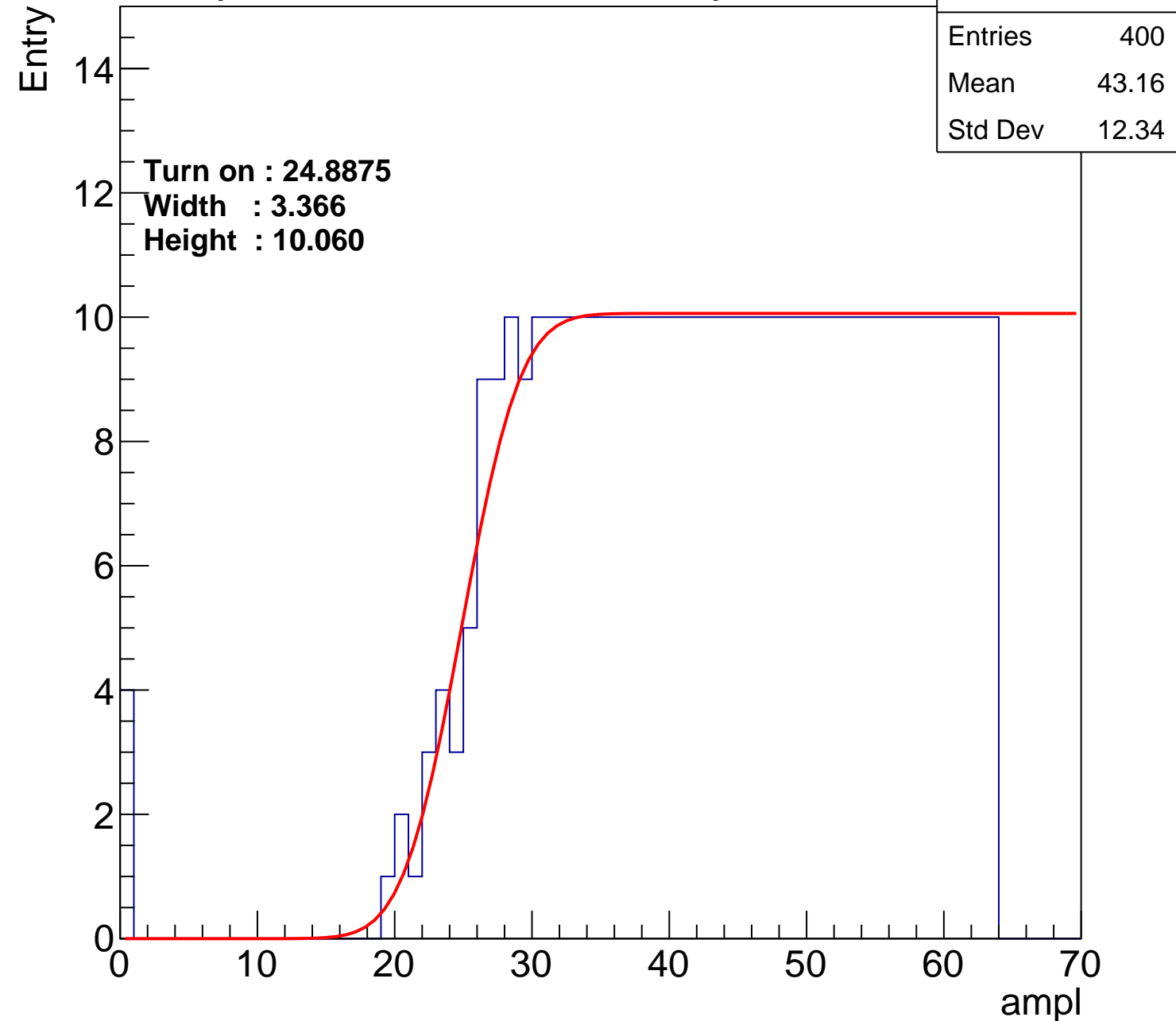
Width : 3.366

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch56

calib_packv5_042523_0143.root, FC#10, port B3

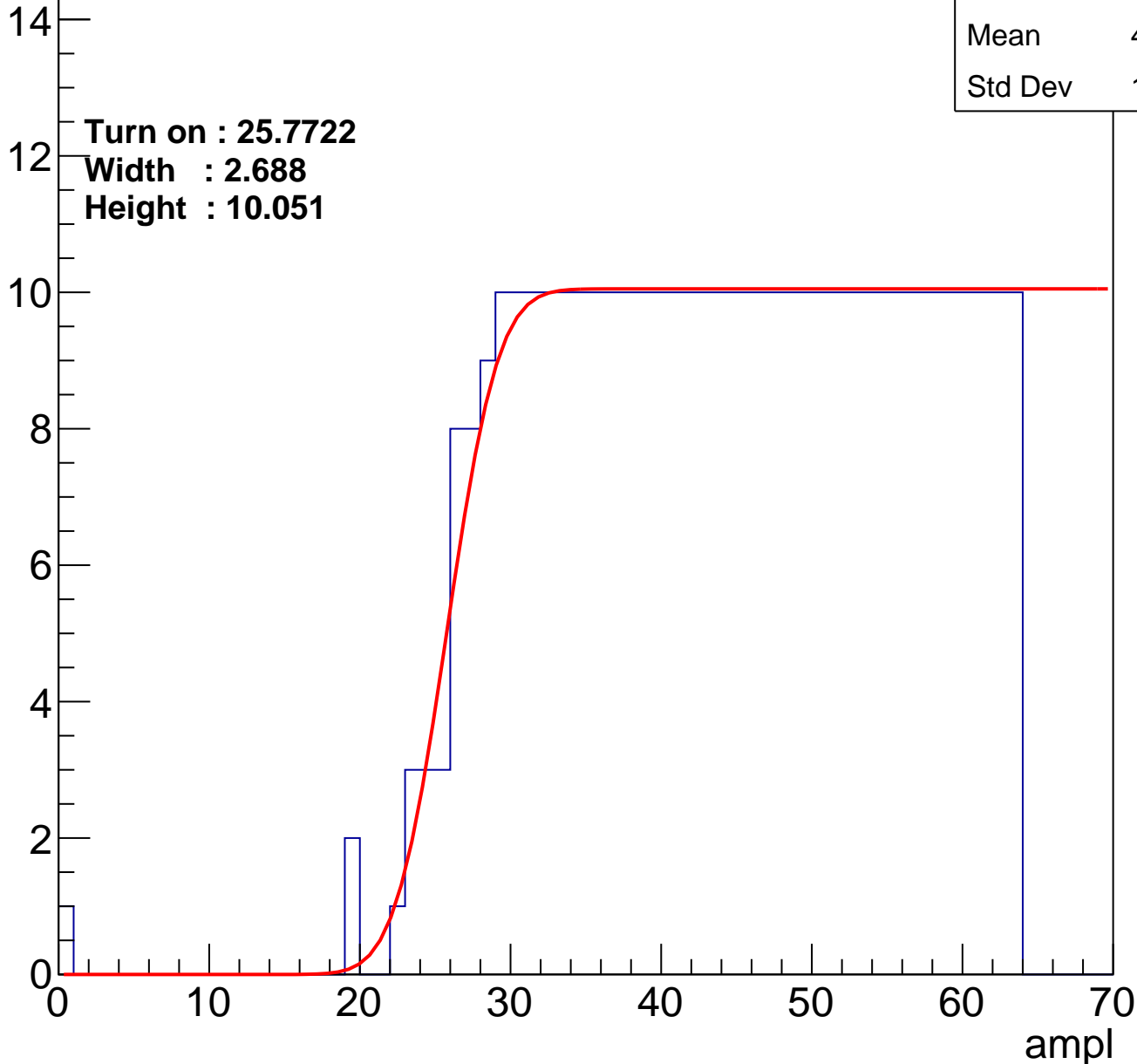
Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 25.7722

Width : 2.688

Height : 10.051

Entry



B1L002S, U17-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 28.5343

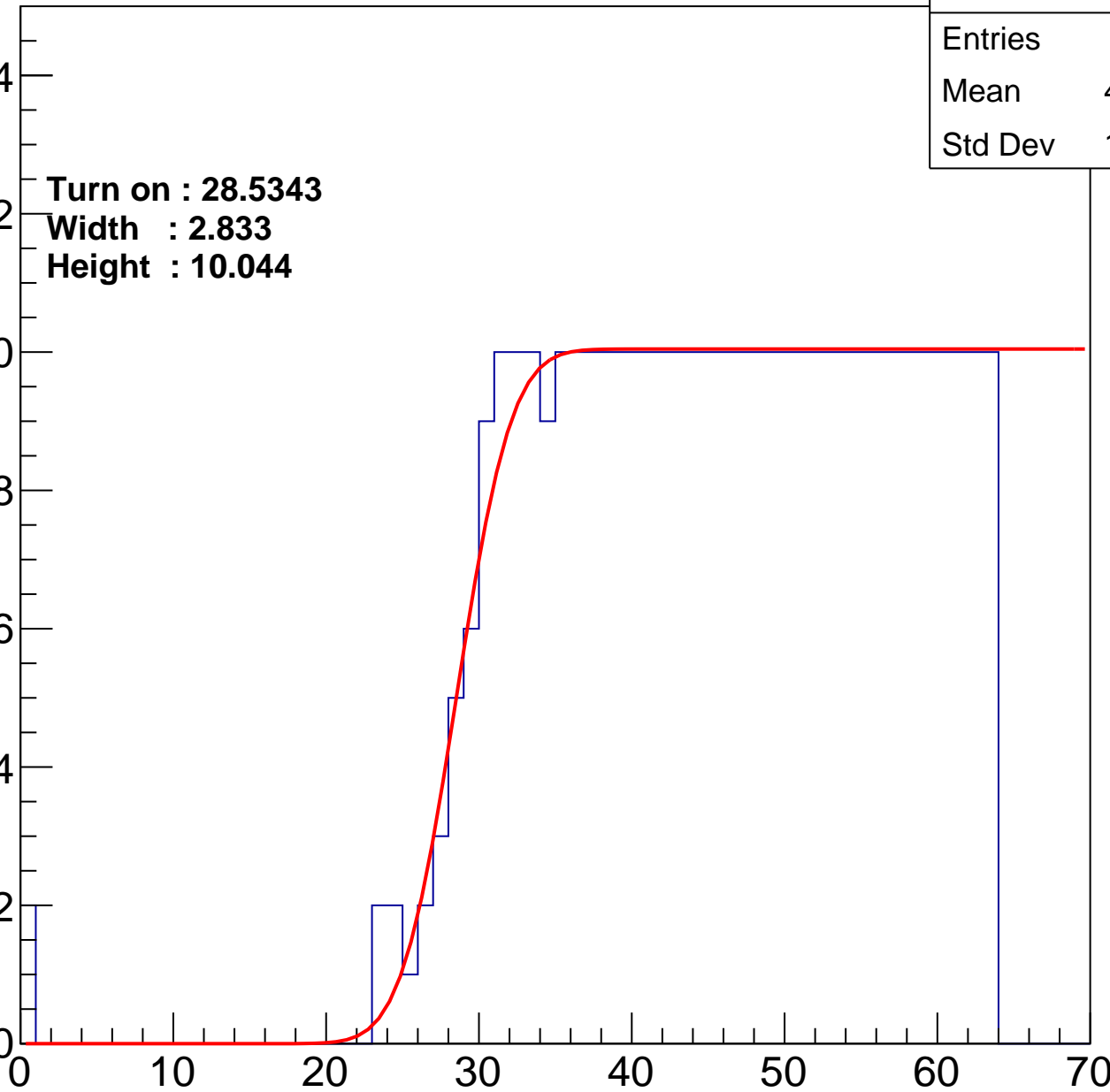
Width : 2.833

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 23.3896
Width : 3.243
Height : 10.015



B1L002S, U17-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.99
Std Dev	12.18

Turn on : 24.9774

Width : 2.829

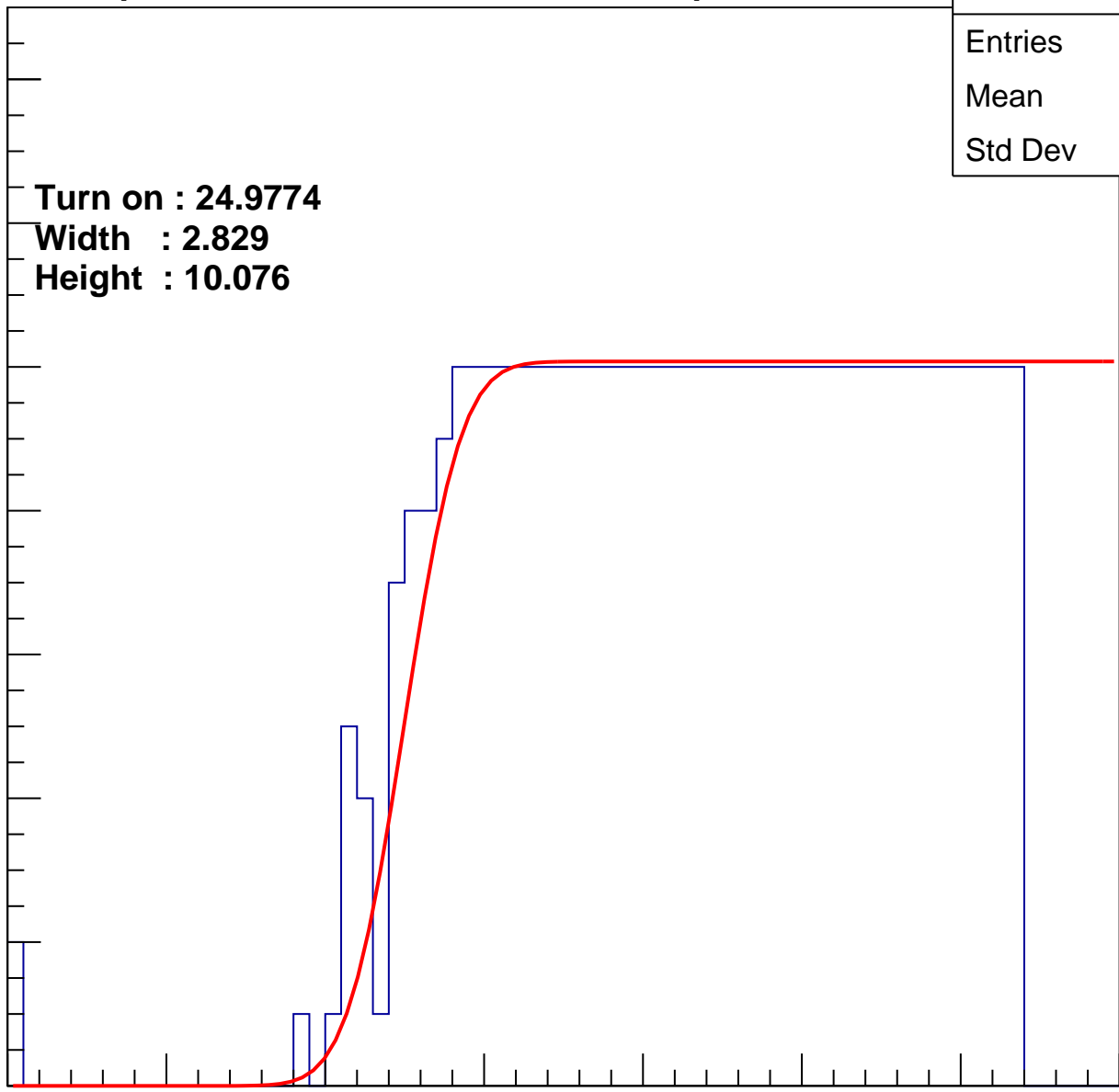
Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U17-ch60

calib_packv5_042523_0143.root, FC#10, port B3

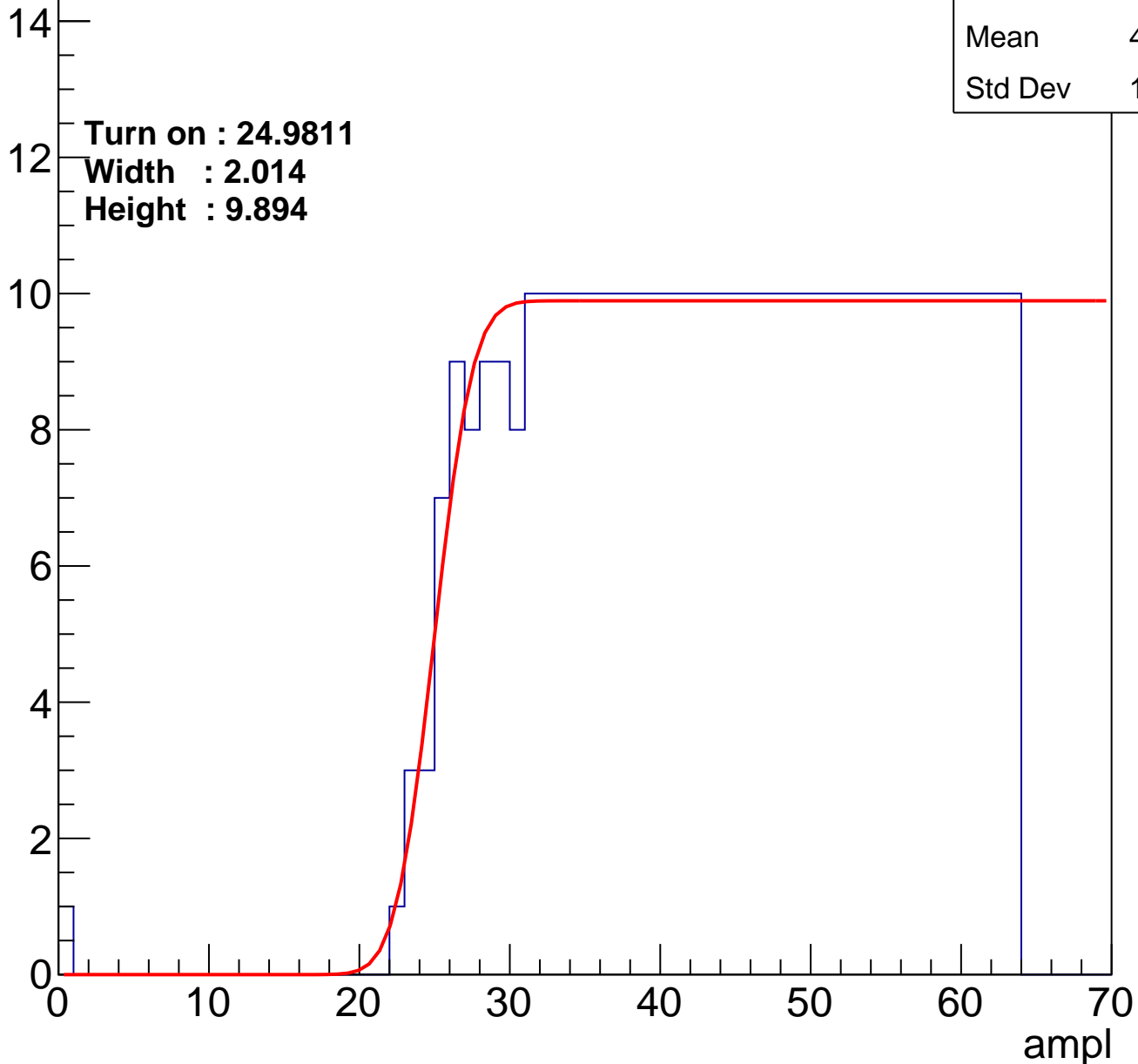
Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 24.9811

Width : 2.014

Height : 9.894

Entry



B1L002S, U17-ch61

calib_packv5_042523_0143.root, FC#10, port B3

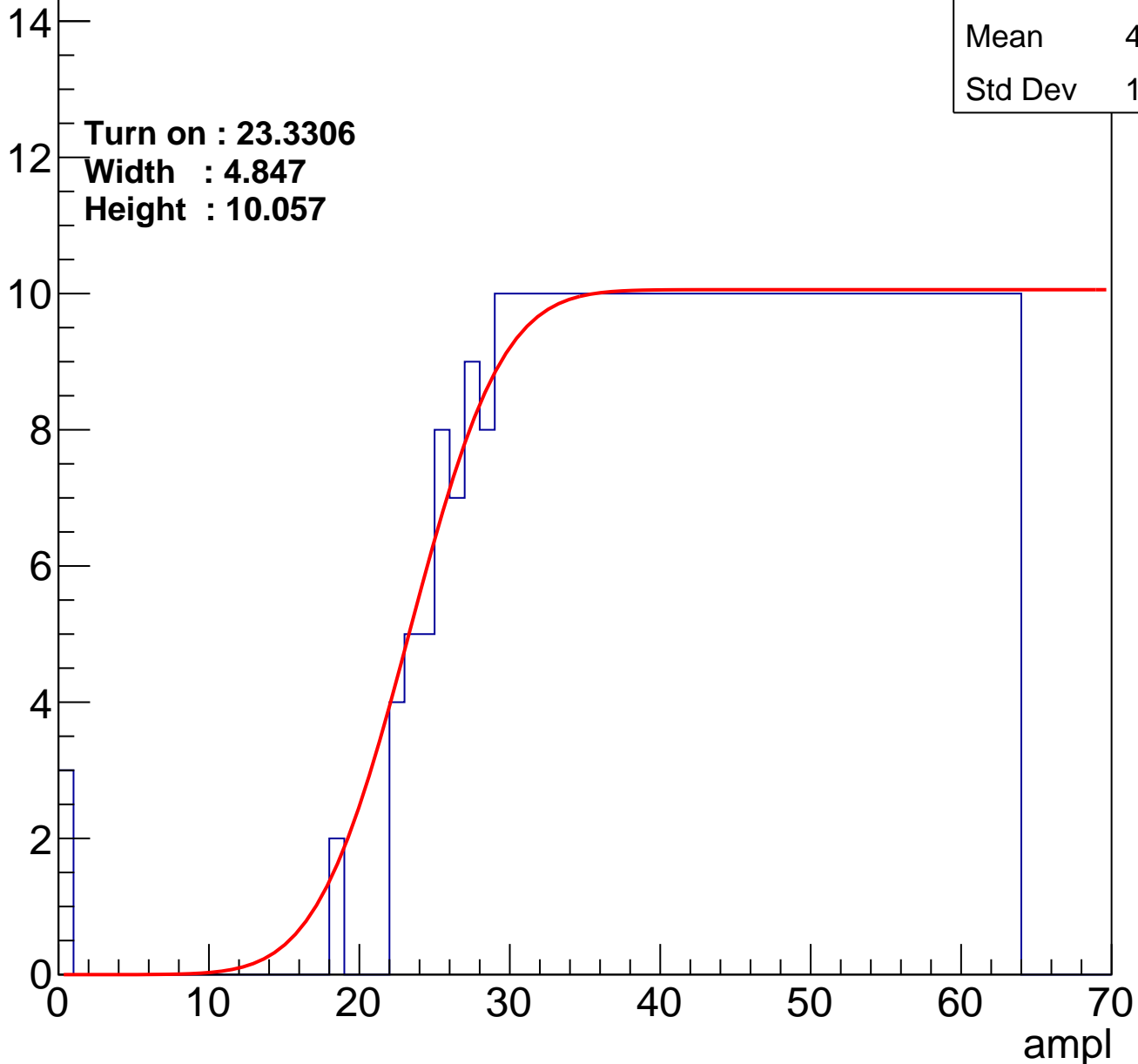
Entries	401
Mean	43.16
Std Dev	12.22

Turn on : 23.3306

Width : 4.847

Height : 10.057

Entry



B1L002S, U17-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.6144

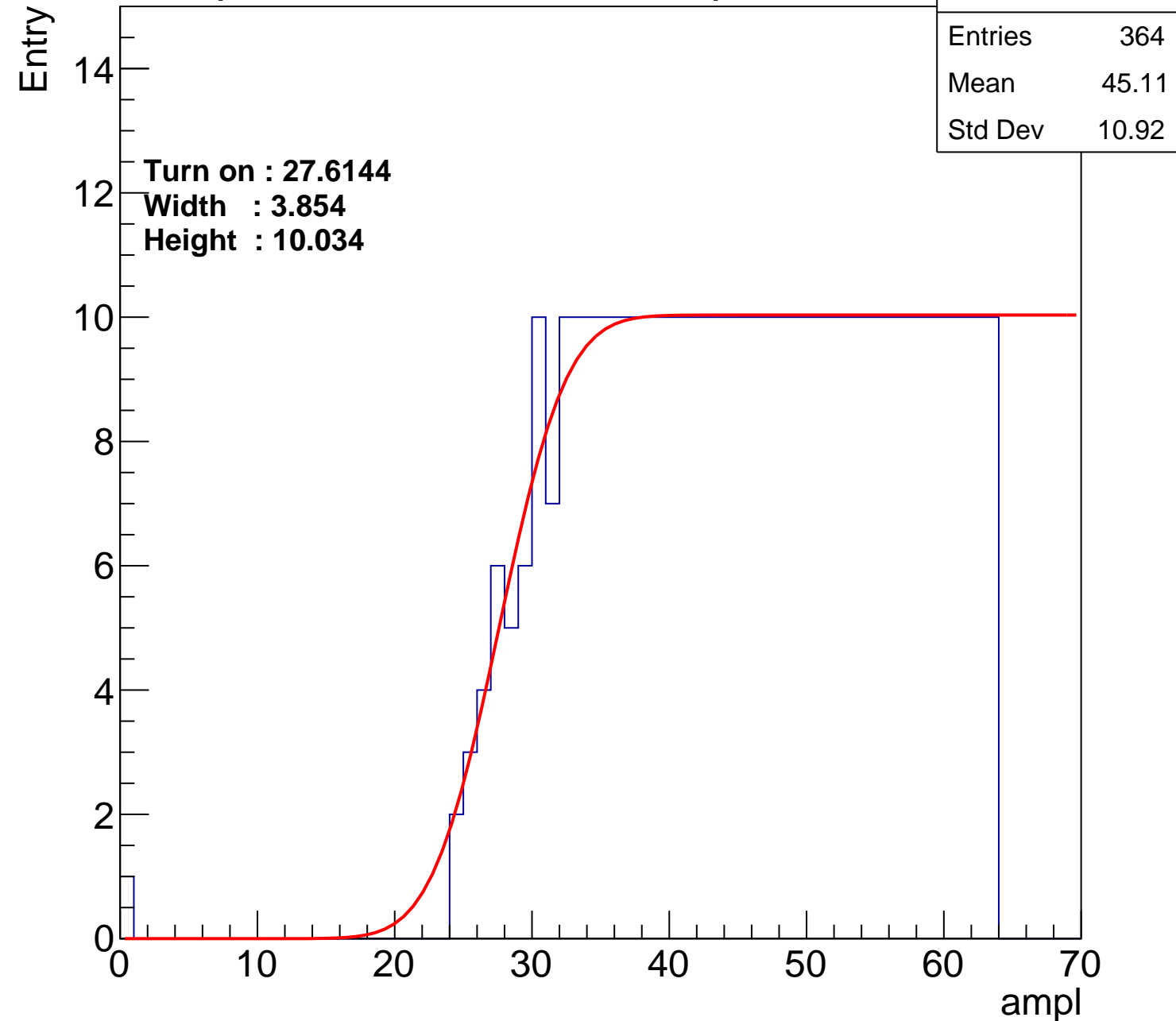
Width : 3.854

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch63

calib_packv5_042523_0143.root, FC#10, port B3

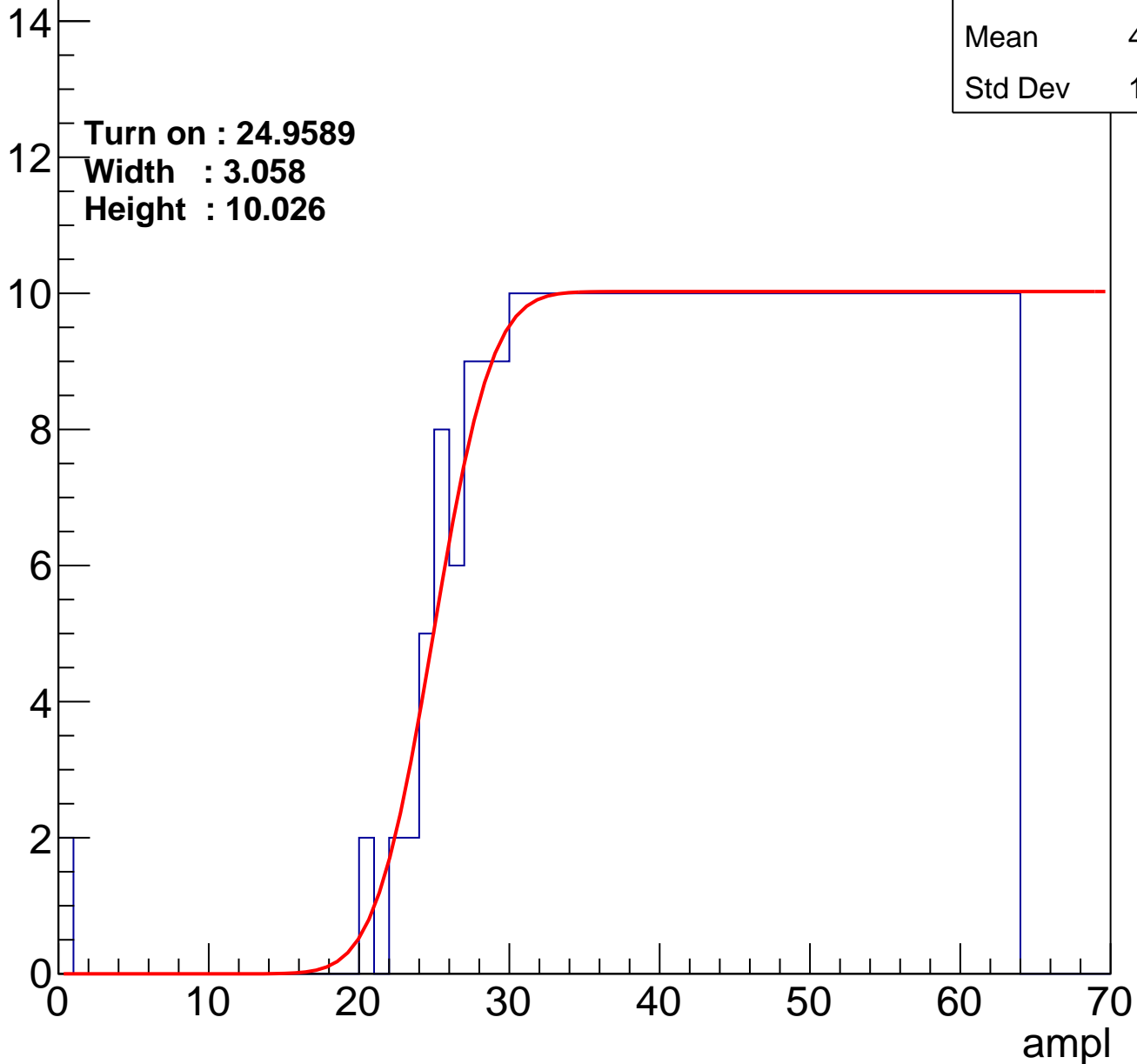
Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 24.9589

Width : 3.058

Height : 10.026

Entry



B1L002S, U17-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.11
Std Dev	11.98

Turn on : 24.0763

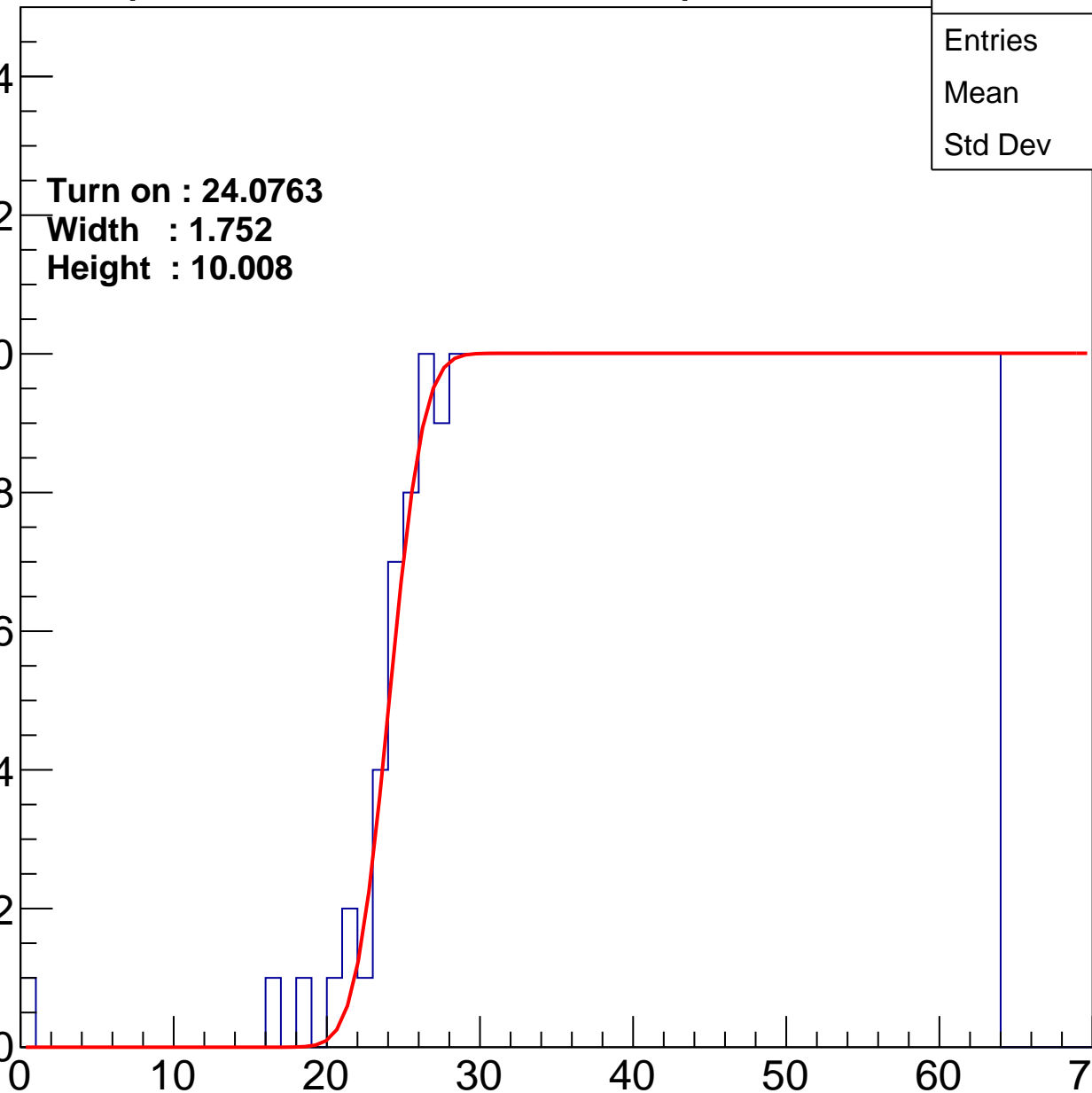
Width : 1.752

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch65

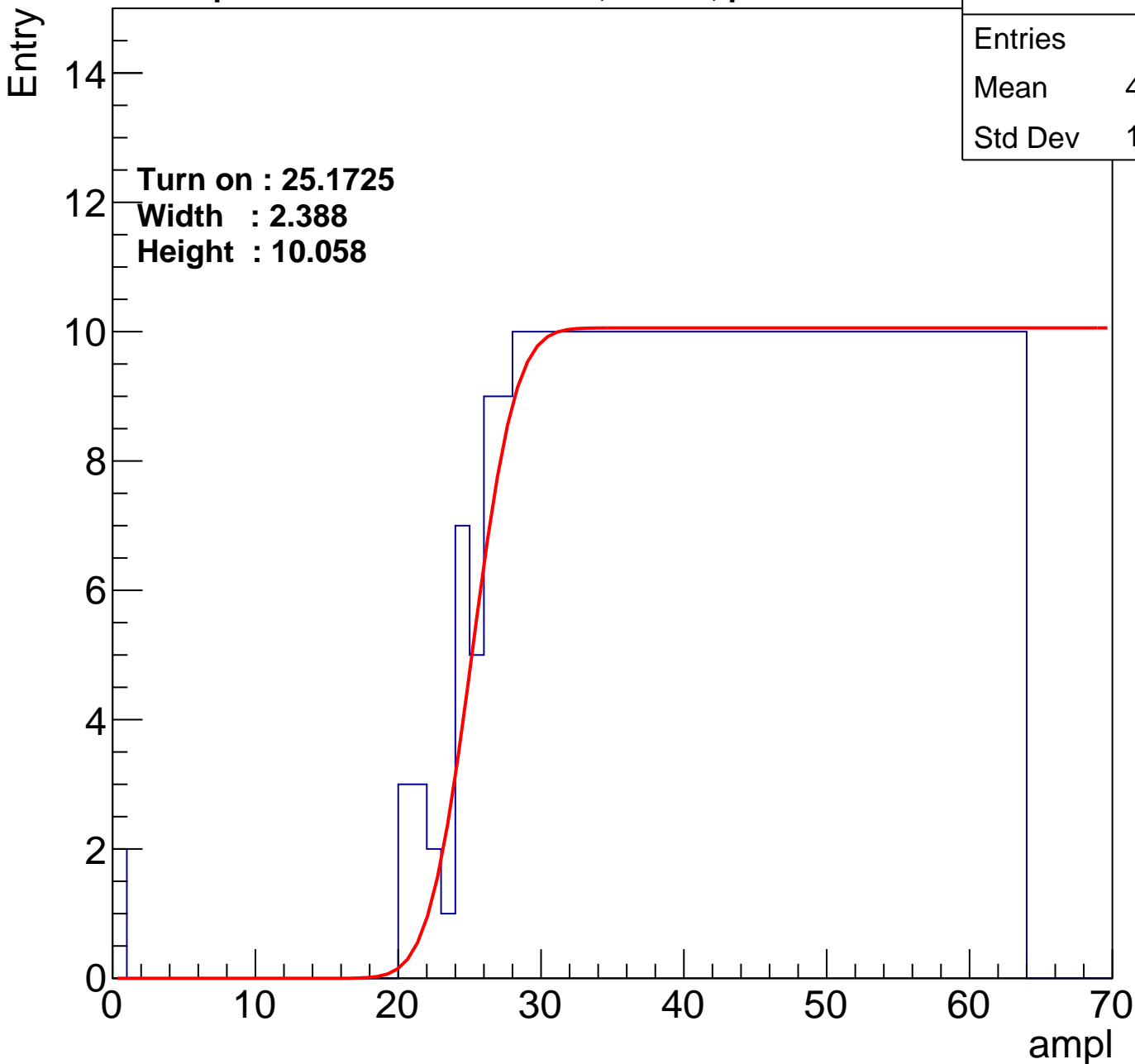
calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.24
Std Dev	12.04

Turn on : 25.1725

Width : 2.388

Height : 10.058



B1L002S, U17-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.17
Std Dev	12.44

Turn on : 25.2961

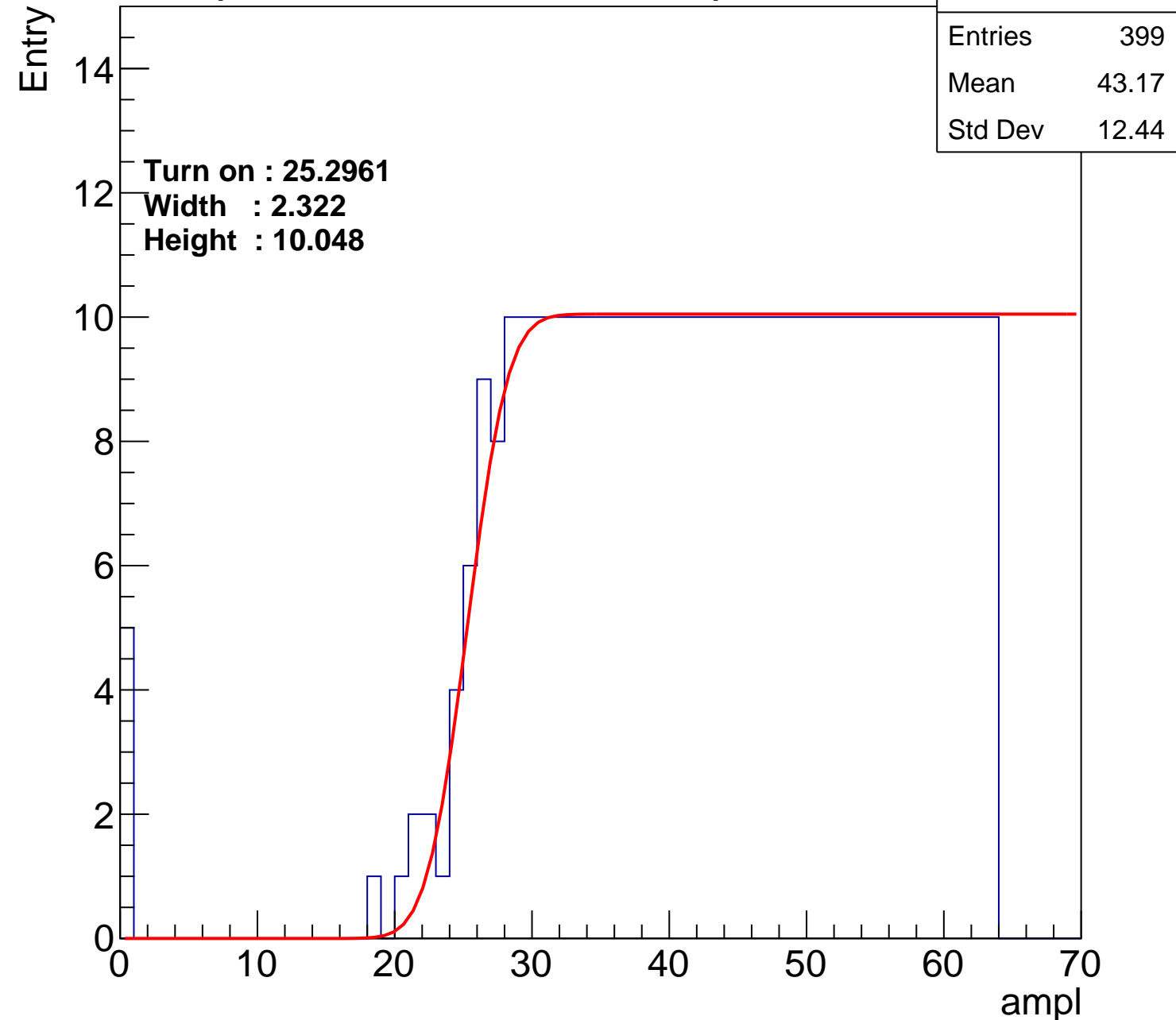
Width : 2.322

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.61
Std Dev	12.28

Turn on : 25.7226

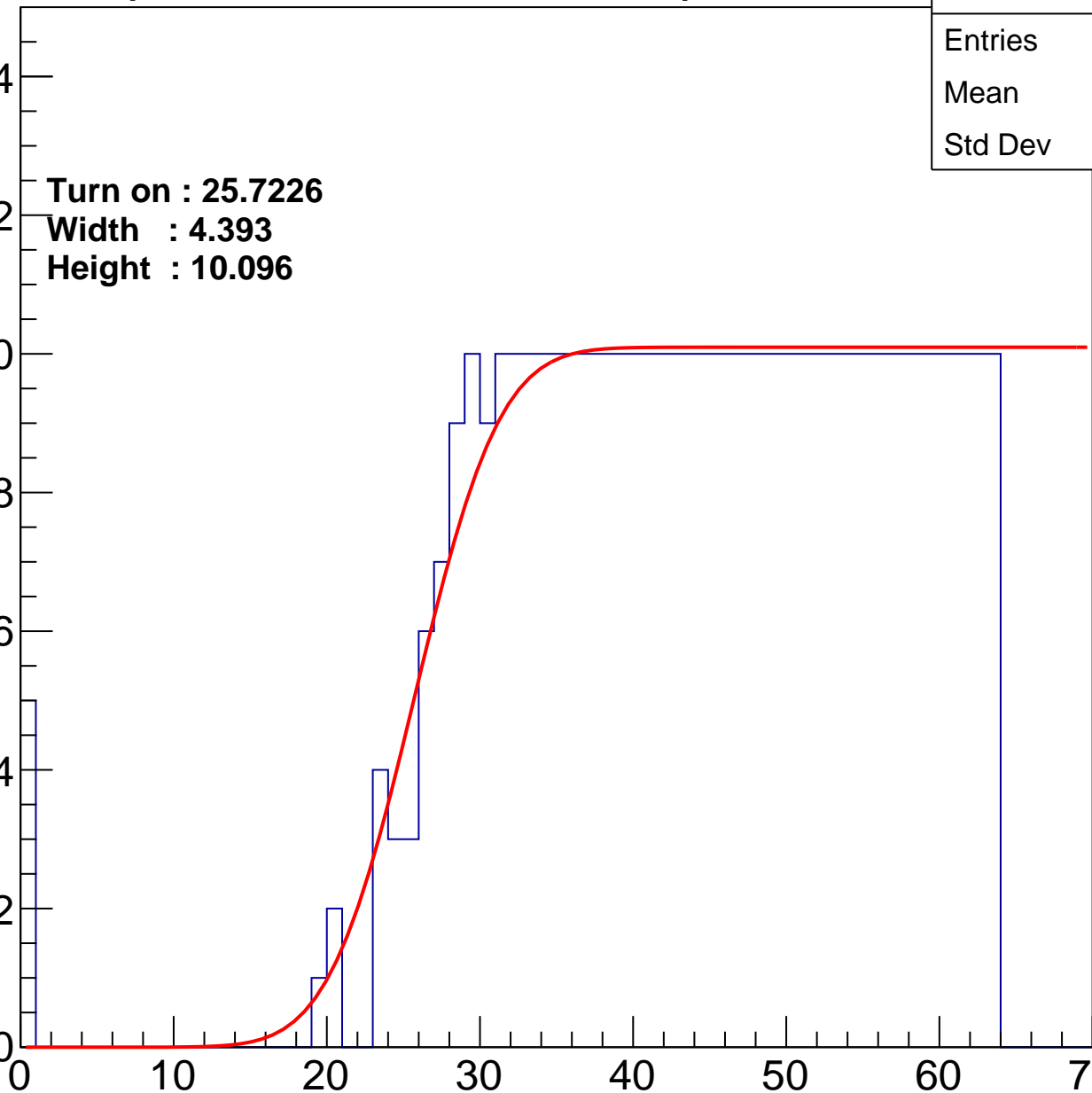
Width : 4.393

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.9
Std Dev	11.84

Turn on : 26.2849

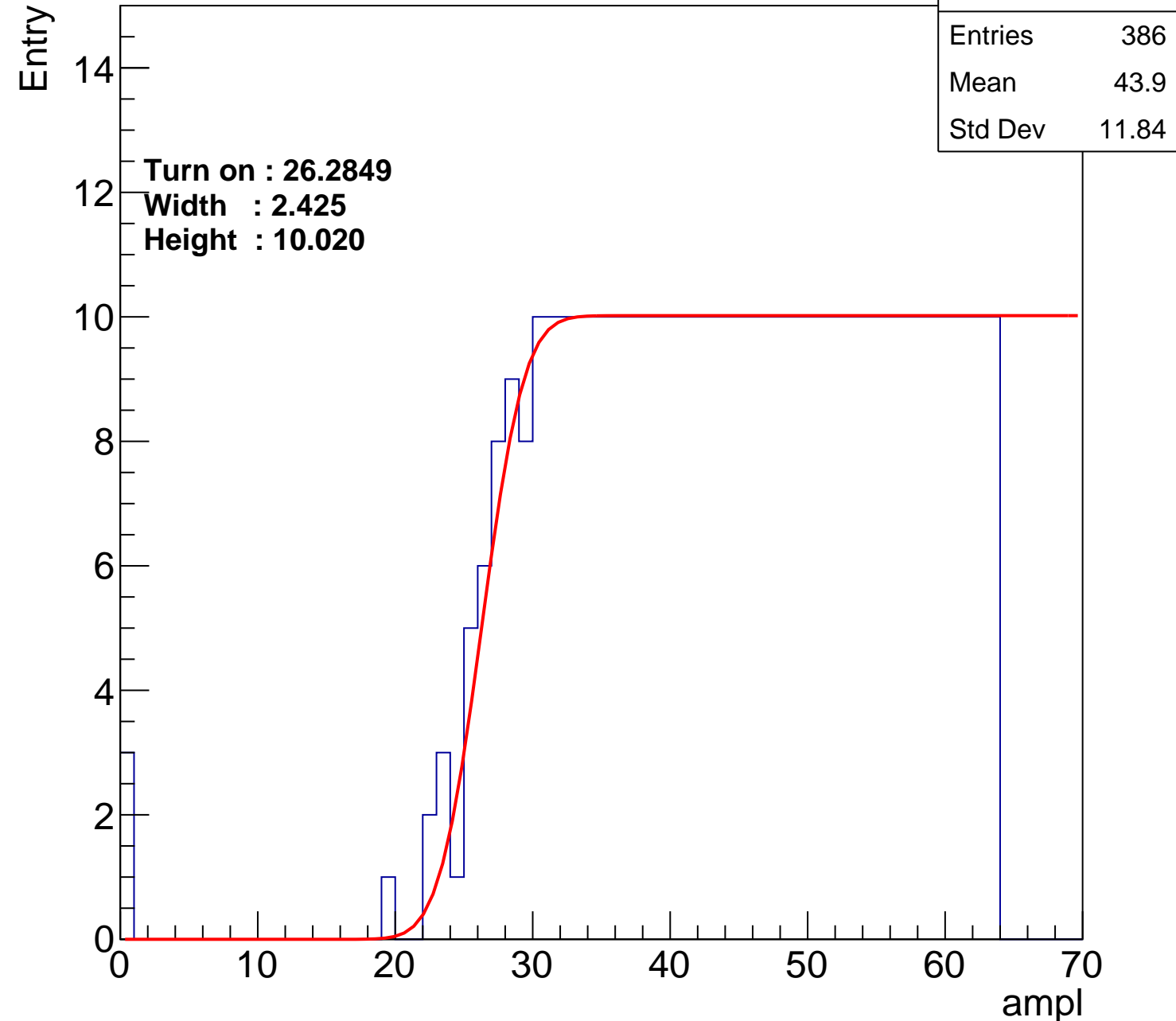
Width : 2.425

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch69

calib_packv5_042523_0143.root, FC#10, port B3

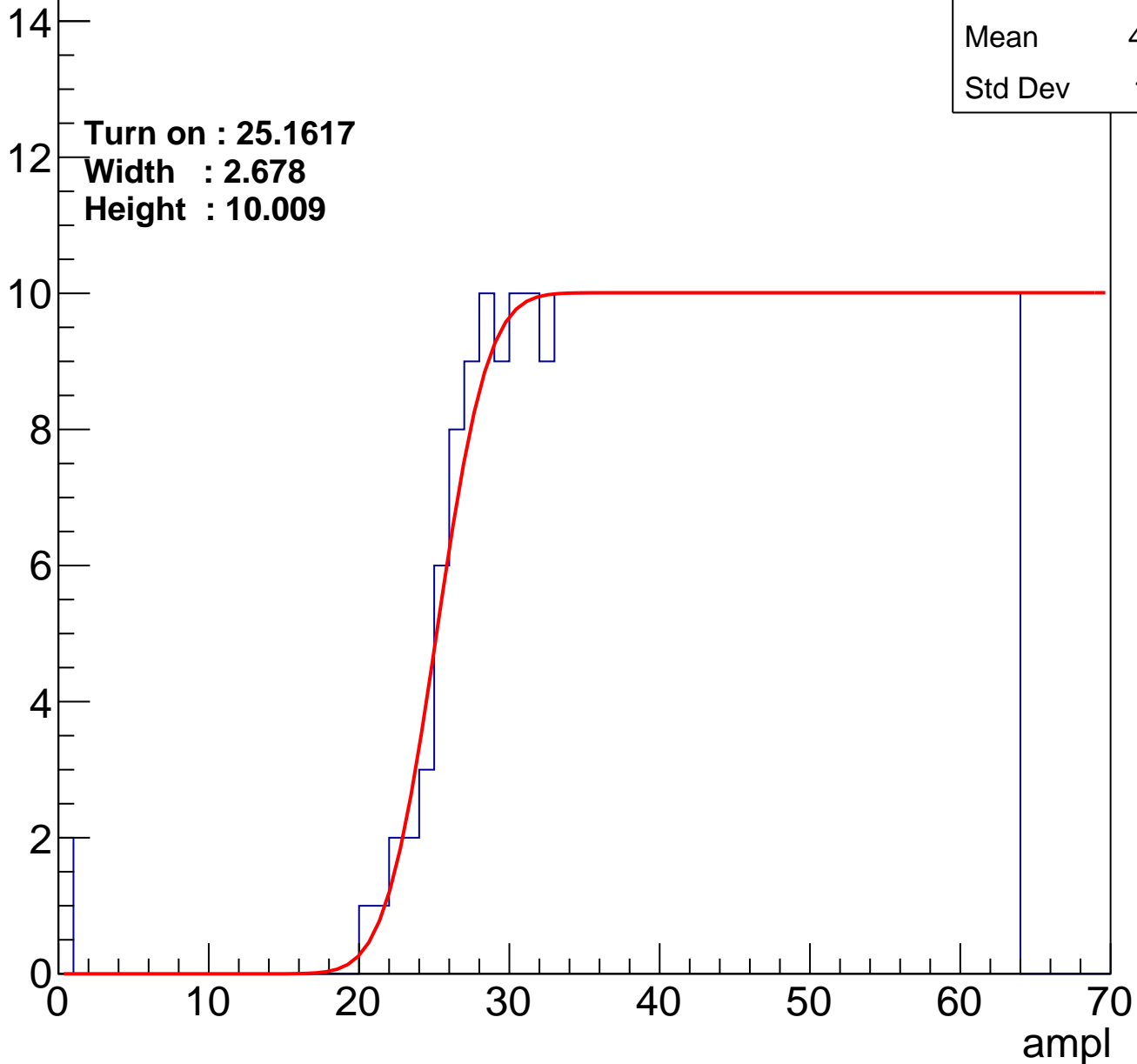
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.1617

Width : 2.678

Height : 10.009

Entry



B1L002S, U17-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.92
Std Dev	12.39

Turn on : 23.7334

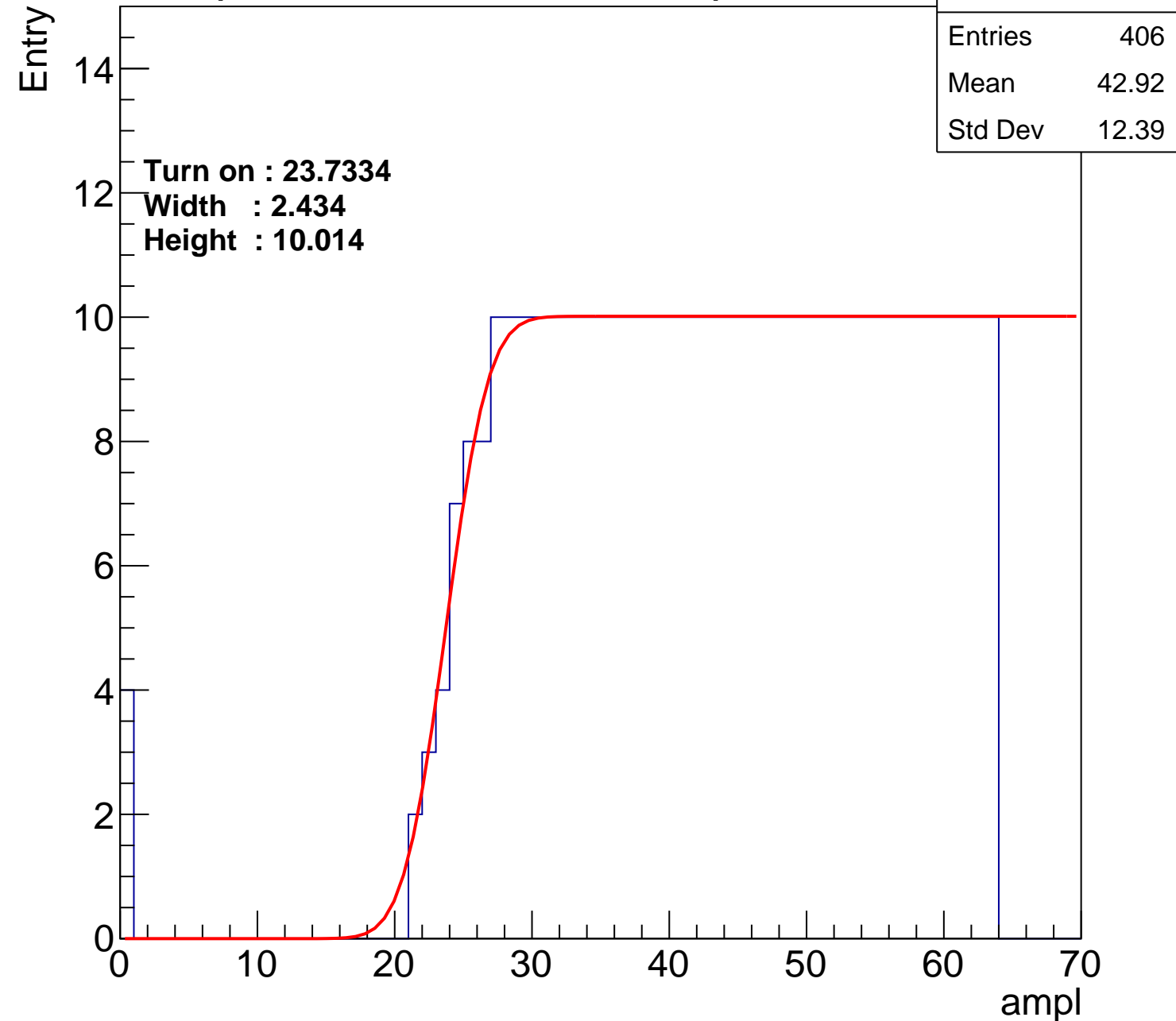
Width : 2.434

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.43
Std Dev	11.78

Turn on : 24.3322

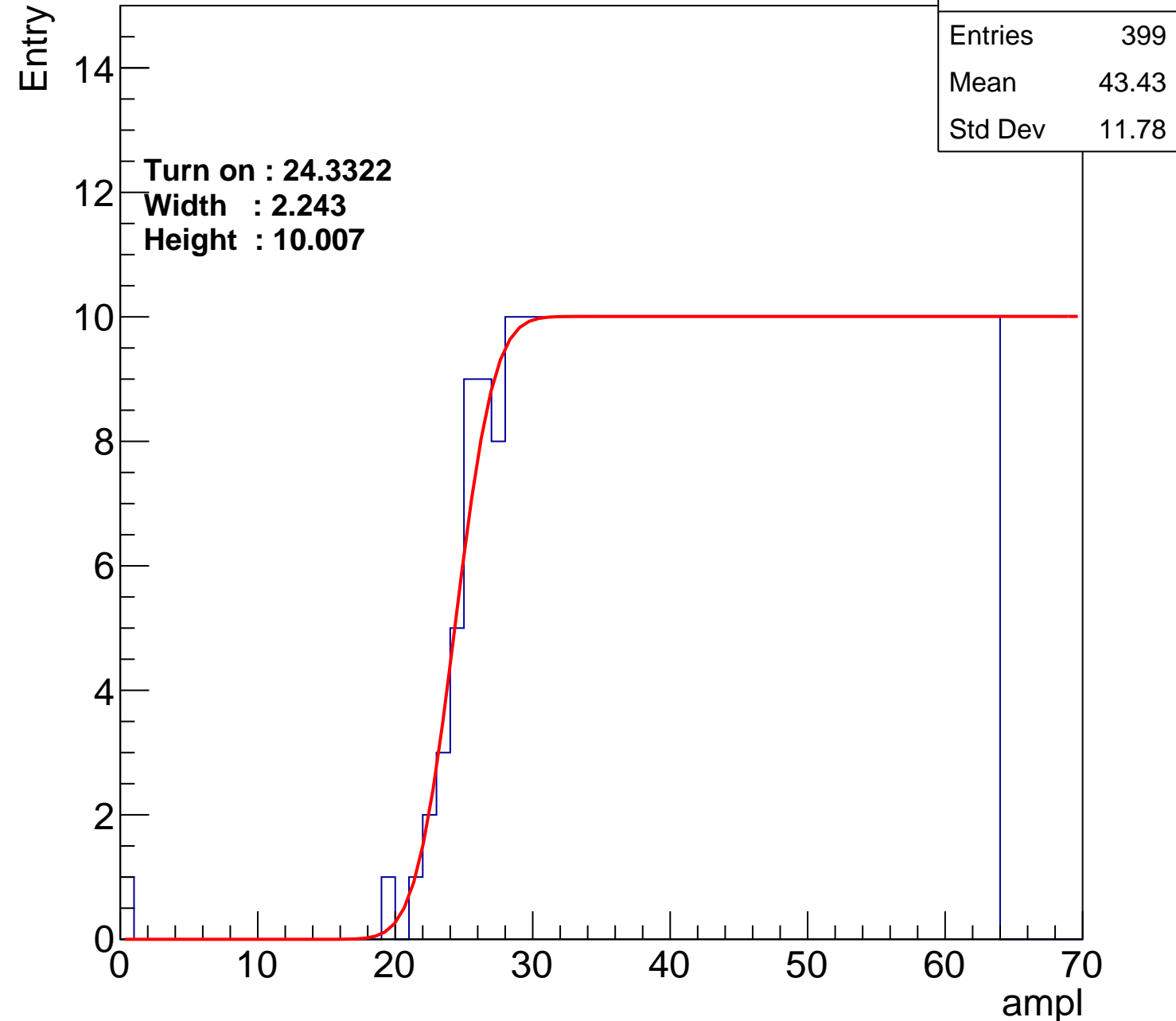
Width : 2.243

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.3
Std Dev	12.26

Turn on : 24.1245

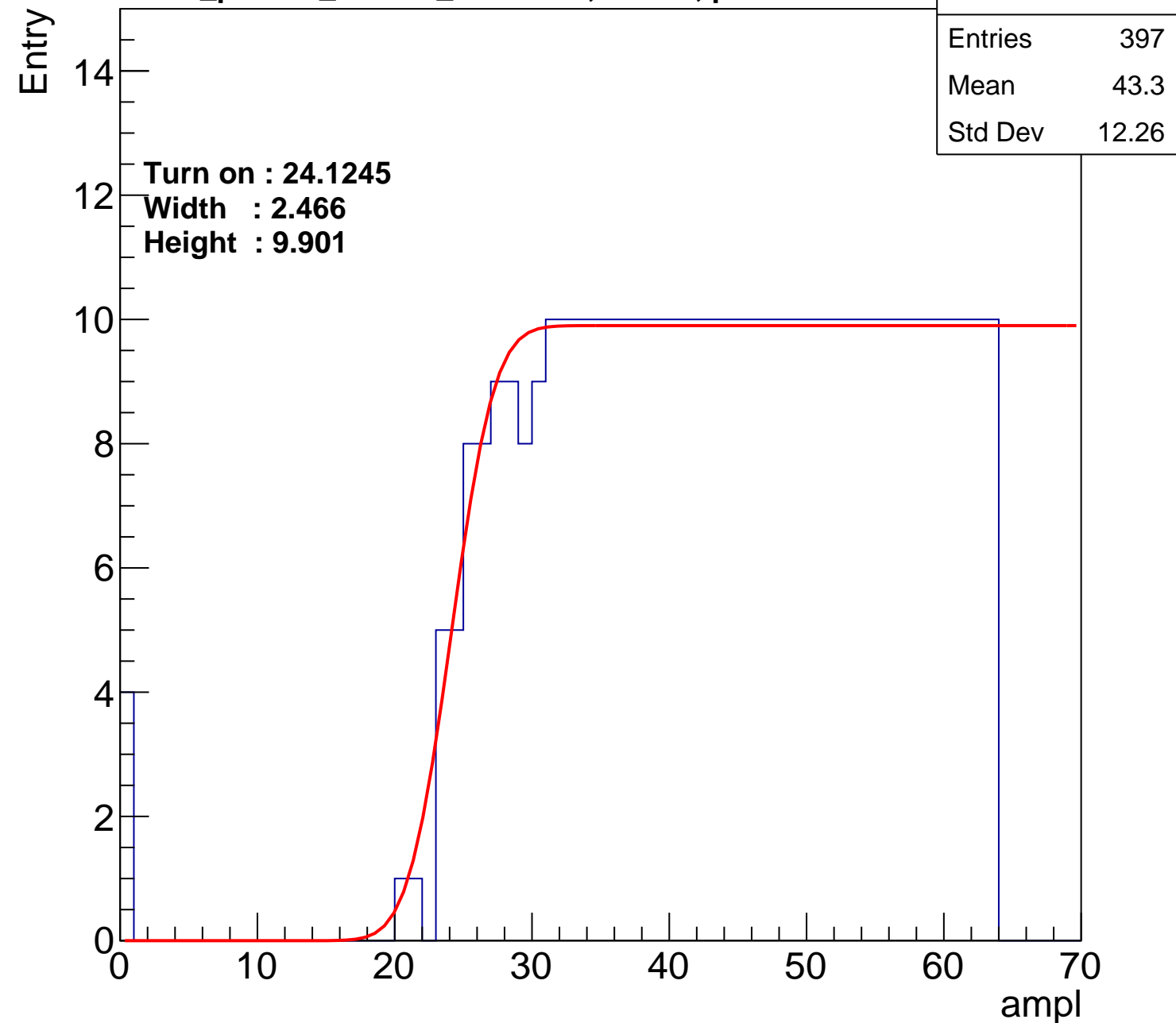
Width : 2.466

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.8
Std Dev	12.53

Turn on : 23.9428

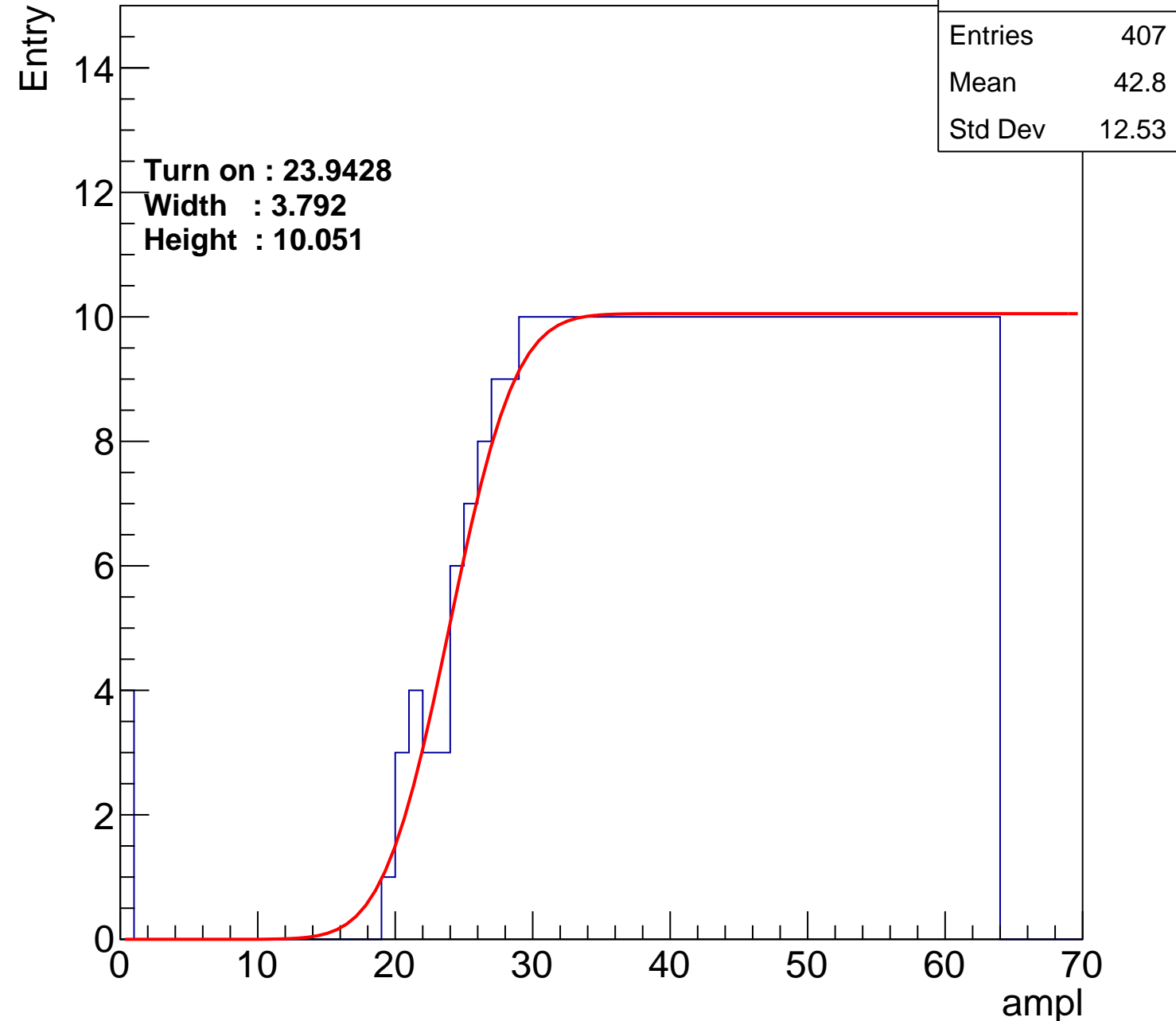
Width : 3.792

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.5267

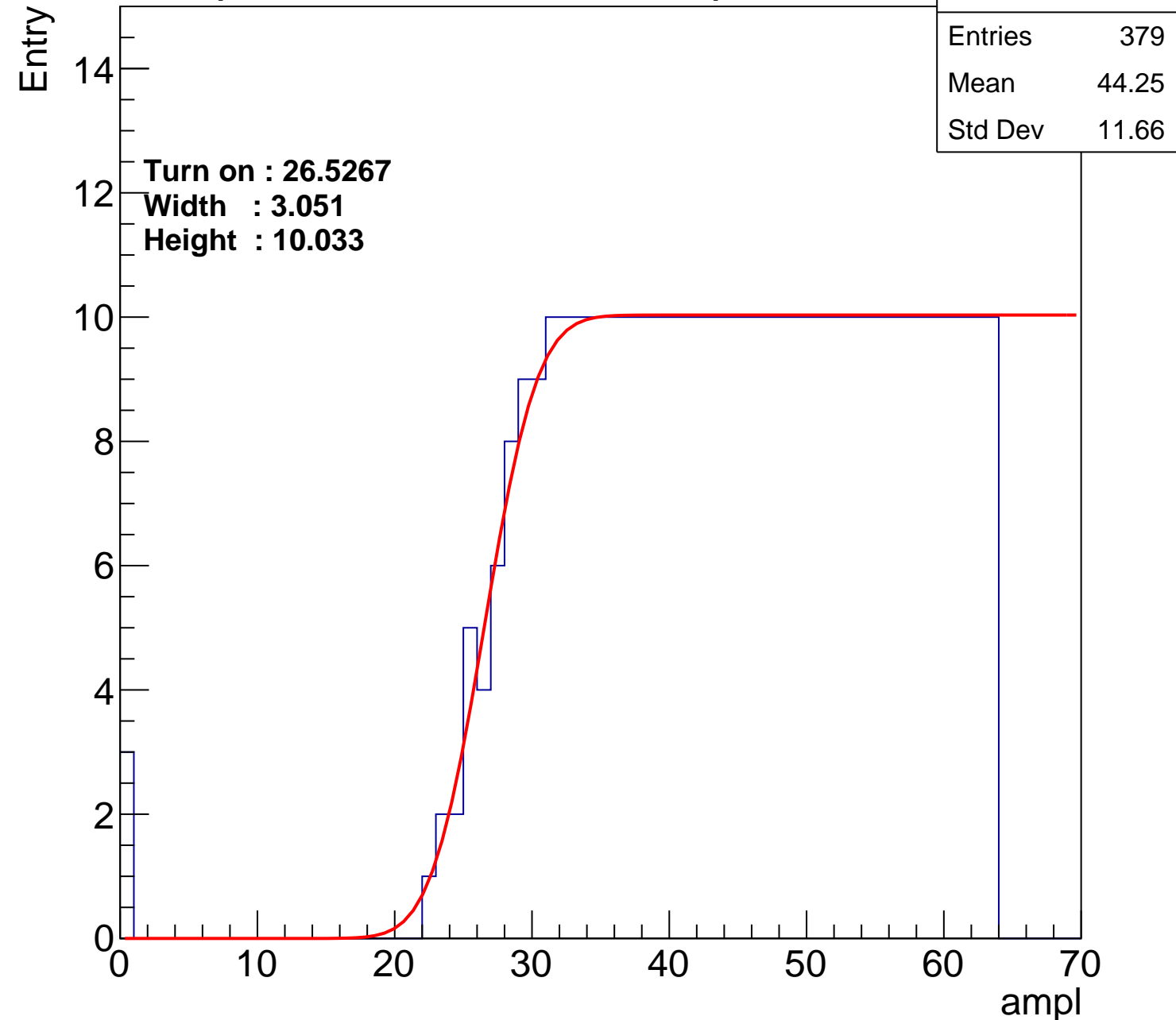
Width : 3.051

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch75

calib_packv5_042523_0143.root, FC#10, port B3

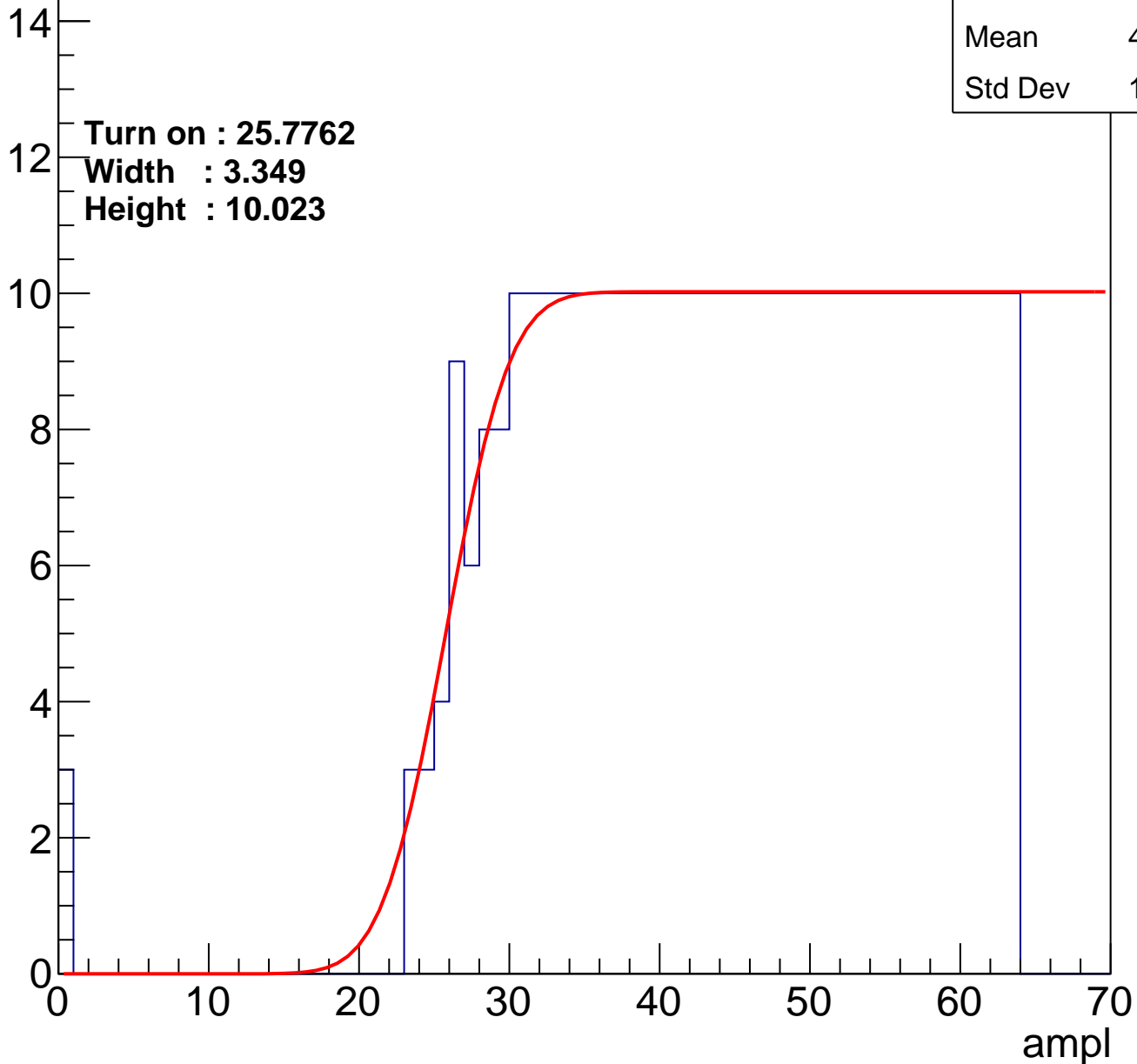
Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 25.7762

Width : 3.349

Height : 10.023

Entry



B1L002S, U17-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.77
Std Dev	12.53

Turn on : 23.8528

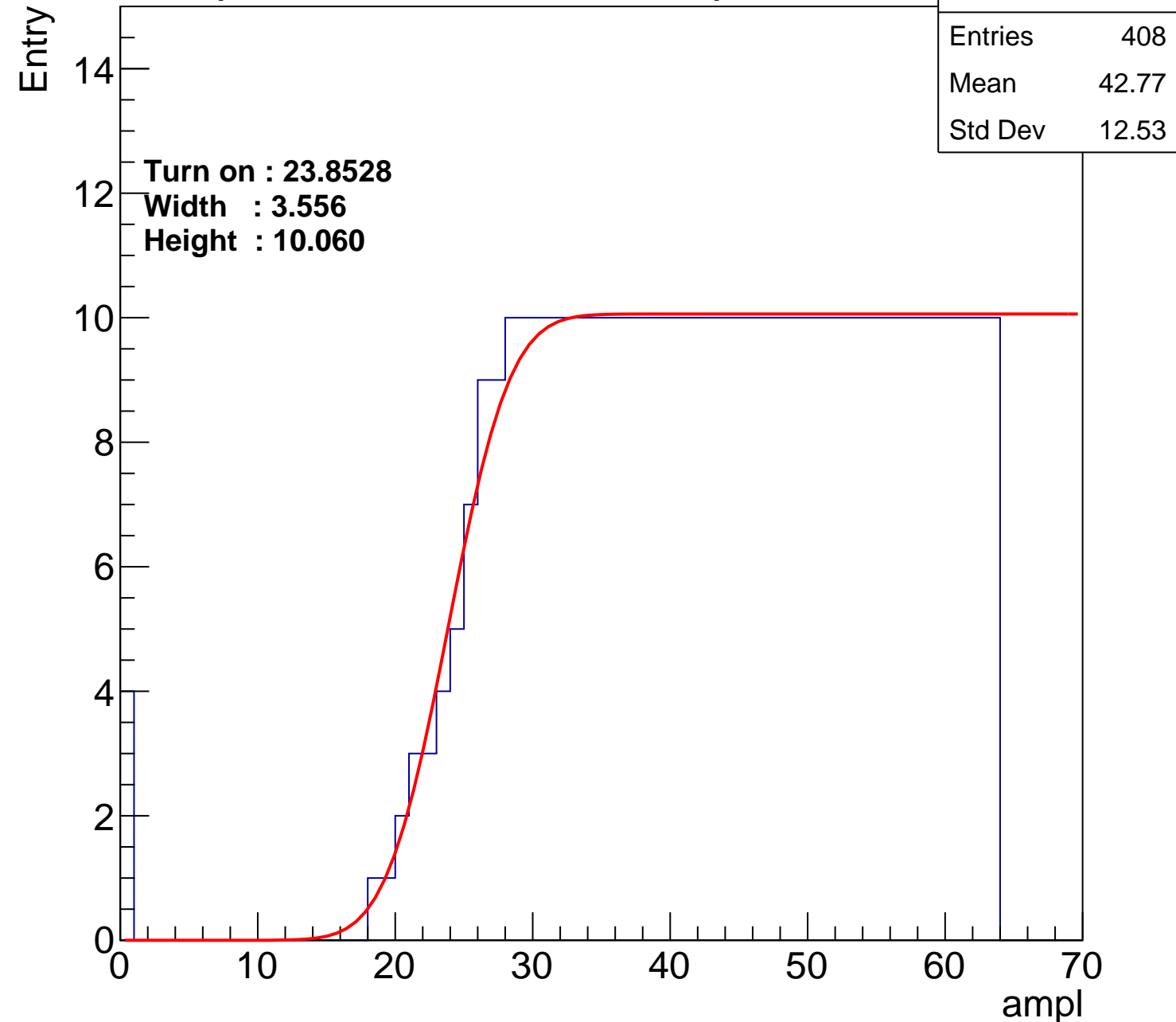
Width : 3.556

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43
Std Dev	12.26

Turn on : 24.4034

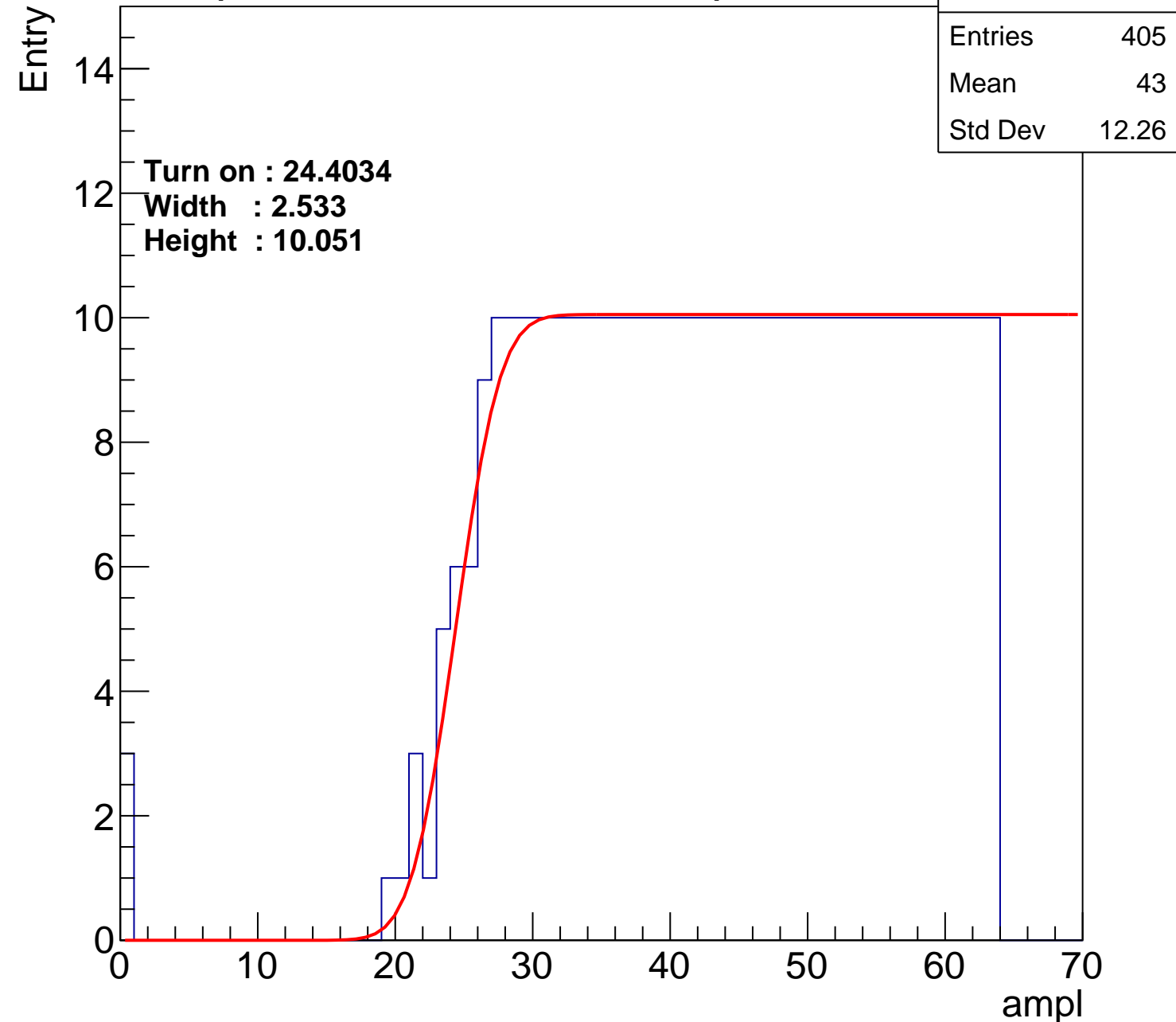
Width : 2.533

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.6997

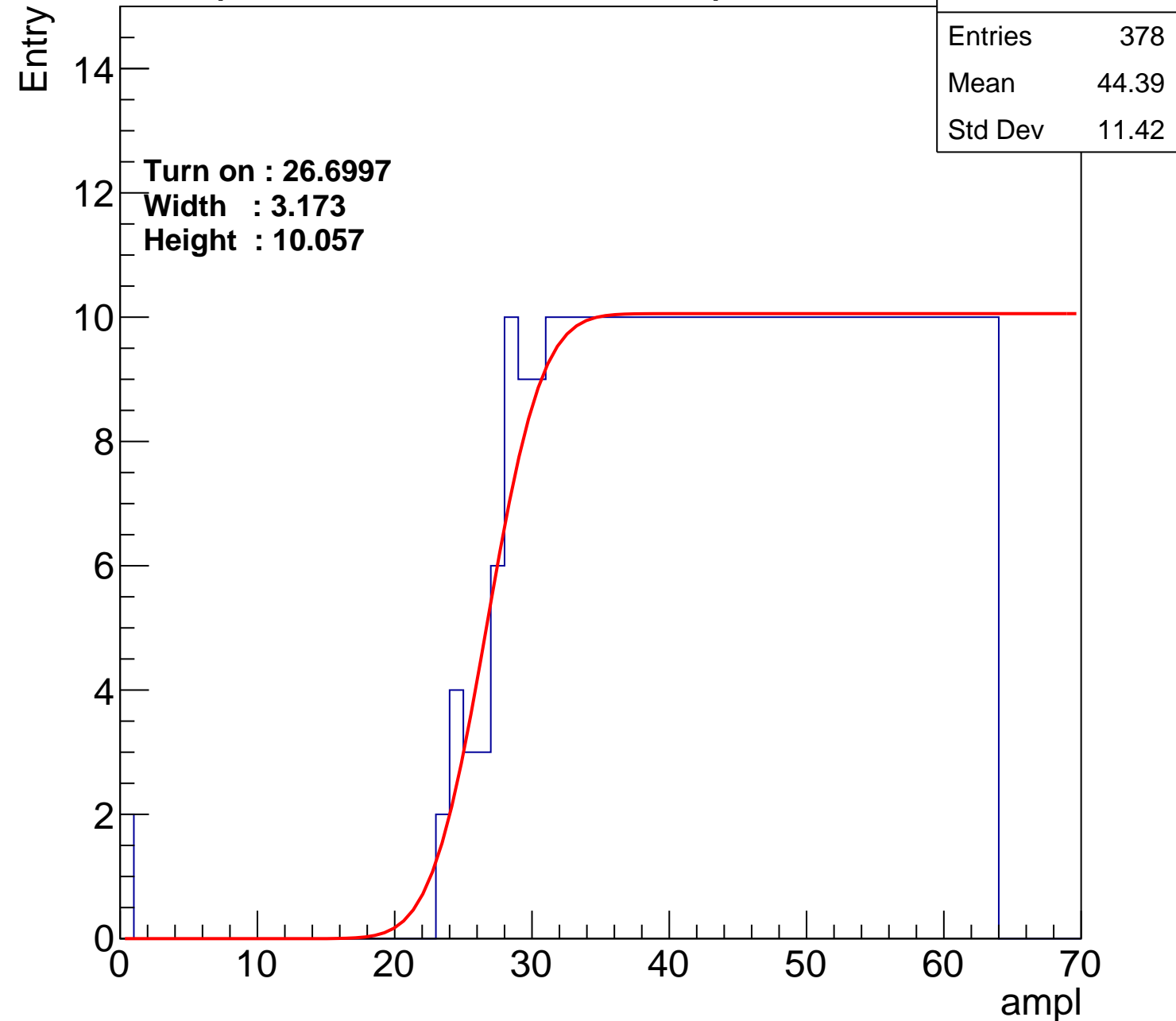
Width : 3.173

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch79

calib_packv5_042523_0143.root, FC#10, port B3

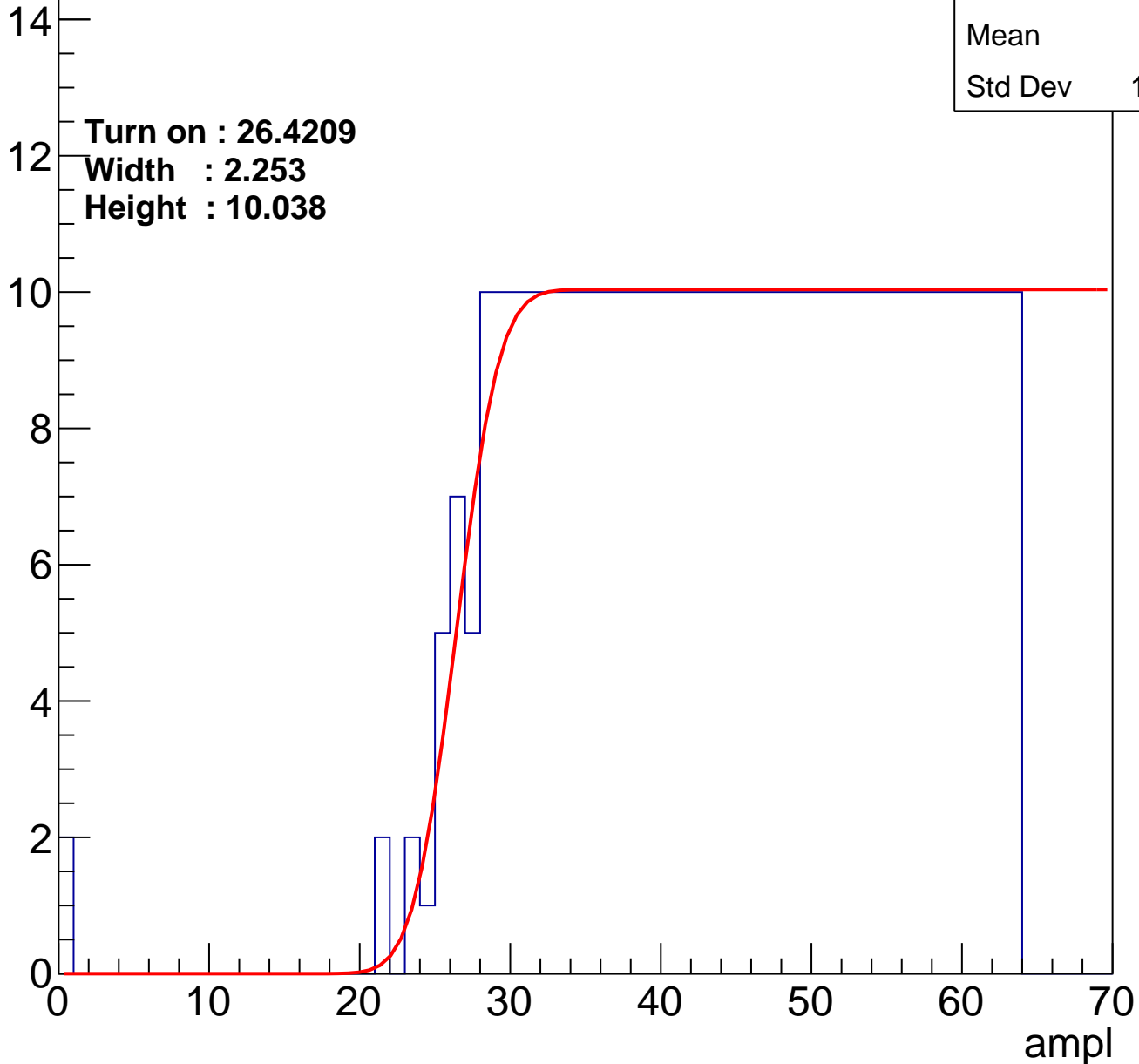
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 26.4209

Width : 2.253

Height : 10.038

Entry



B1L002S, U17-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 24.9921

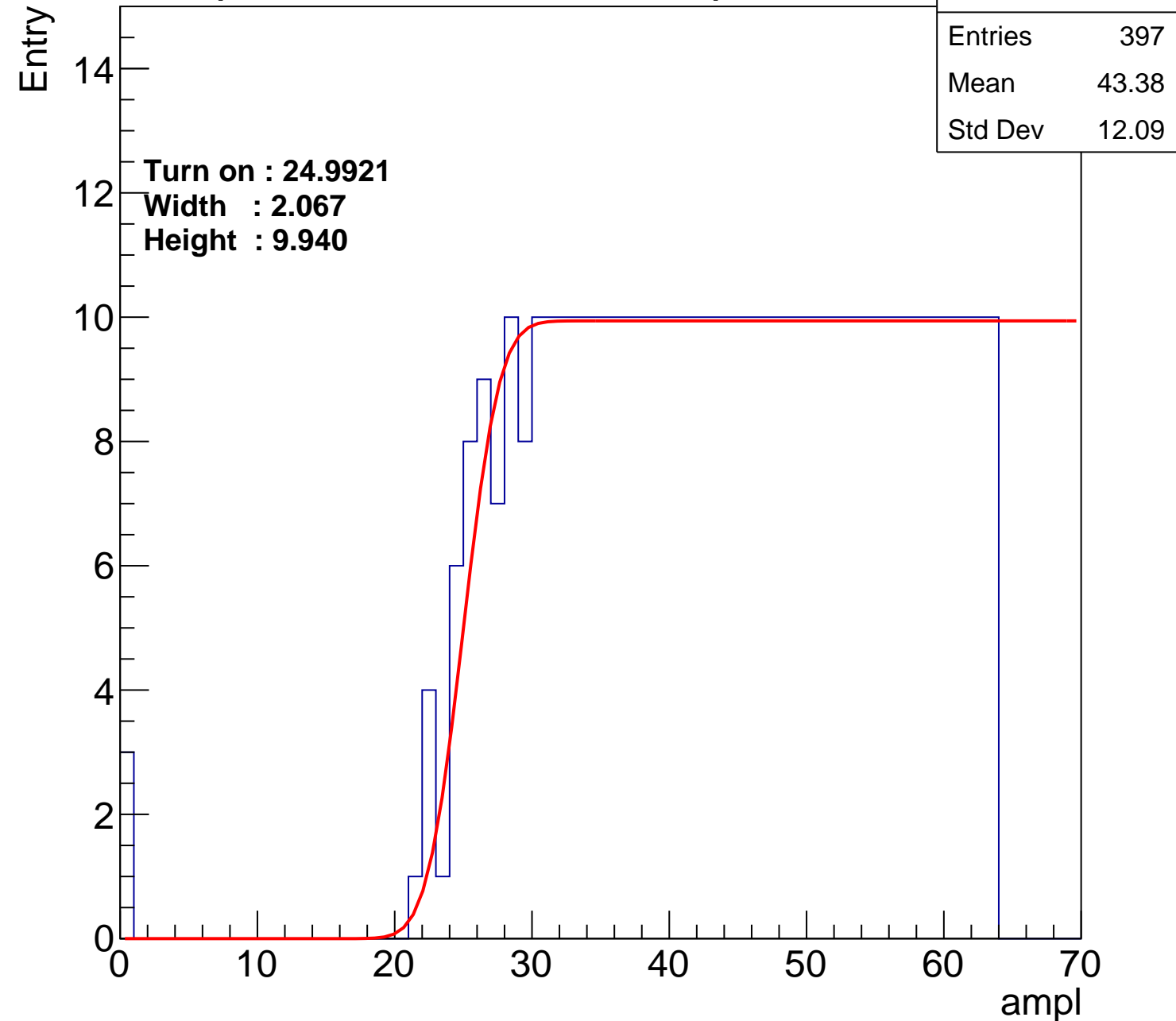
Width : 2.067

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.21
Std Dev	12.14

Turn on : 24.1521

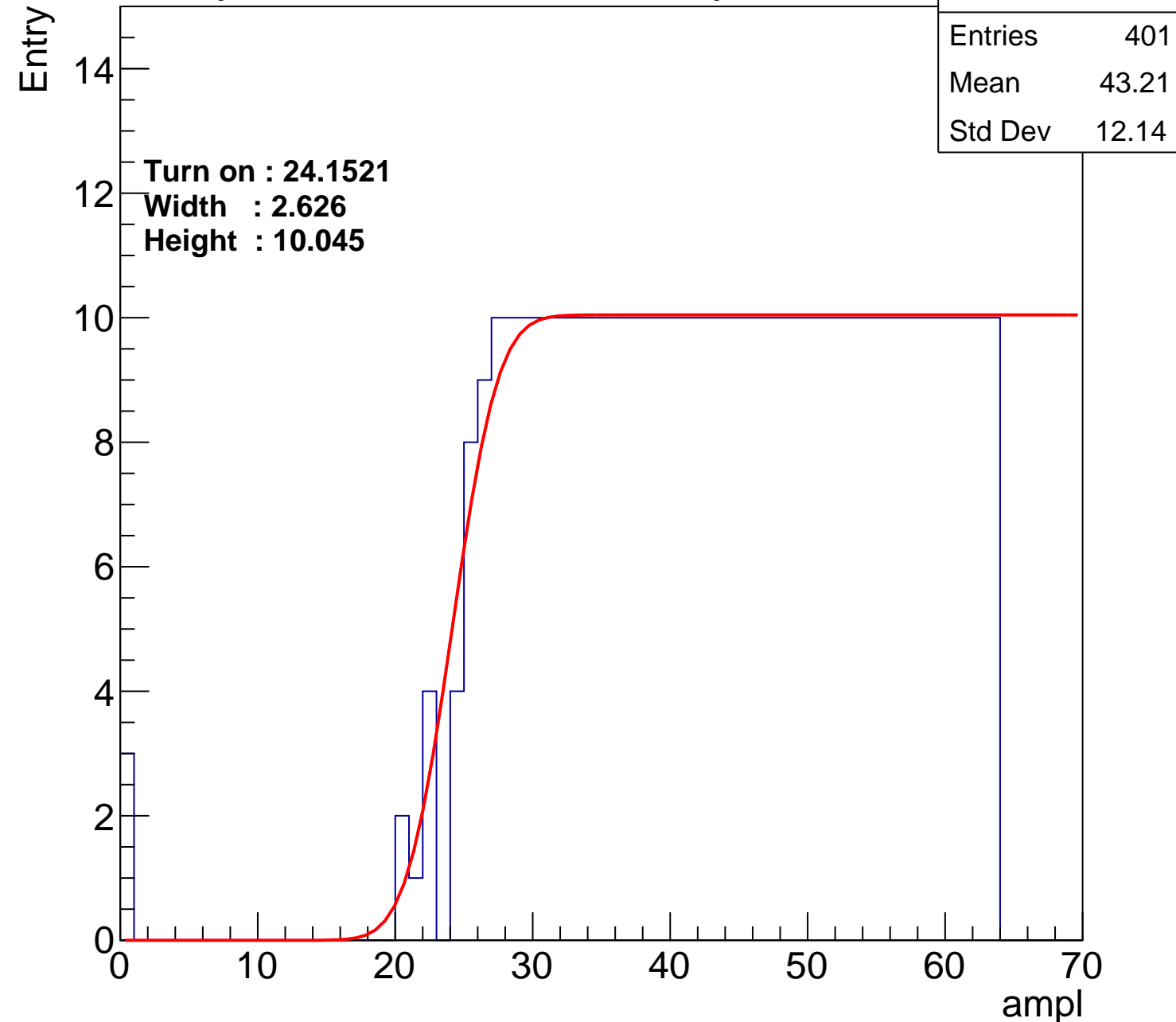
Width : 2.626

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 25.3232

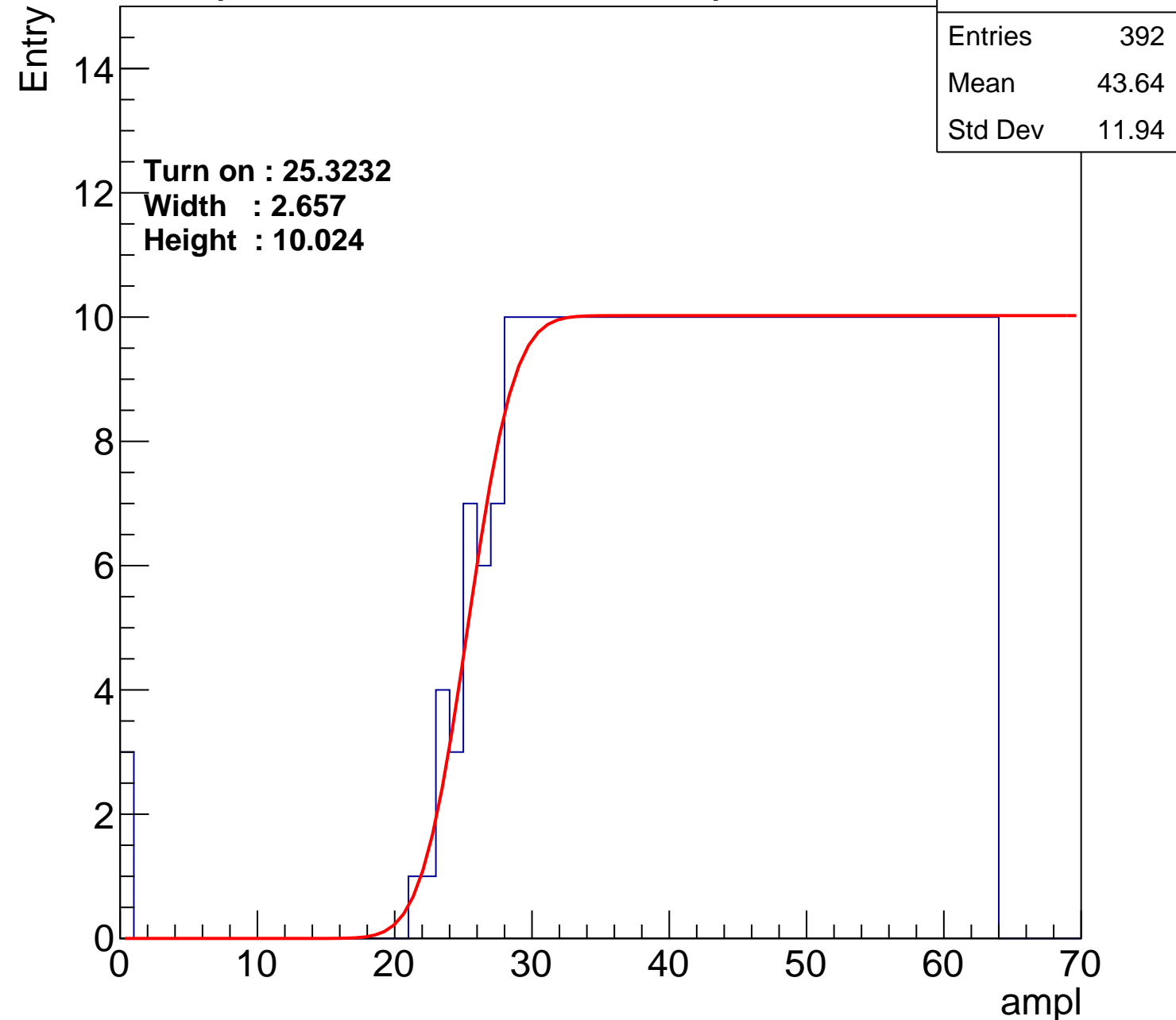
Width : 2.657

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch83

calib_packv5_042523_0143.root, FC#10, port B3

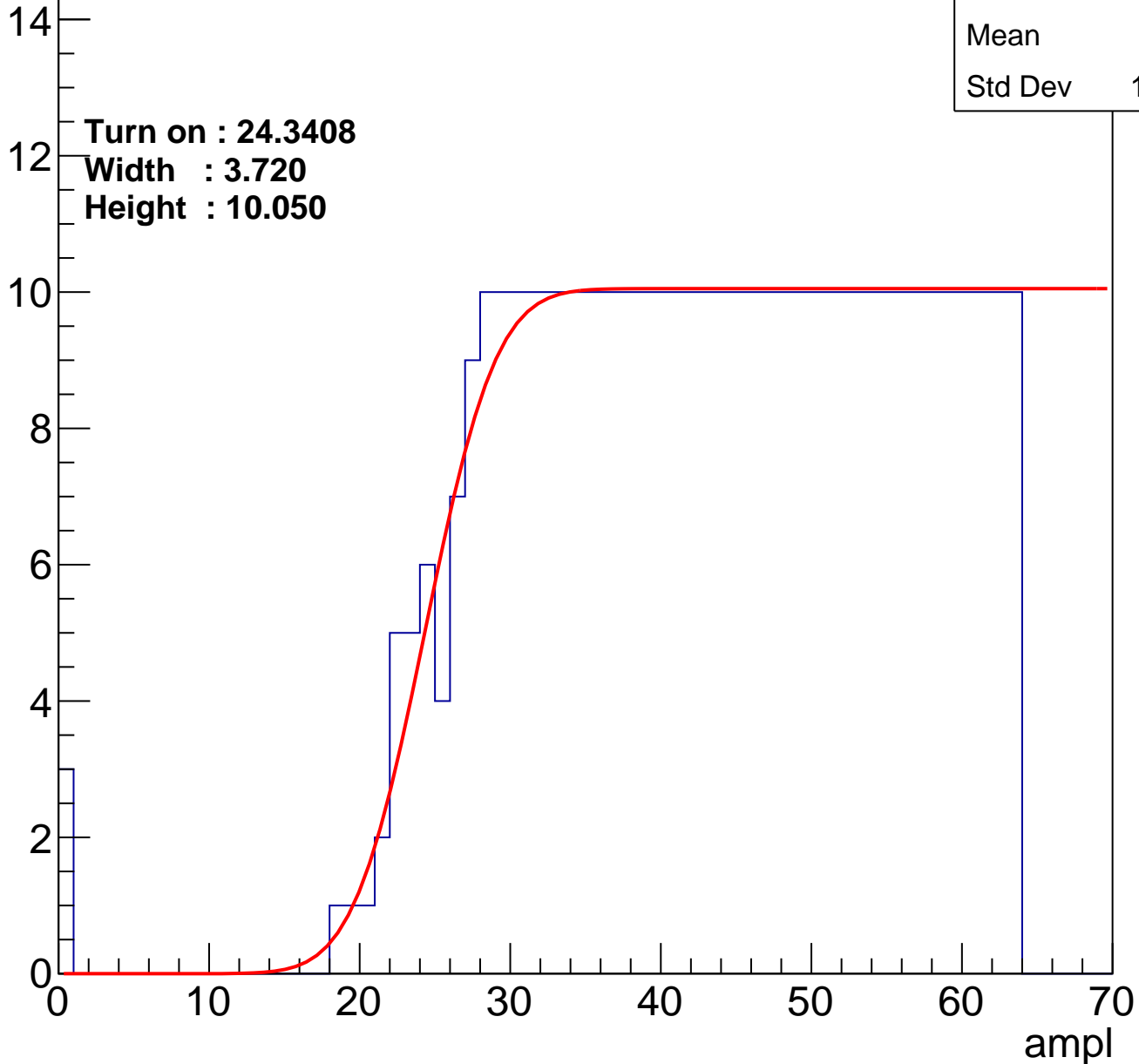
Entries	404
Mean	43
Std Dev	12.32

Turn on : 24.3408

Width : 3.720

Height : 10.050

Entry



B1L002S, U17-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.86

Turn on : 24.7187

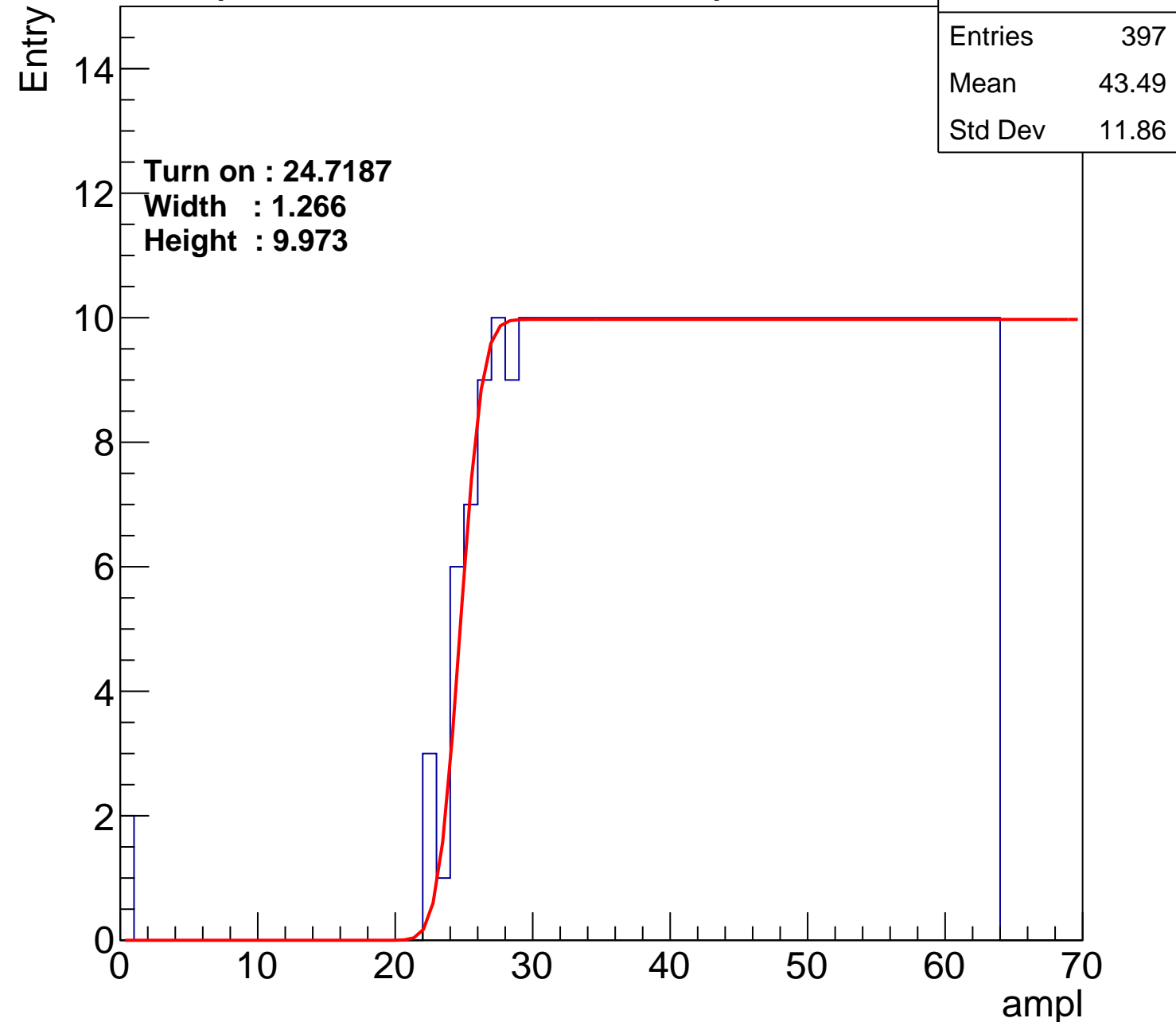
Width : 1.266

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.61
Std Dev	12.37

Turn on : 23.0700

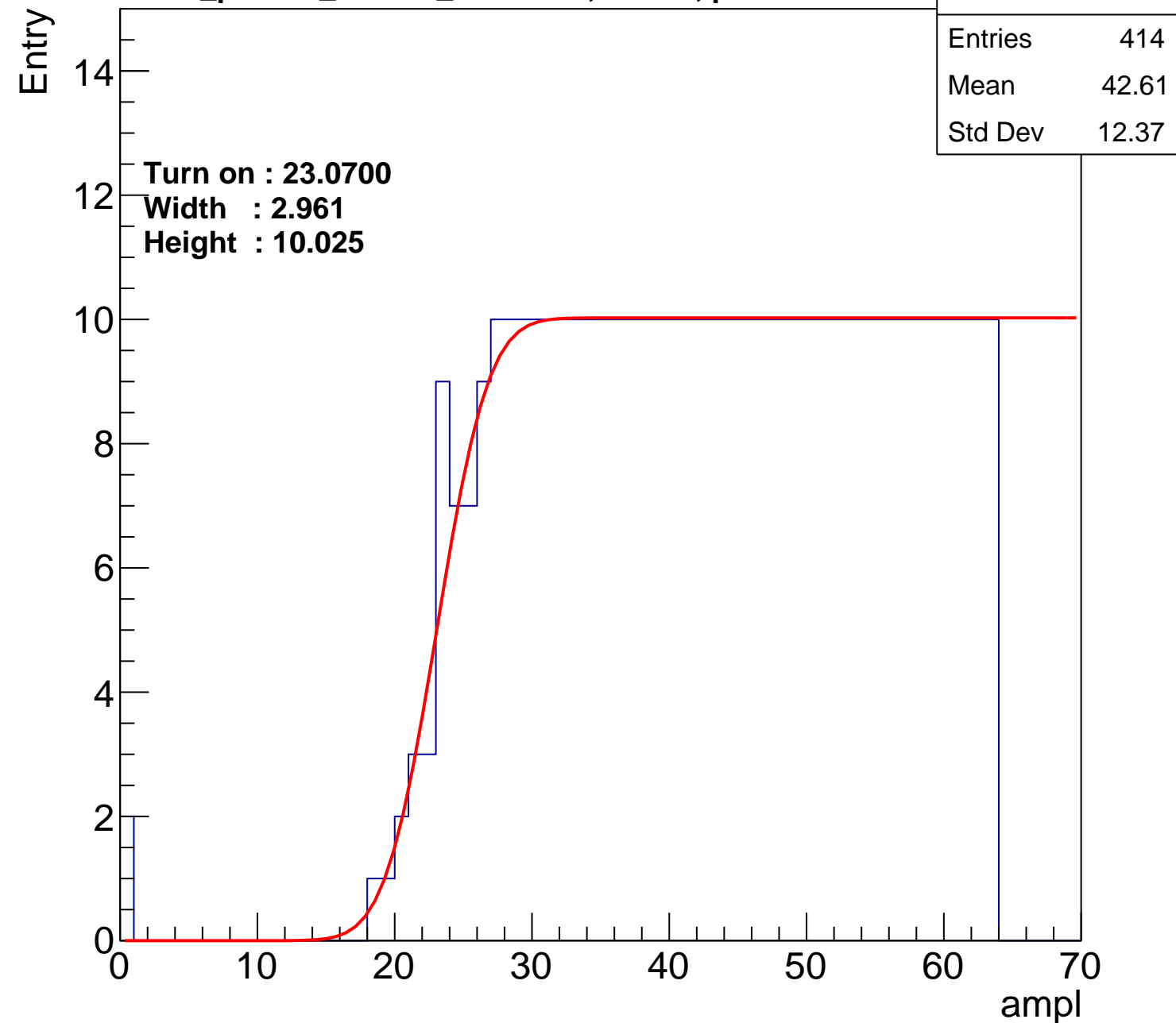
Width : 2.961

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch86

calib_packv5_042523_0143.root, FC#10, port B3

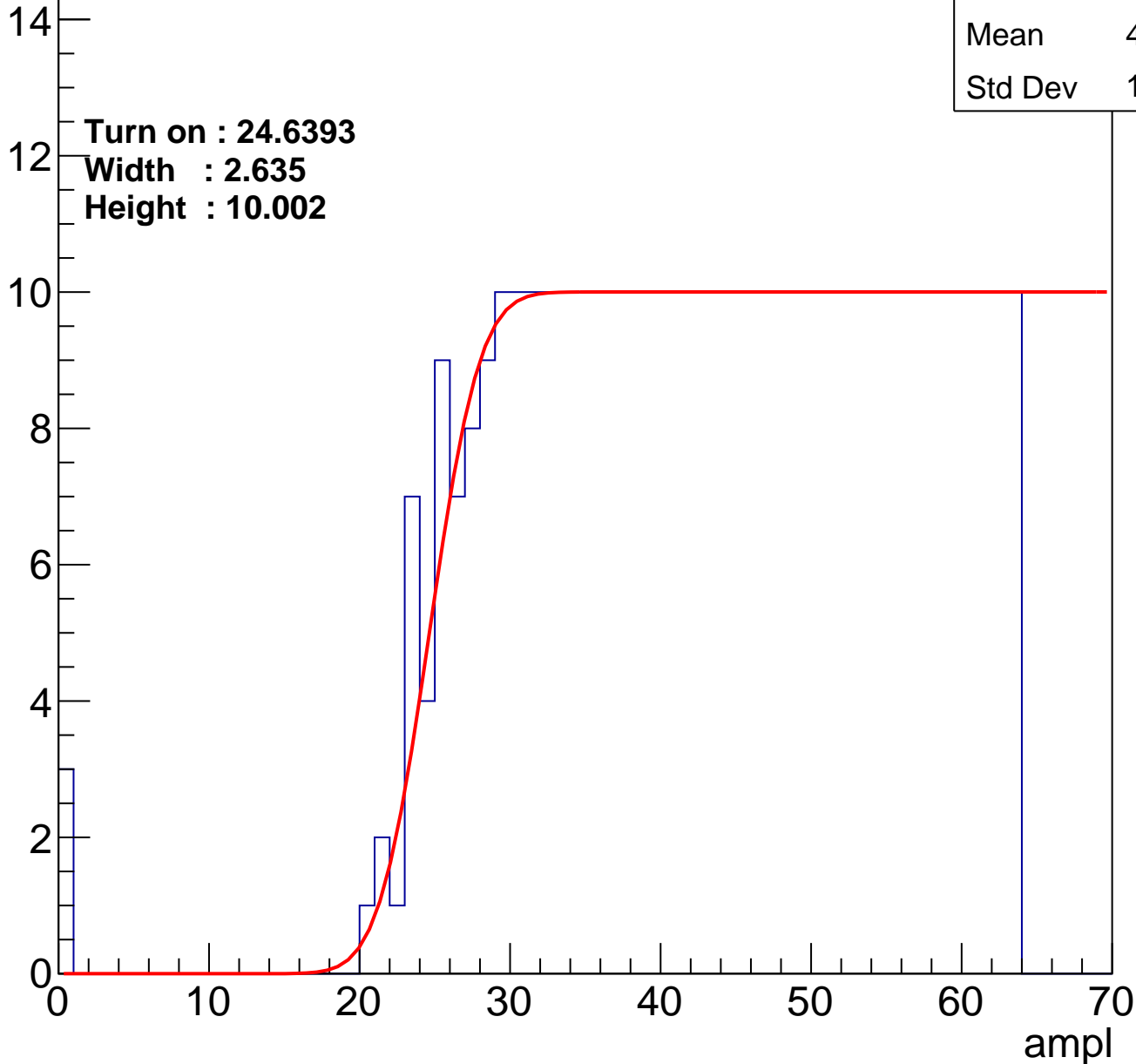
Entries	401
Mean	43.18
Std Dev	12.19

Turn on : 24.6393

Width : 2.635

Height : 10.002

Entry



B1L002S, U17-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.39
Std Dev	11.95

Turn on : 24.0578

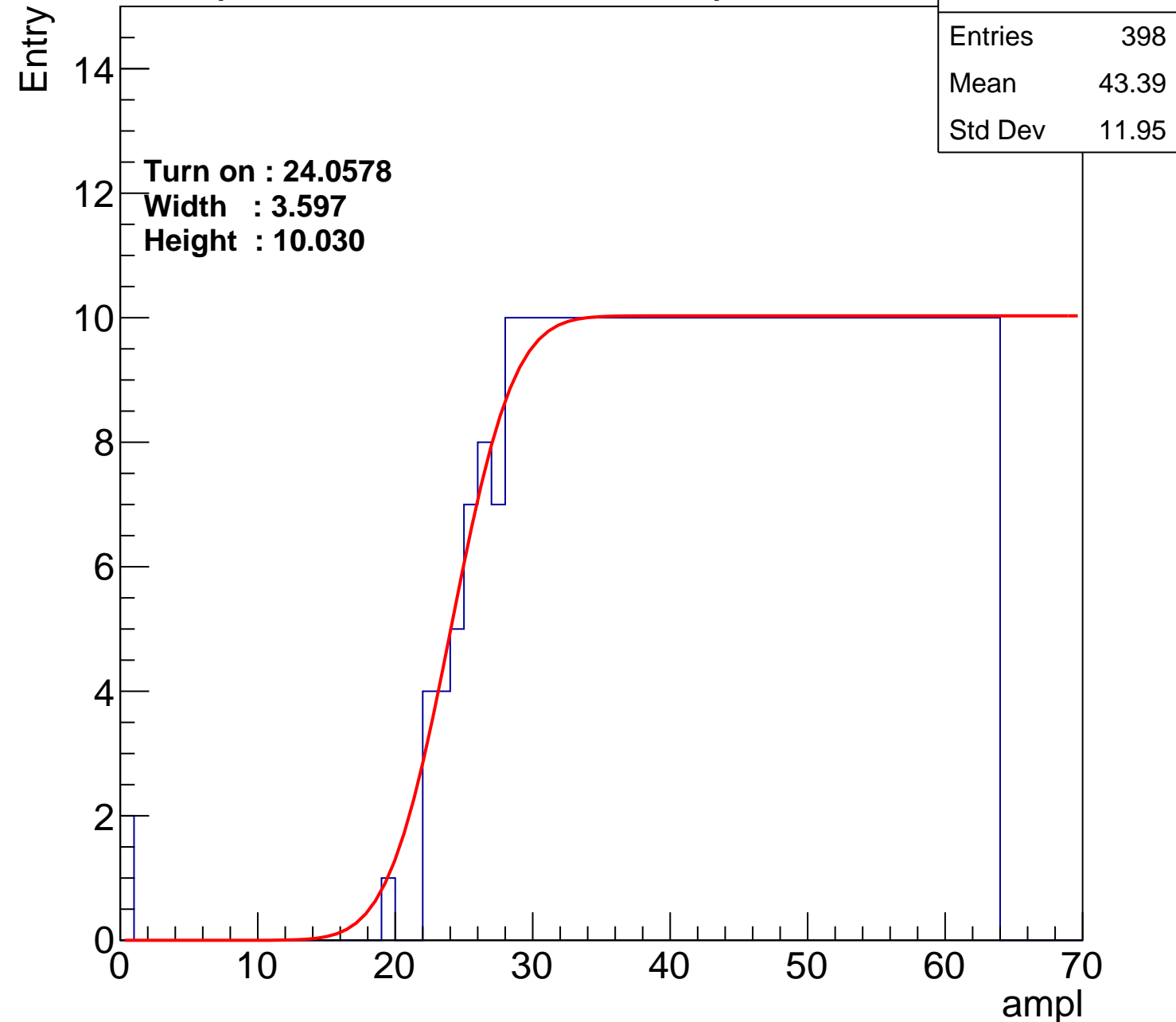
Width : 3.597

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.25
Std Dev	12.3

Turn on : 24.8844

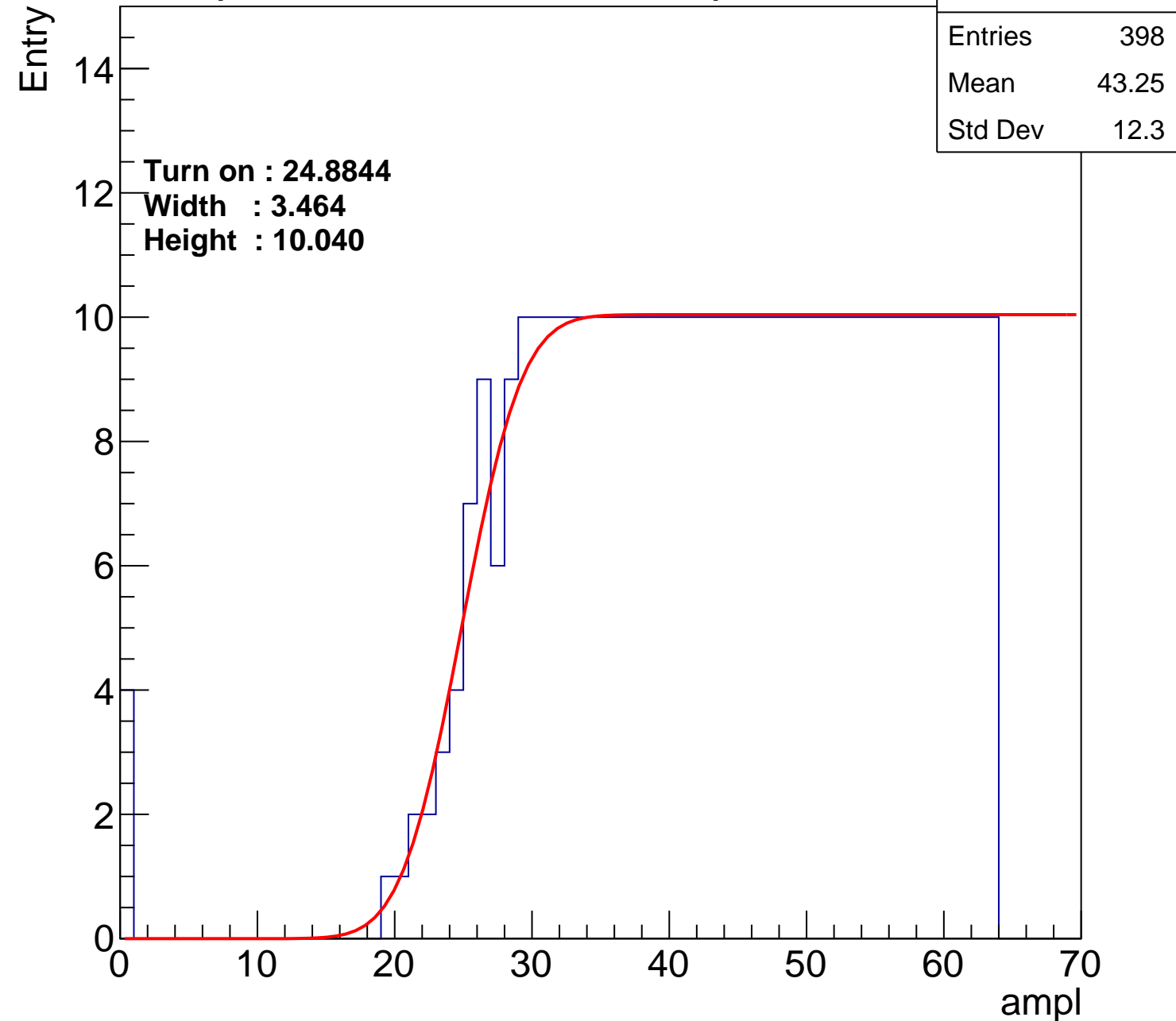
Width : 3.464

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.08
Std Dev	12.62

Turn on : 24.8451

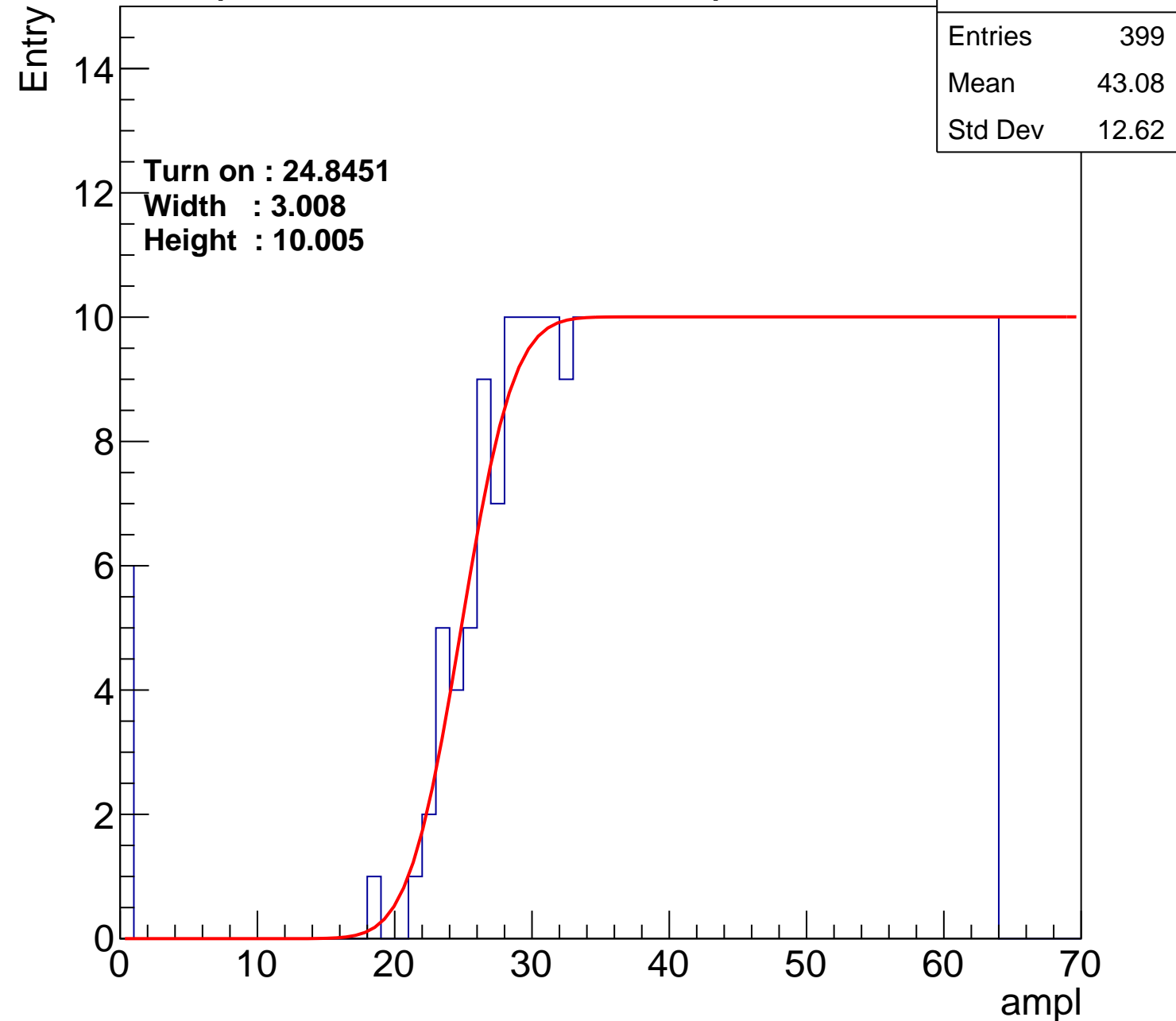
Width : 3.008

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 27.6353

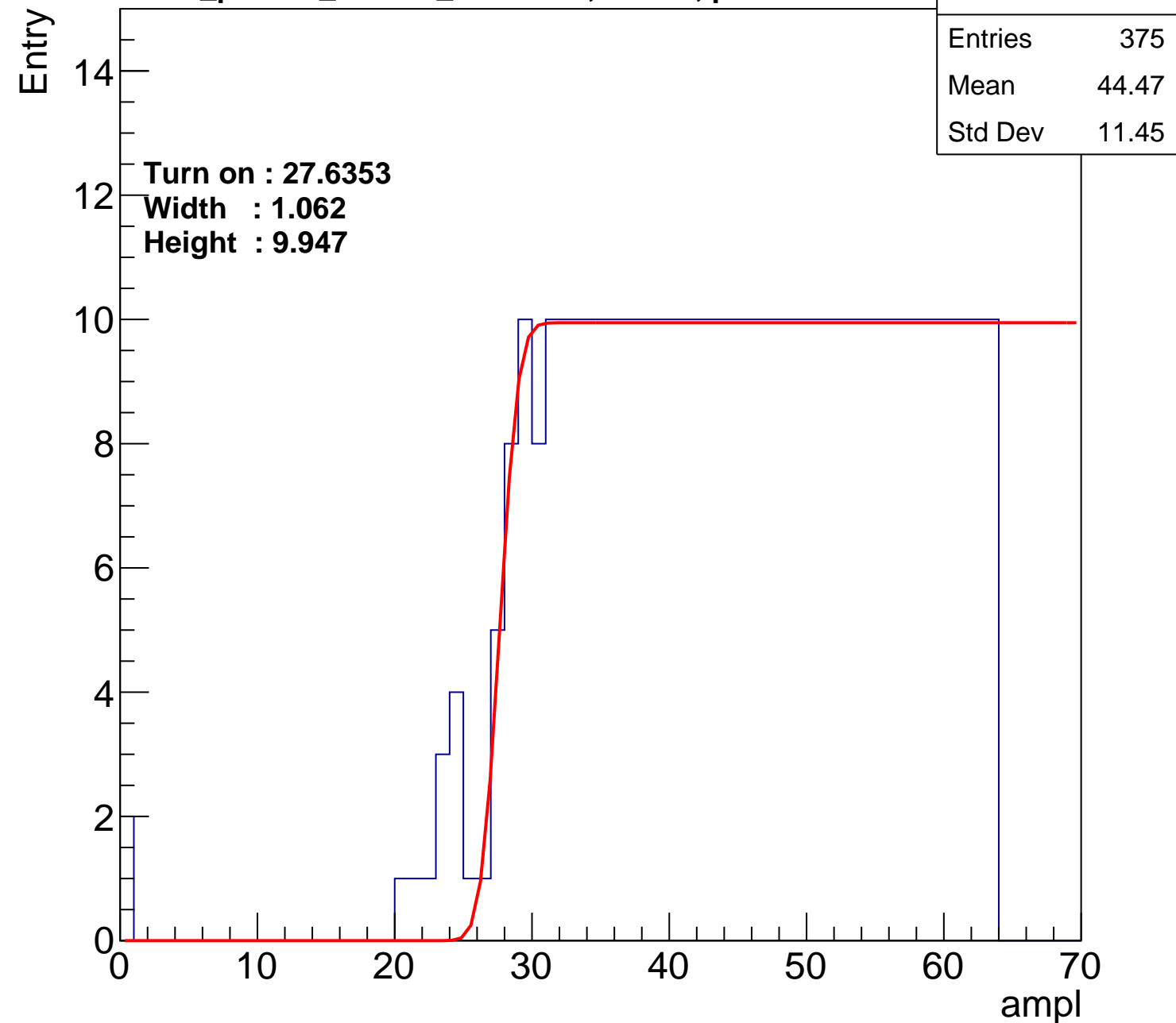
Width : 1.062

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch91

calib_packv5_042523_0143.root, FC#10, port B3

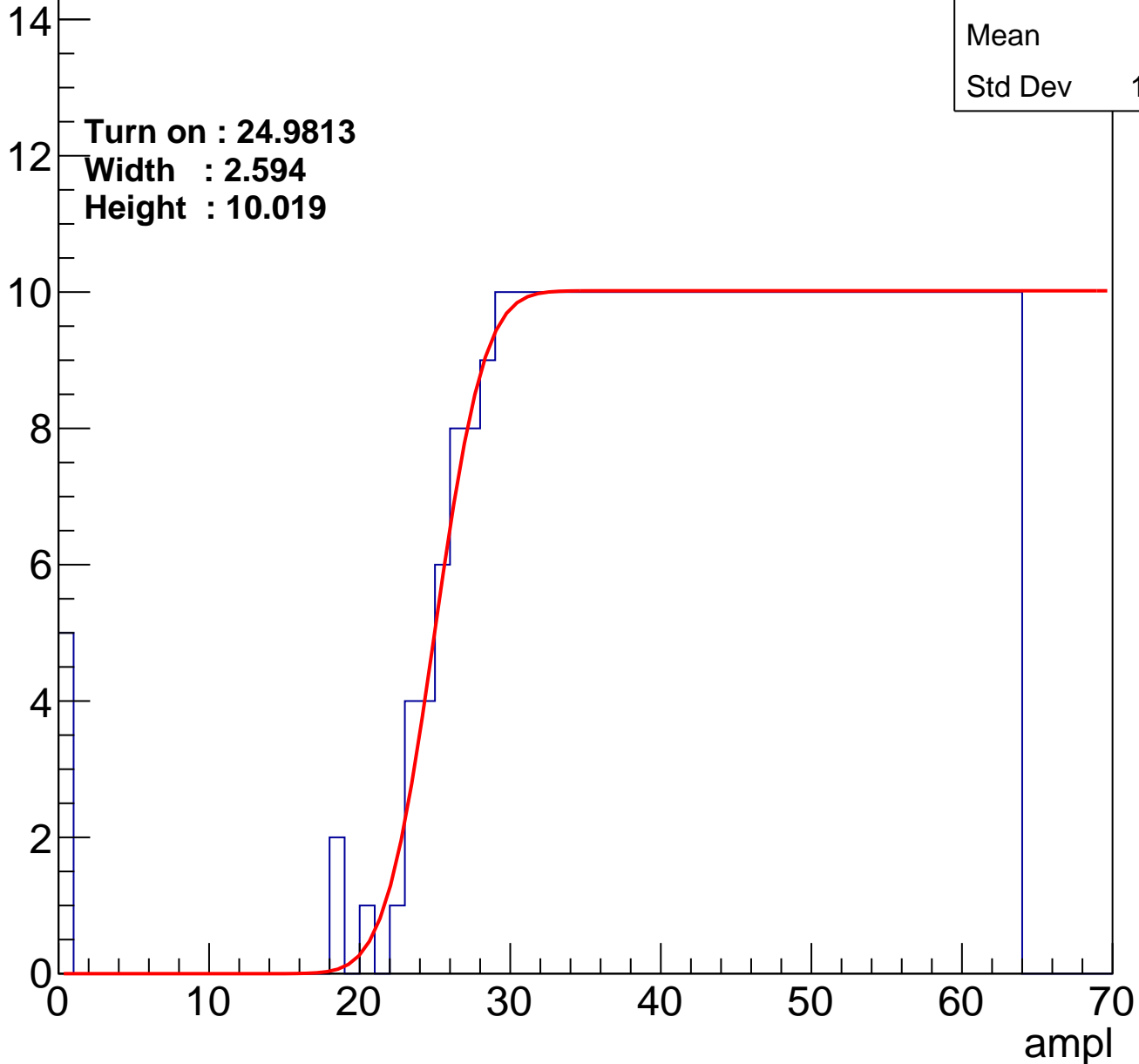
Entries	398
Mean	43.2
Std Dev	12.45

Turn on : 24.9813

Width : 2.594

Height : 10.019

Entry



B1L002S, U17-ch92

calib_packv5_042523_0143.root, FC#10, port B3

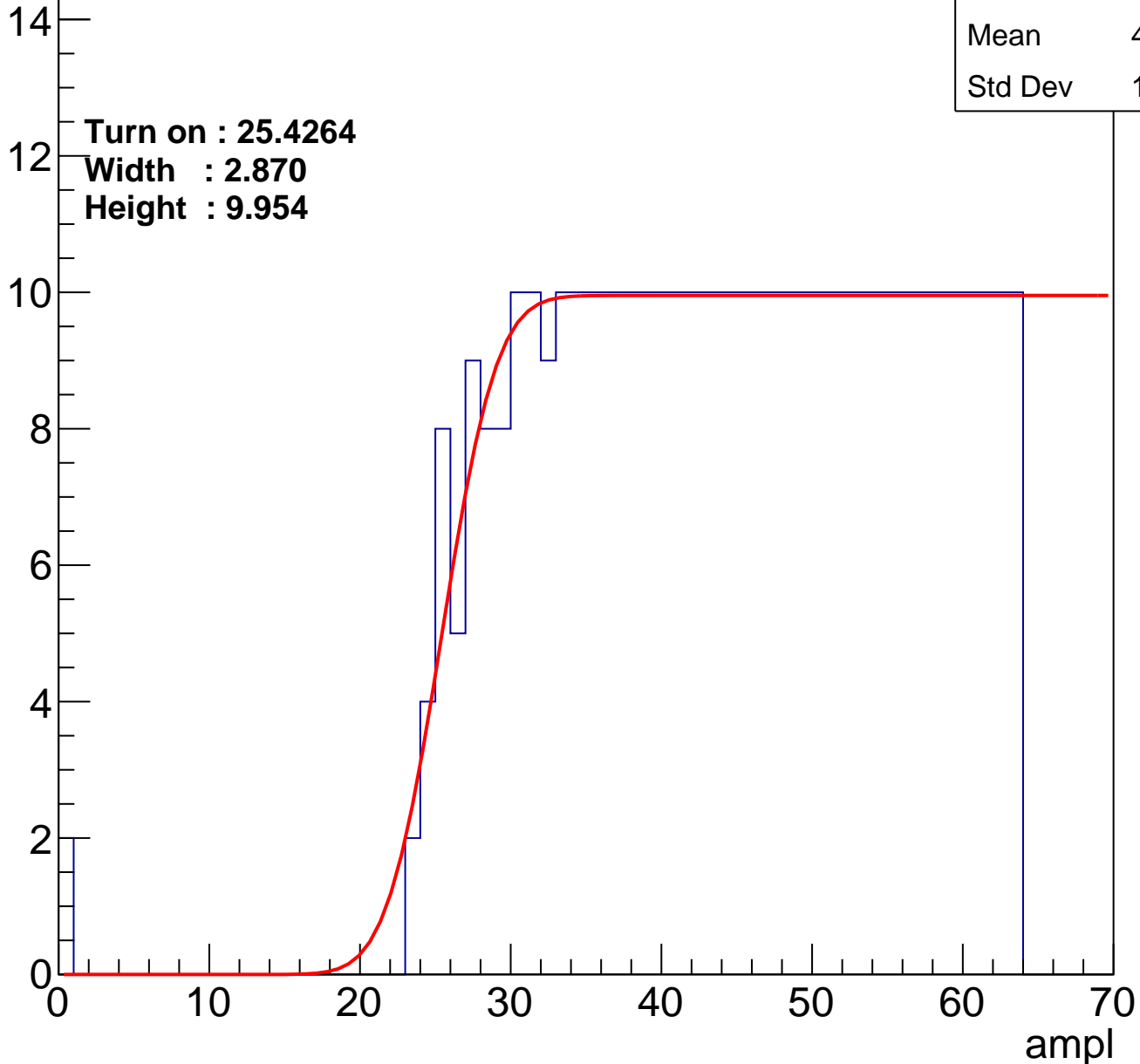
Entries	385
Mean	44.02
Std Dev	11.62

Turn on : 25.4264

Width : 2.870

Height : 9.954

Entry



B1L002S, U17-ch93

calib_packv5_042523_0143.root, FC#10, port B3

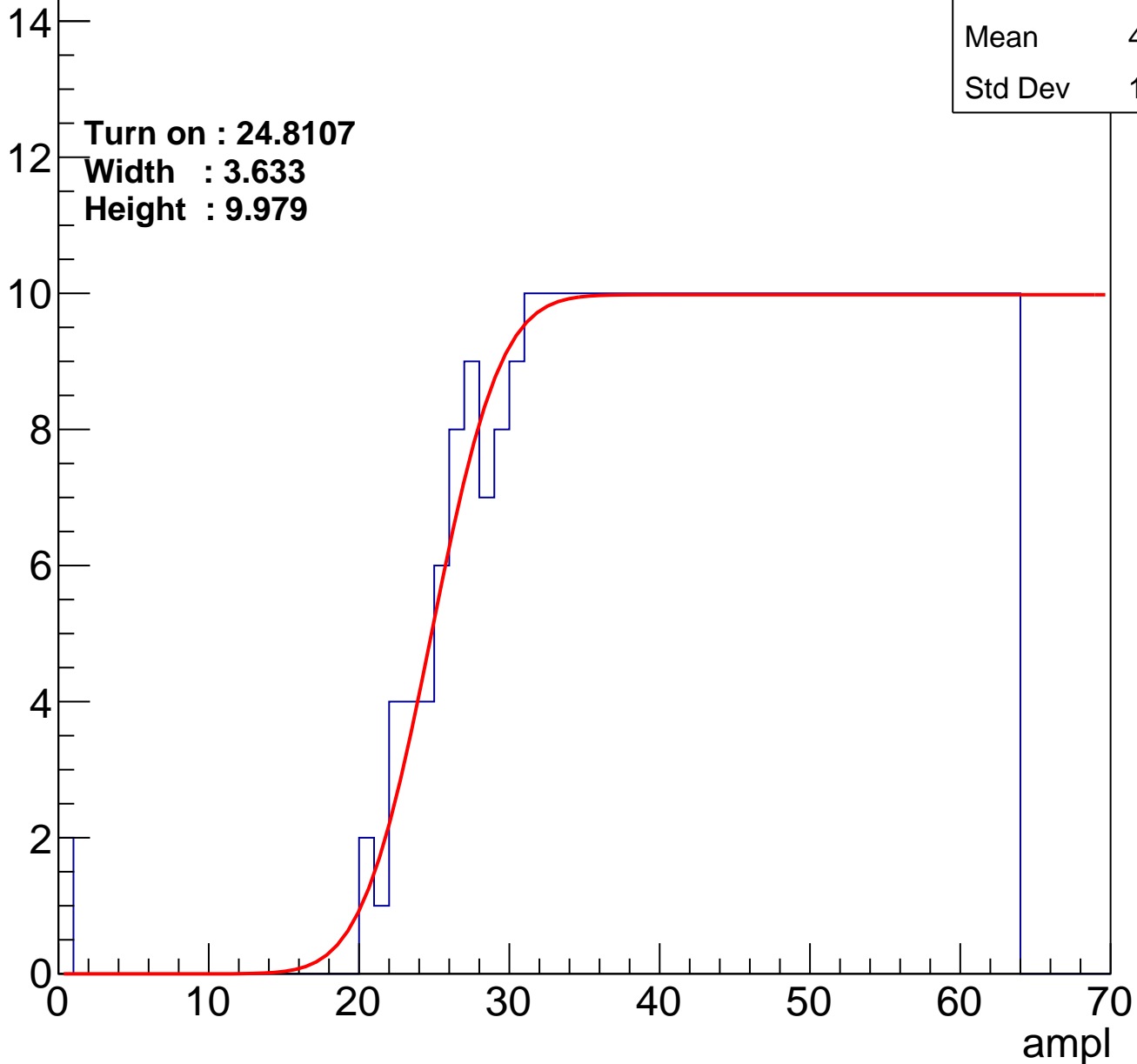
Entries	394
Mean	43.52
Std Dev	11.96

Turn on : 24.8107

Width : 3.633

Height : 9.979

Entry



B1L002S, U17-ch94

calib_packv5_042523_0143.root, FC#10, port B3

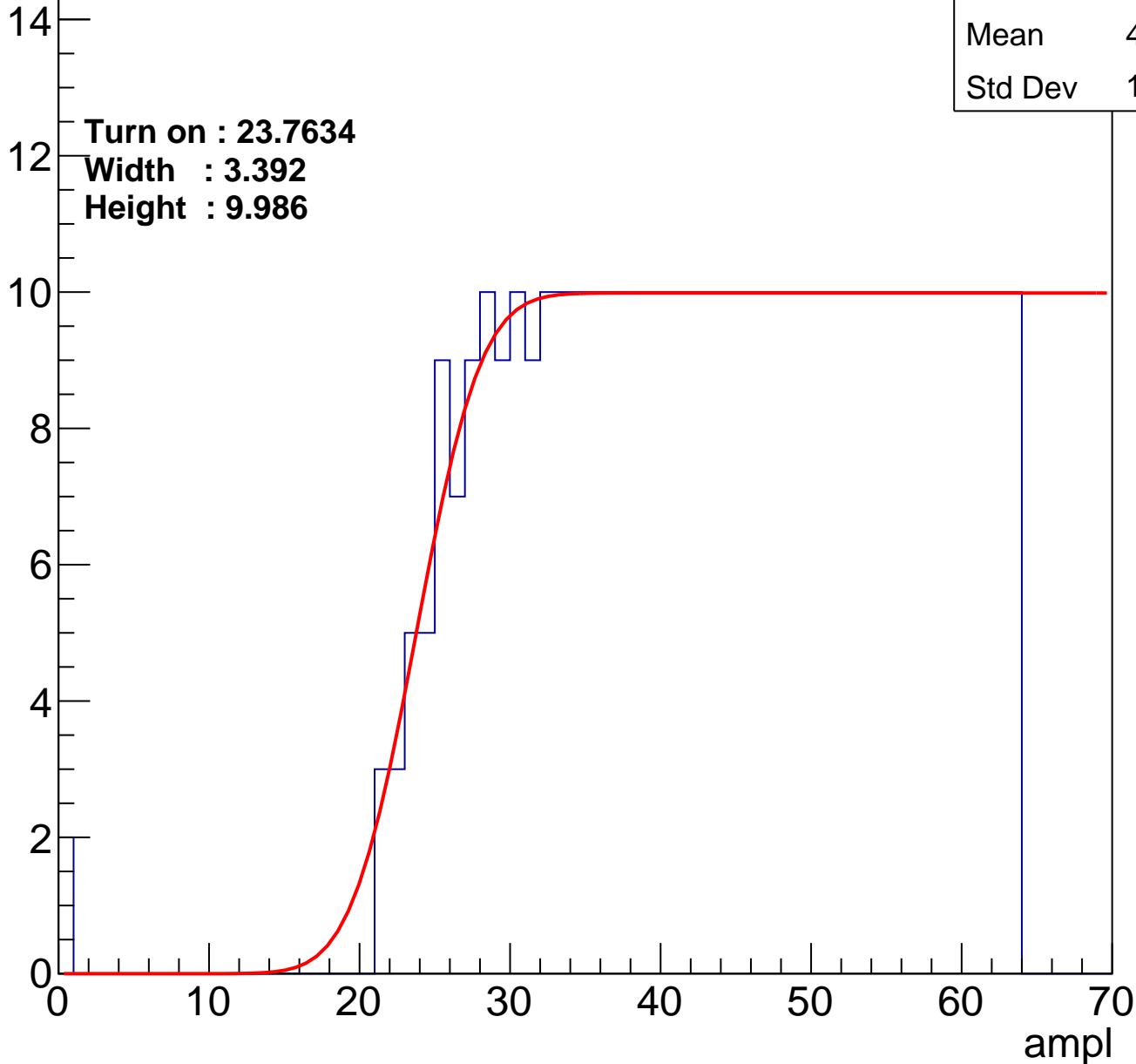
Entries	401
Mean	43.23
Std Dev	12.05

Turn on : 23.7634

Width : 3.392

Height : 9.986

Entry



B1L002S, U17-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	419
Mean	42.28
Std Dev	12.72

Turn on : 22.2495

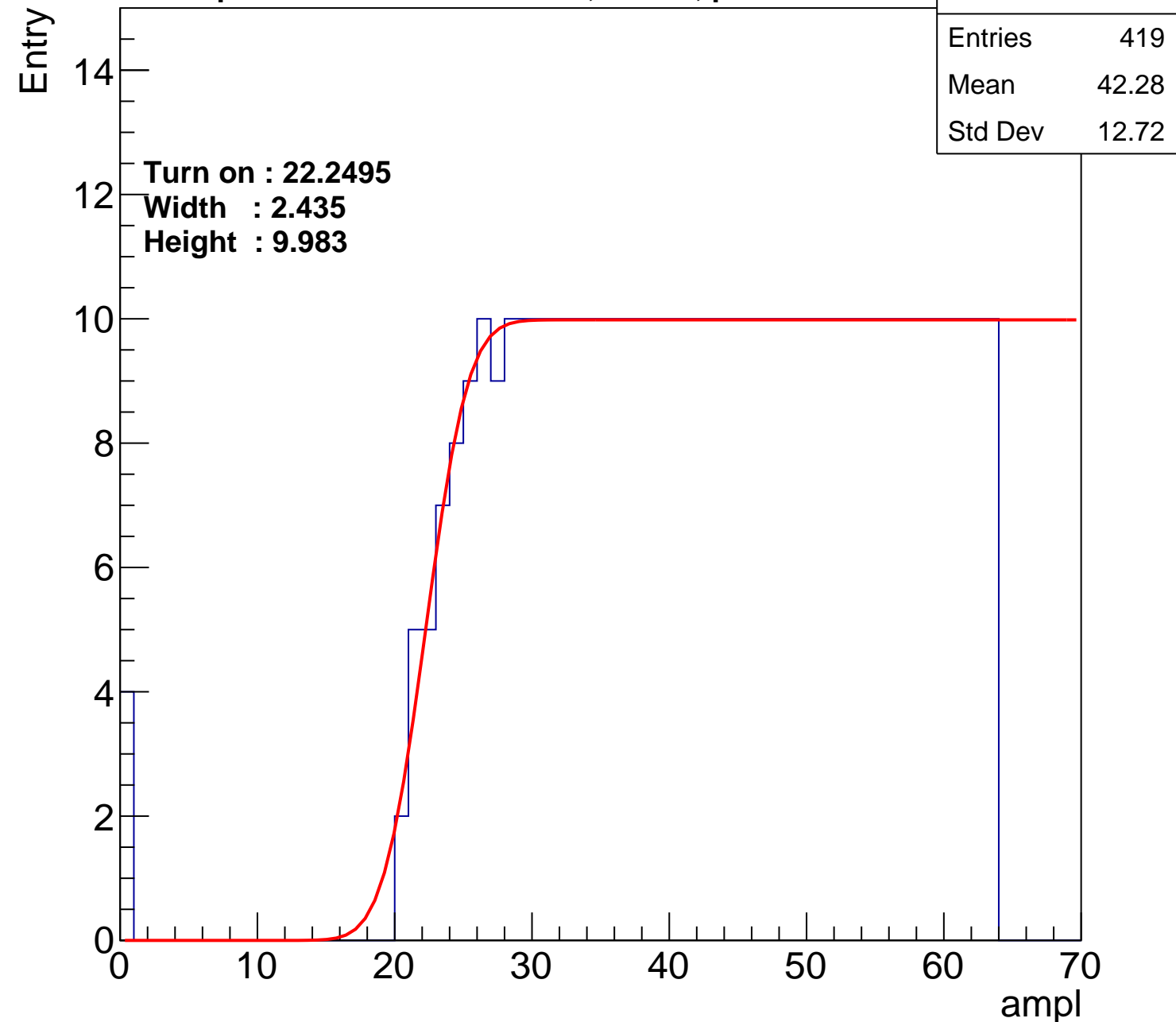
Width : 2.435

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.35
Std Dev	11.44

Turn on : 26.5428

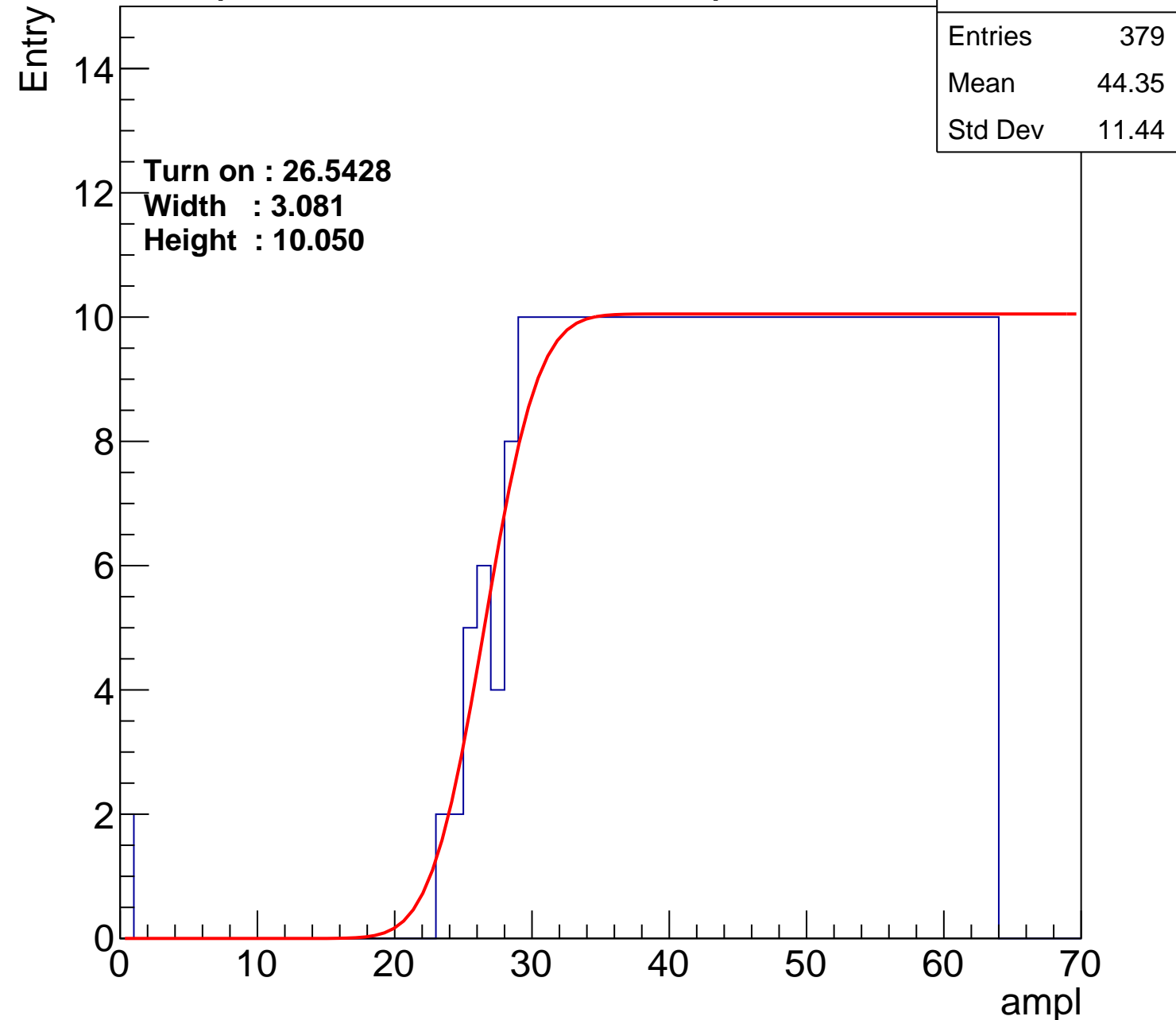
Width : 3.081

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.95
Std Dev	11.51

Turn on : 25.2426

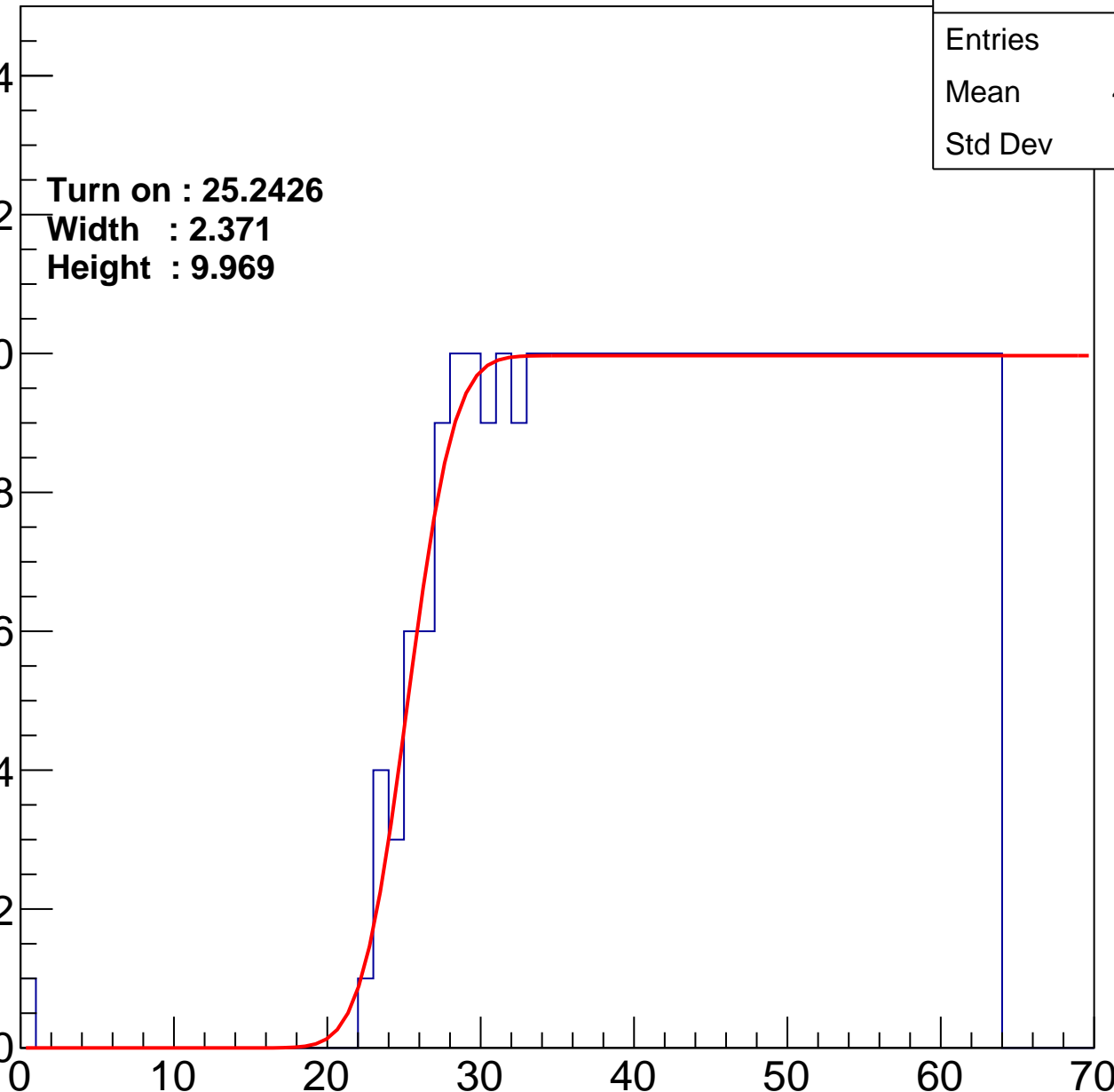
Width : 2.371

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.28
Std Dev	12.14

Turn on : 24.5290

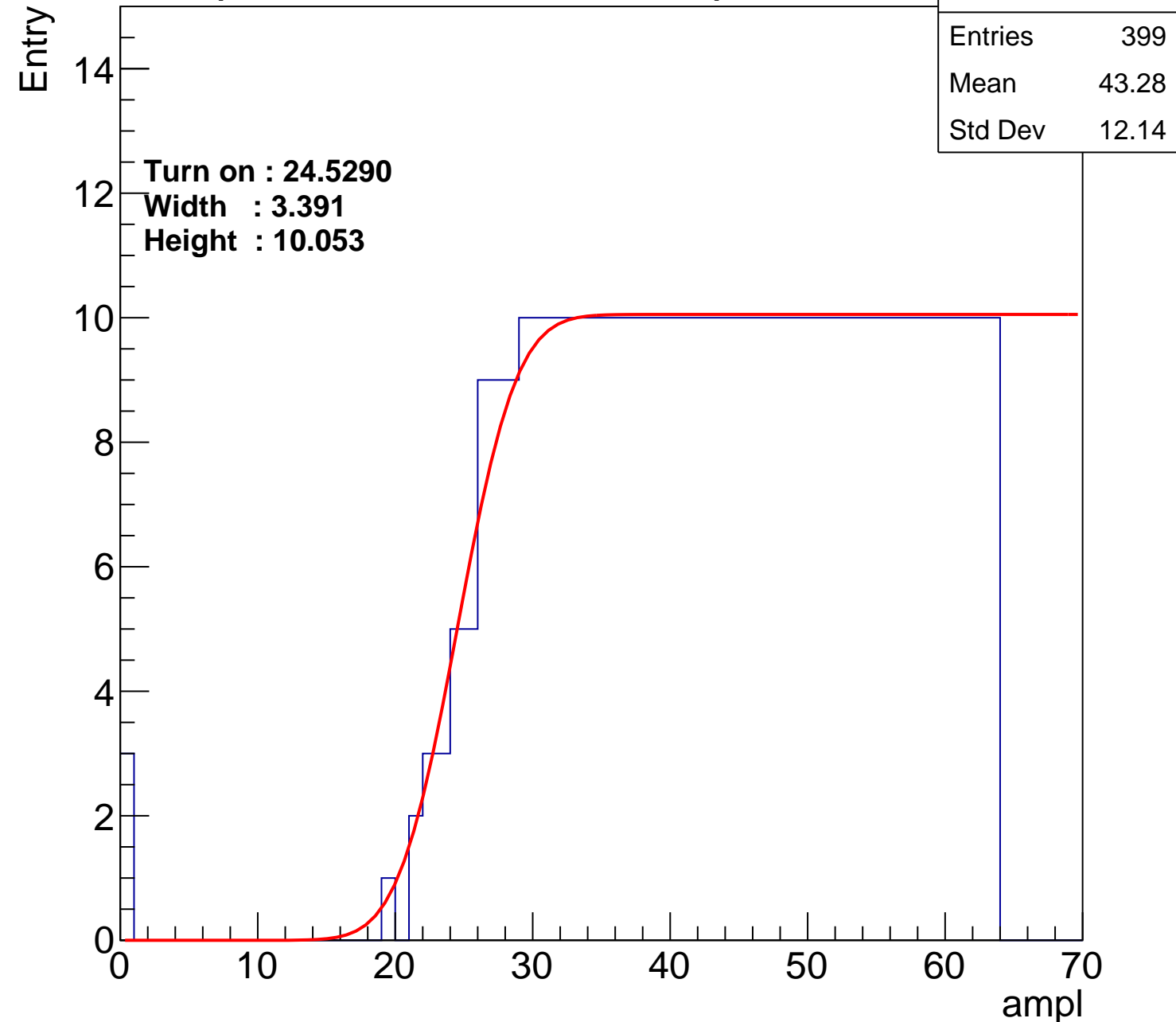
Width : 3.391

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch99

calib_packv5_042523_0143.root, FC#10, port B3

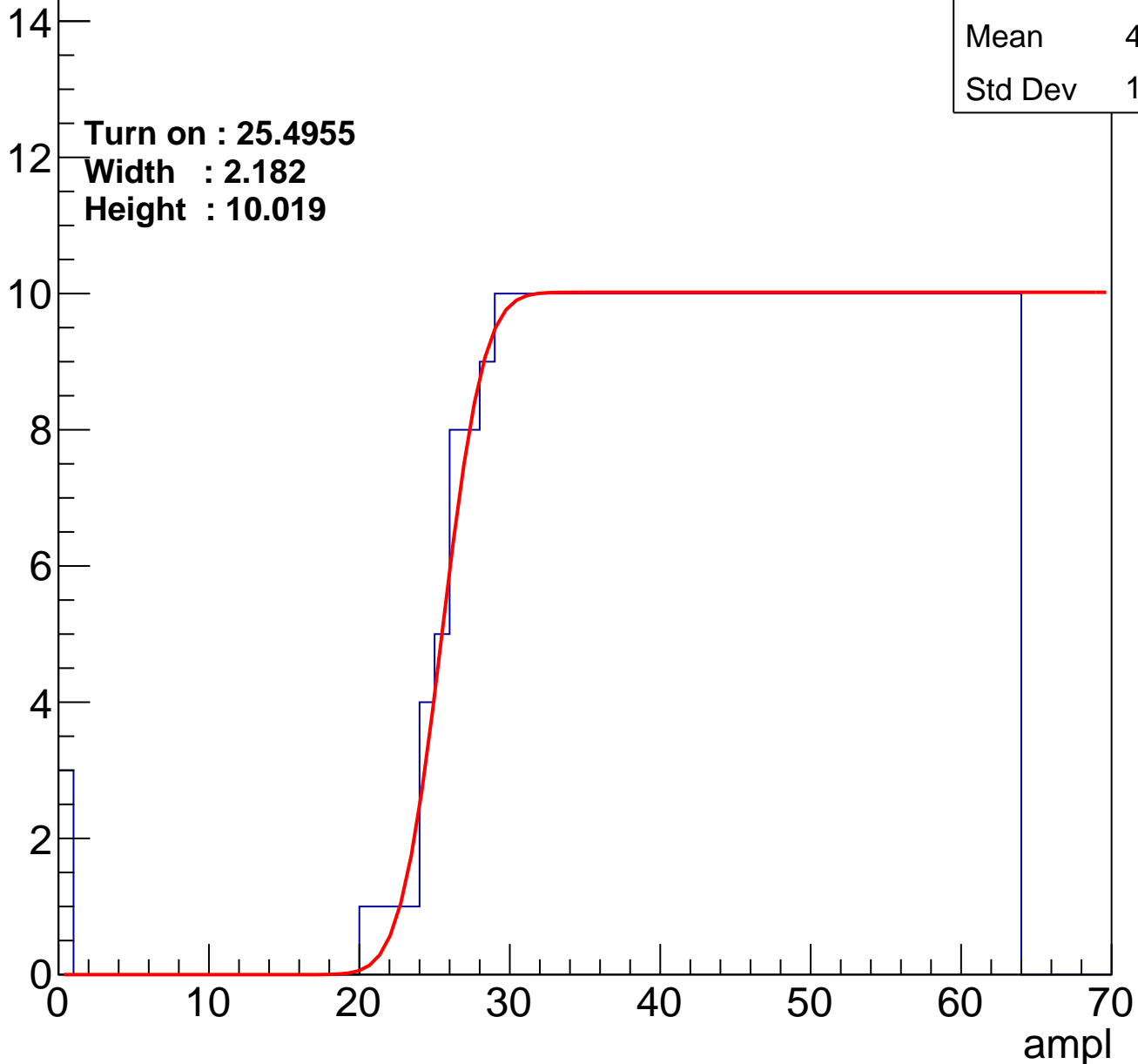
Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.4955

Width : 2.182

Height : 10.019

Entry



B1L002S, U17-ch100

calib_packv5_042523_0143.root, FC#10, port B3

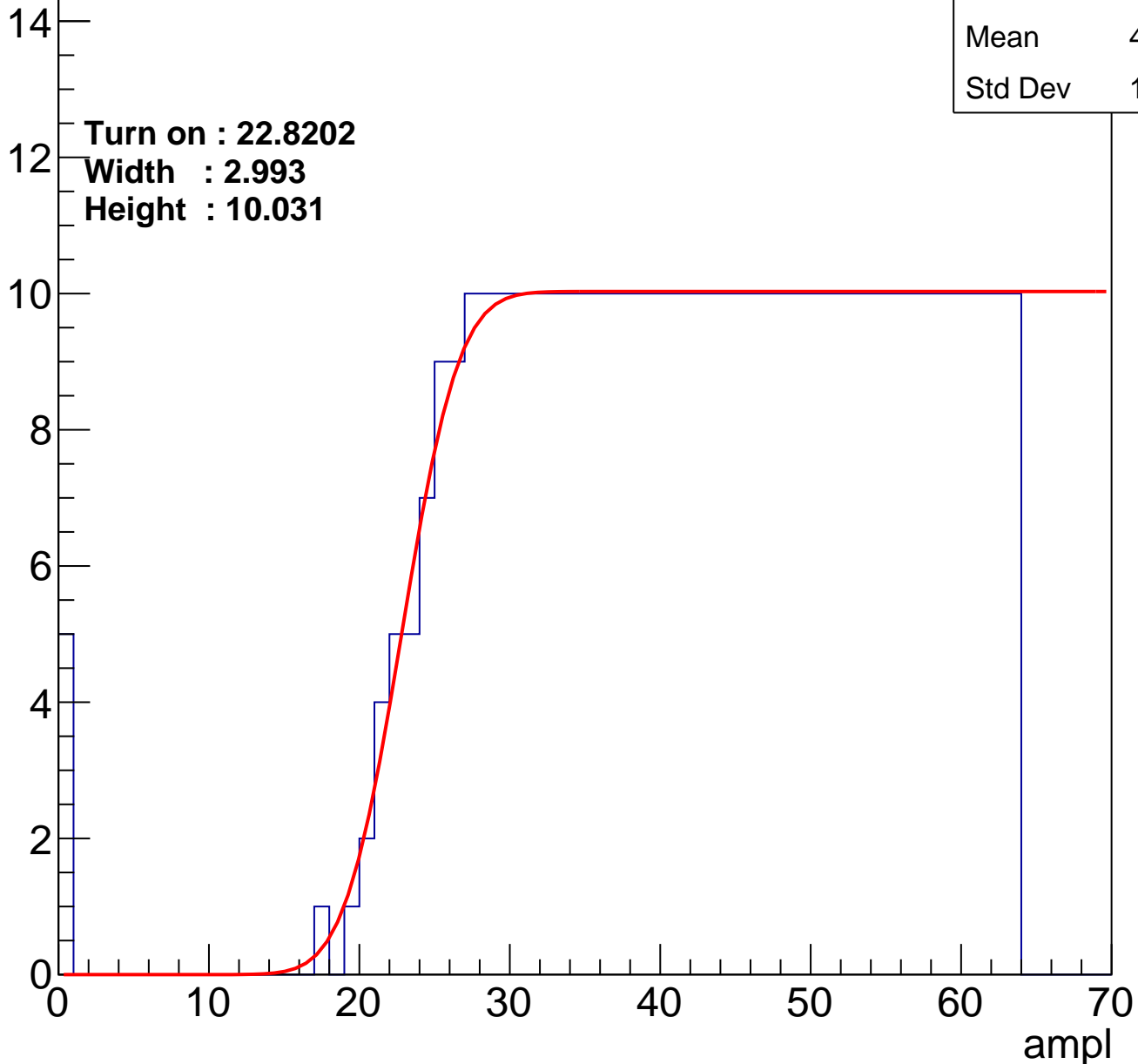
Entries	418
Mean	42.25
Std Dev	12.86

Turn on : 22.8202

Width : 2.993

Height : 10.031

Entry



B1L002S, U17-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.21
Std Dev	12.08

Turn on : 24.7708

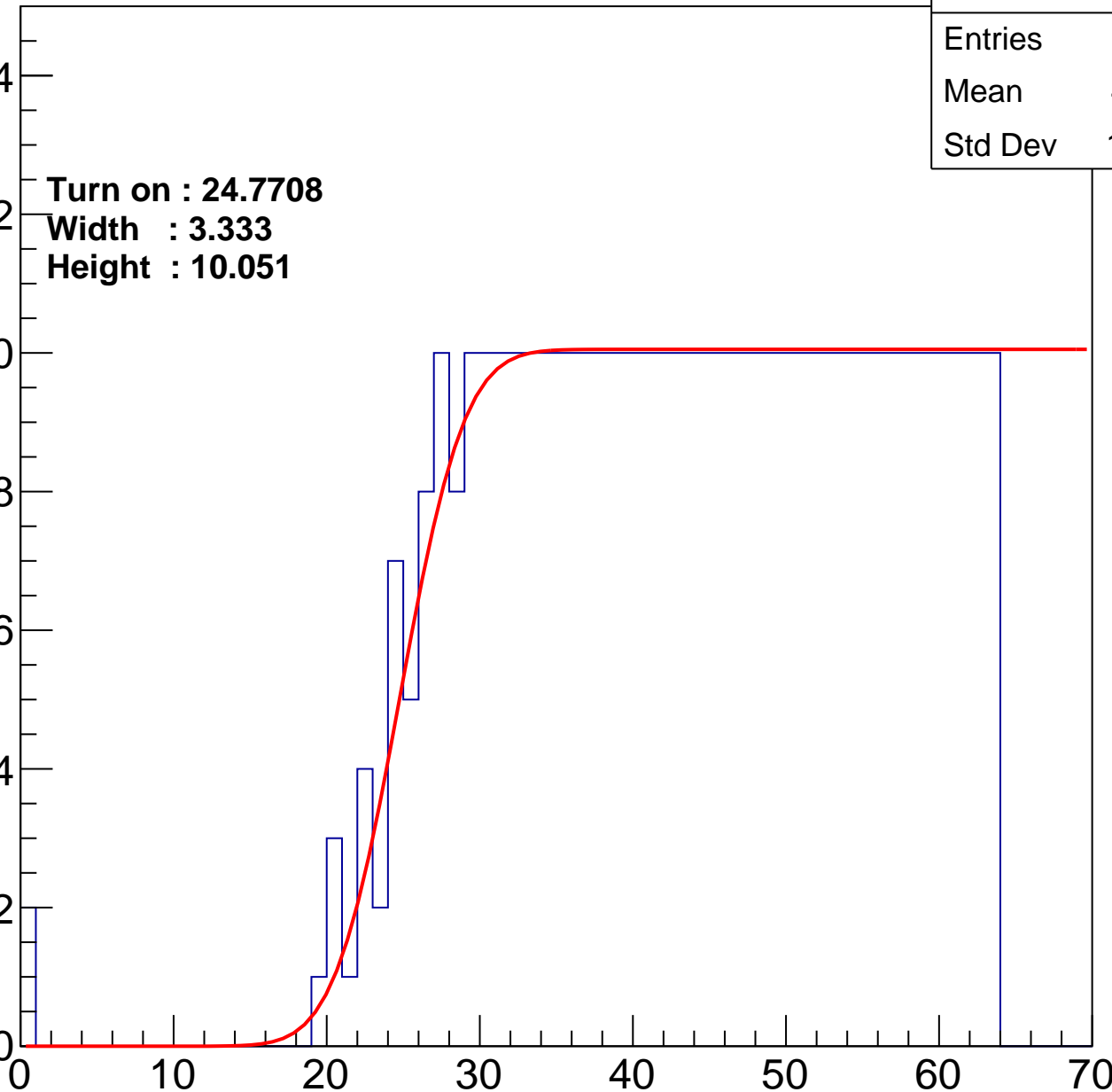
Width : 3.333

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.11
Std Dev	12.37

Turn on : 24.5114

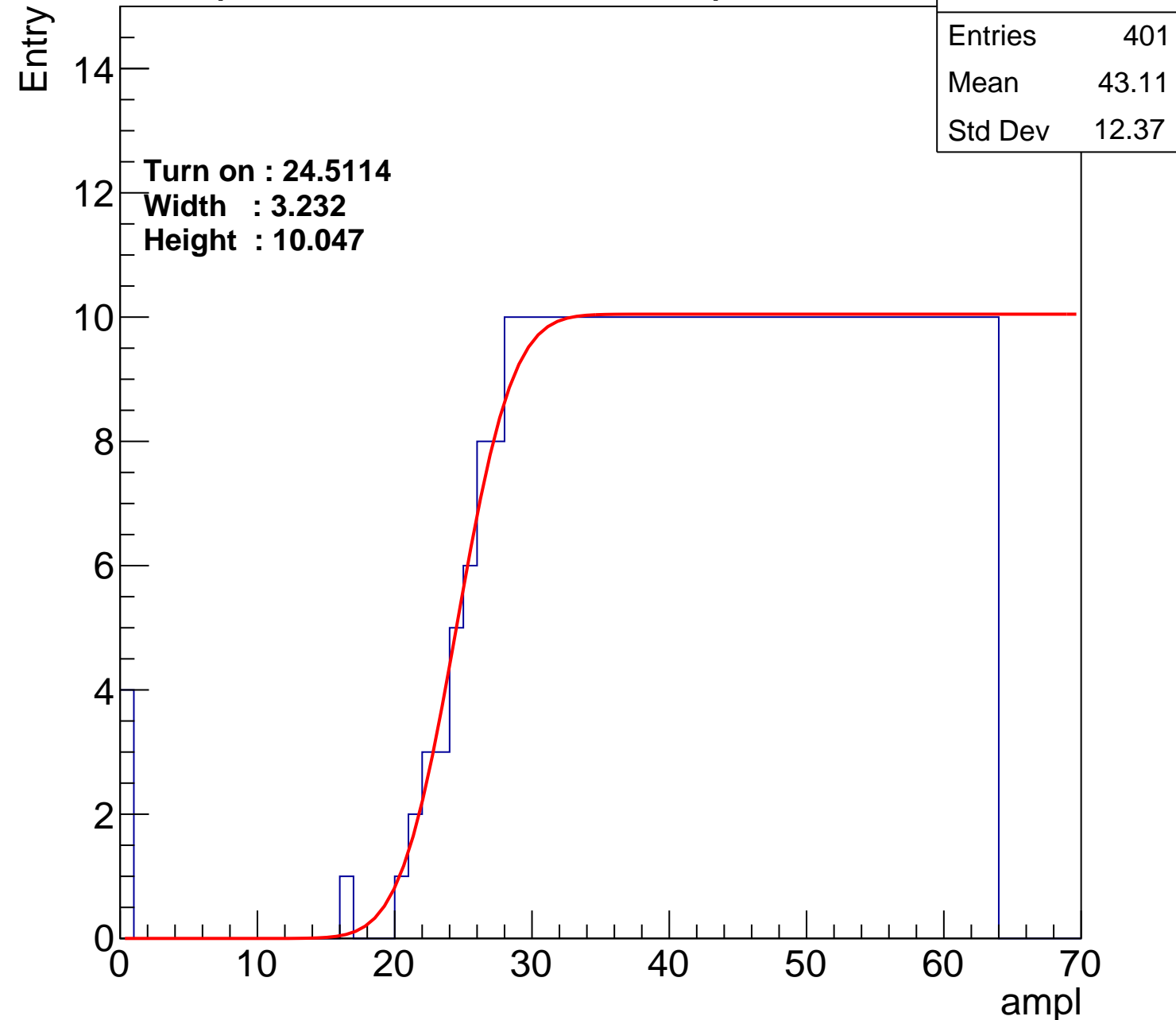
Width : 3.232

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.78
Std Dev	11.63

Turn on : 25.0784

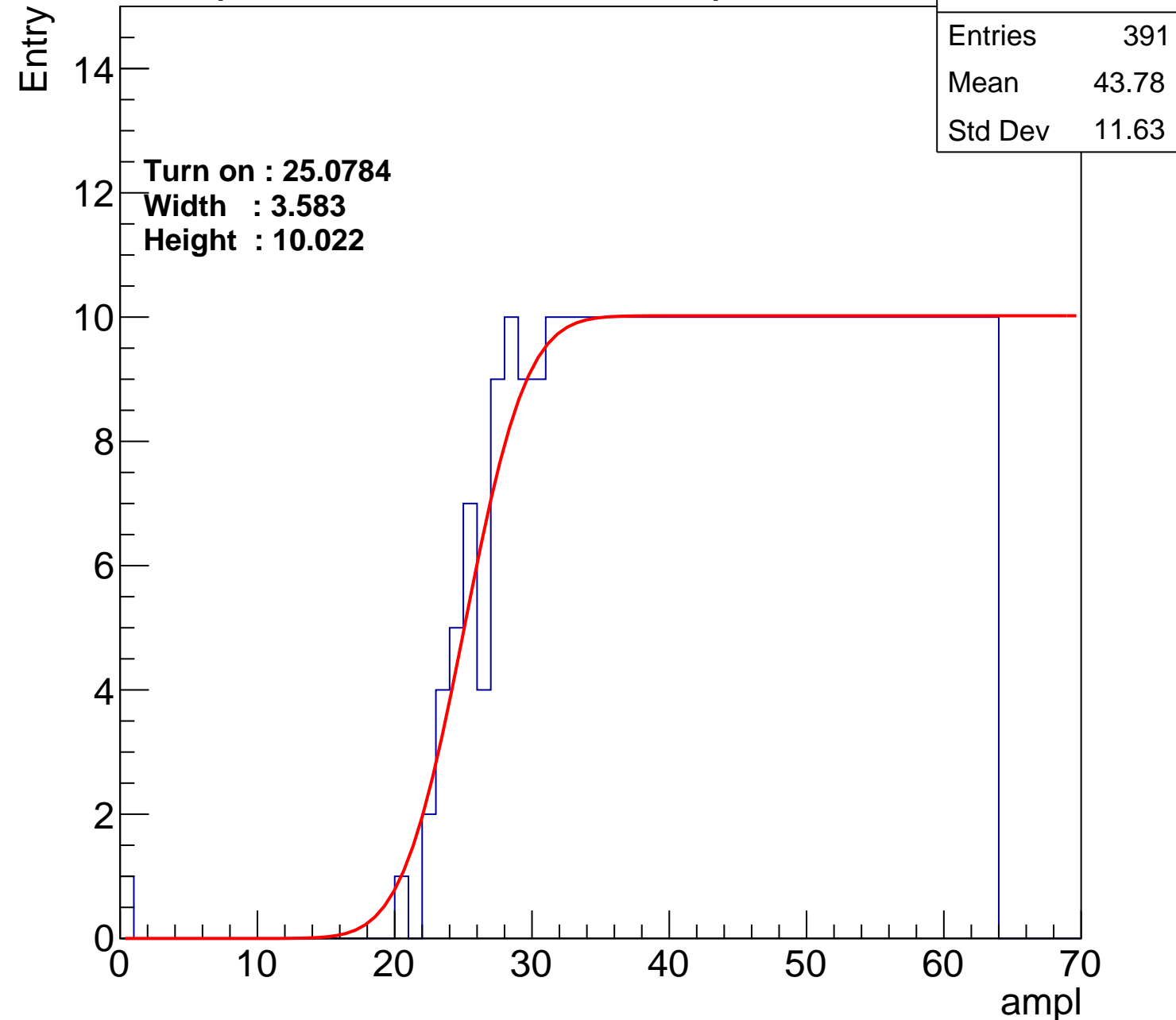
Width : 3.583

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch104

calib_packv5_042523_0143.root, FC#10, port B3

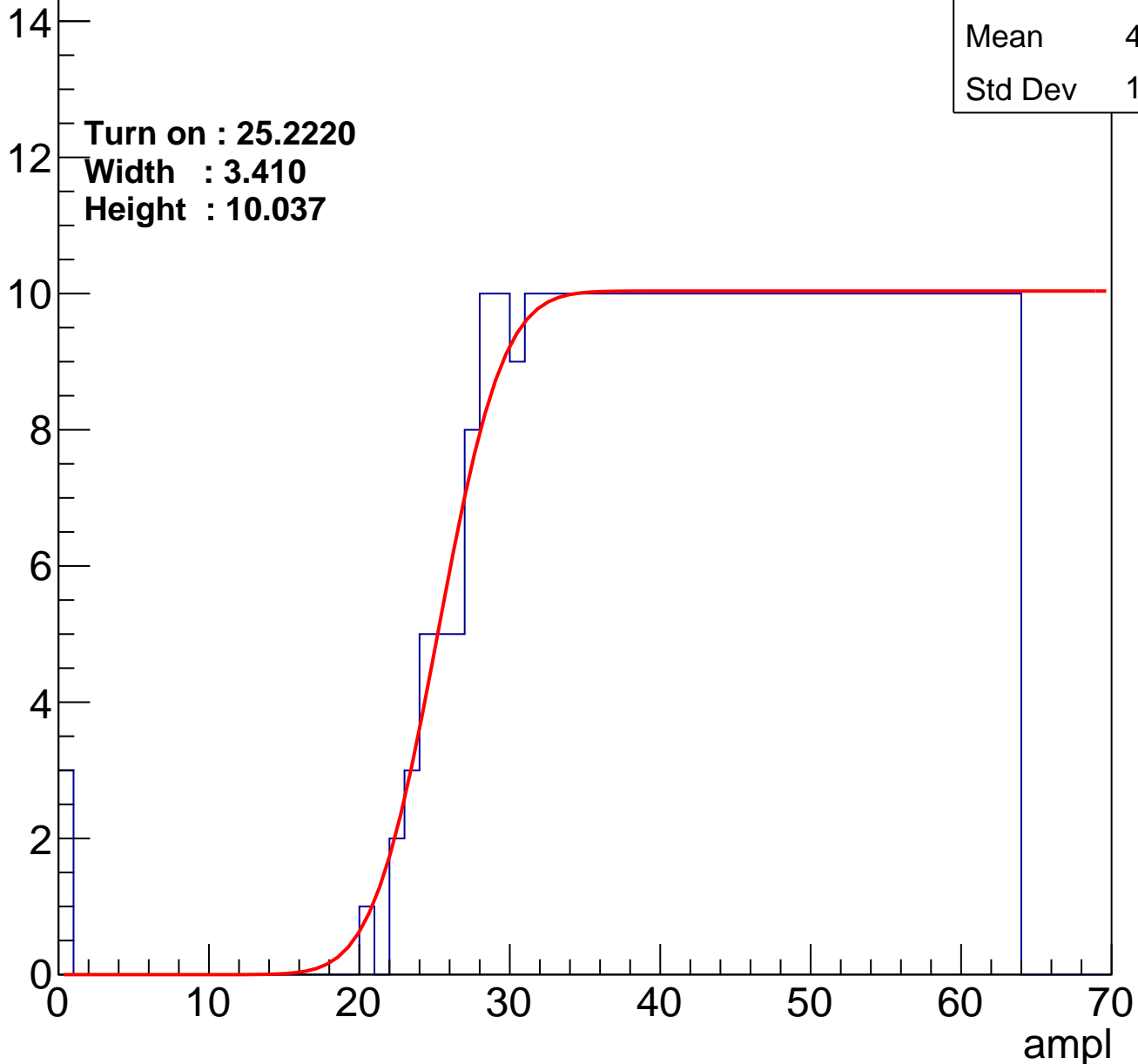
Entries	391
Mean	43.67
Std Dev	11.95

Turn on : 25.2220

Width : 3.410

Height : 10.037

Entry



B1L002S, U17-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.41

Turn on : 26.0972

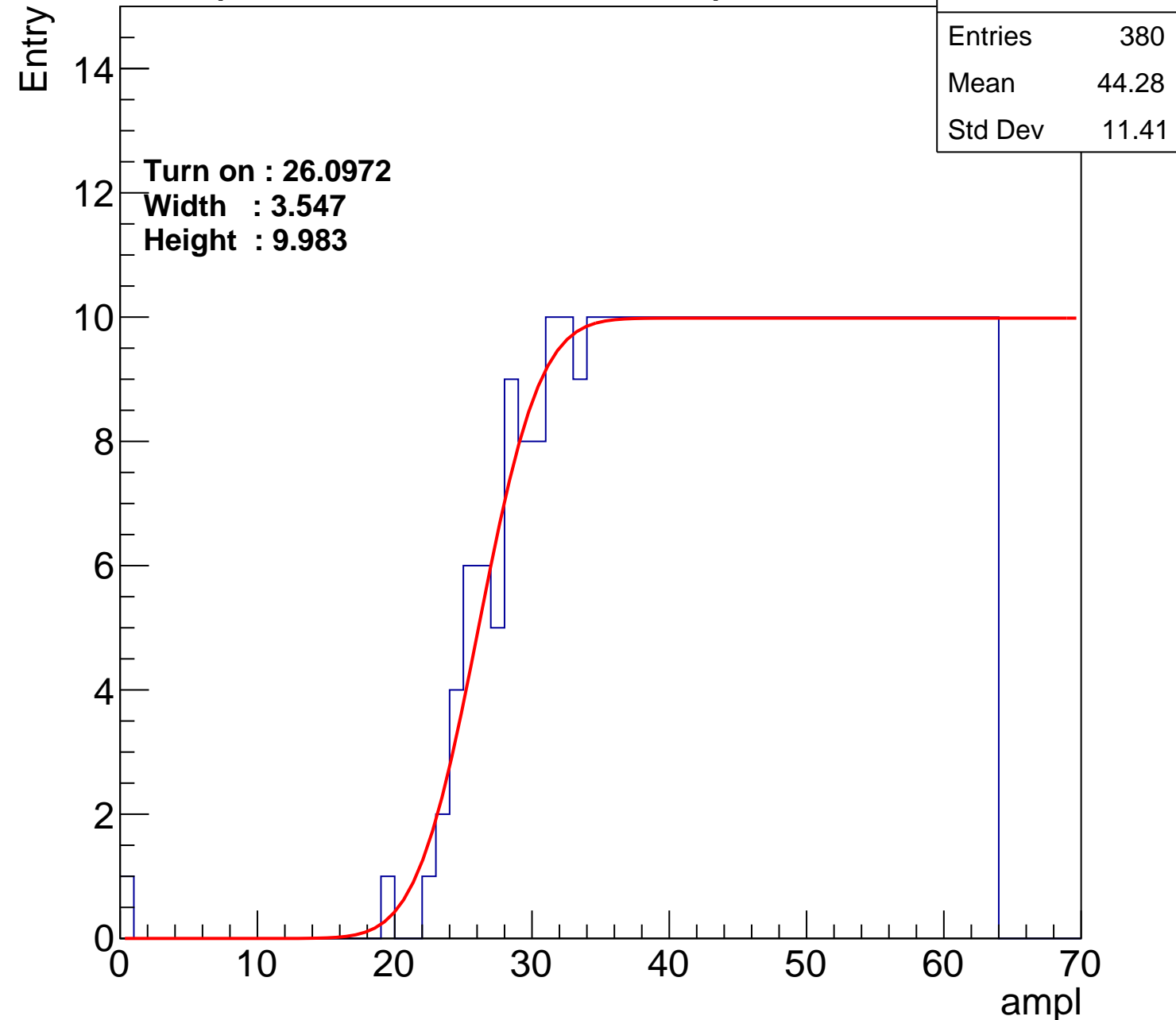
Width : 3.547

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.05
Std Dev	12.35

Turn on : 24.2678

Width : 2.821

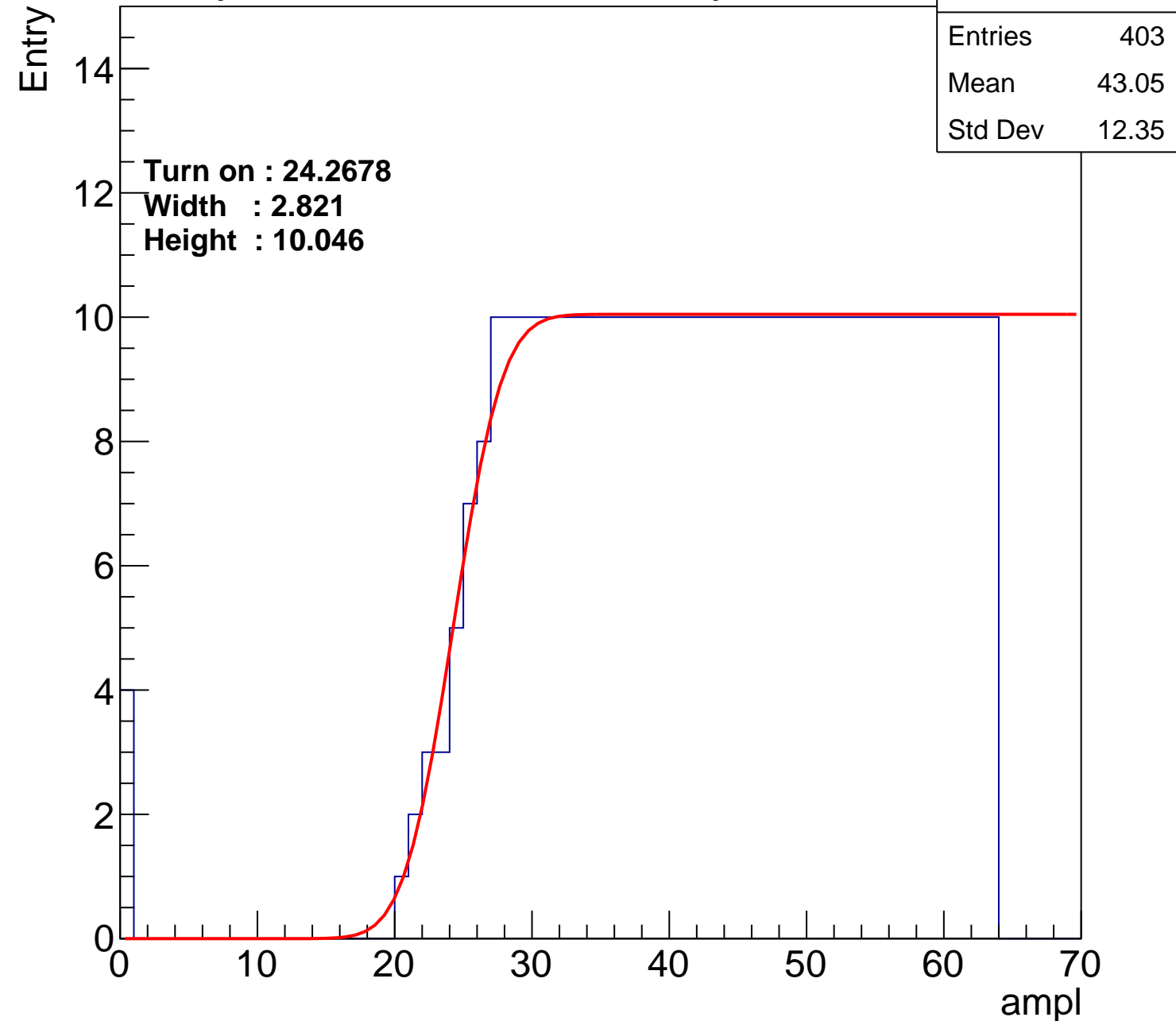
Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U17-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.0201

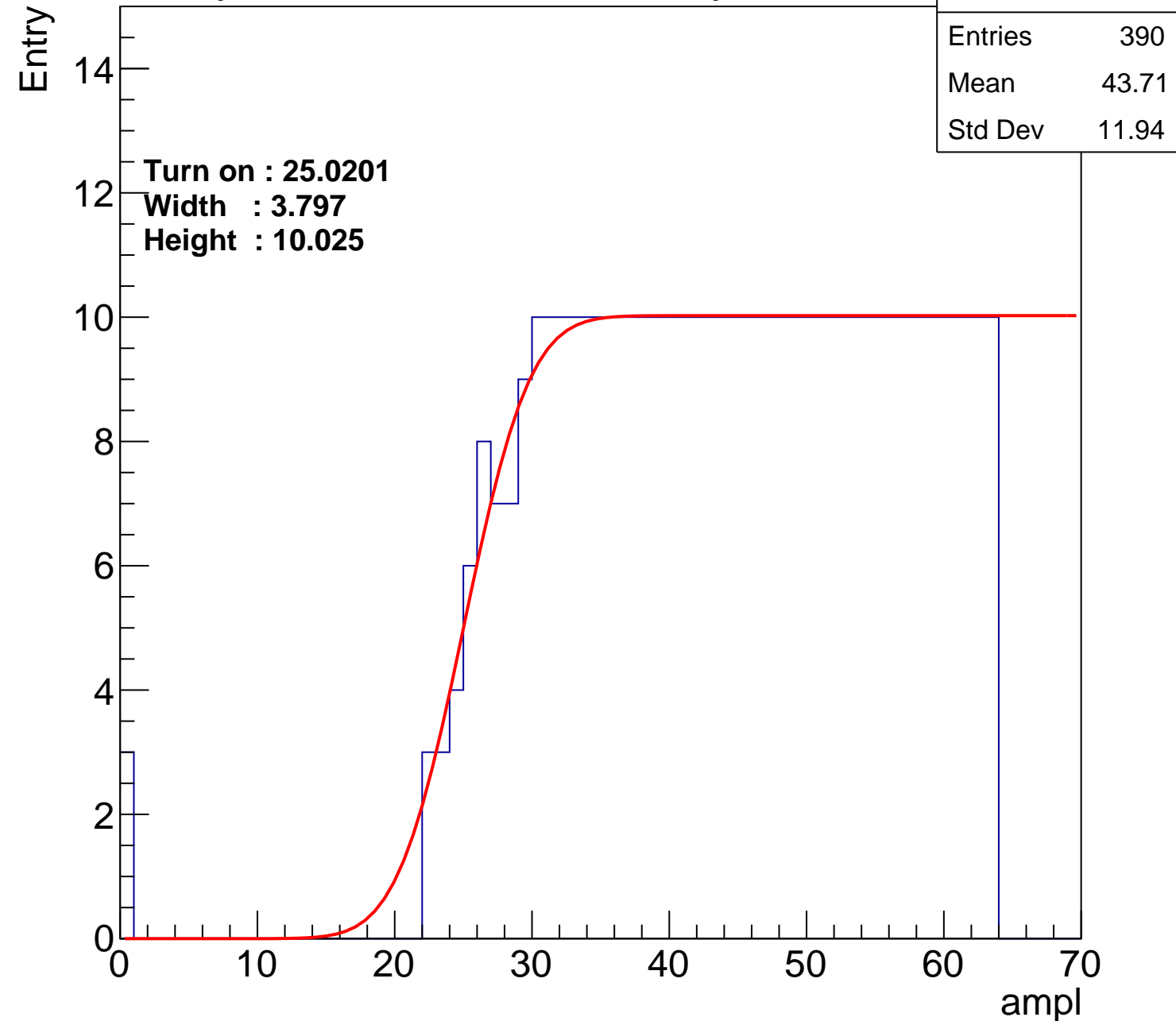
Width : 3.797

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.22
Std Dev	12.25

Turn on : 24.0433

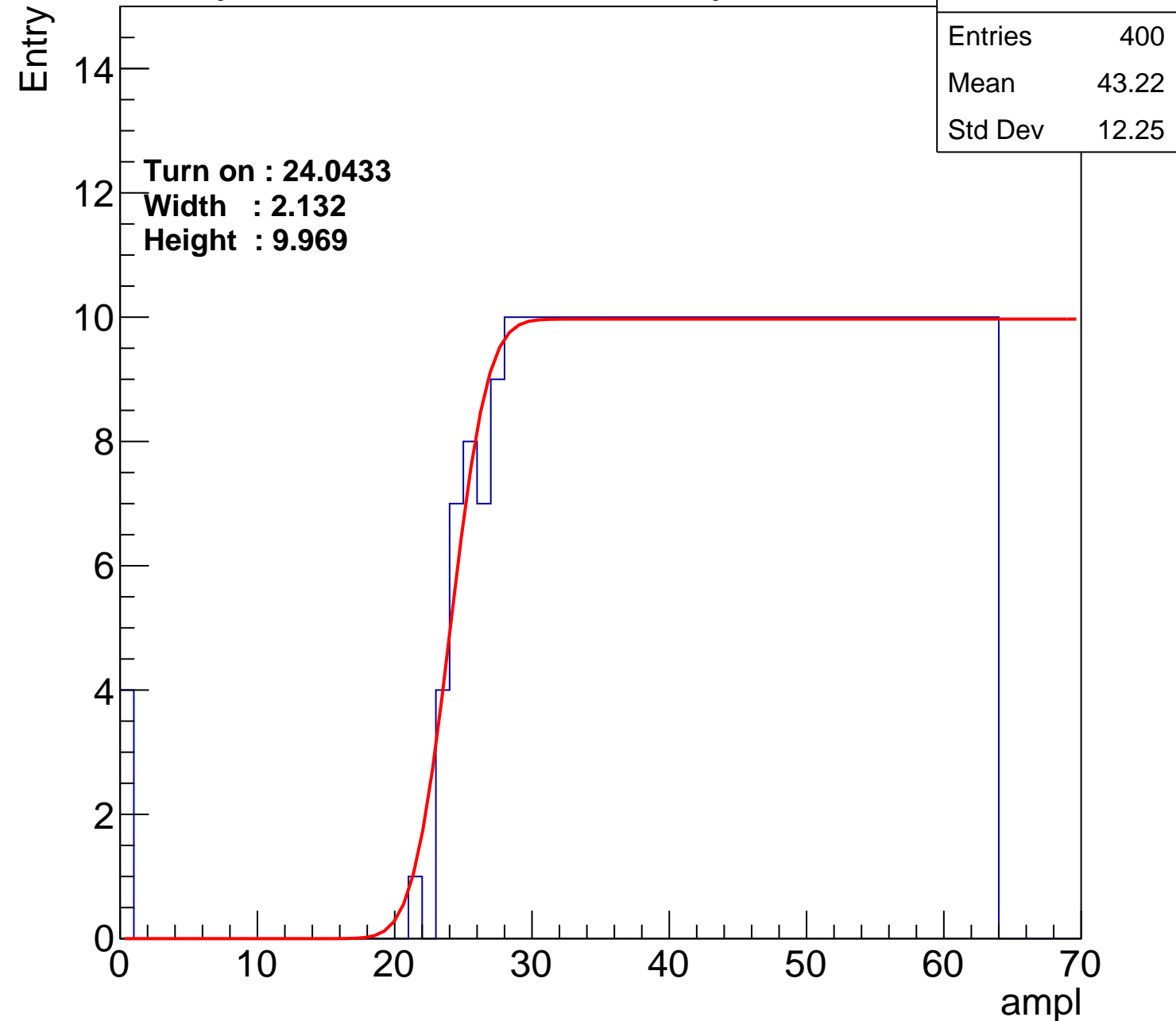
Width : 2.132

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.16
Std Dev	12

Turn on : 24.0695

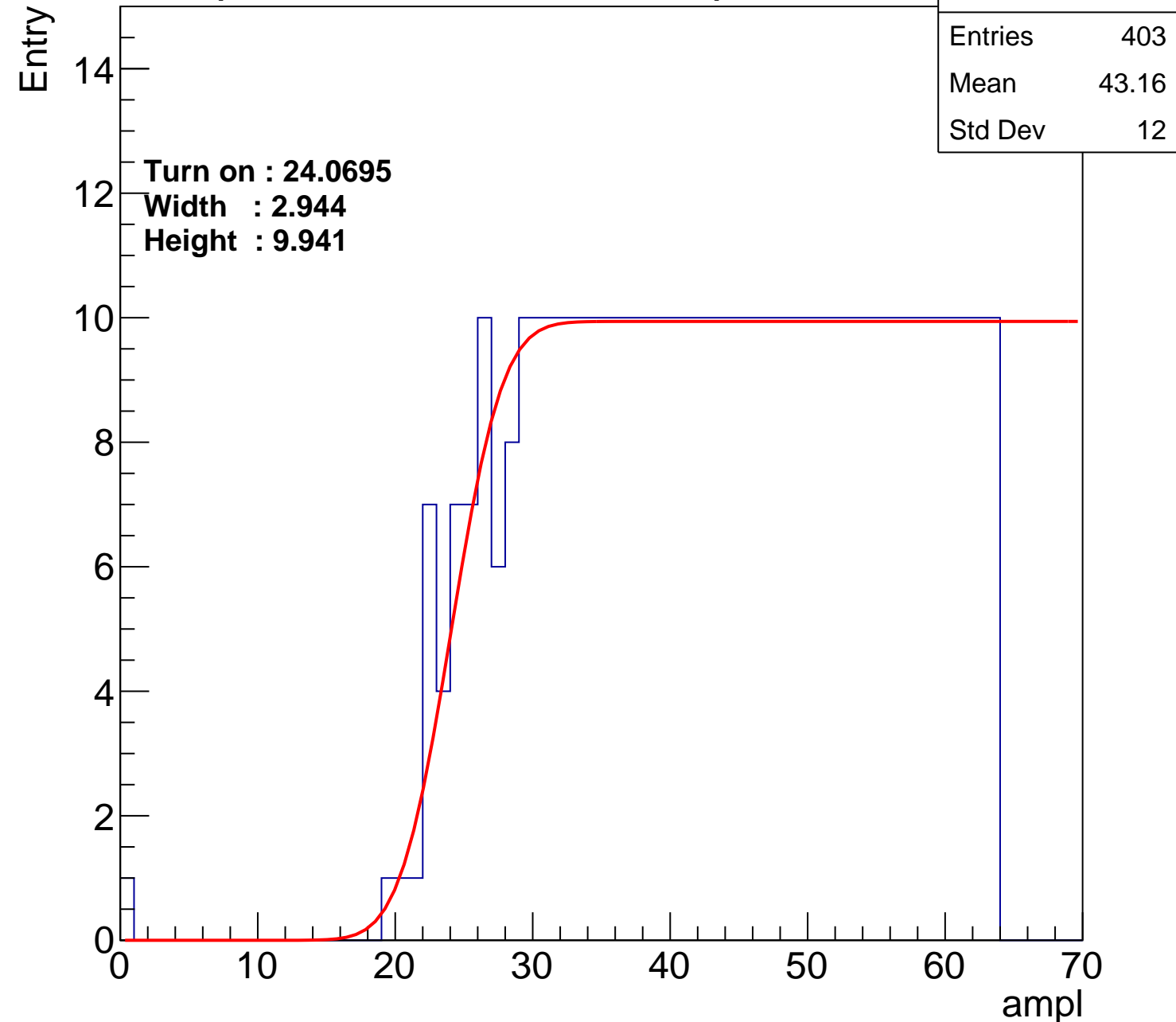
Width : 2.944

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.07
Std Dev	12.15

Turn on : 24.0965

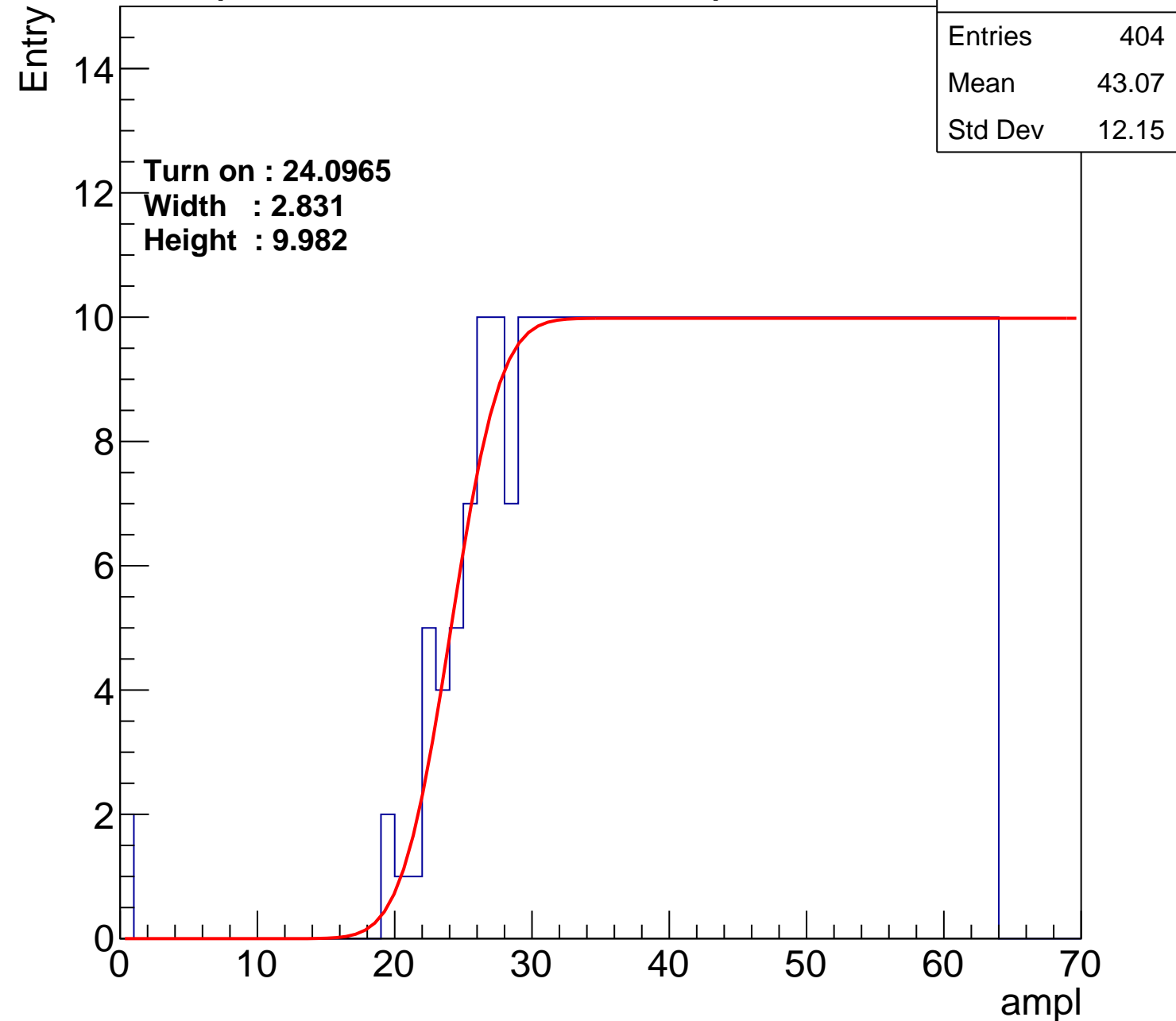
Width : 2.831

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entry

Entries 378

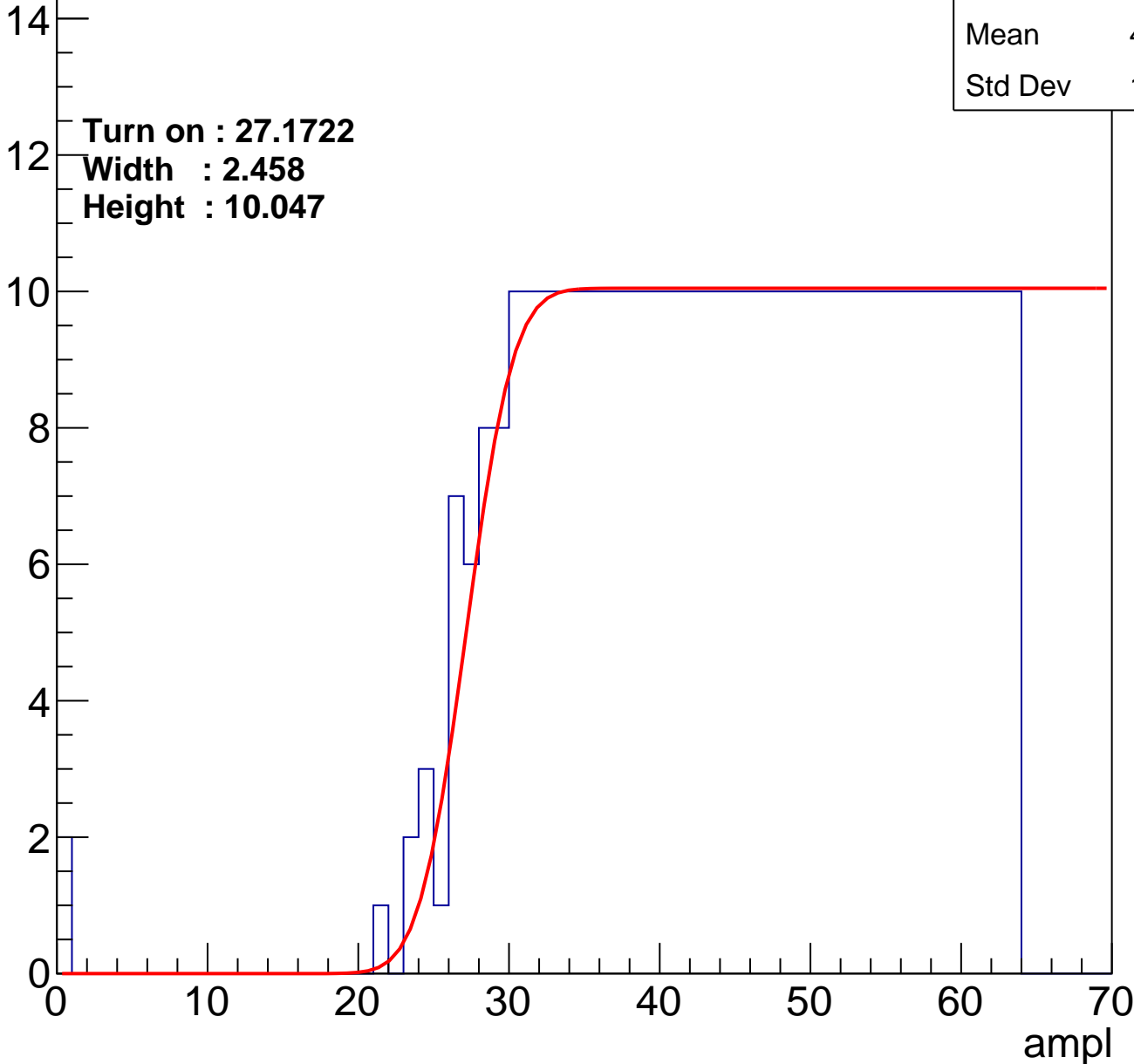
Mean 44.38

Std Dev 11.44

Turn on : 27.1722

Width : 2.458

Height : 10.047



B1L002S, U17-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.68
Std Dev	11.97

Turn on : 25.2053

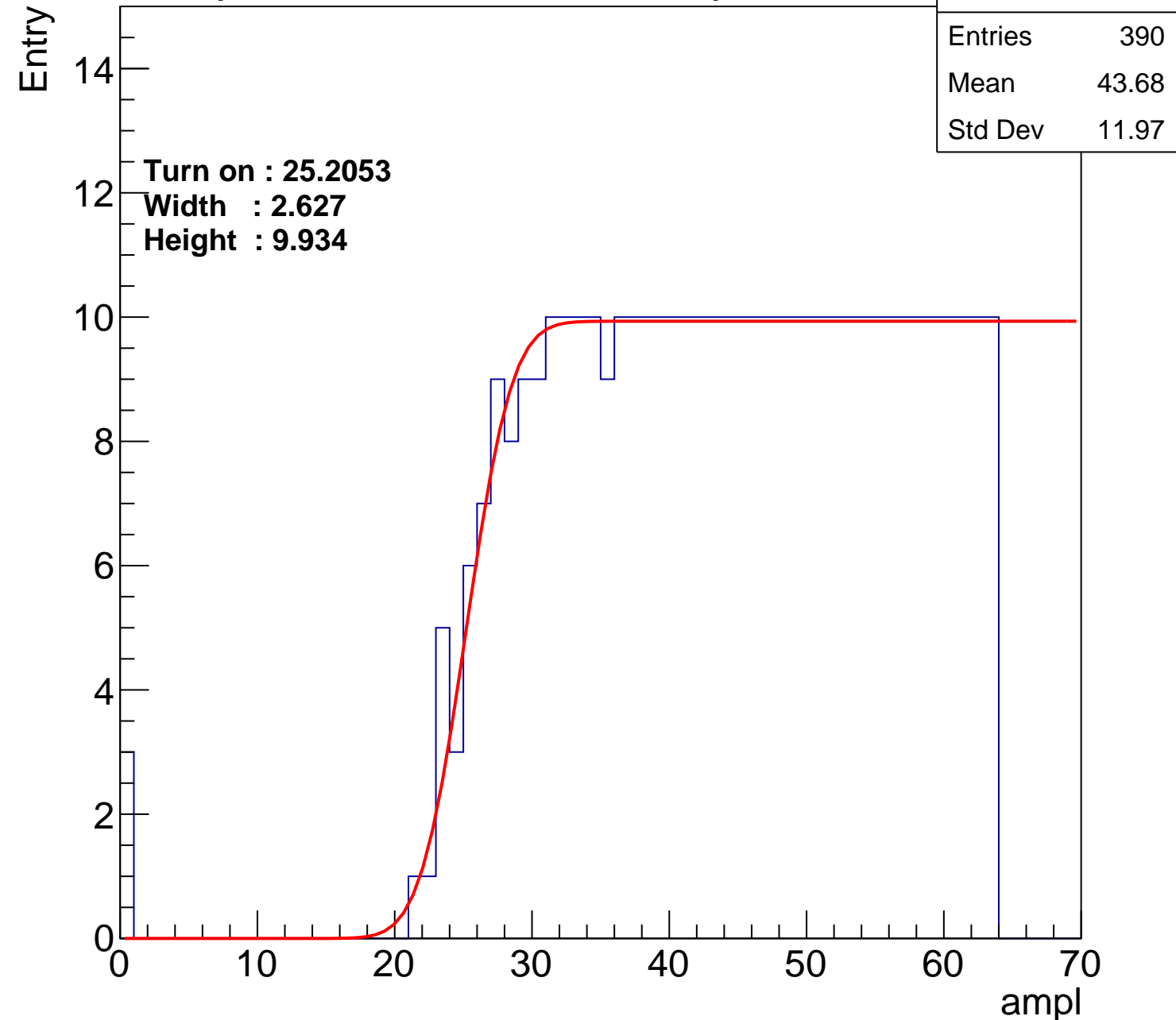
Width : 2.627

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.34
Std Dev	12.15

Turn on : 25.0168

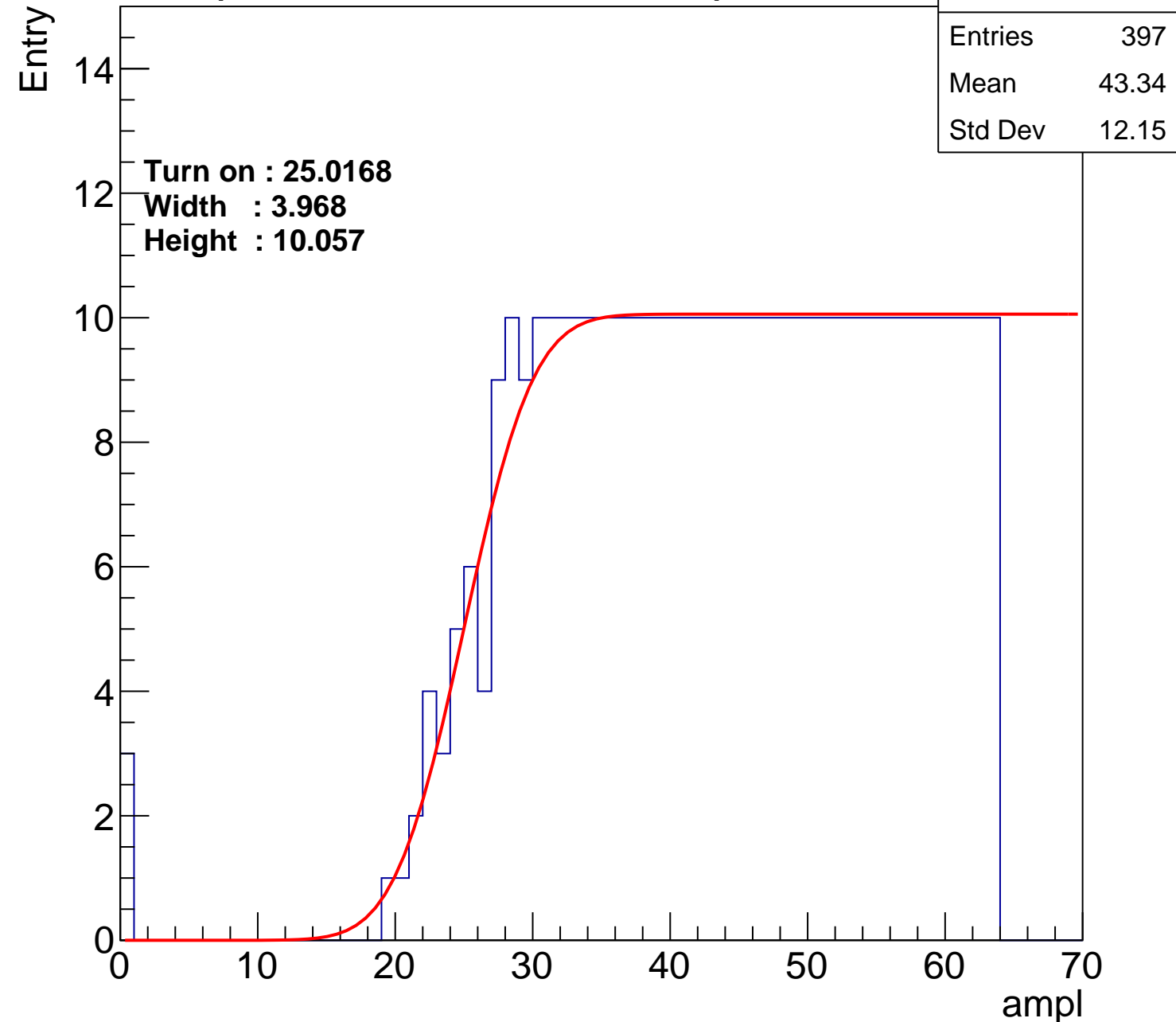
Width : 3.968

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.41
Std Dev	12.21

Turn on : 24.9609

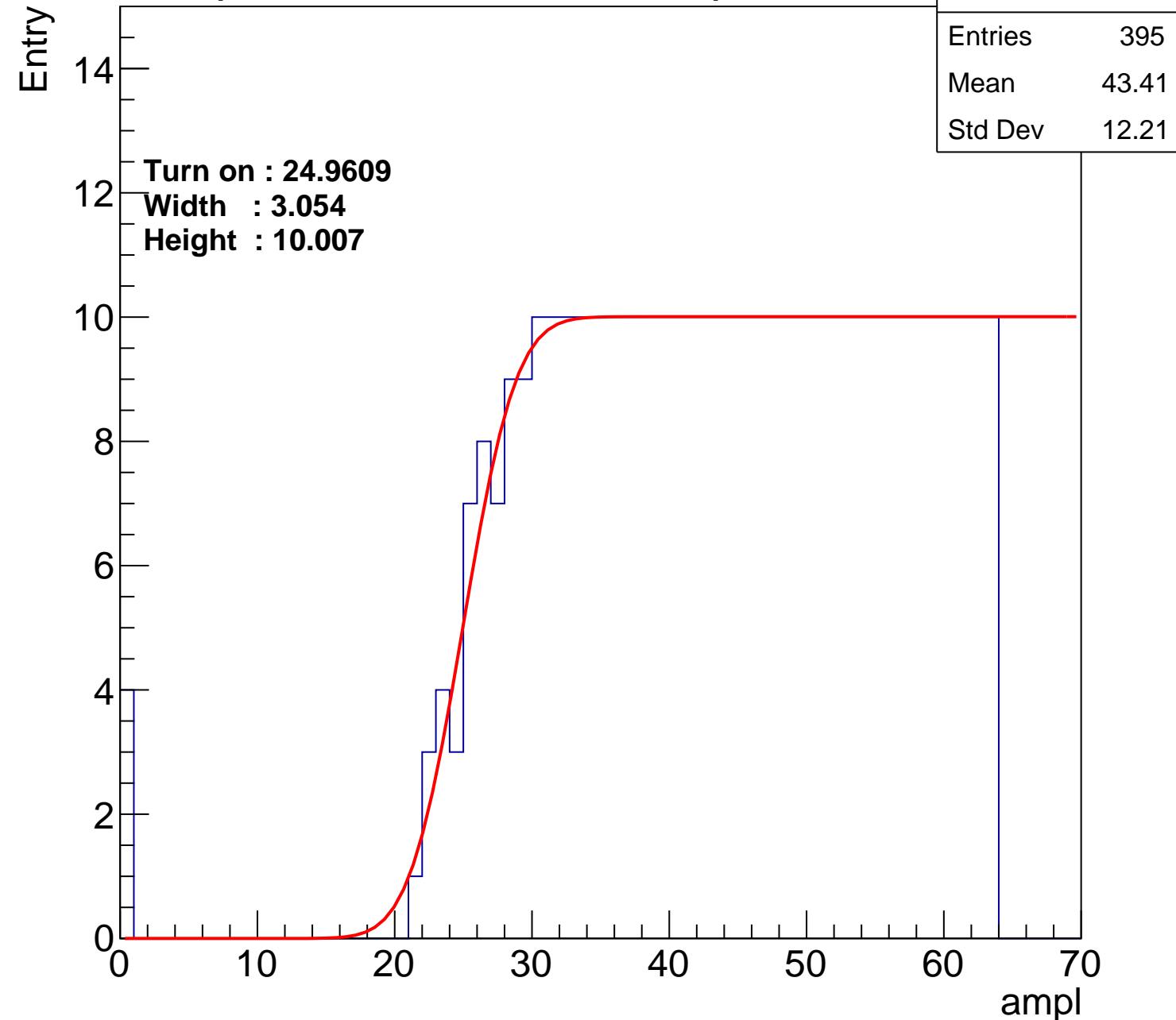
Width : 3.054

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.71
Std Dev	12.28

Turn on : 26.2614

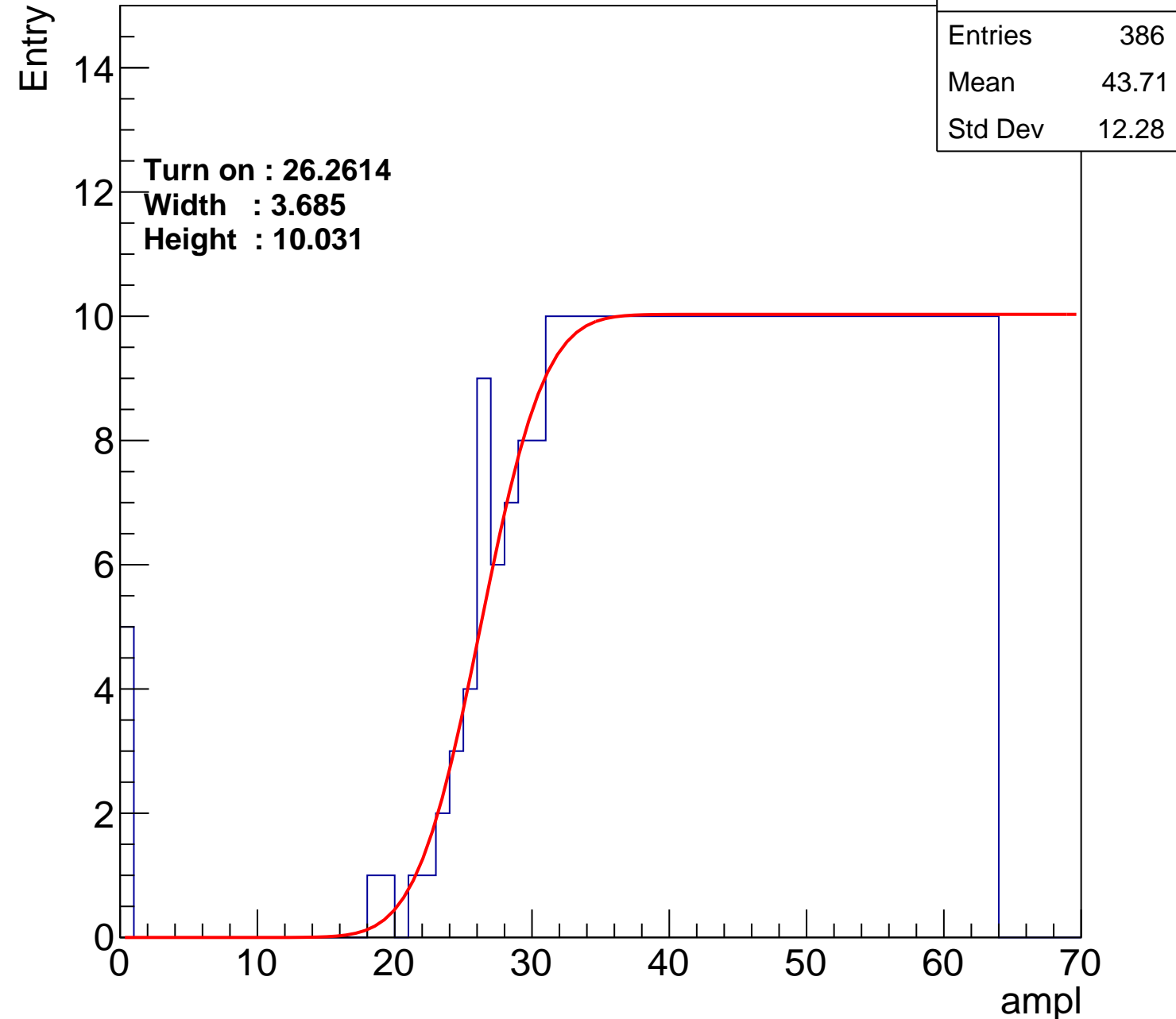
Width : 3.685

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch116

calib_packv5_042523_0143.root, FC#10, port B3

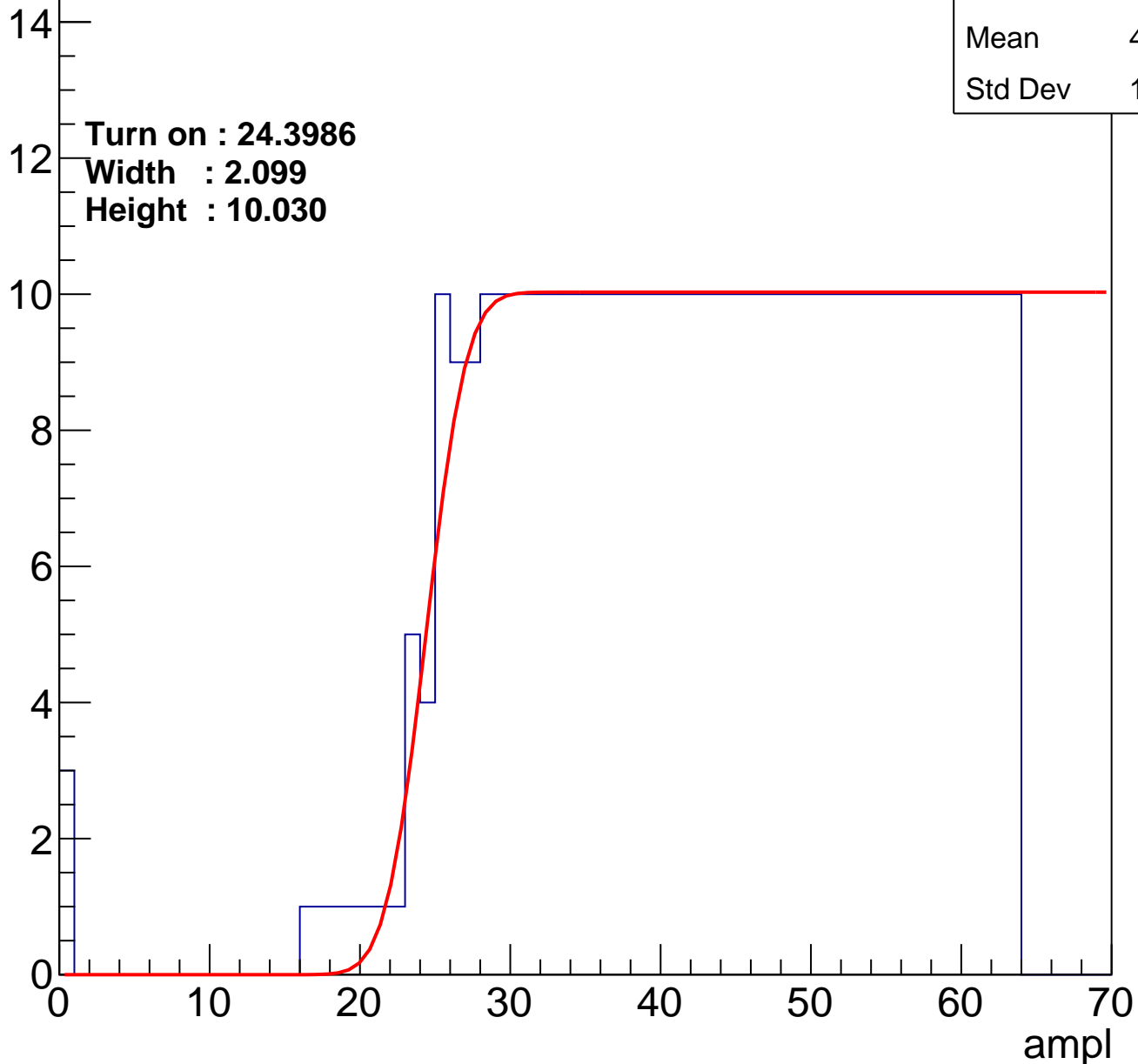
Entries	407
Mean	42.88
Std Dev	12.37

Turn on : 24.3986

Width : 2.099

Height : 10.030

Entry



B1L002S, U17-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.04
Std Dev	12.27

Turn on : 27.2531

Width : 3.957

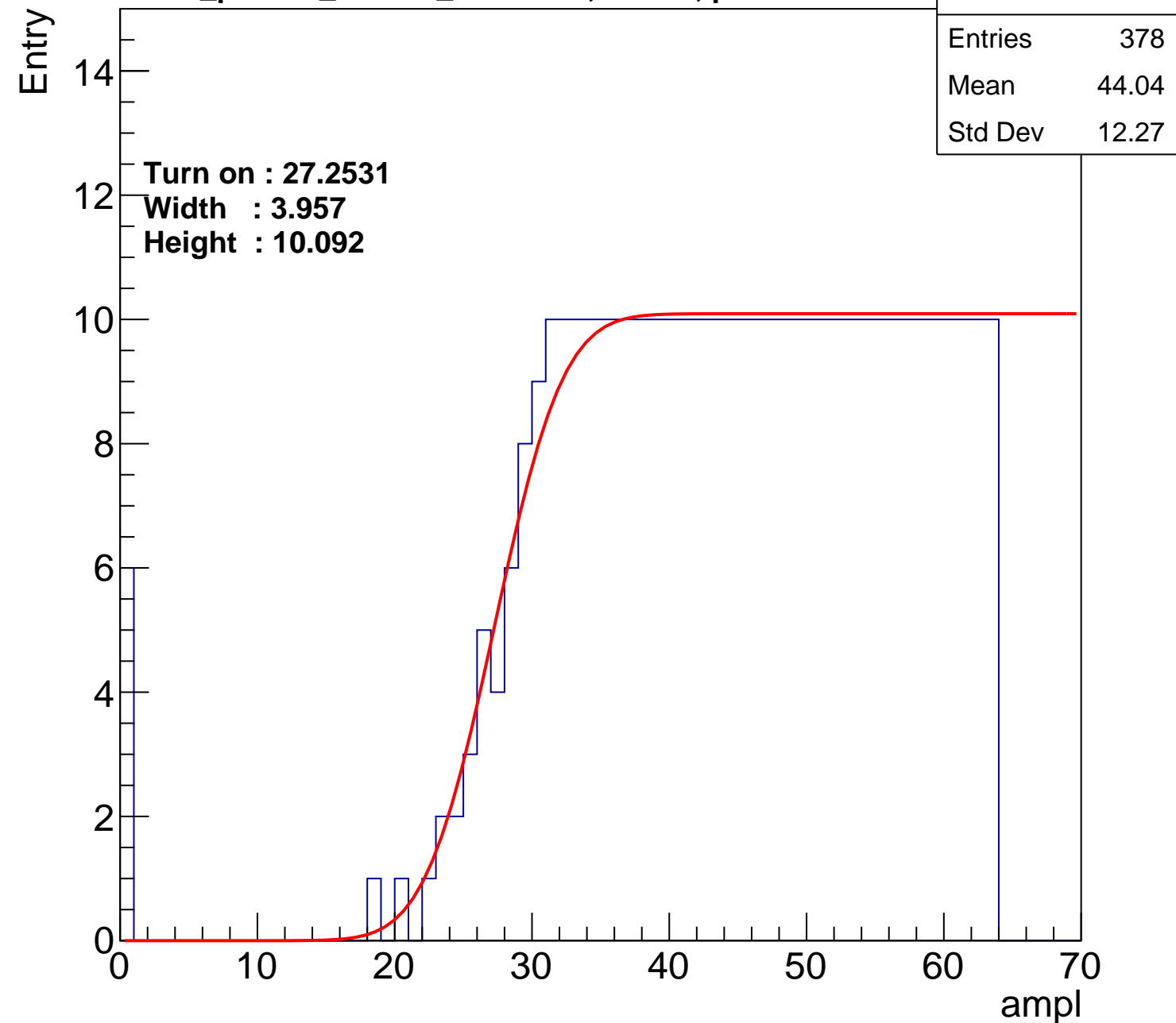
Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U17-ch118

calib_packv5_042523_0143.root, FC#10, port B3

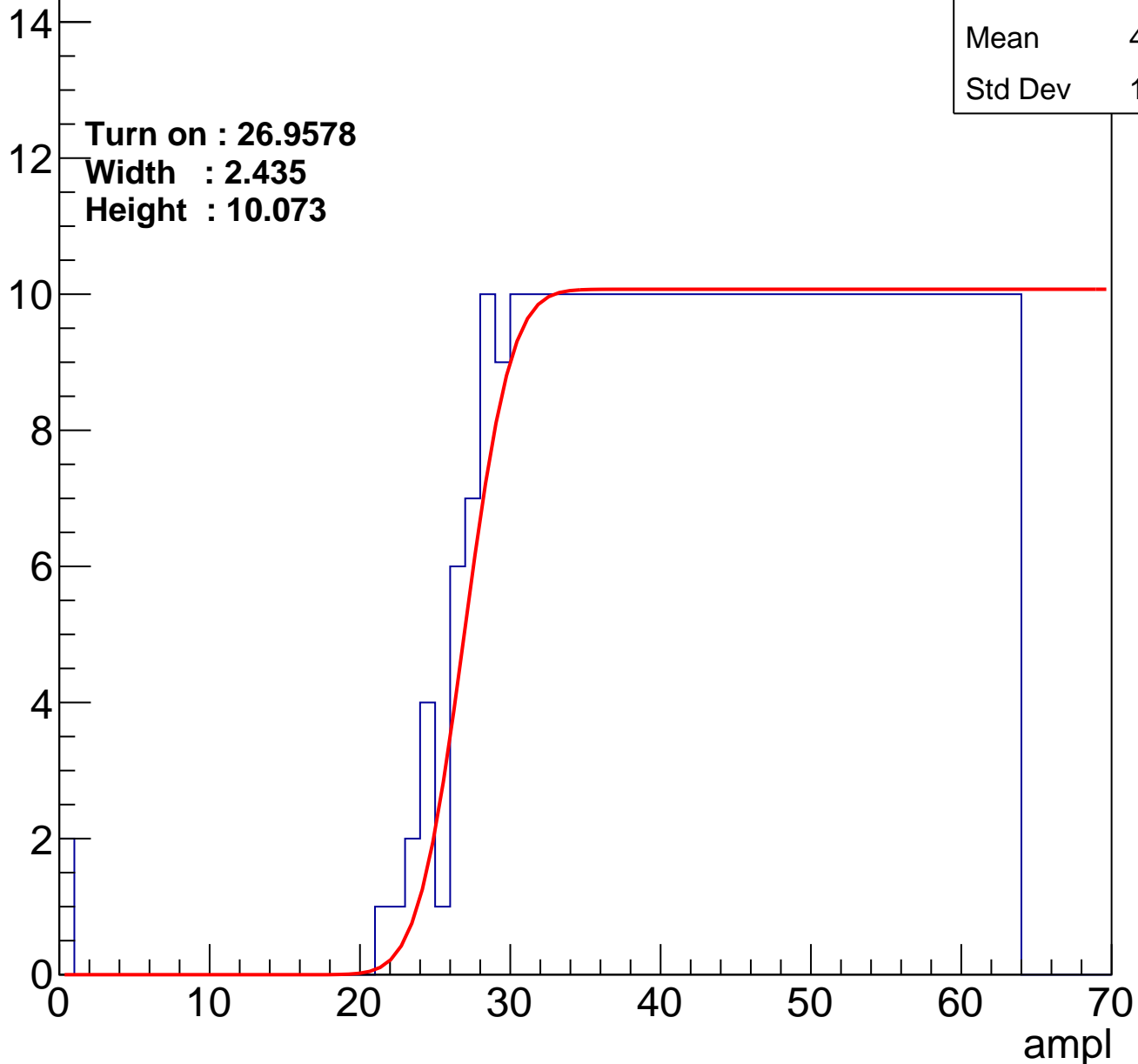
Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.9578

Width : 2.435

Height : 10.073

Entry



B1L002S, U17-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.63
Std Dev	12.08

Turn on : 25.8641

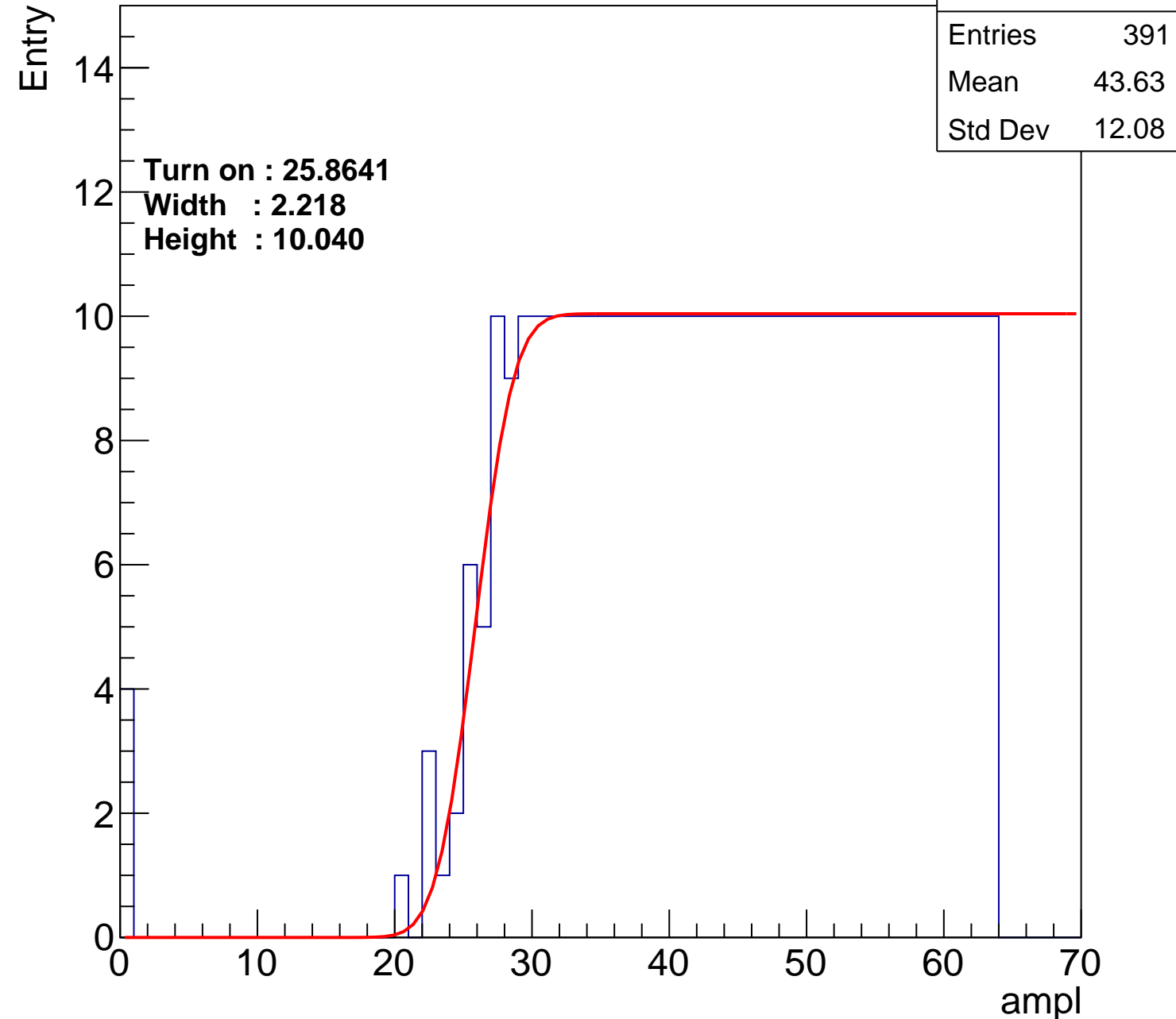
Width : 2.218

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.6077

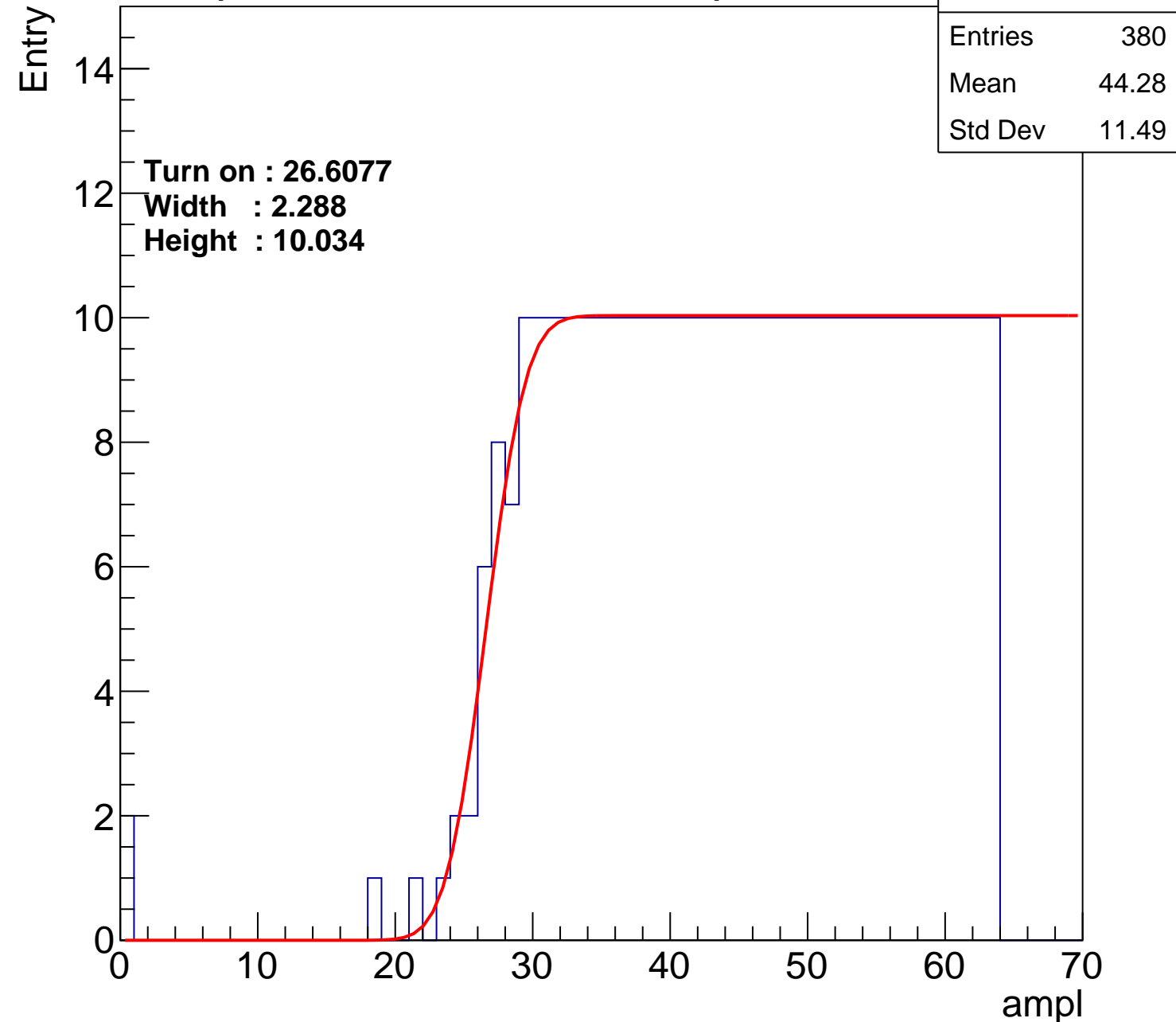
Width : 2.288

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.85
Std Dev	12.3

Turn on : 23.4234

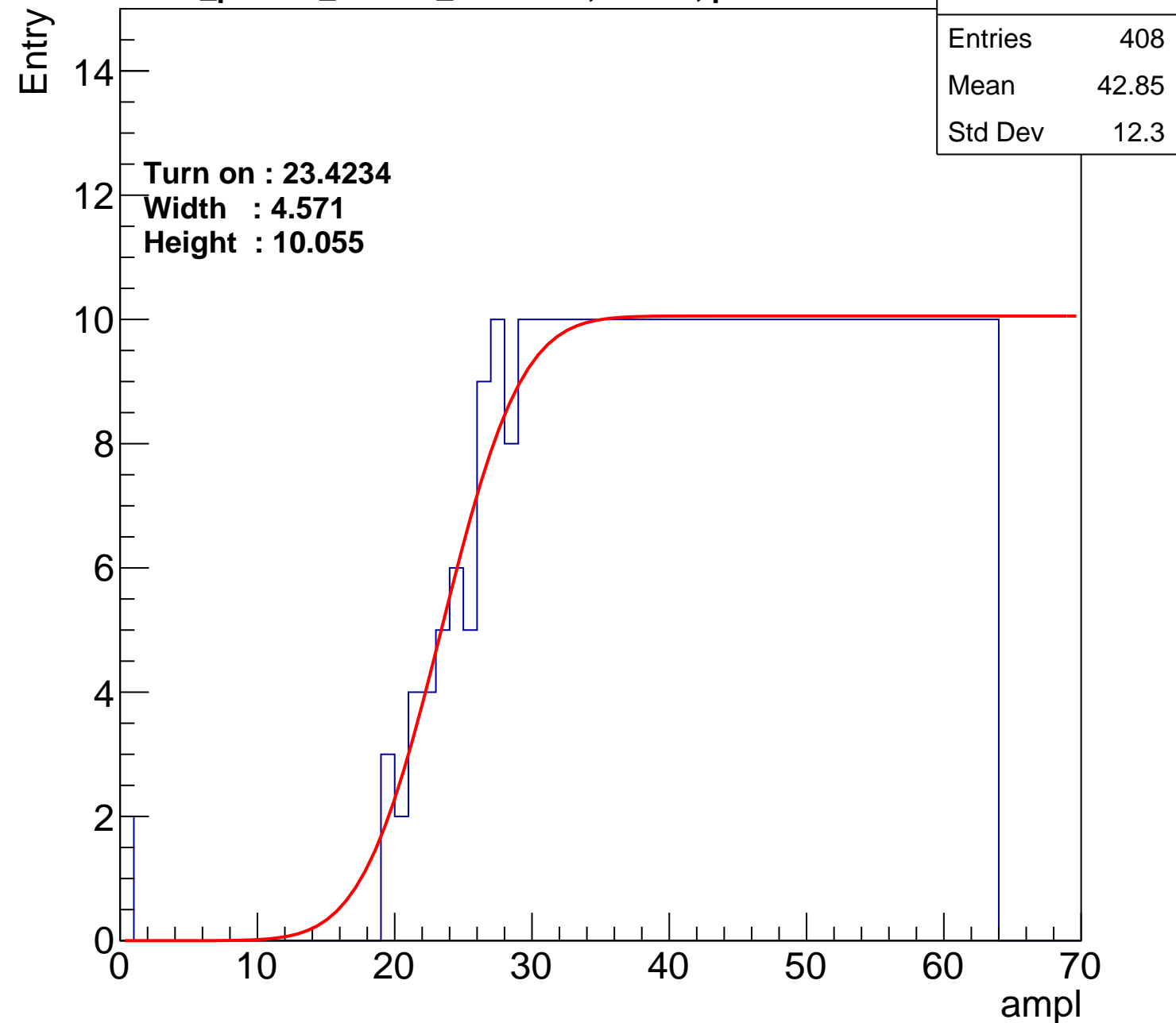
Width : 4.571

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.64
Std Dev	11.93

Turn on : 24.9047

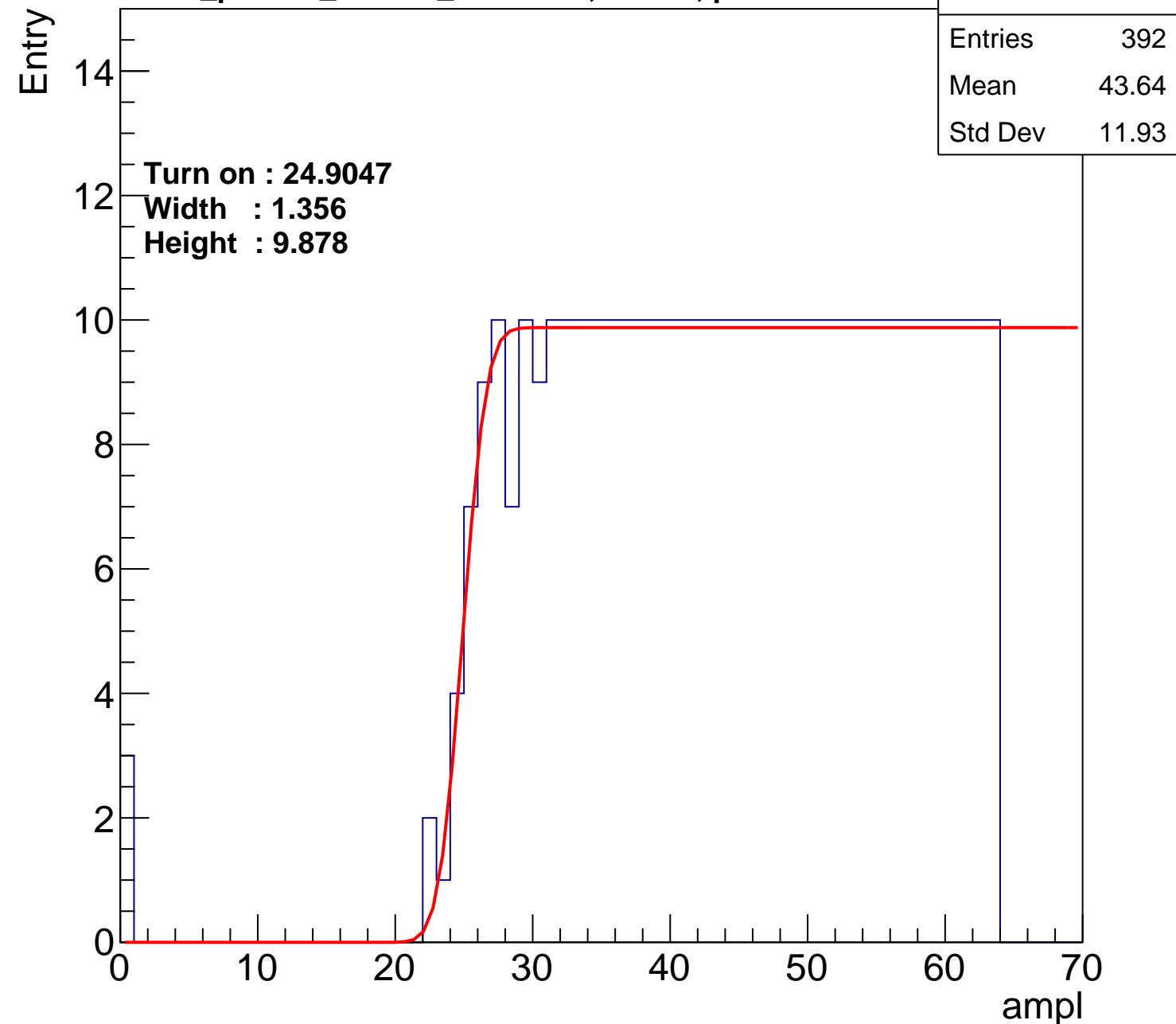
Width : 1.356

Height : 9.878

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.2823

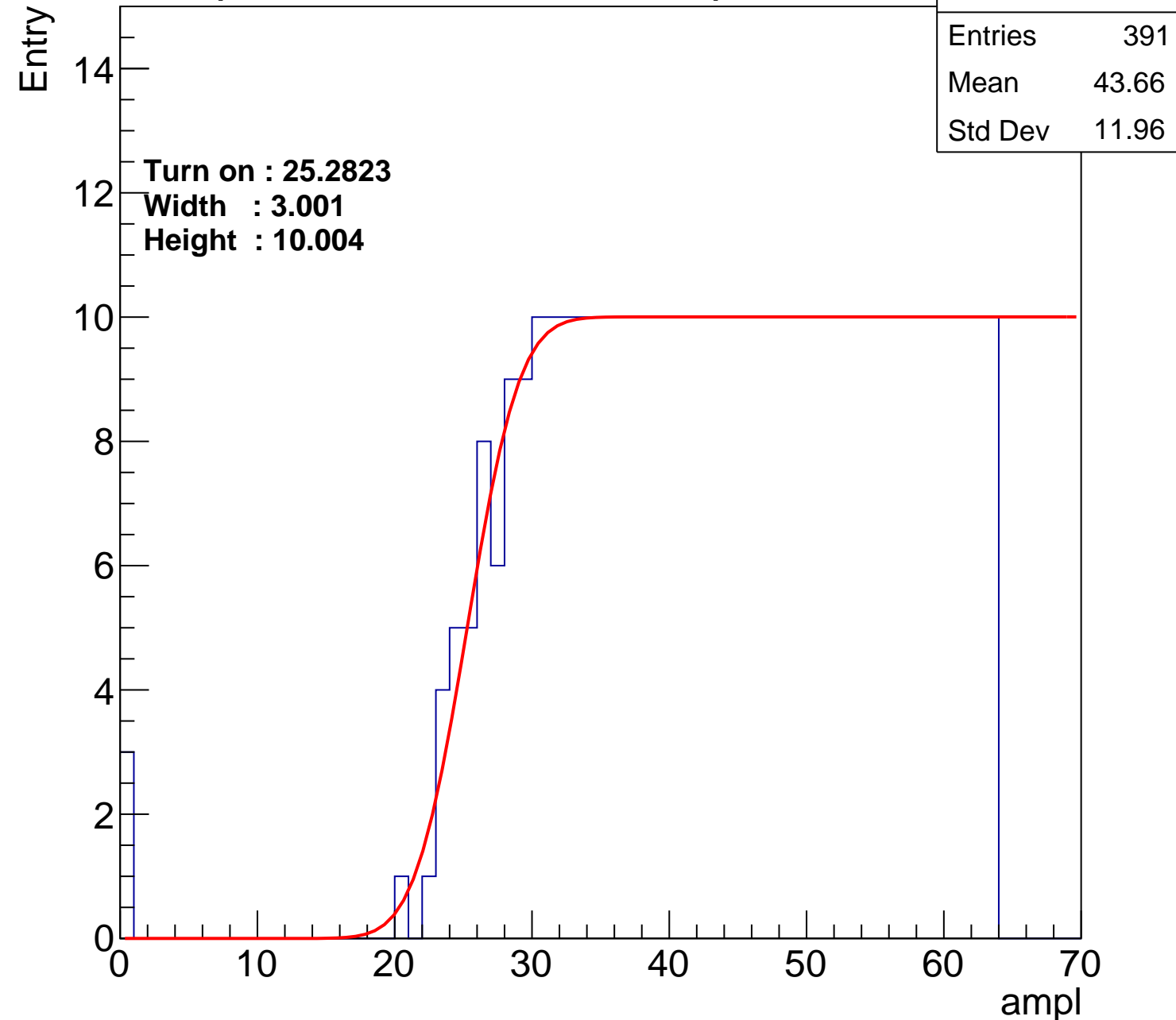
Width : 3.001

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.59
Std Dev	12.14

Turn on : 25.4004

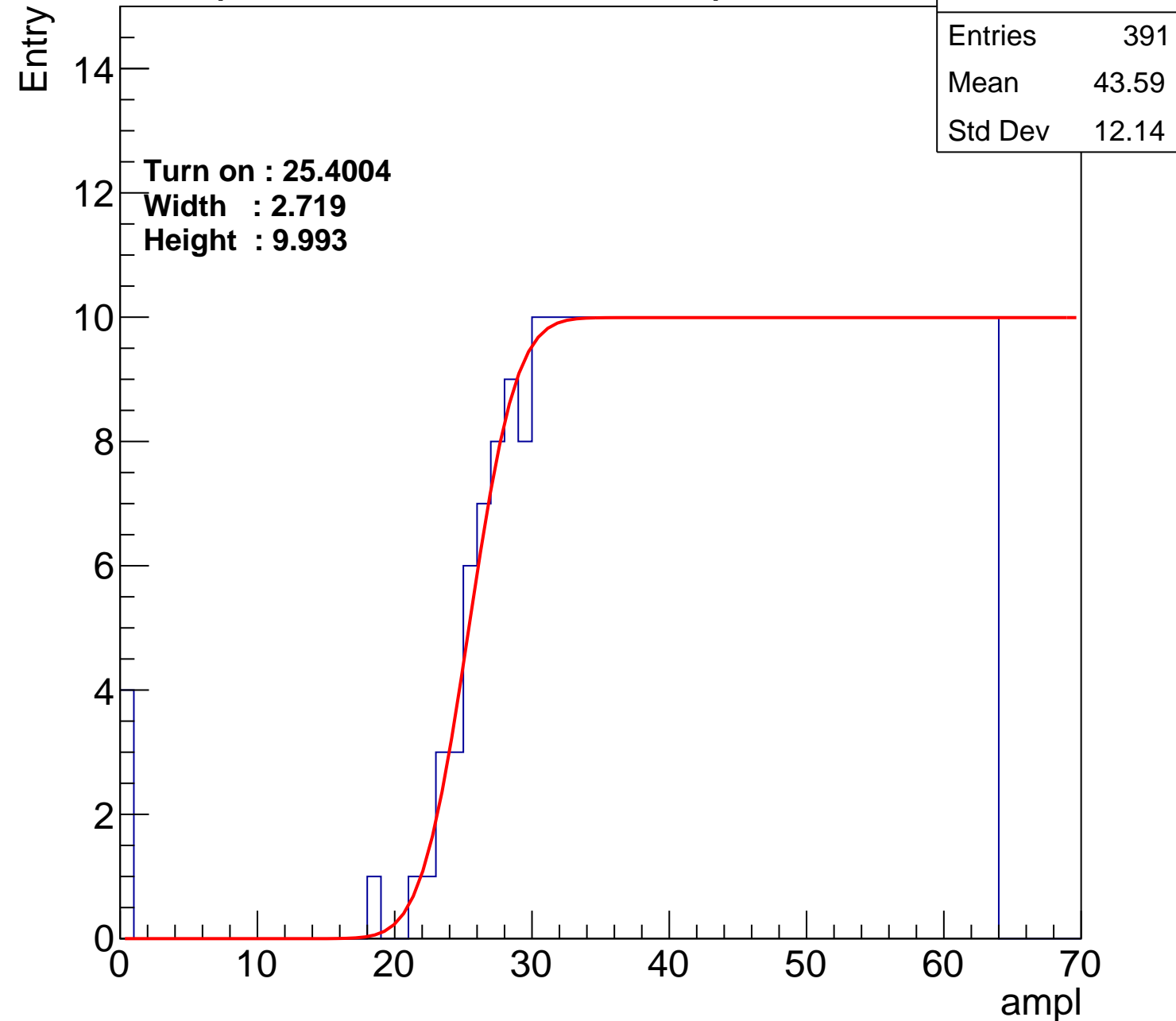
Width : 2.719

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.75
Std Dev	12.51

Turn on : 23.9591

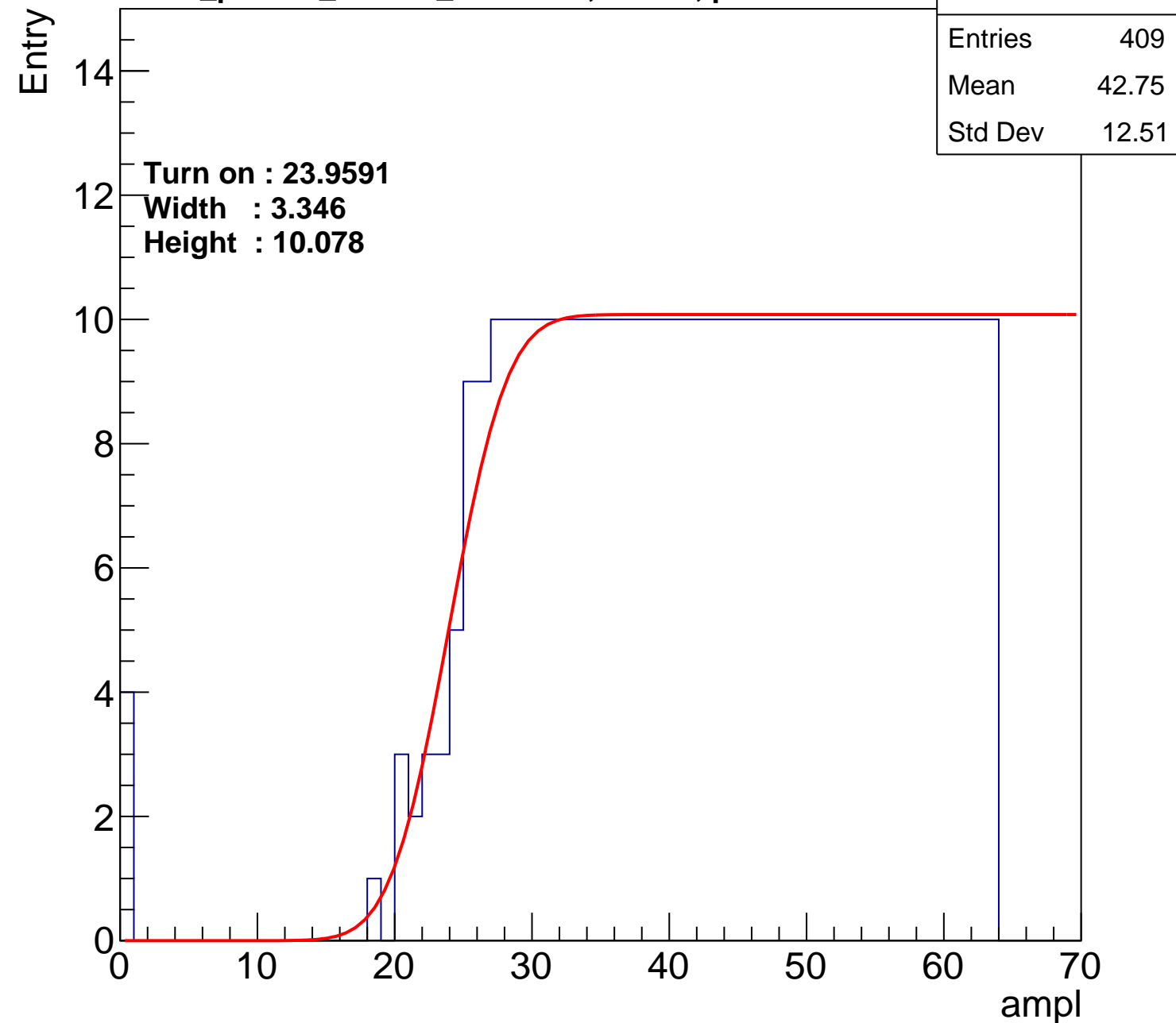
Width : 3.346

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.24
Std Dev	12.04

Turn on : 24.3743

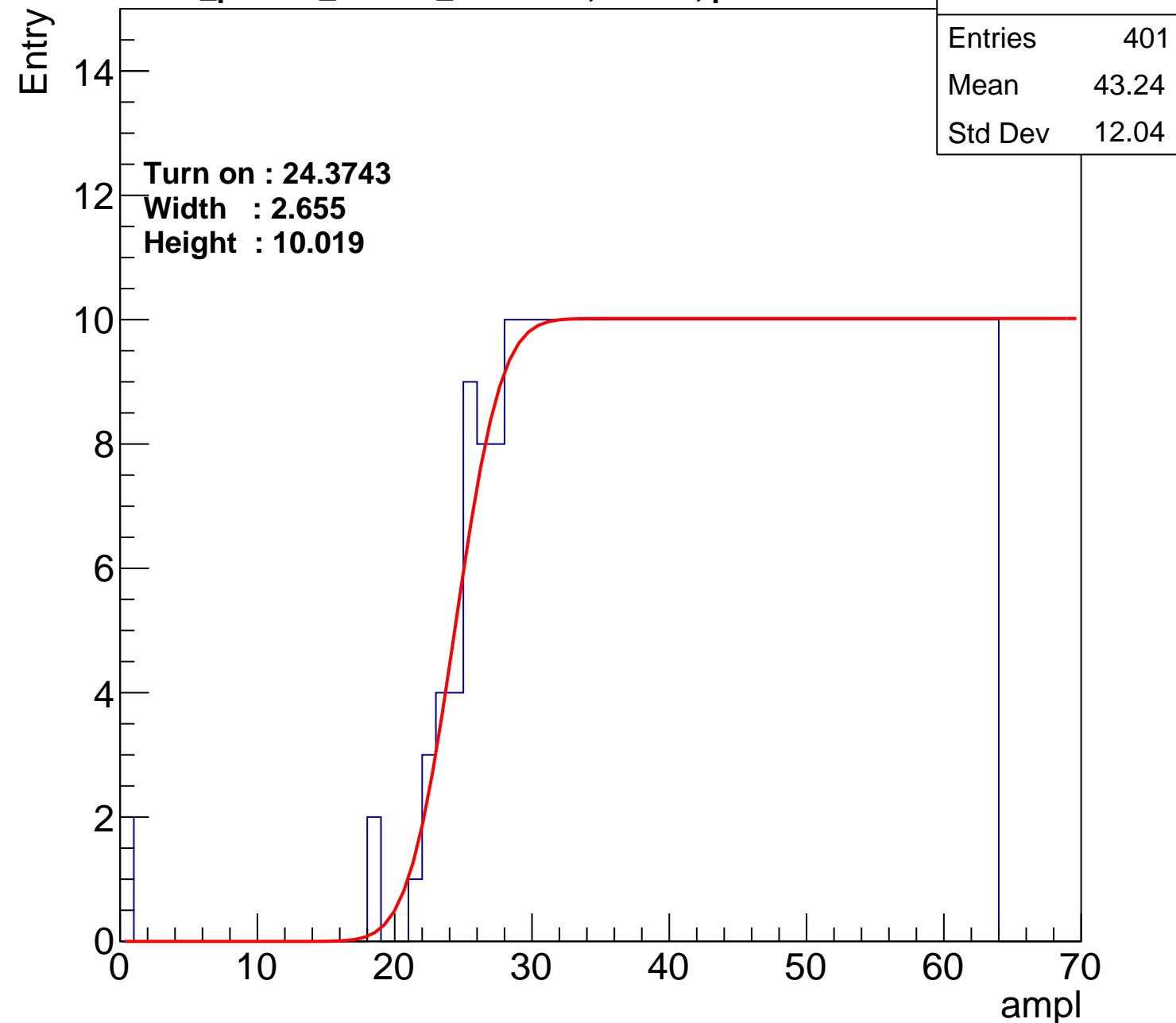
Width : 2.655

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U17-ch127

calib_packv5_042523_0143.root, FC#10, port B3

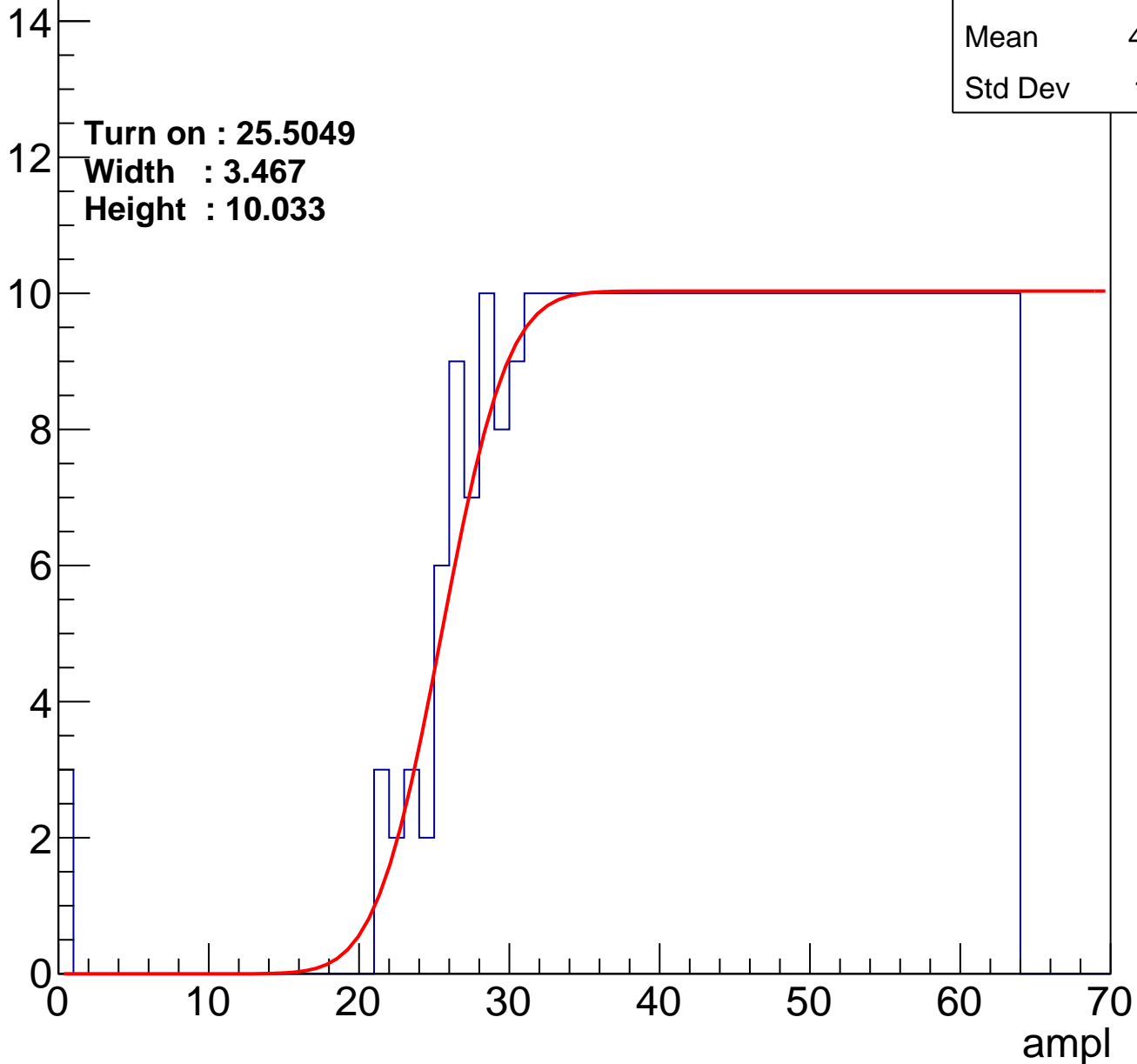
Entries	392
Mean	43.59
Std Dev	12.01

Turn on : 25.5049

Width : 3.467

Height : 10.033

Entry



B1L002S, U17-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.59
Std Dev	12.01

Turn on : 25.5049

Width : 3.467

Height : 10.033

Entry

