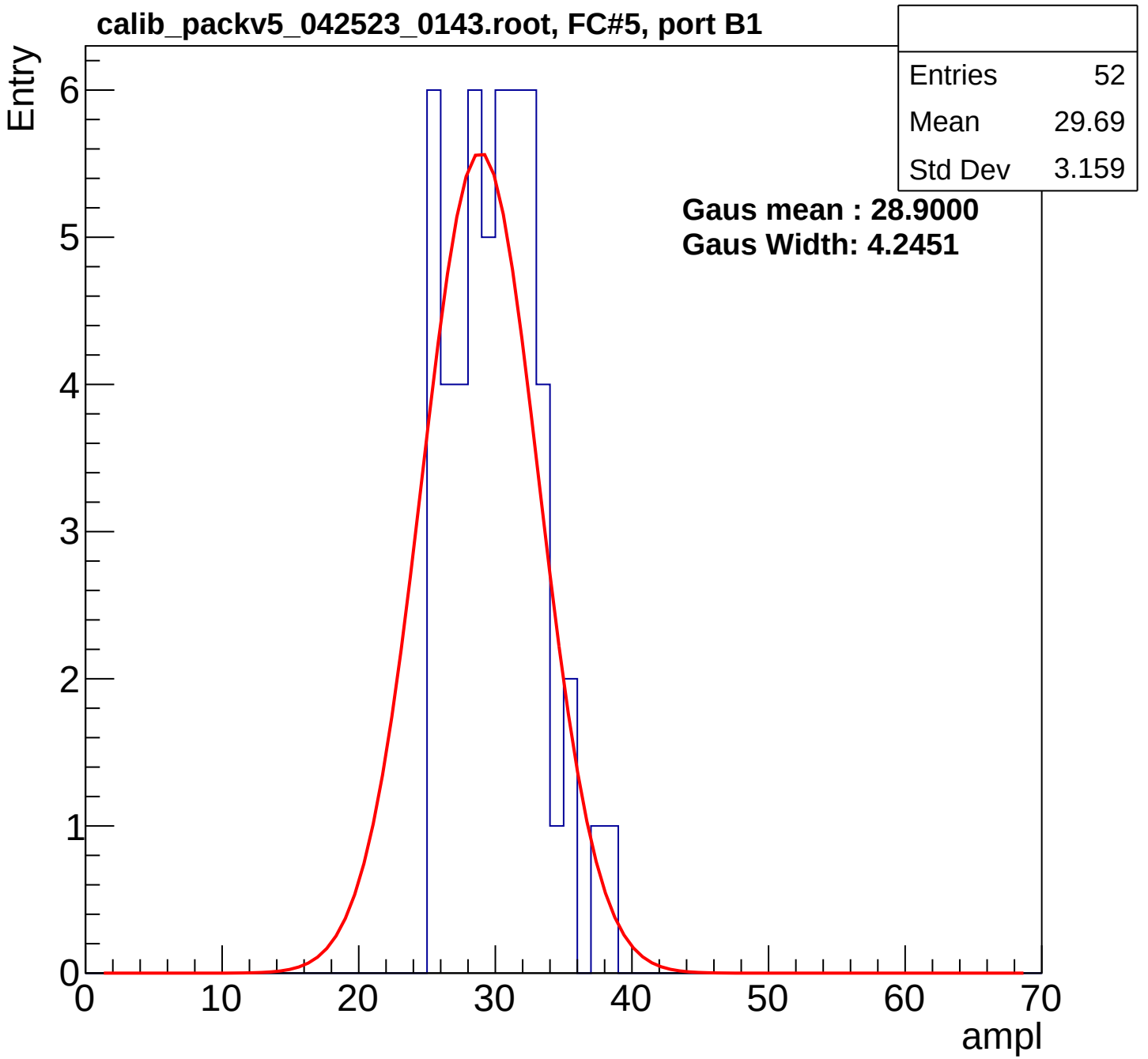
B0L000S, U12-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 29.69
Std Dev 3.159
Gaus mean : 28.9000
Gaus Width: 4.2451
Entry
ampl
0
1
2
3
4
5
6
0
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40
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60
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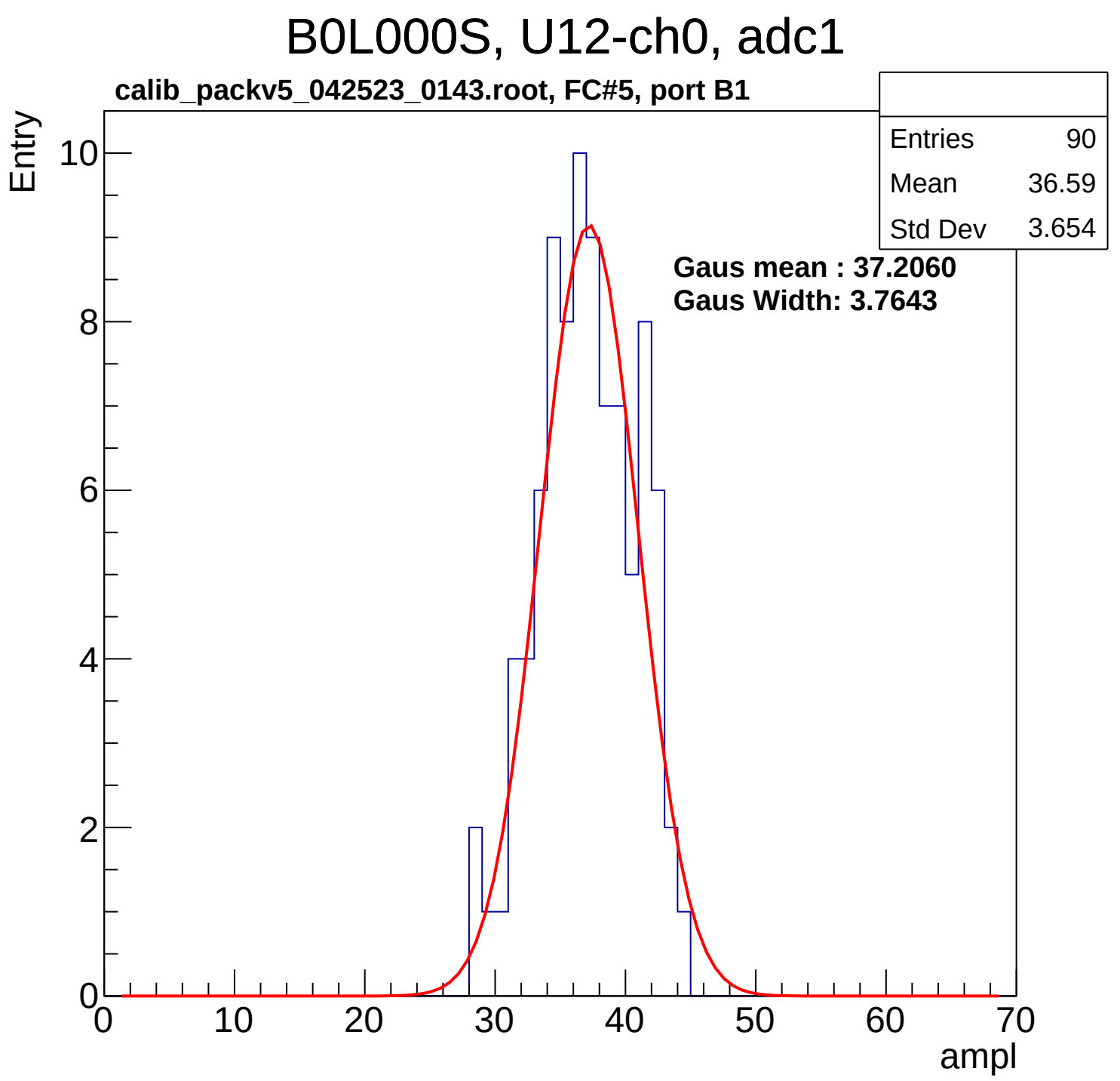
B0L000S, U12-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 36.59
Std Dev 3.654
Gaus mean : 37.2060
Gaus Width: 3.7643
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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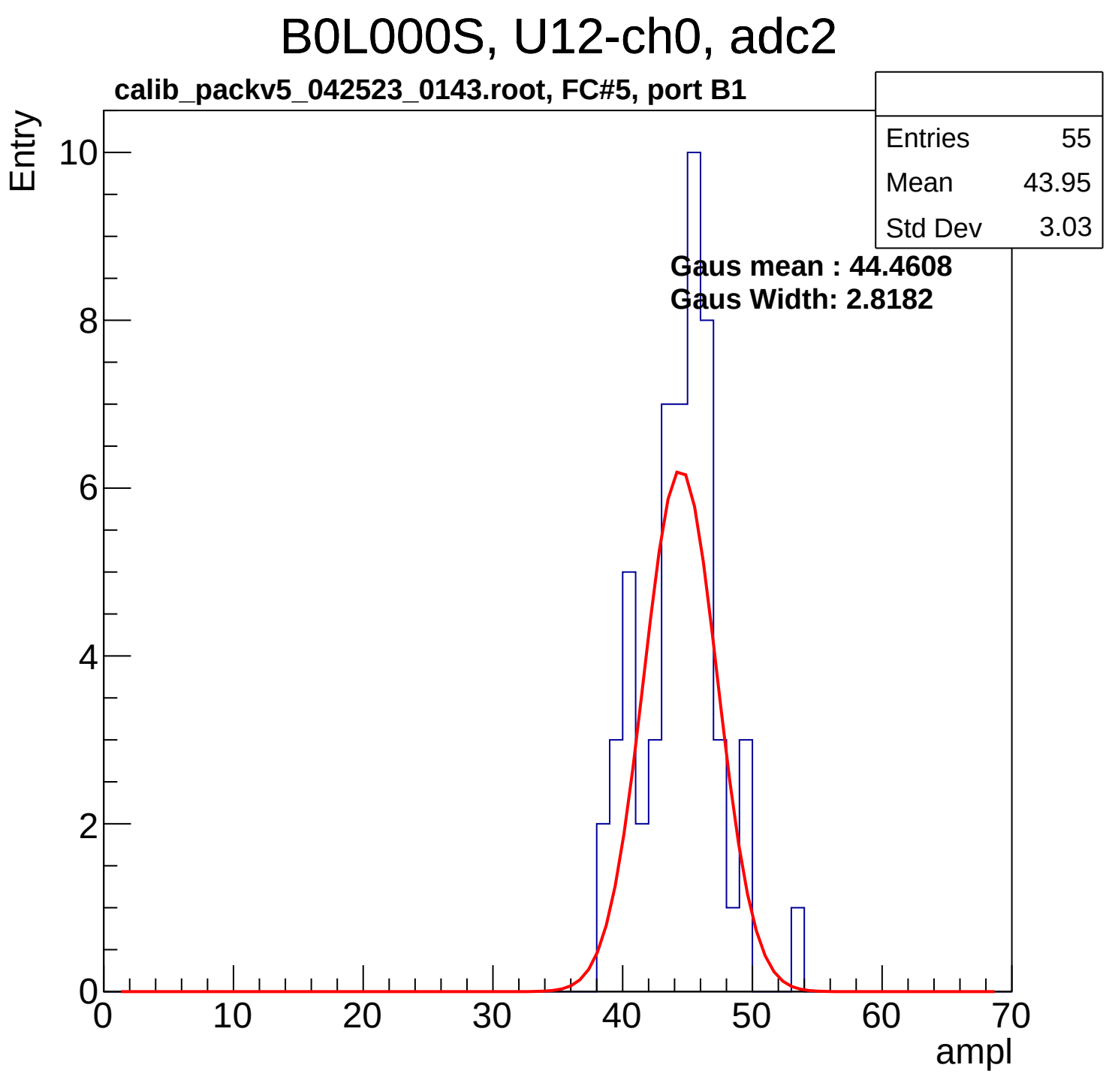
B0L000S, U12-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.95
Std Dev 3.03
Gaus mean : 44.4608
Gaus Width: 2.8182
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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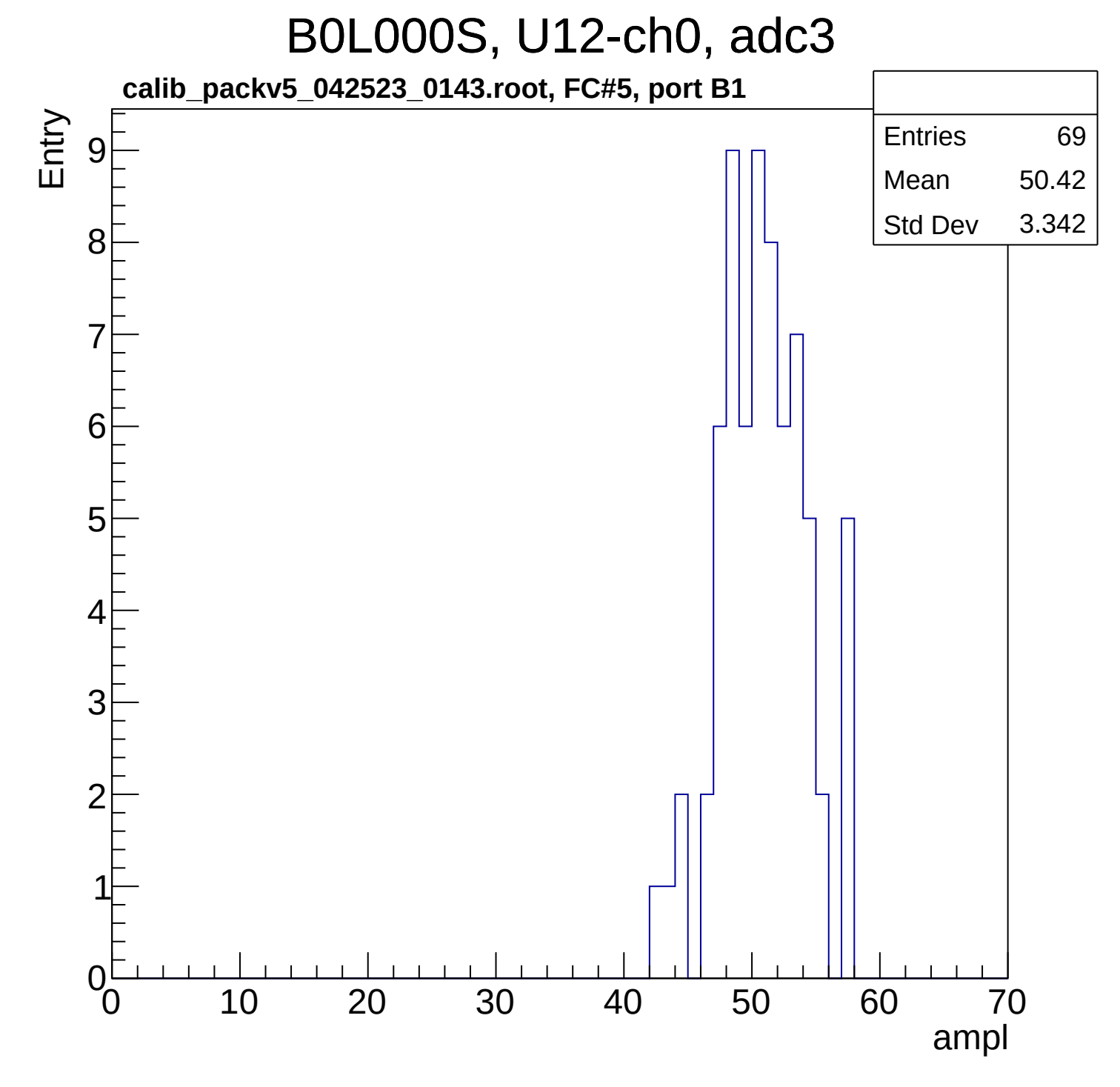
B0L000S, U12-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.42
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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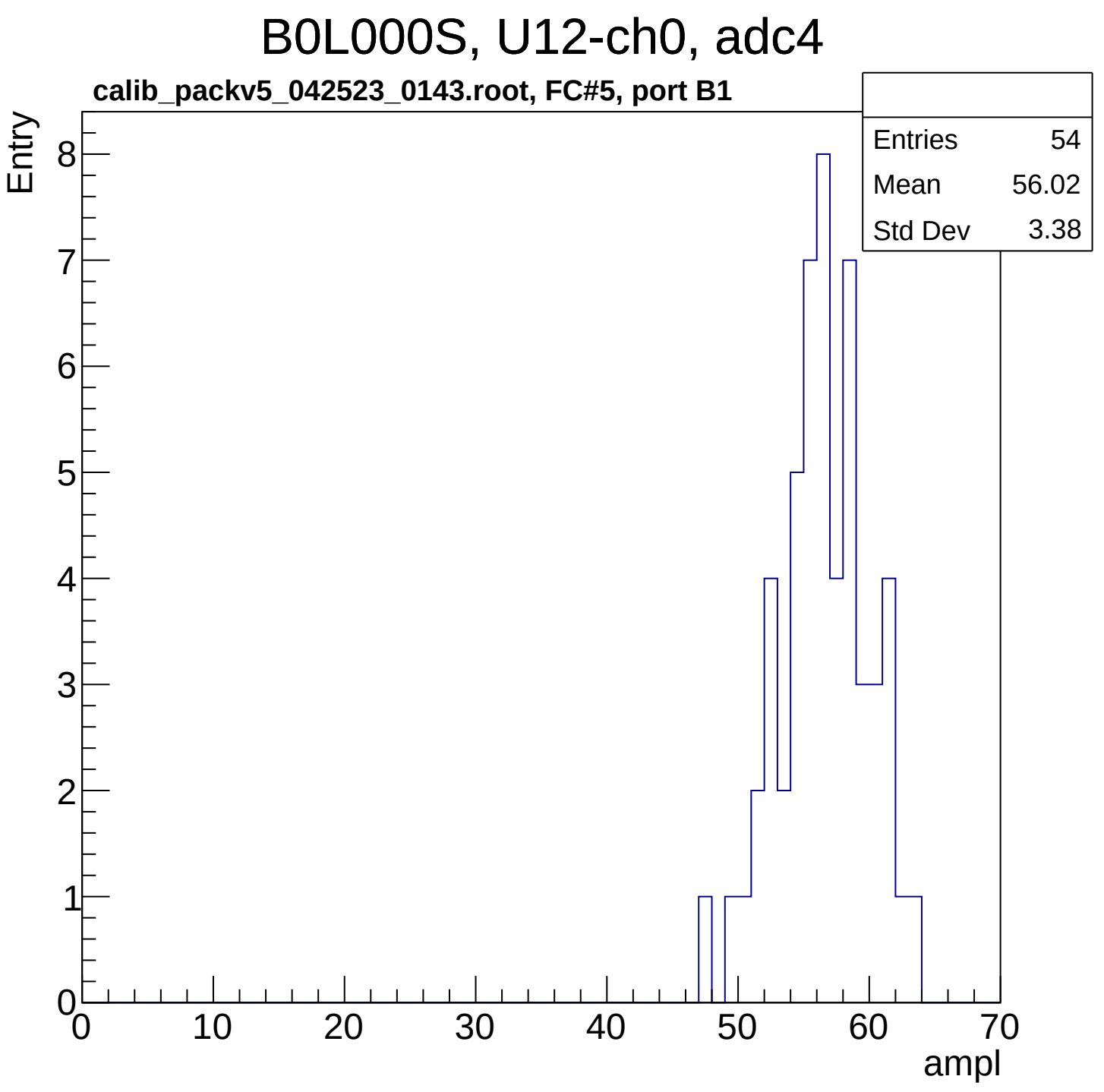
B0L000S, U12-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.02
Std Dev 3.38
Entry
0
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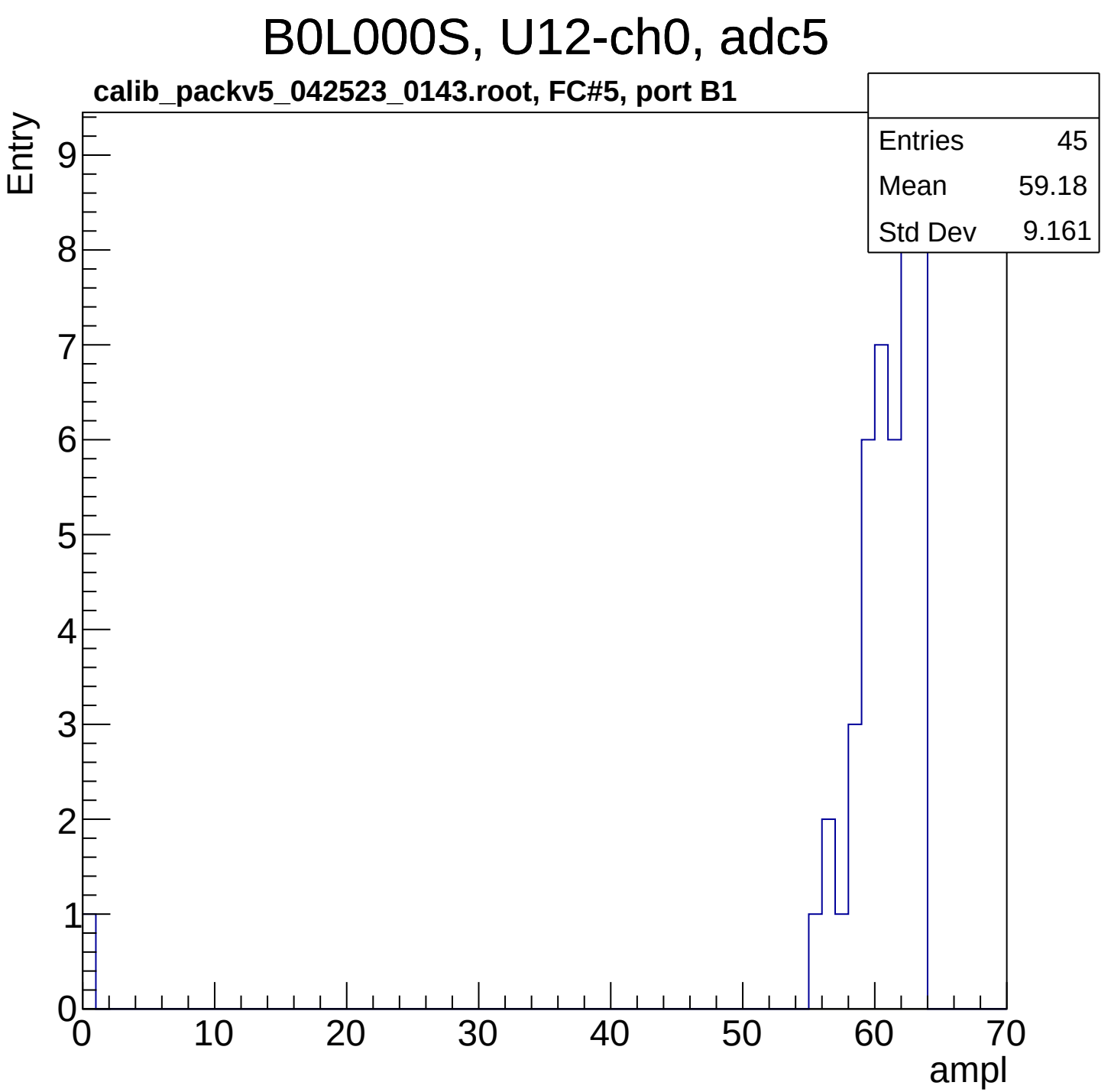
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.18
Std Dev 9.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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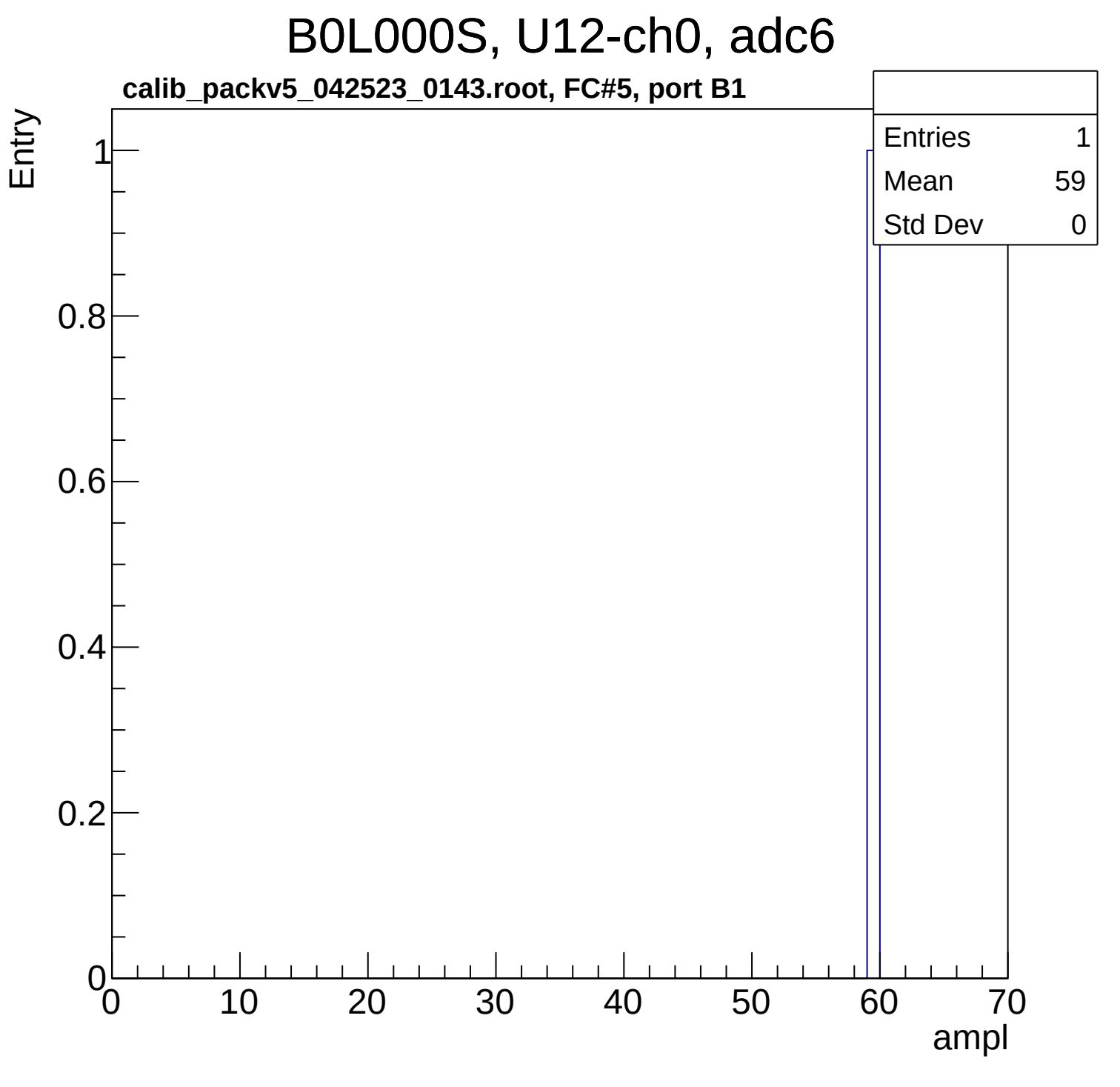
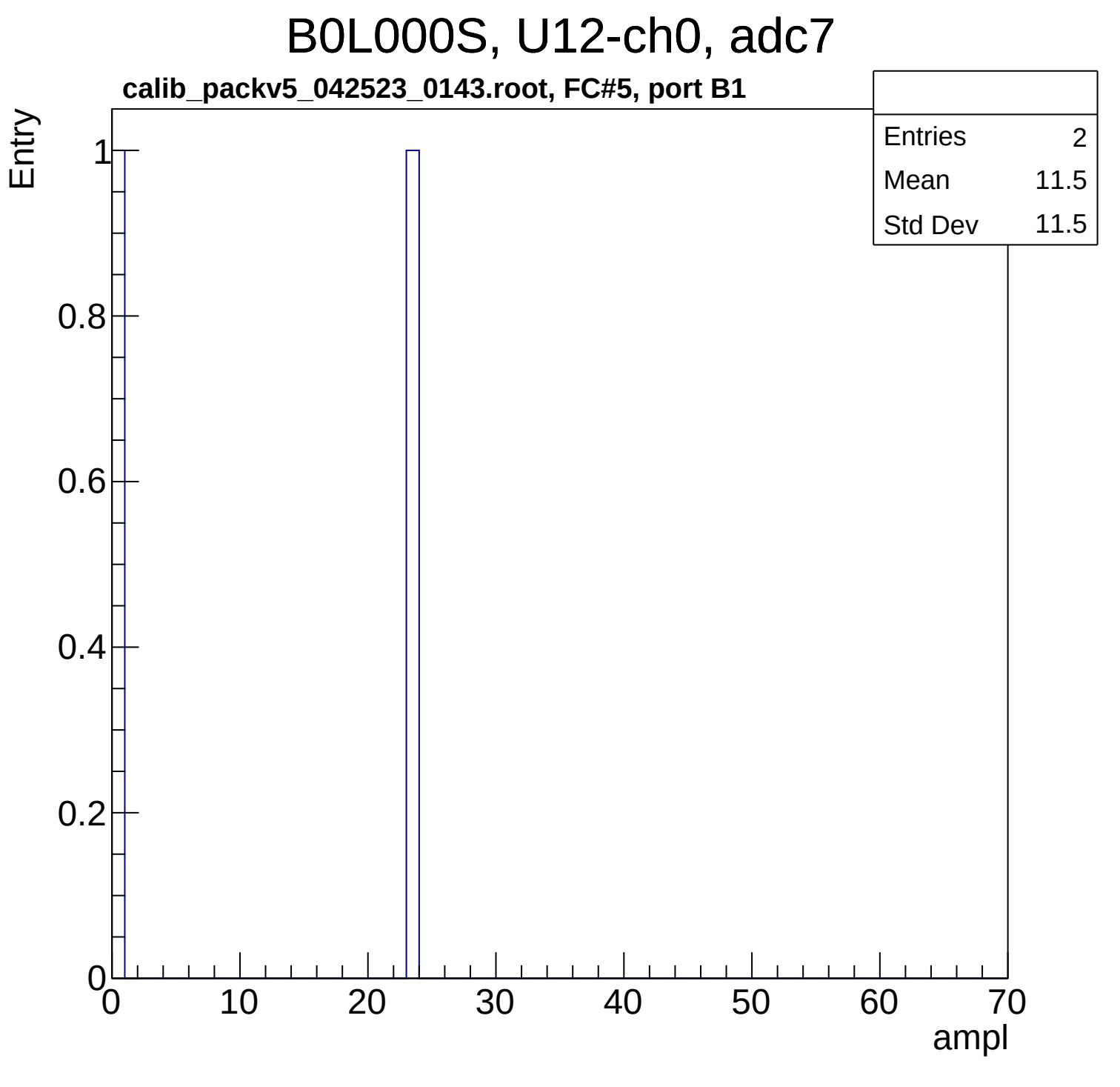

B0L000S, U12-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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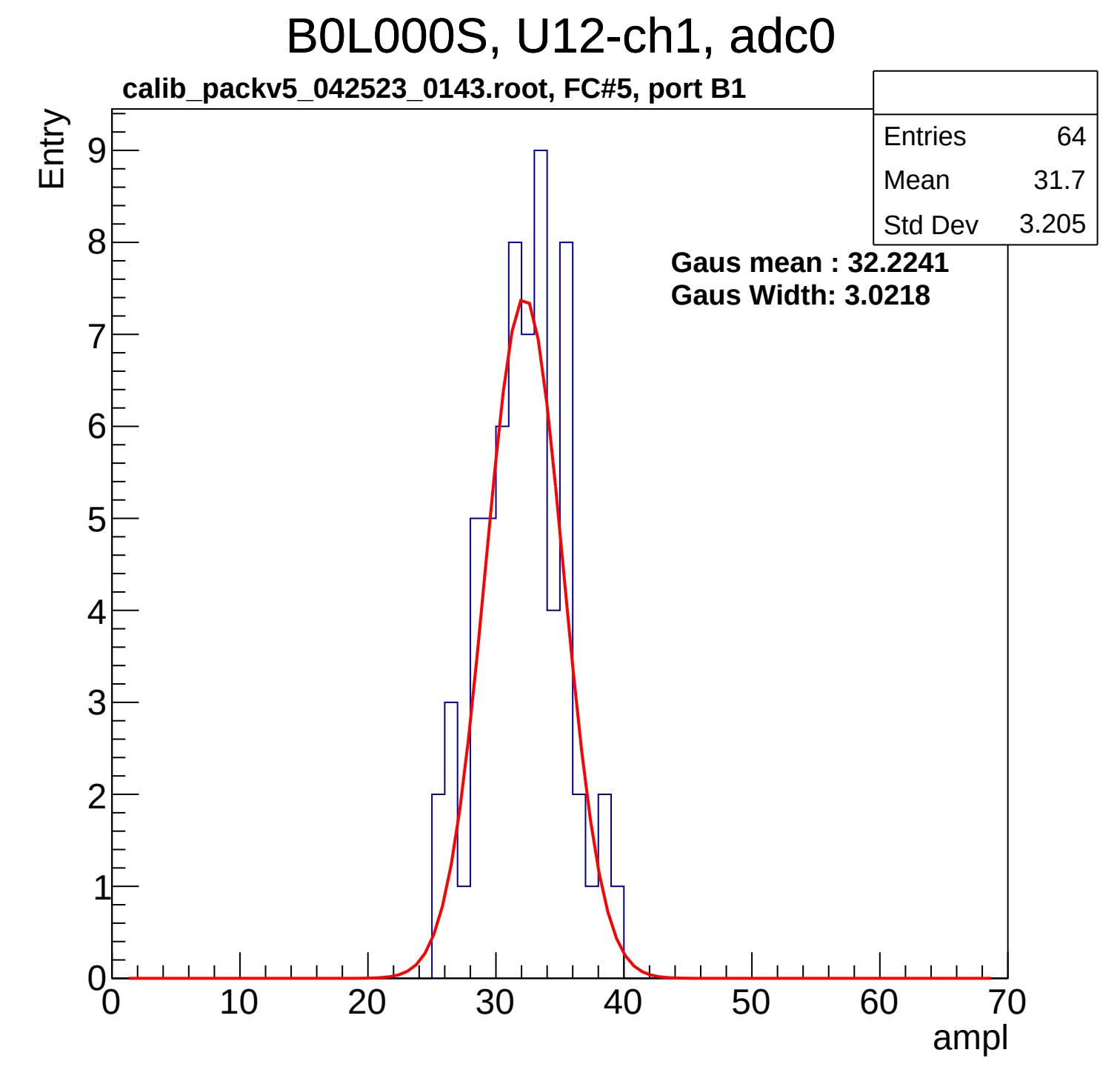
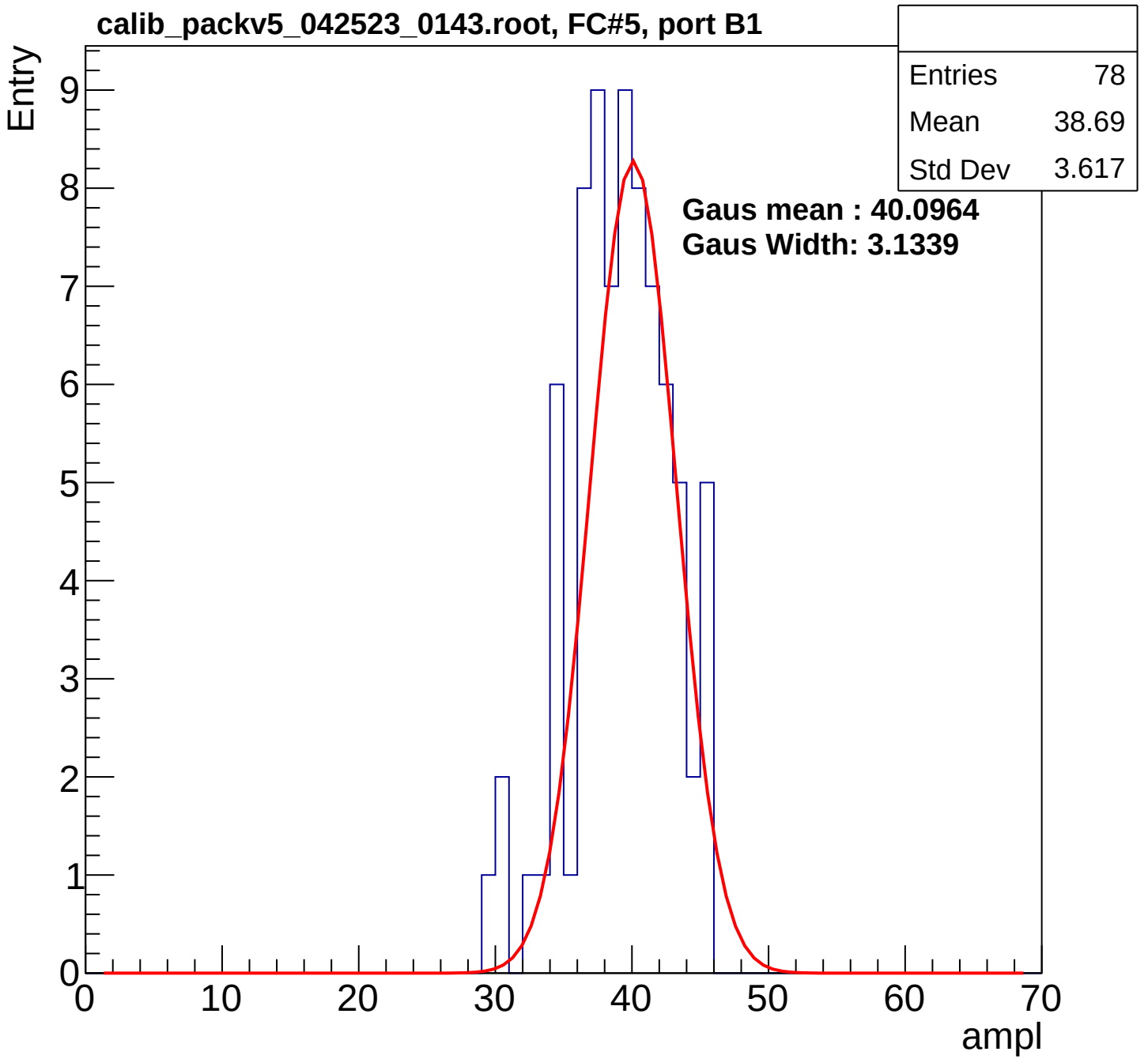

B0L000S, U12-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 31.7
Std Dev 3.205
Gaus mean : 32.2241
Gaus Width: 3.0218
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 38.69
Std Dev 3.617
Gaus mean : 40.0964
Gaus Width: 3.1339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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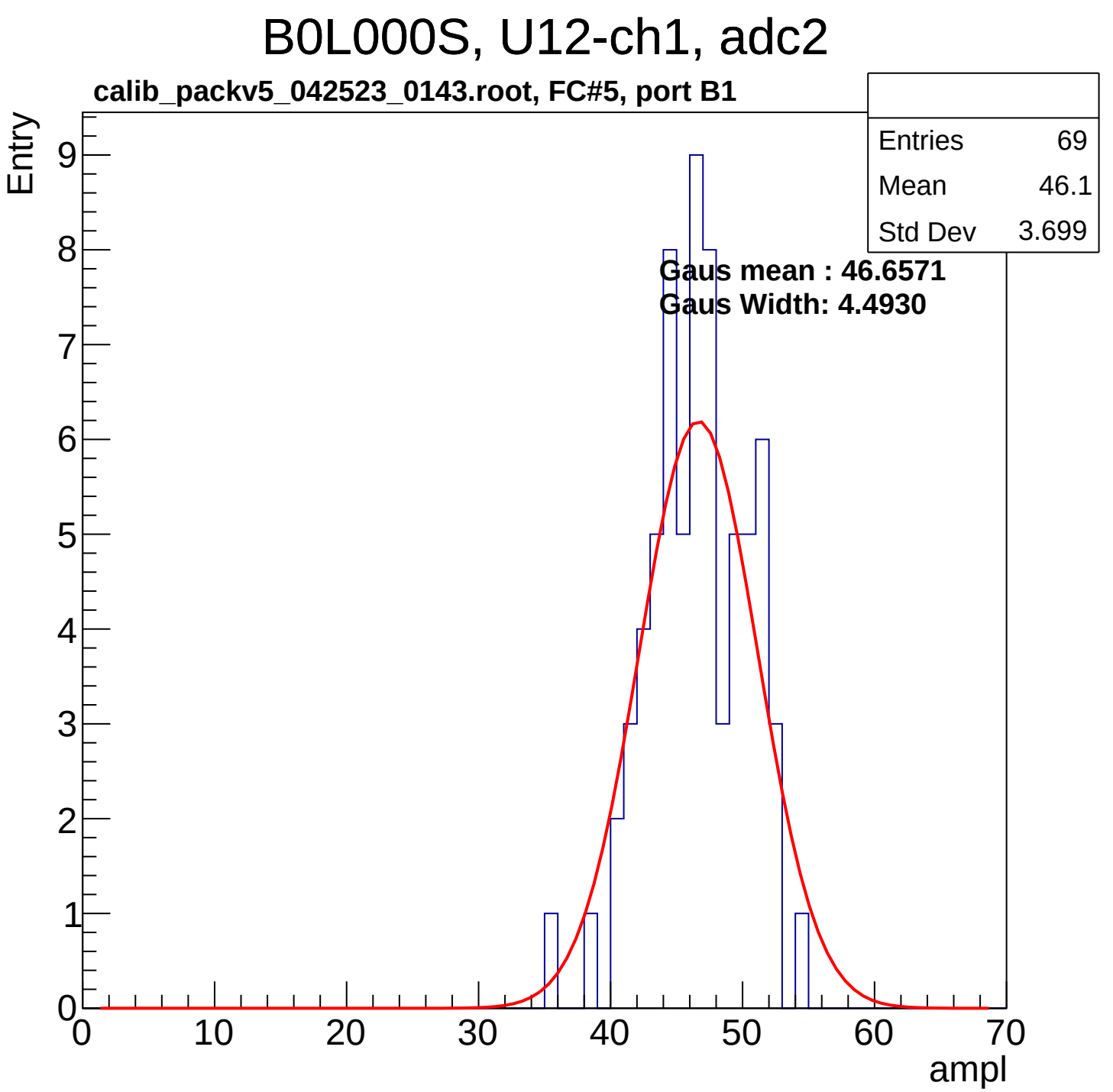
B0L000S, U12-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 46.1
Std Dev 3.699
Gaus mean : 46.6571
Gaus Width: 4.4930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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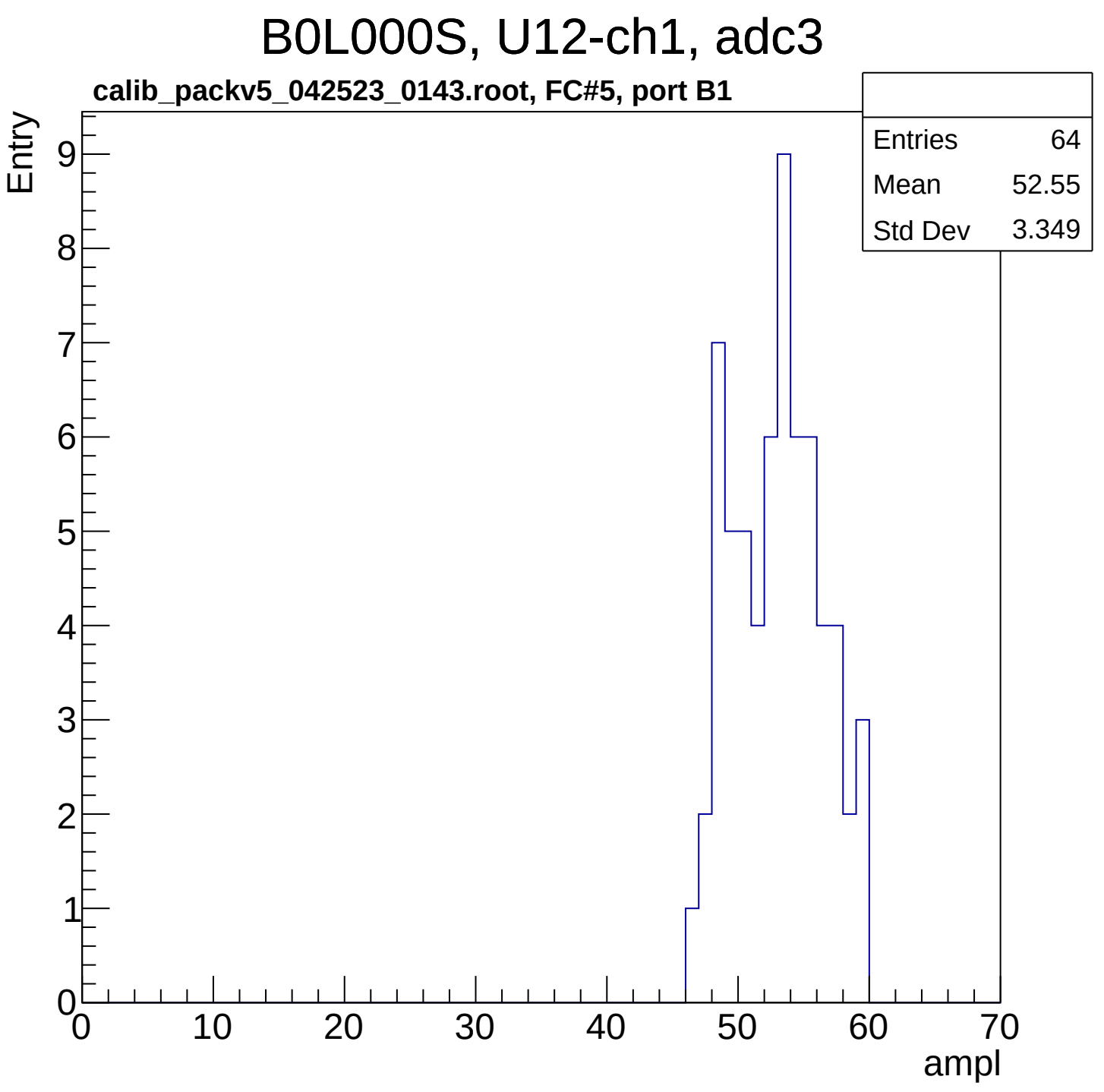
B0L000S, U12-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 52.55
Std Dev 3.349
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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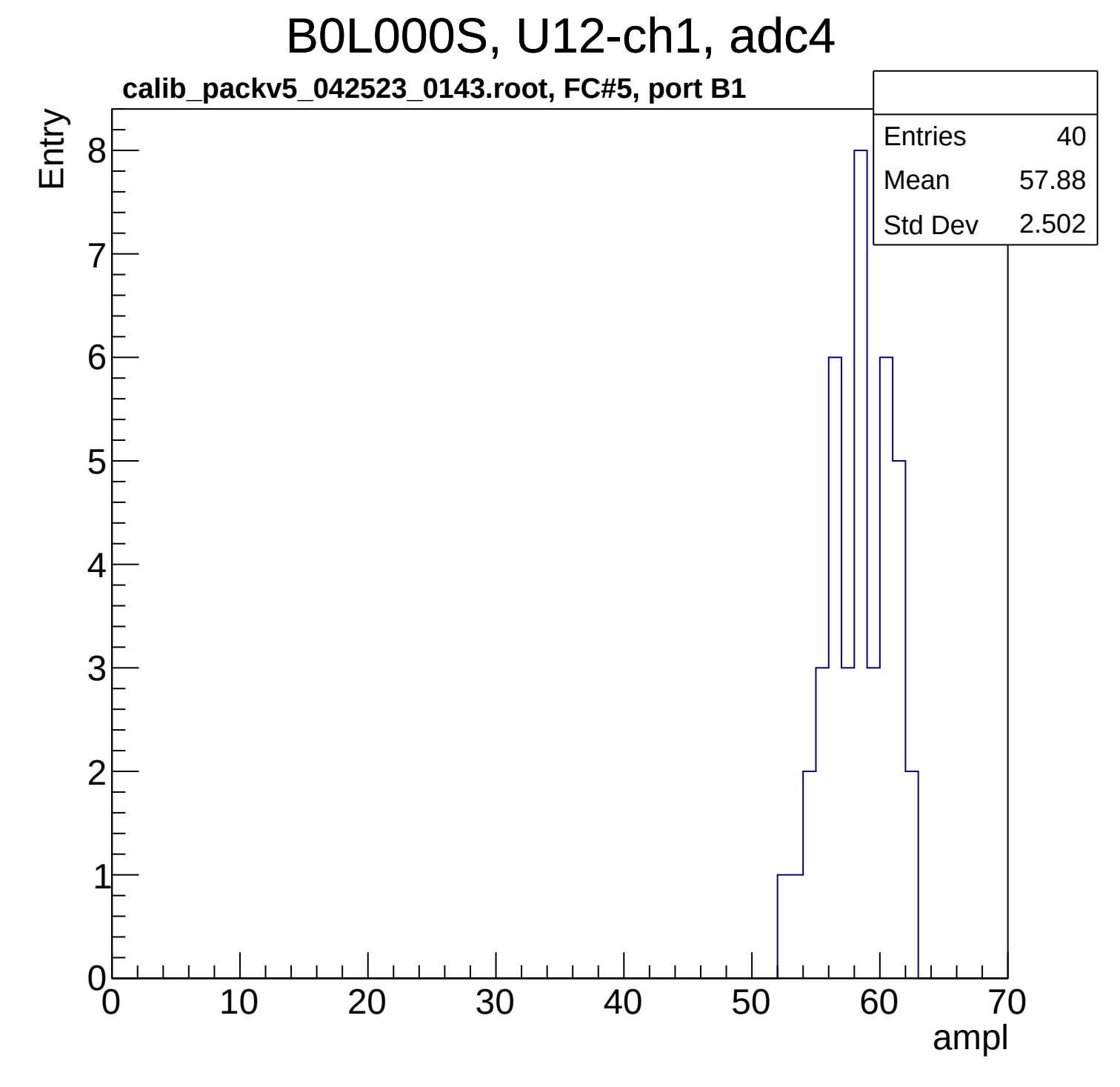
B0L000S, U12-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 57.88
Std Dev 2.502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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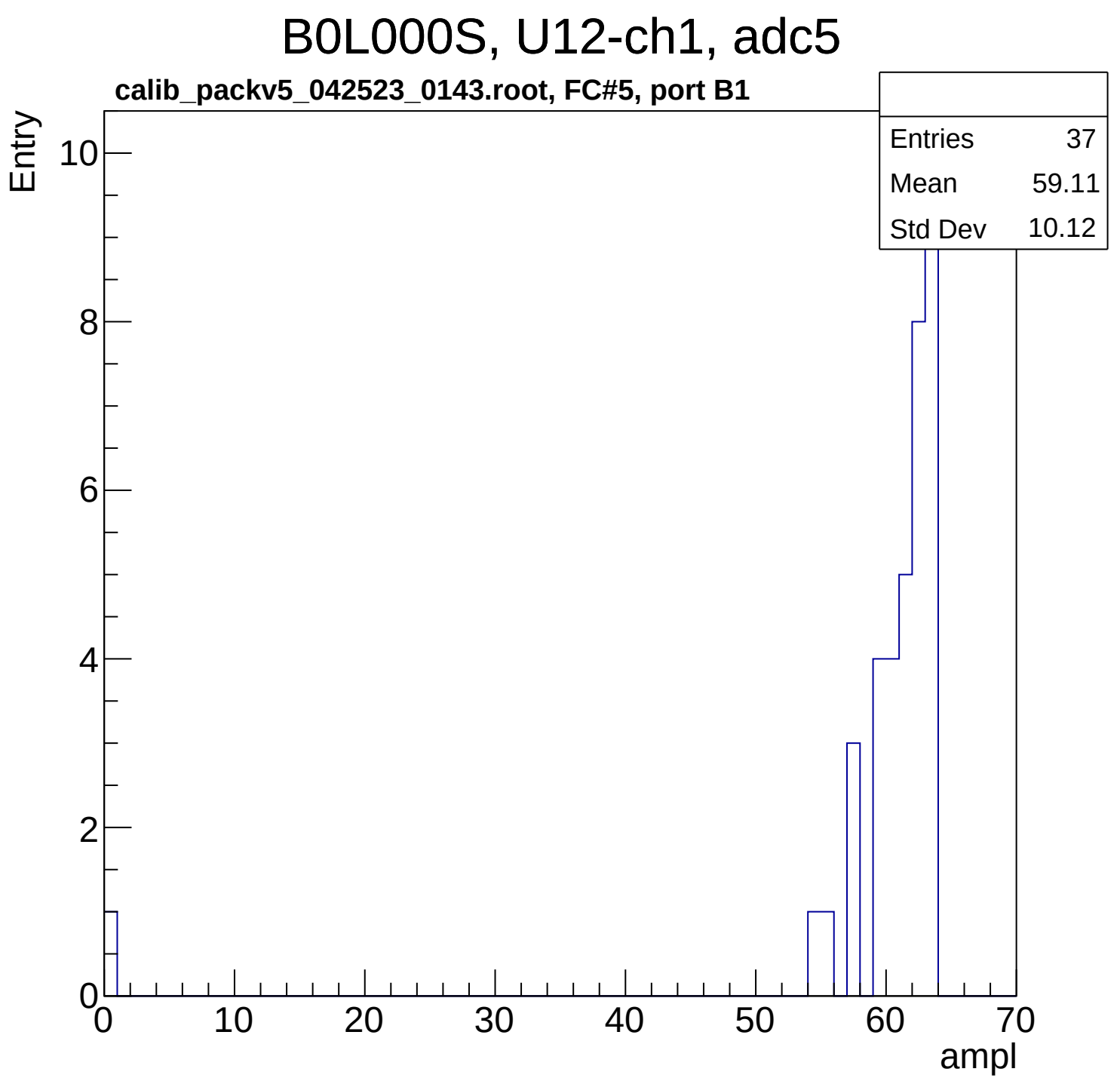
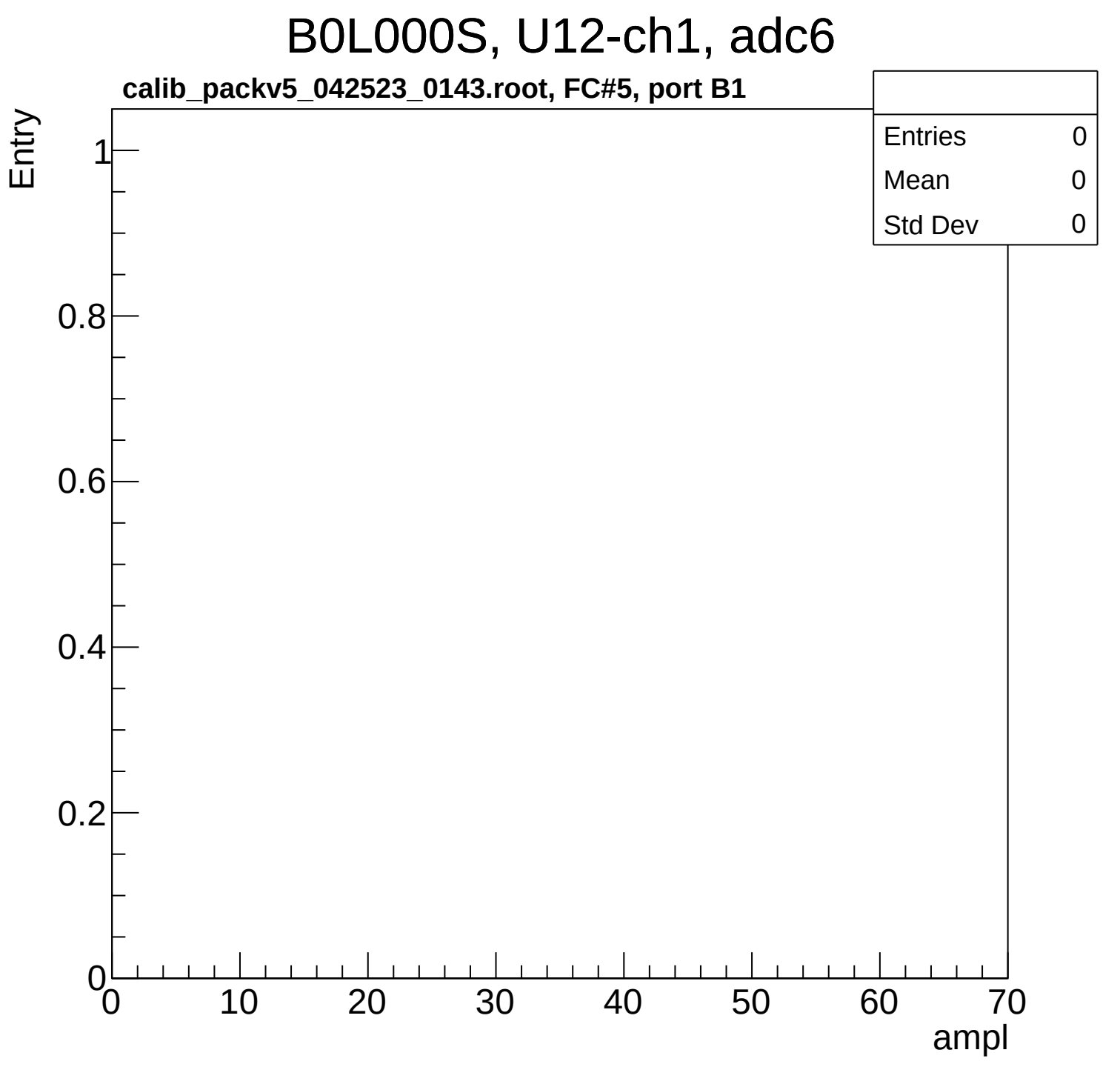
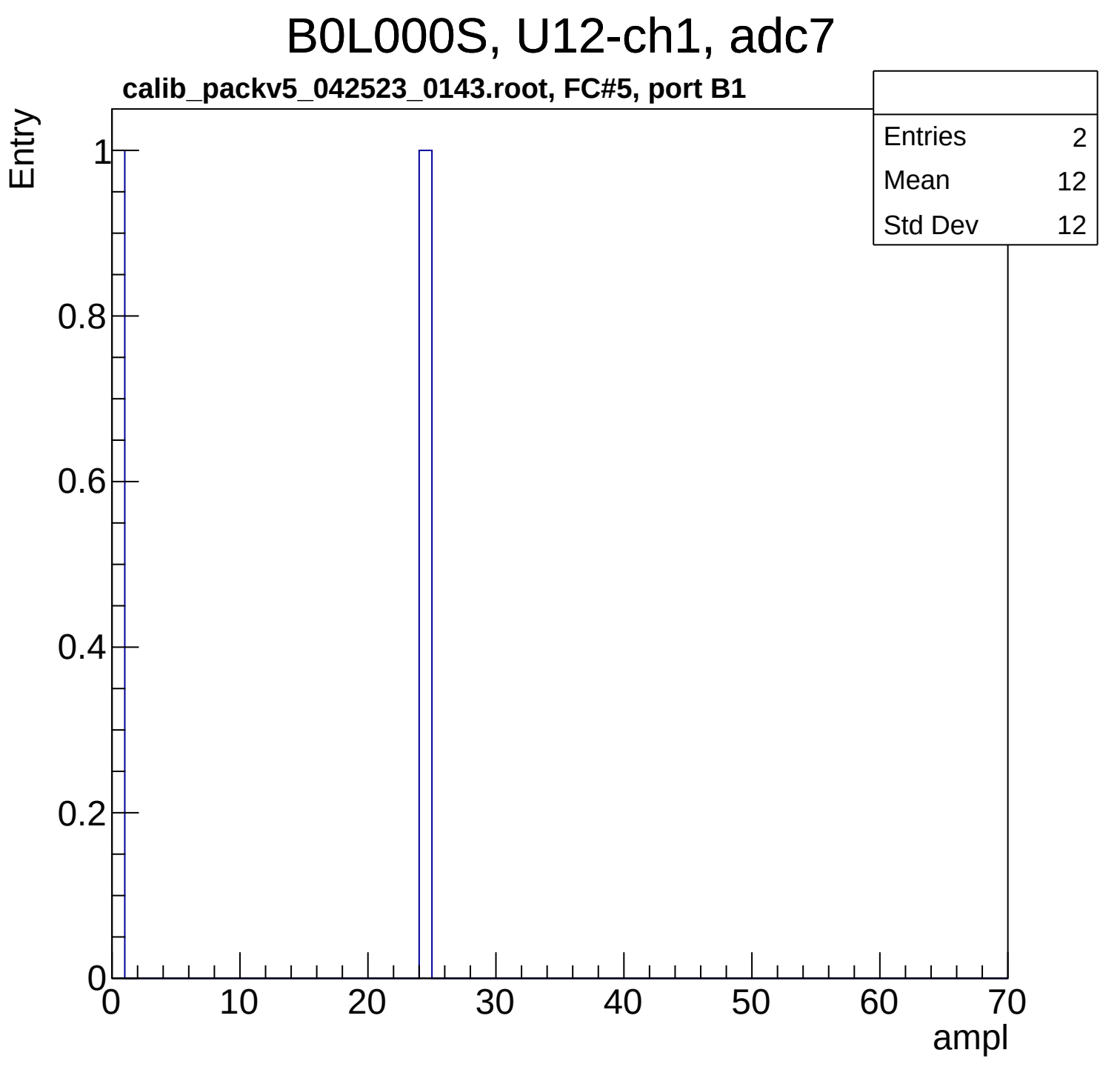

B0L000S, U12-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.11
Std Dev 10.12
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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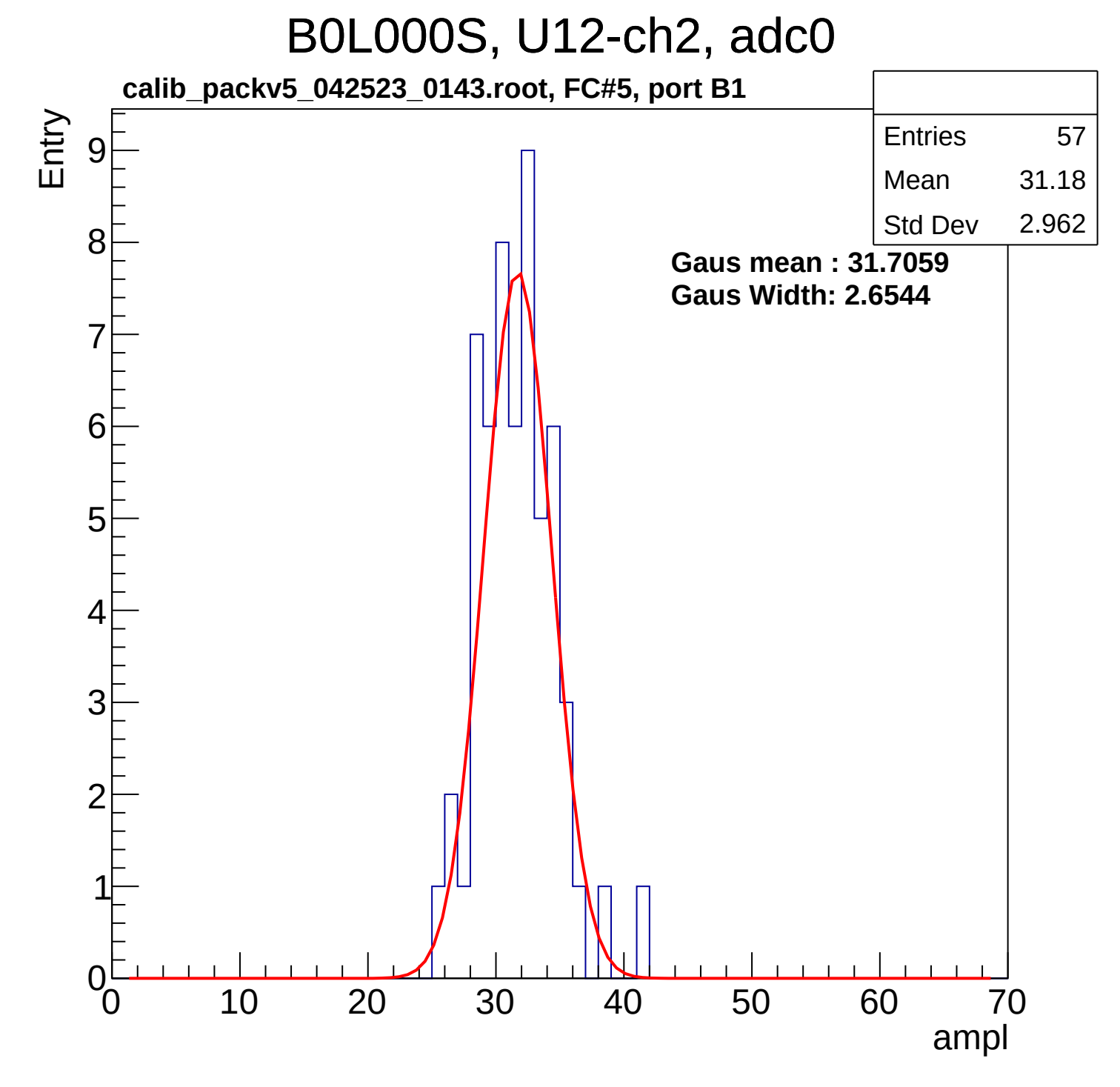
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 31.18
Std Dev 2.962
Gaus mean : 31.7059
Gaus Width: 2.6544
Entry
ampl
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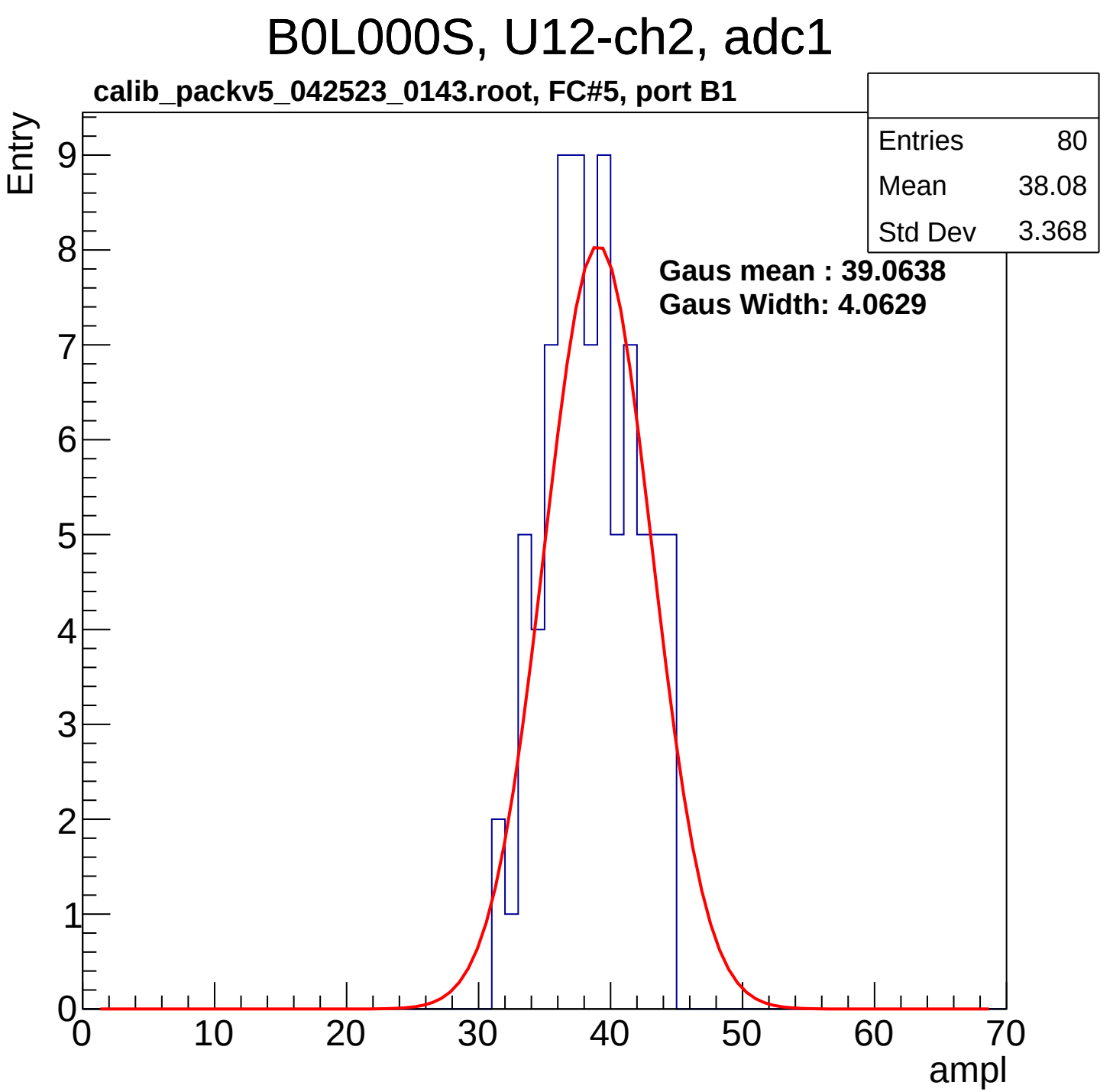
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U12-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 38.08
Std Dev 3.368
Gaus mean : 39.0638
Gaus Width: 4.0629
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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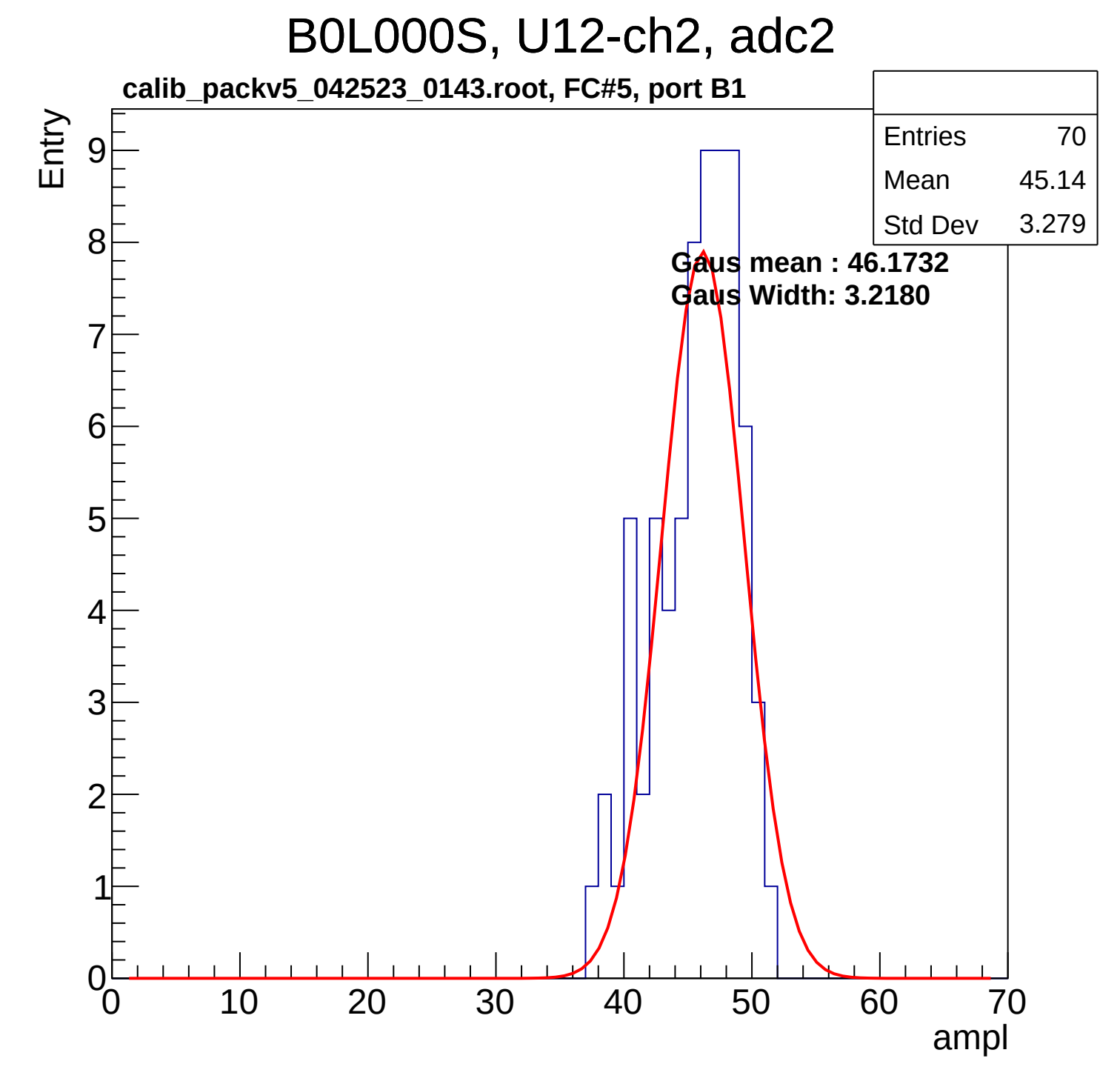
B0L000S, U12-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.14
Std Dev 3.279
Gaus mean : 46.1732
Gaus Width: 3.2180
Entry
ampl
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2
3
4
5
6
7
8
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0
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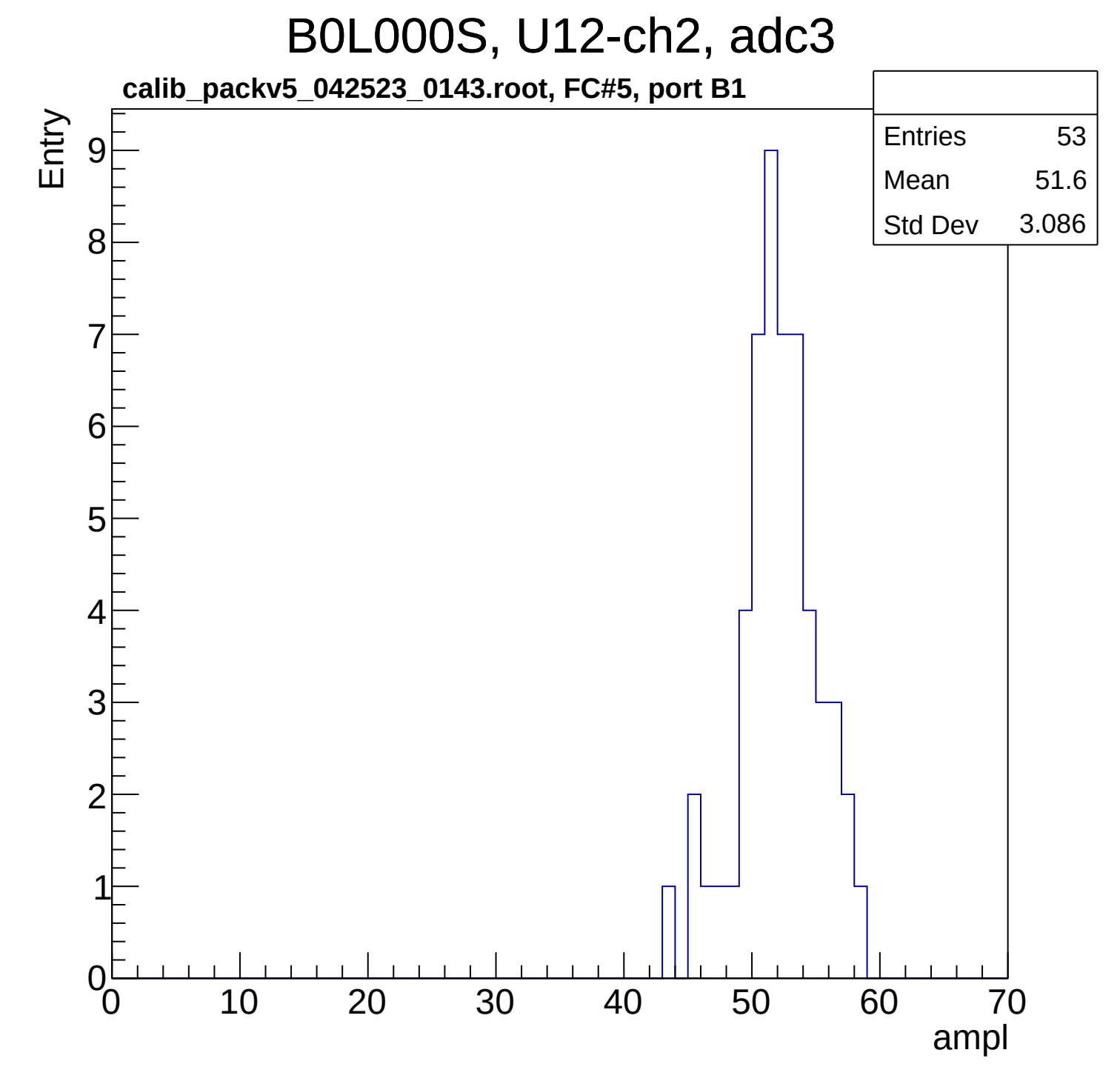
B0L000S, U12-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 51.6
Std Dev 3.086
Entry
ampl
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4
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6
7
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0
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60
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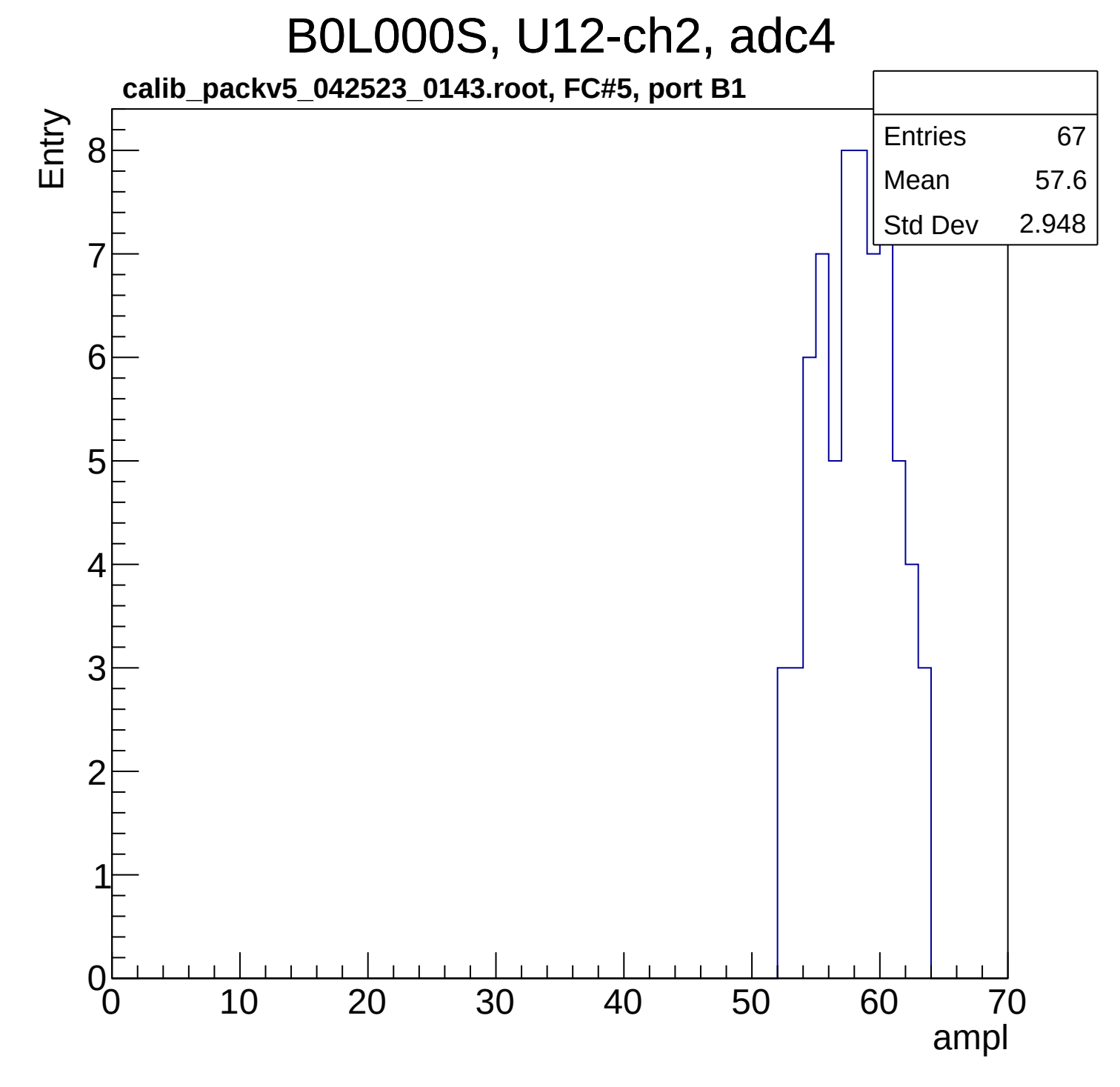
B0L000S, U12-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 57.6
Std Dev 2.948
Entry
ampl
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2
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4
5
6
7
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0
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40
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60
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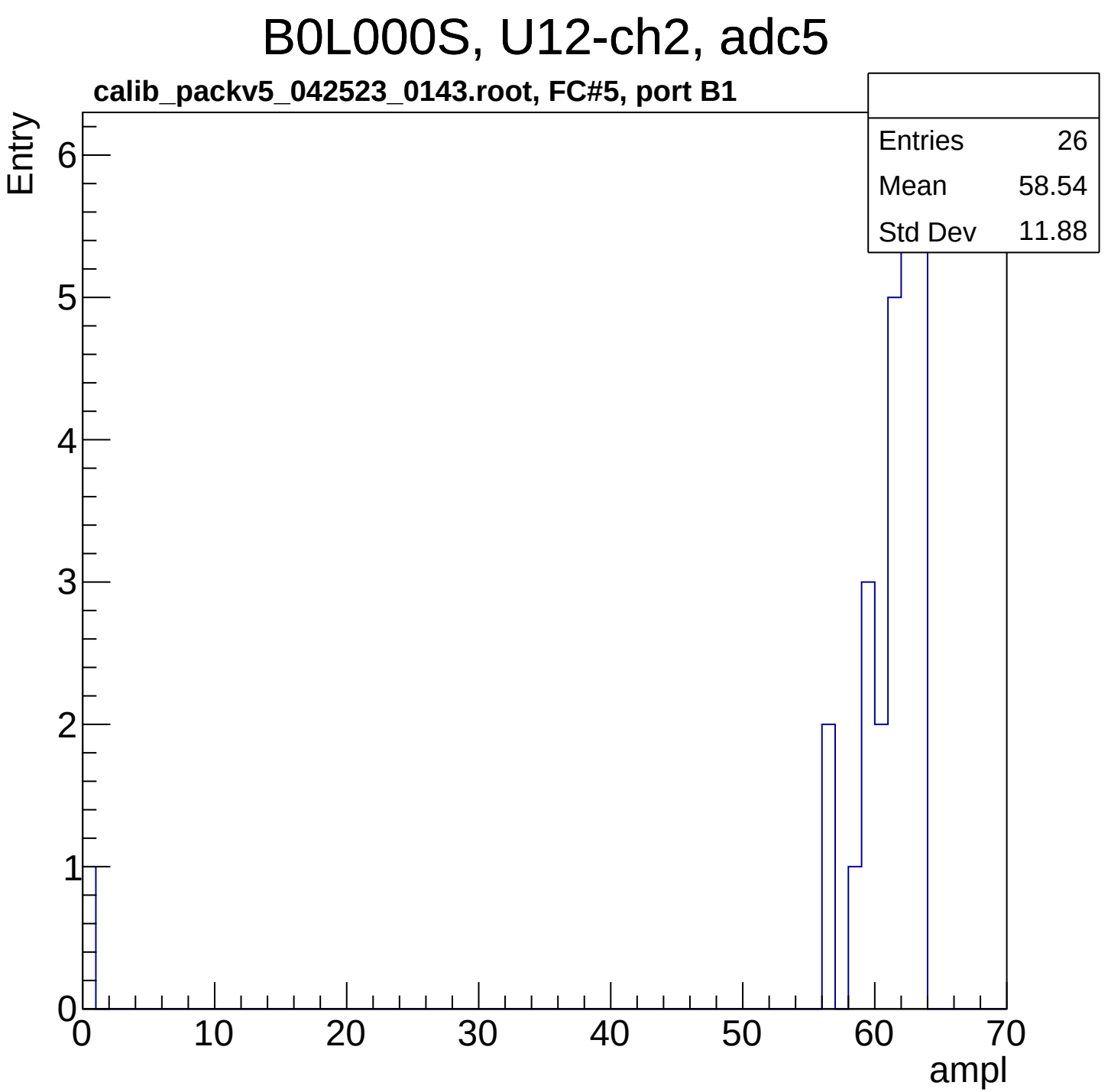
B0L000S, U12-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 58.54
Std Dev 11.88
Entry
ampl
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3
4
5
6
0
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60
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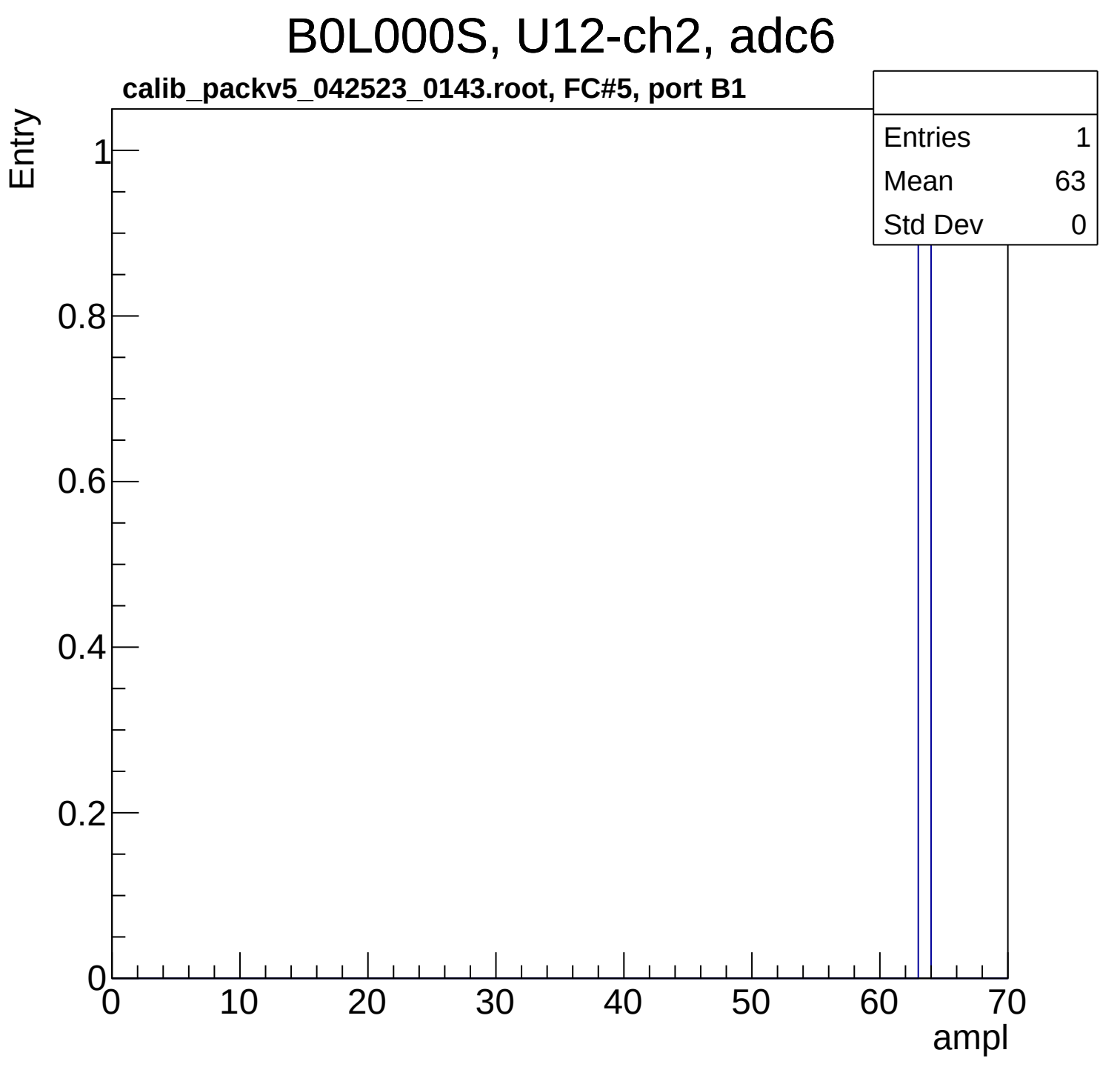
B0L000S, U12-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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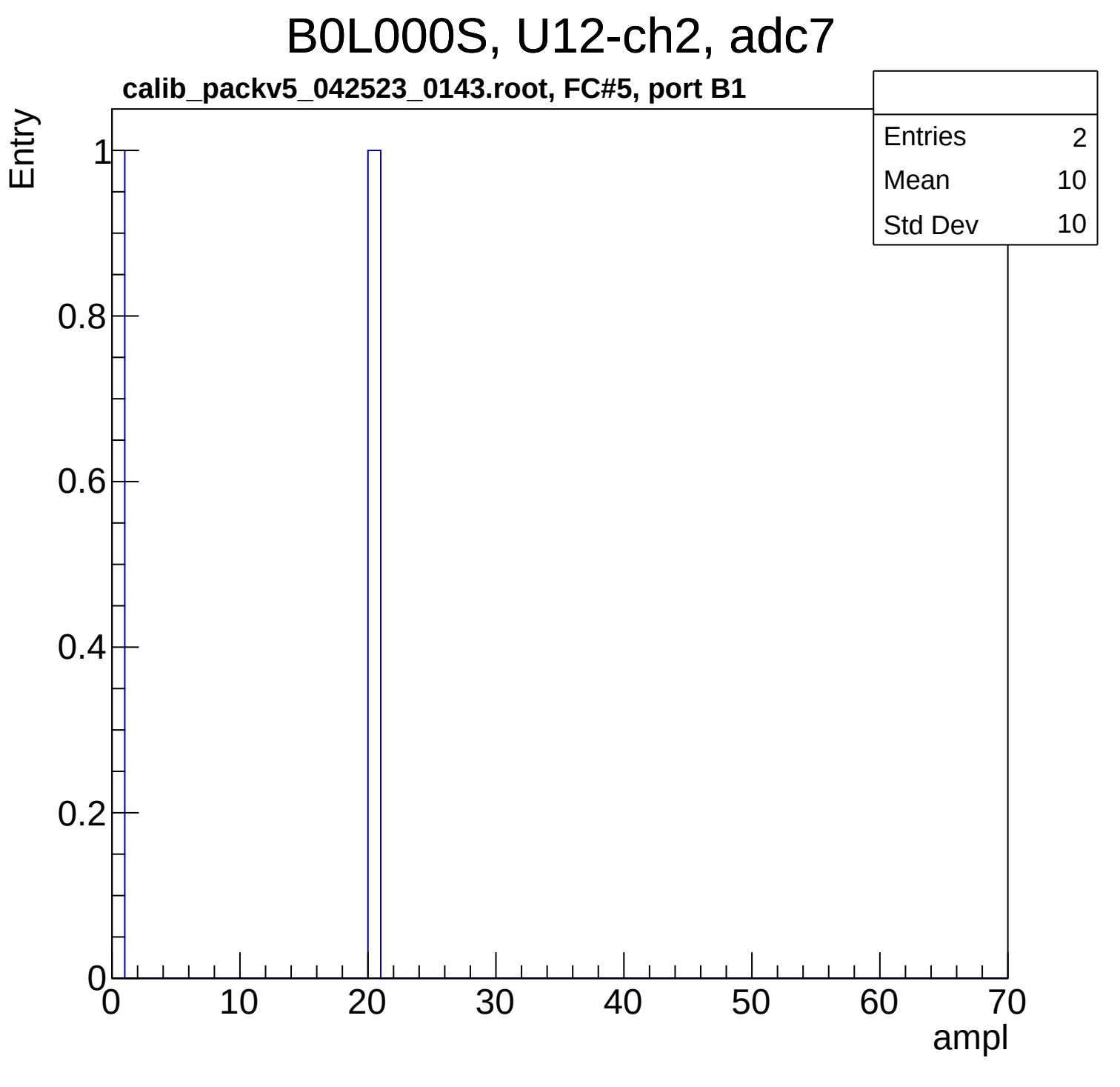
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
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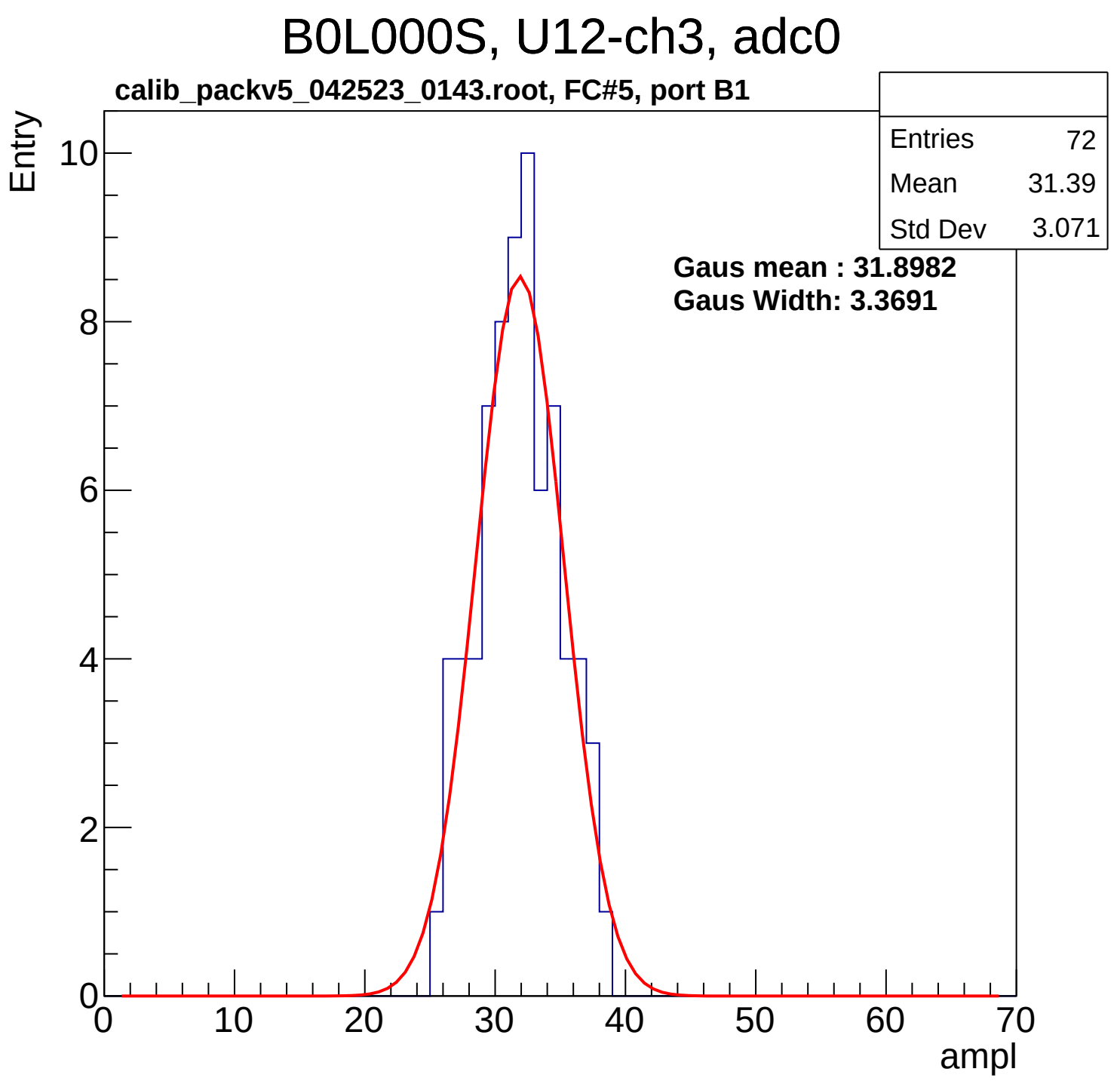
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 31.39
Std Dev 3.071
Gaus mean : 31.8982
Gaus Width: 3.3691
Entry
ampl
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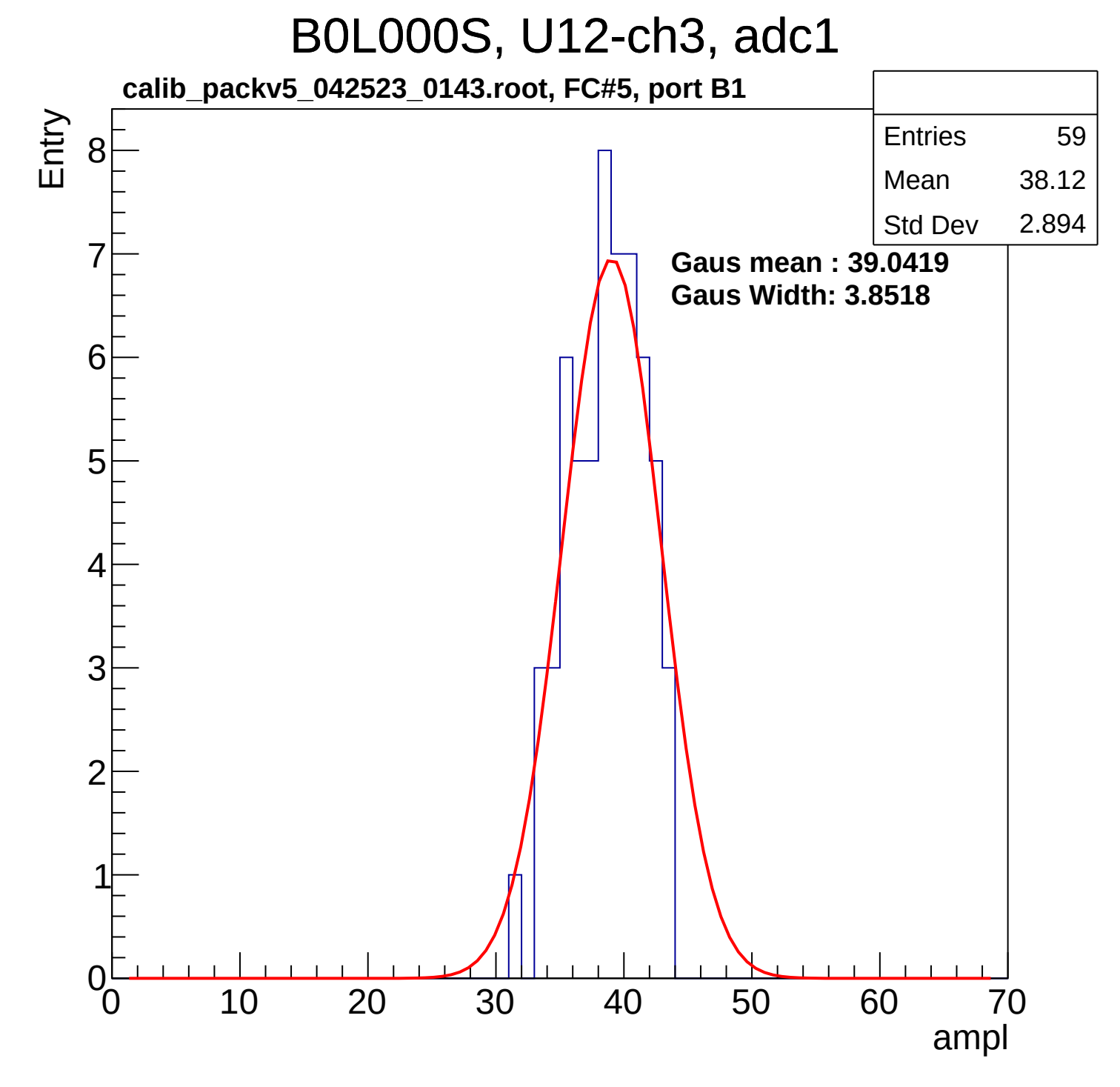
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 38.12
Std Dev 2.894
Gaus mean : 39.0419
Gaus Width: 3.8518
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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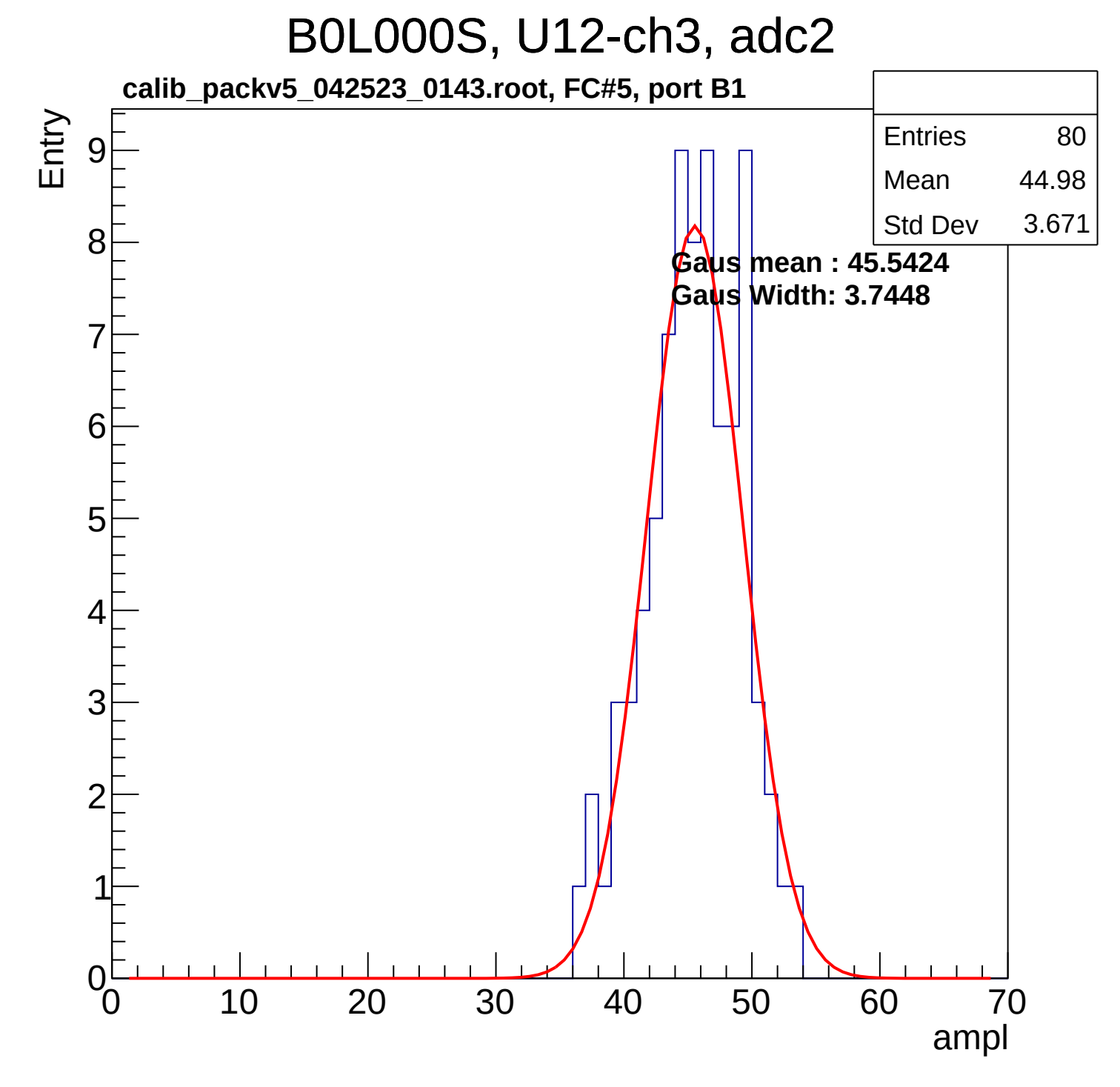
B0L000S, U12-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 44.98
Std Dev 3.671
Gaus mean : 45.5424
Gaus Width: 3.7448
Entry
ampl
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2
3
4
5
6
7
8
9
0
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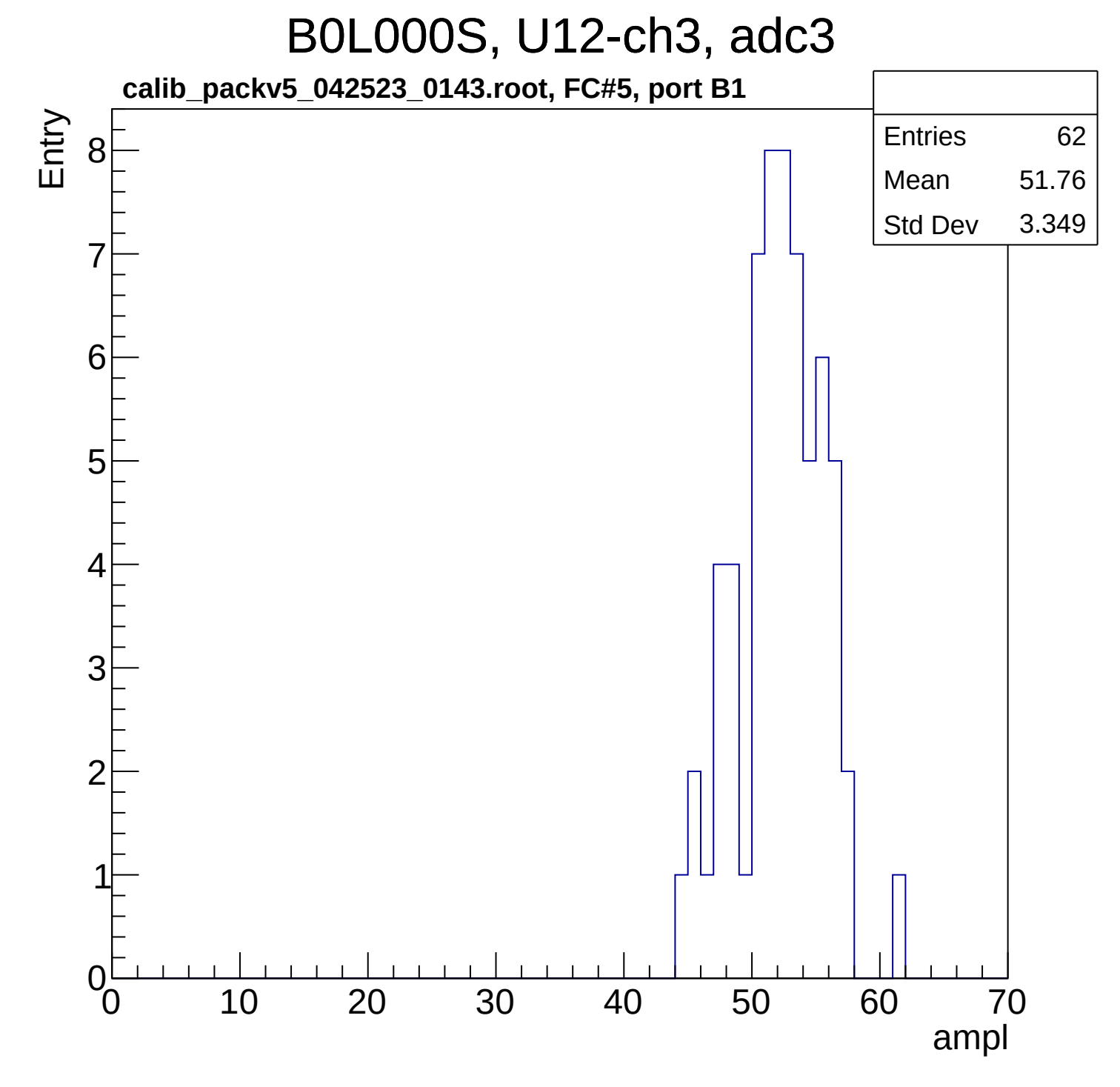
B0L000S, U12-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.76
Std Dev 3.349
Entry
ampl
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2
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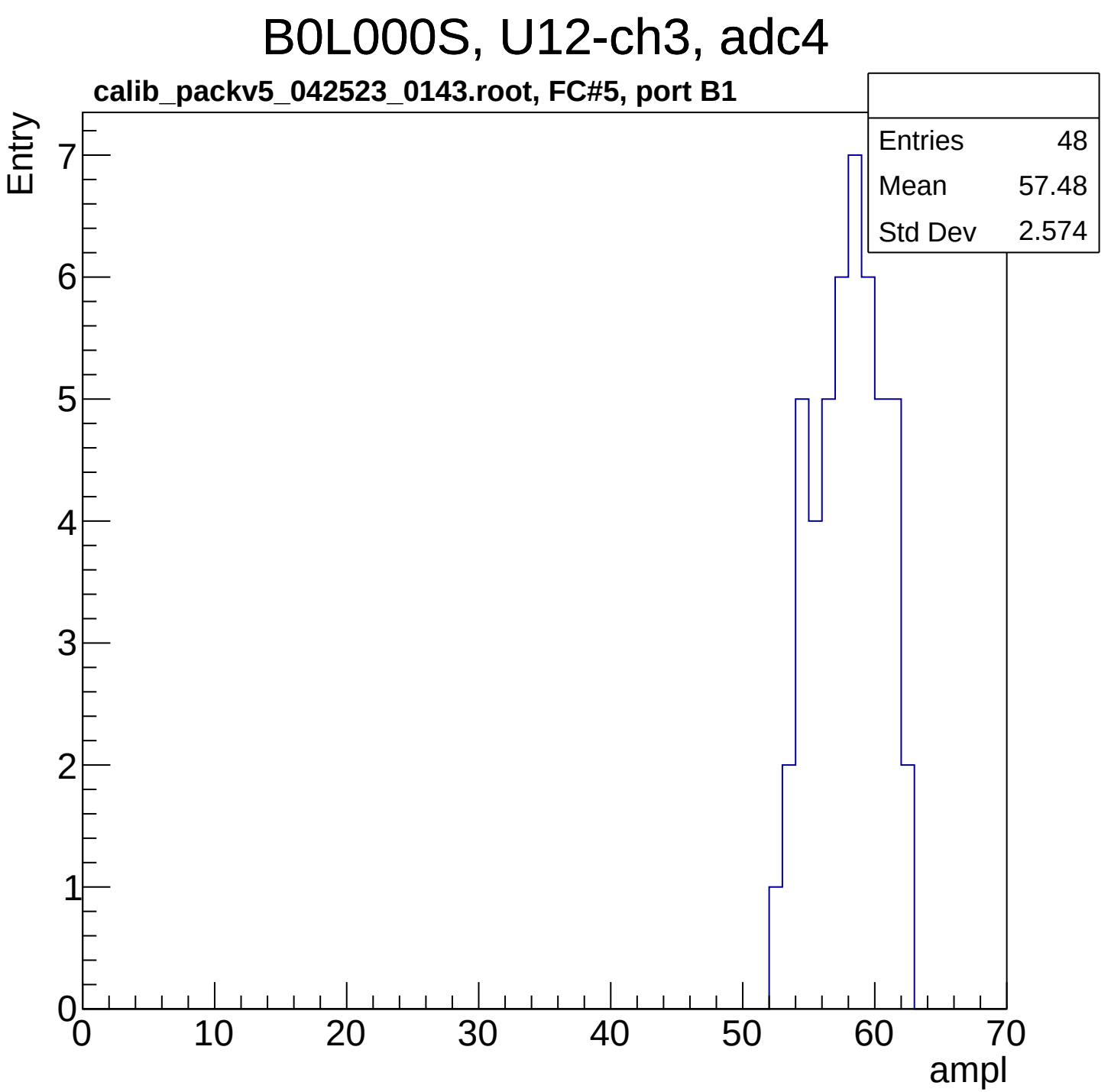
B0L000S, U12-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 57.48
Std Dev 2.574
Entry
ampl
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20
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60
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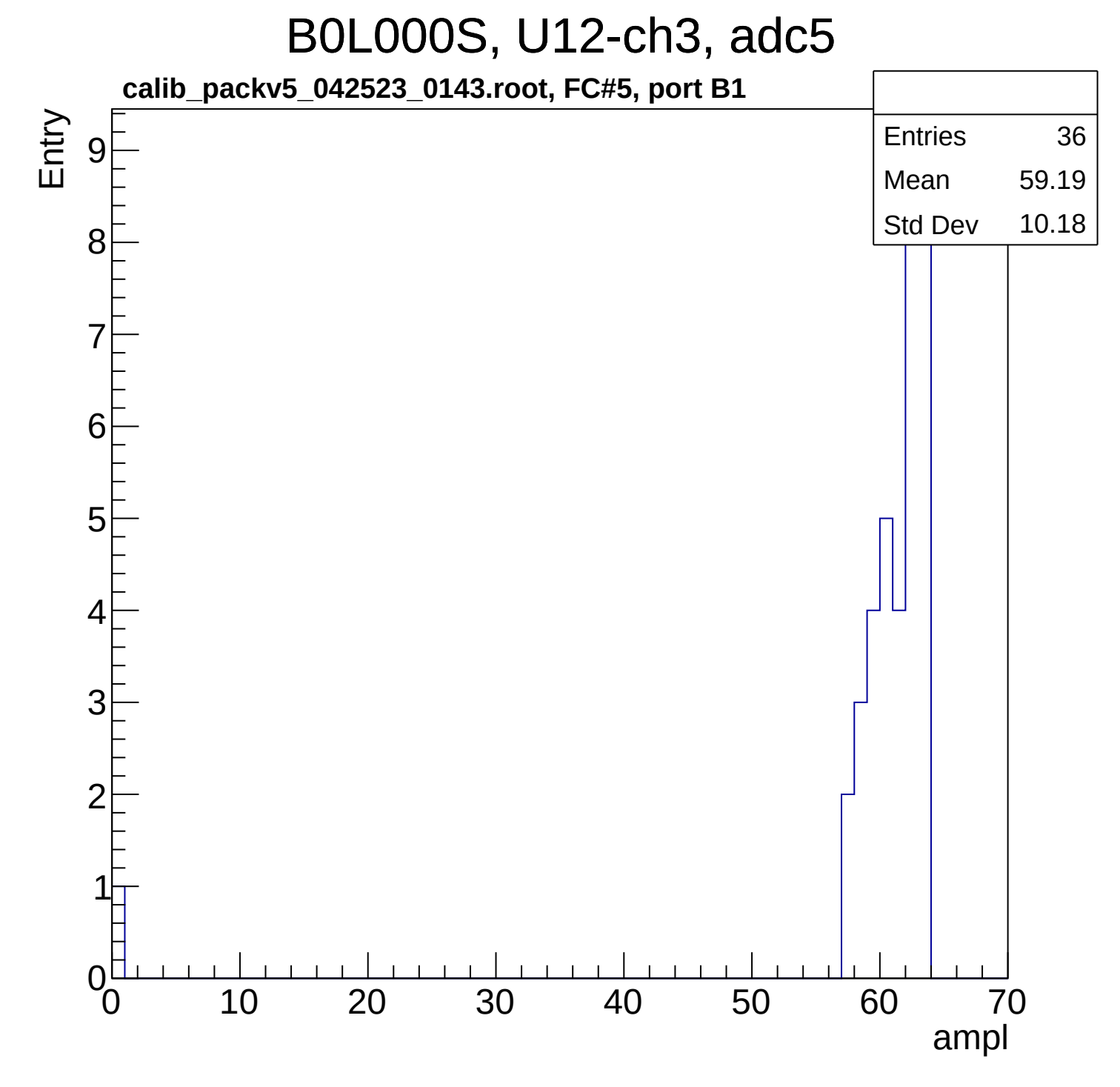
B0L000S, U12-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 59.19
Std Dev 10.18
Entry
ampl
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4
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6
7
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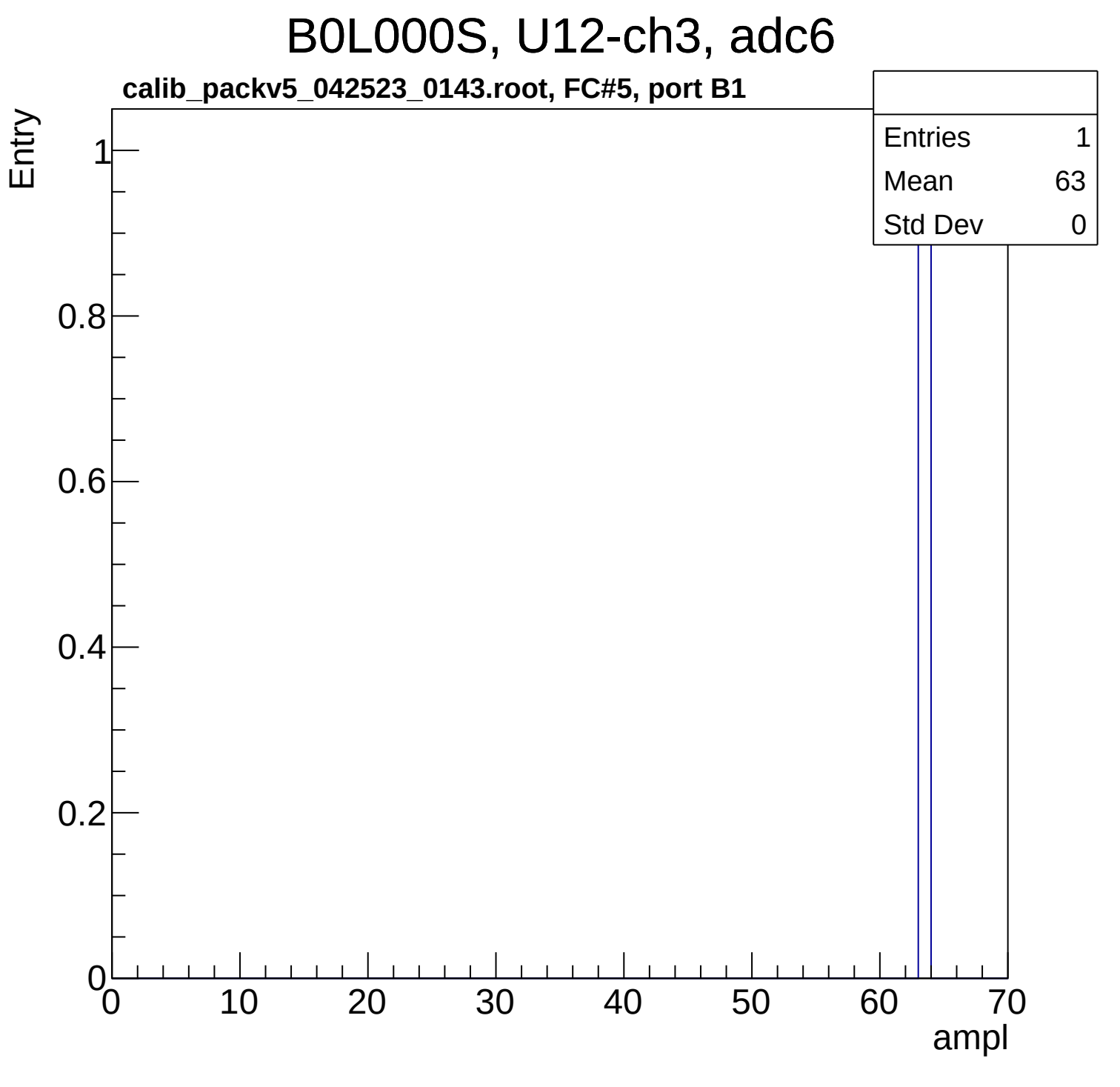
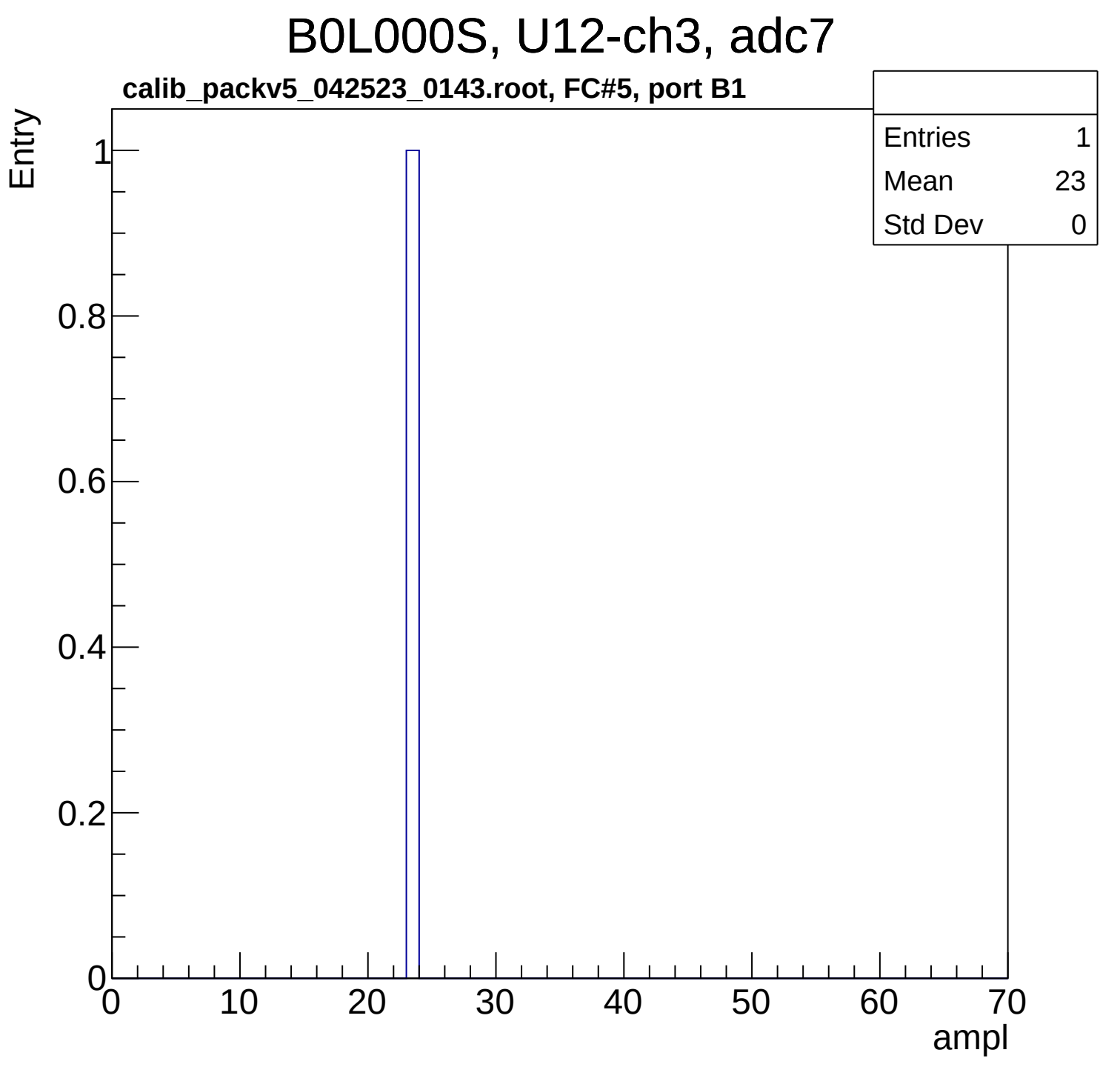

B0L000S, U12-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.8
0.6
0.4
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50
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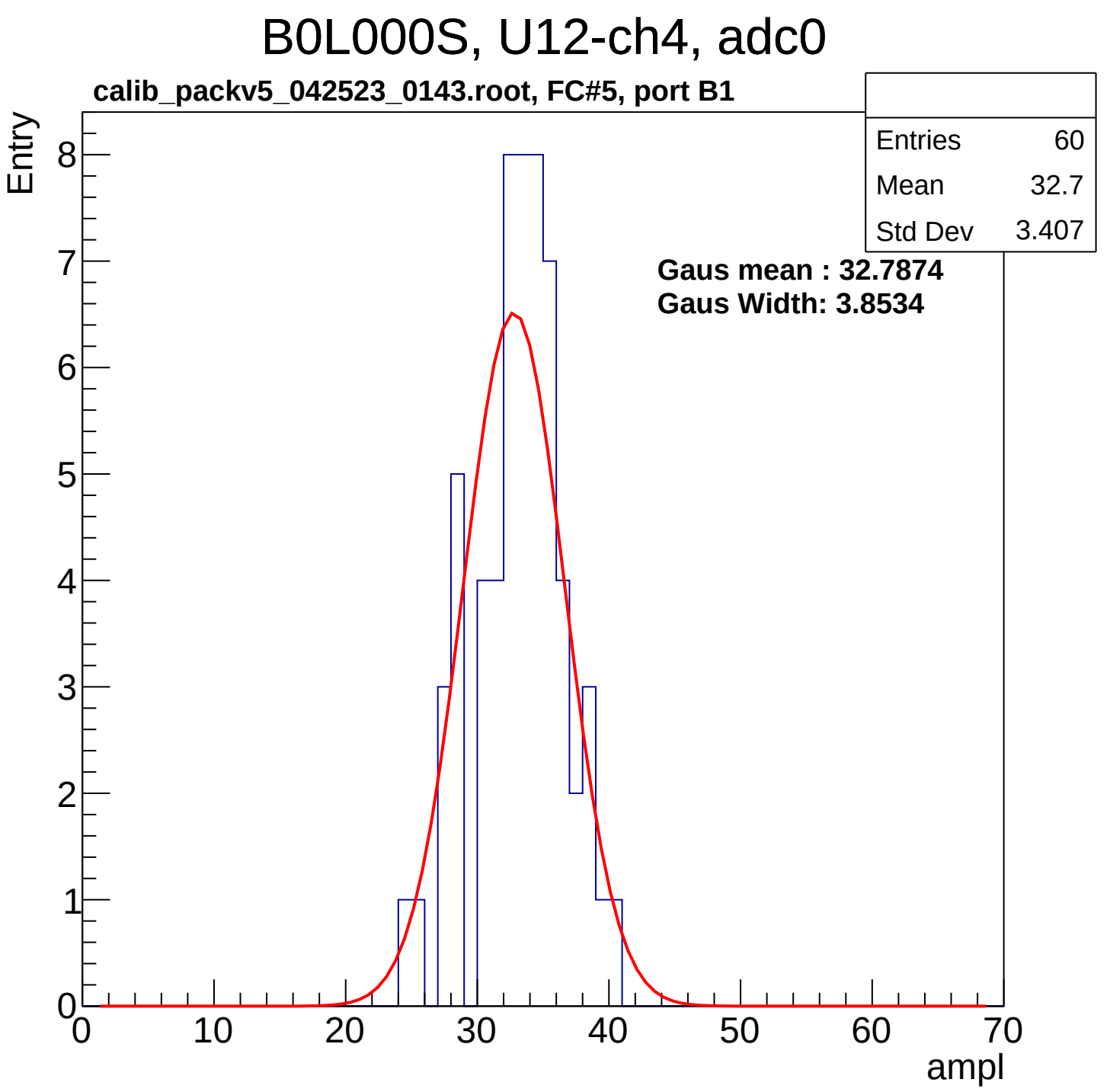
ampl


B0L000S, U12-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 32.7
Std Dev 3.407
Gaus mean : 32.7874
Gaus Width: 3.8534
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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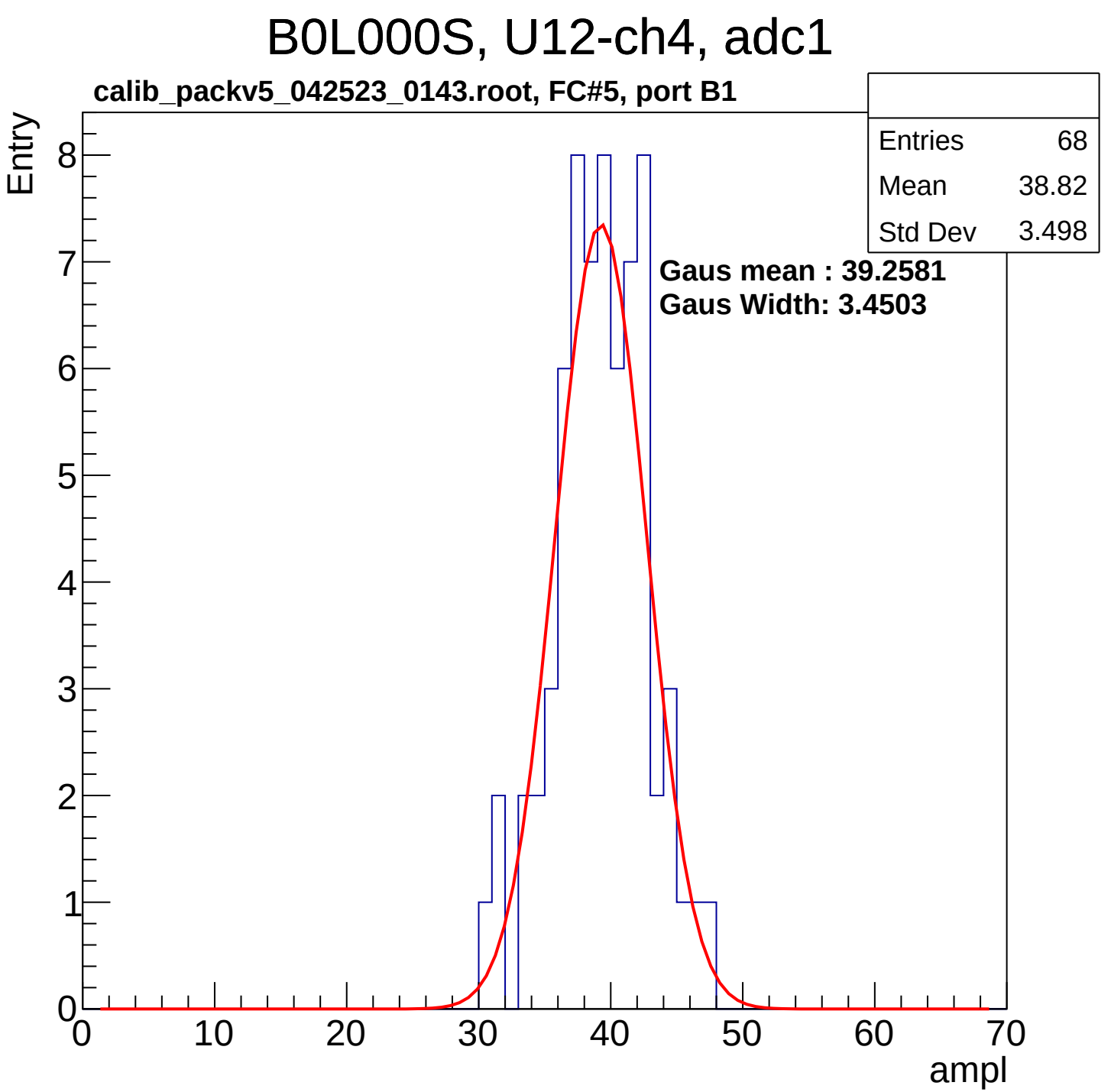
B0L000S, U12-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 38.82
Std Dev 3.498
Gaus mean : 39.2581
Gaus Width: 3.4503
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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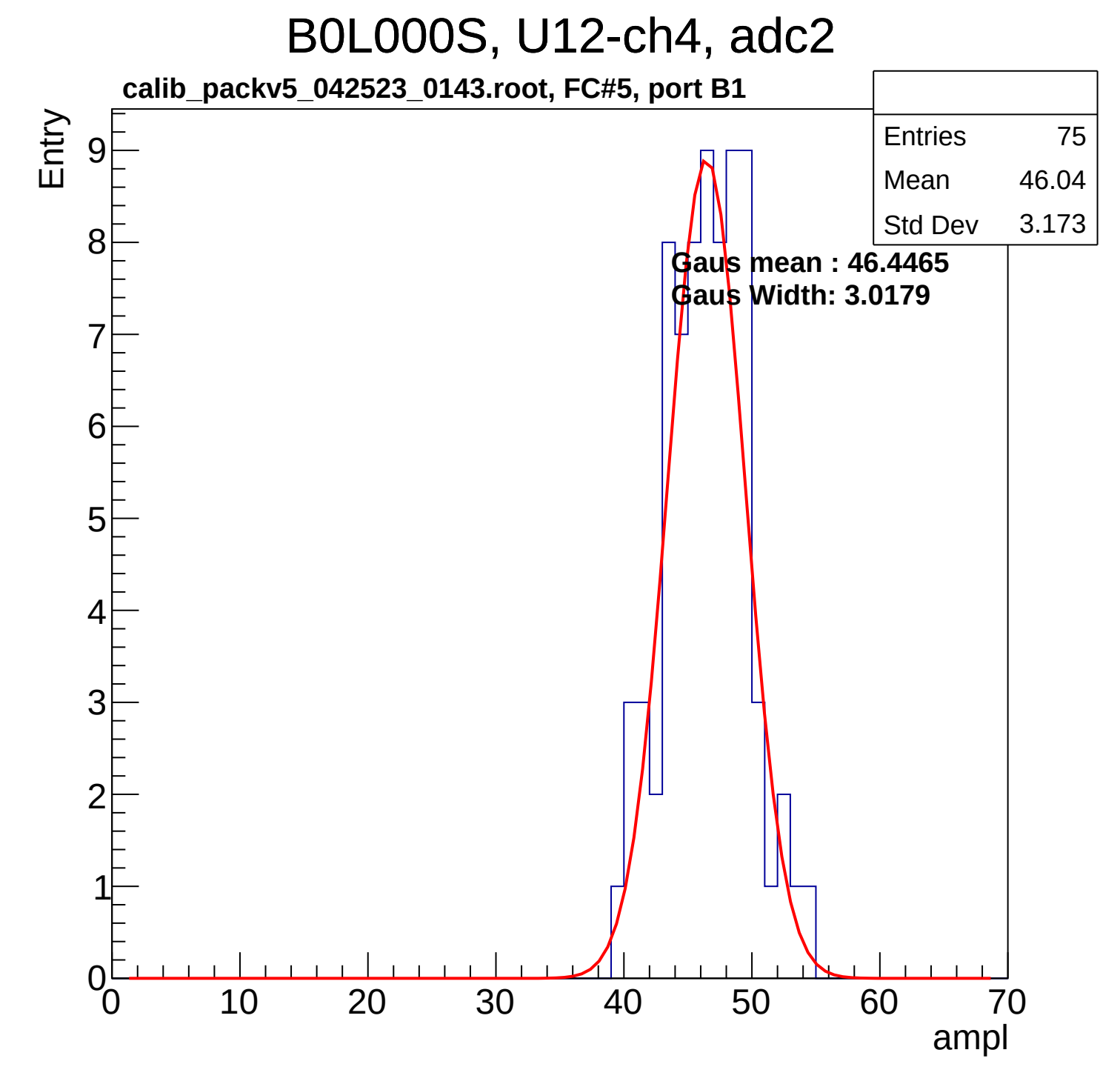
B0L000S, U12-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 46.04
Std Dev 3.173
Gaus mean : 46.4465
Gaus Width: 3.0179
Entry
ampl
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2
3
4
5
6
7
8
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0
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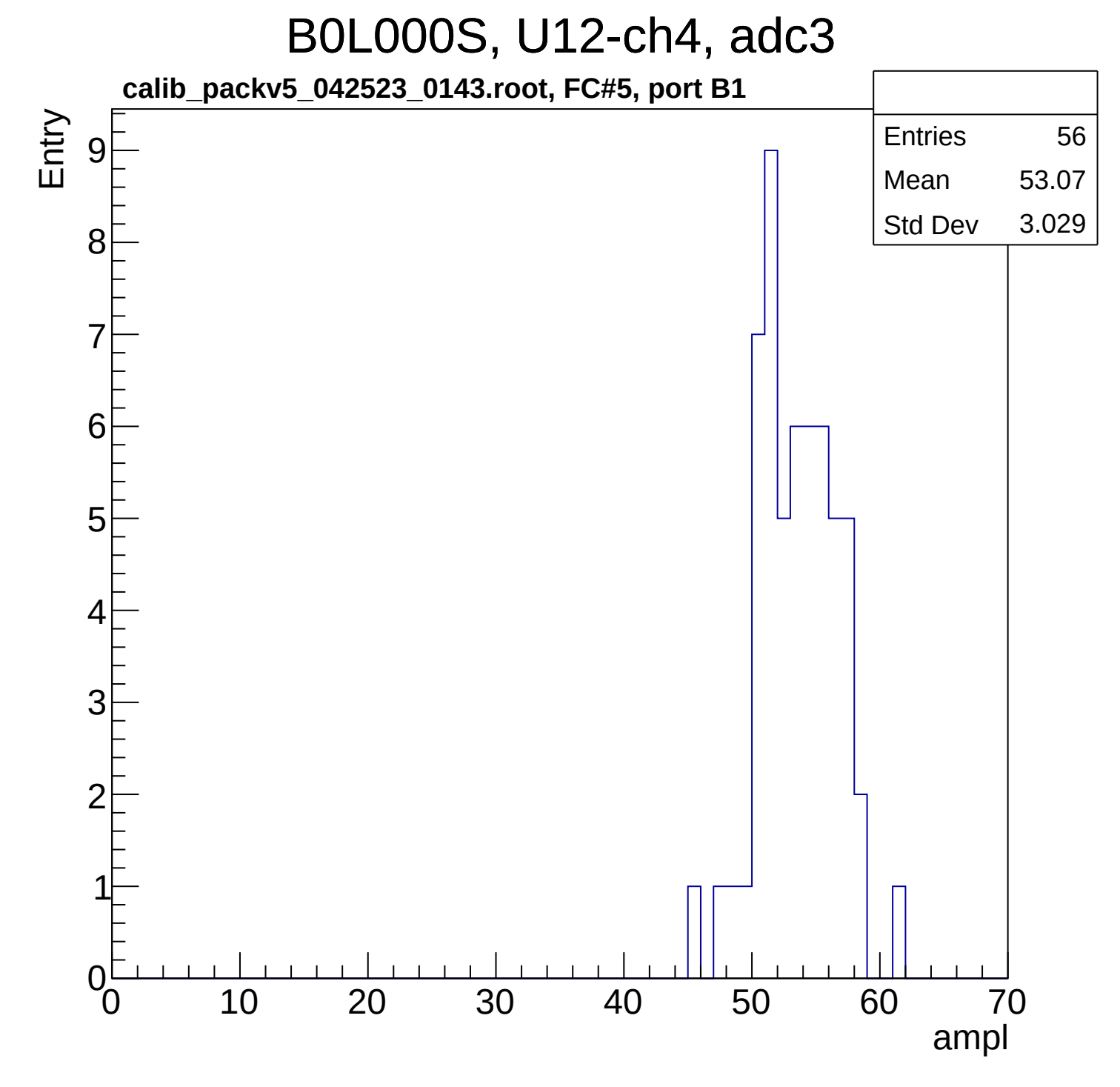
B0L000S, U12-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.07
Std Dev 3.029
Entry
ampl
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2
3
4
5
6
7
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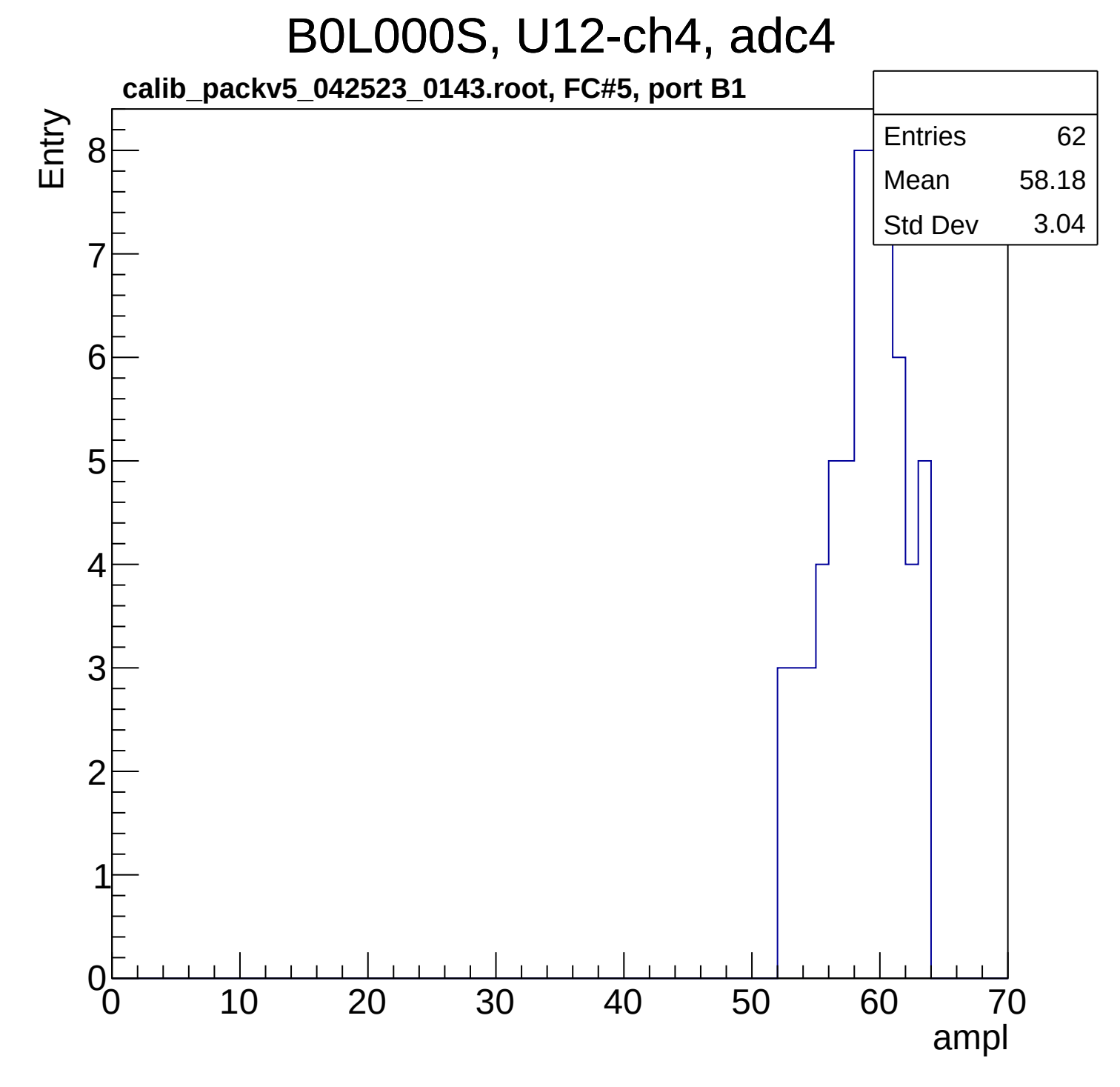
B0L000S, U12-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.18
Std Dev 3.04
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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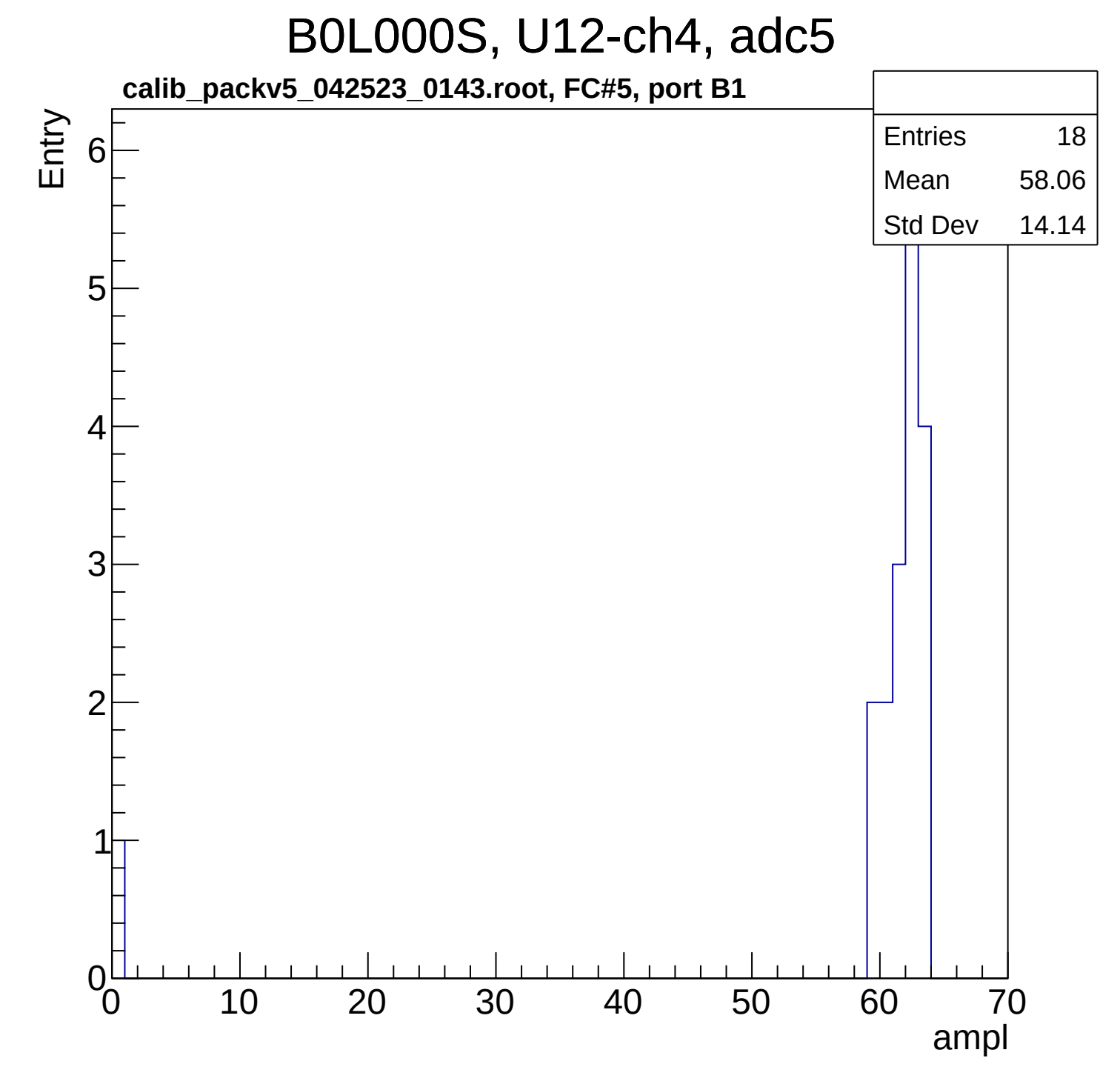
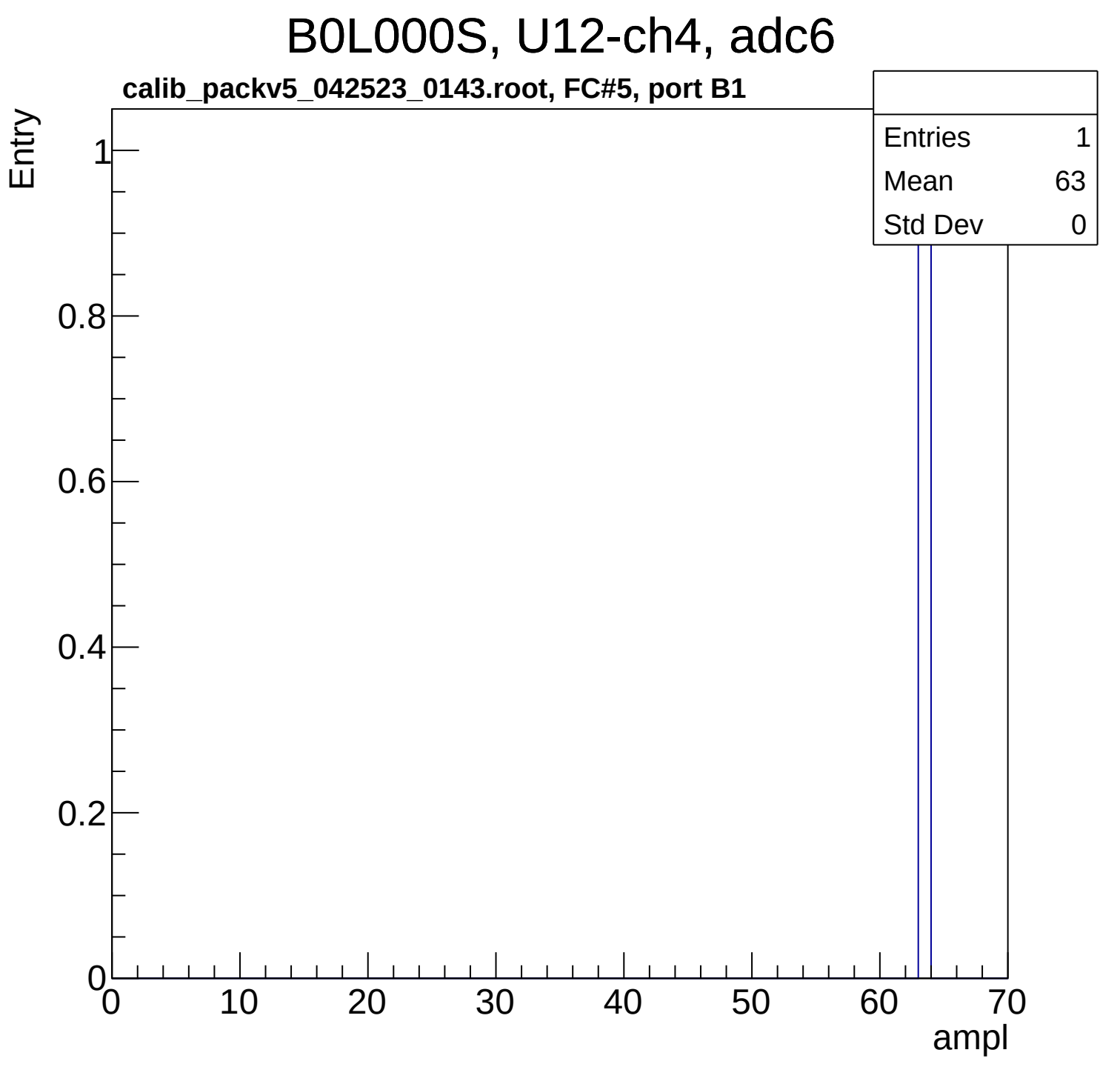

B0L000S, U12-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.06
Std Dev 14.14
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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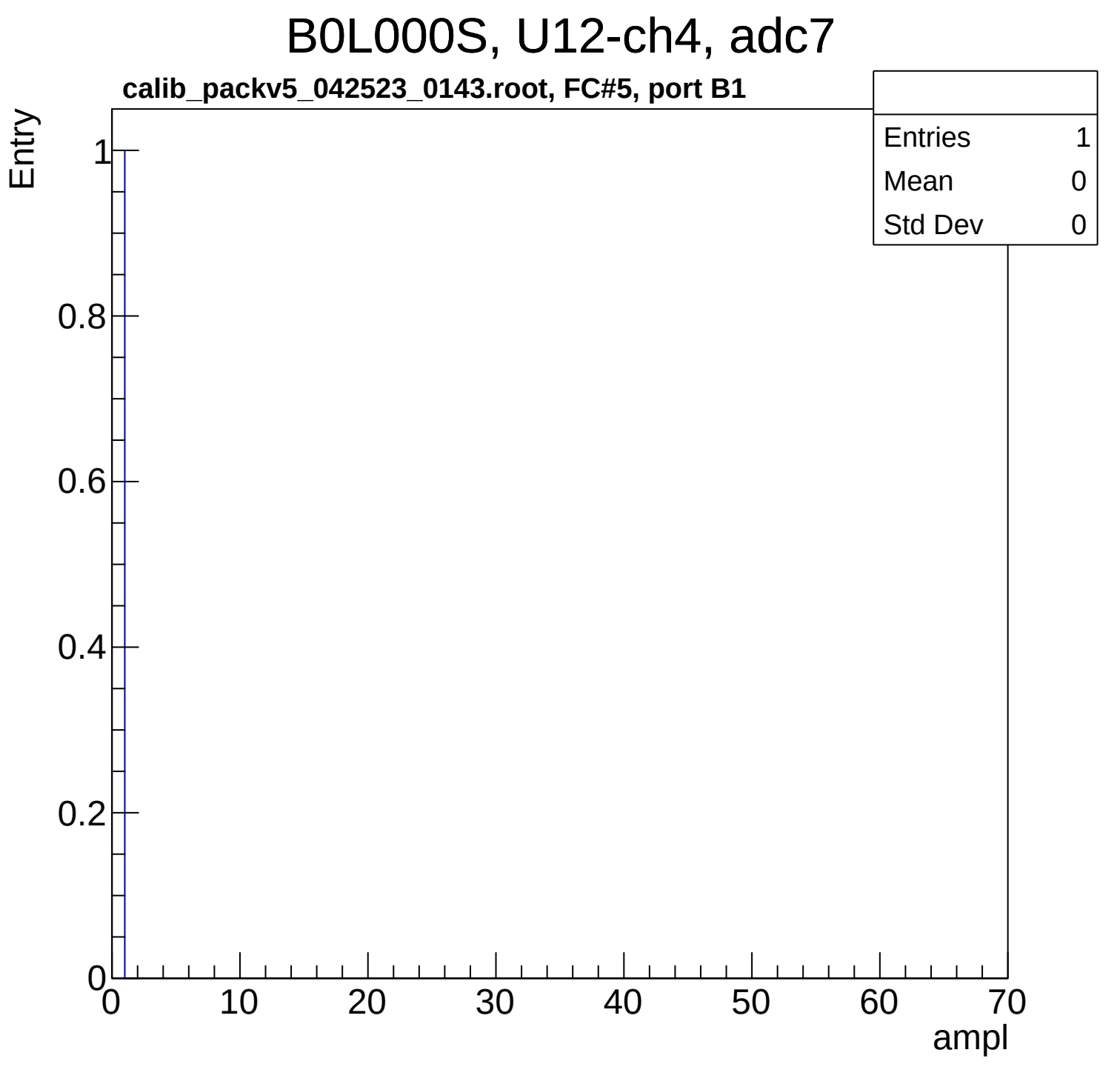
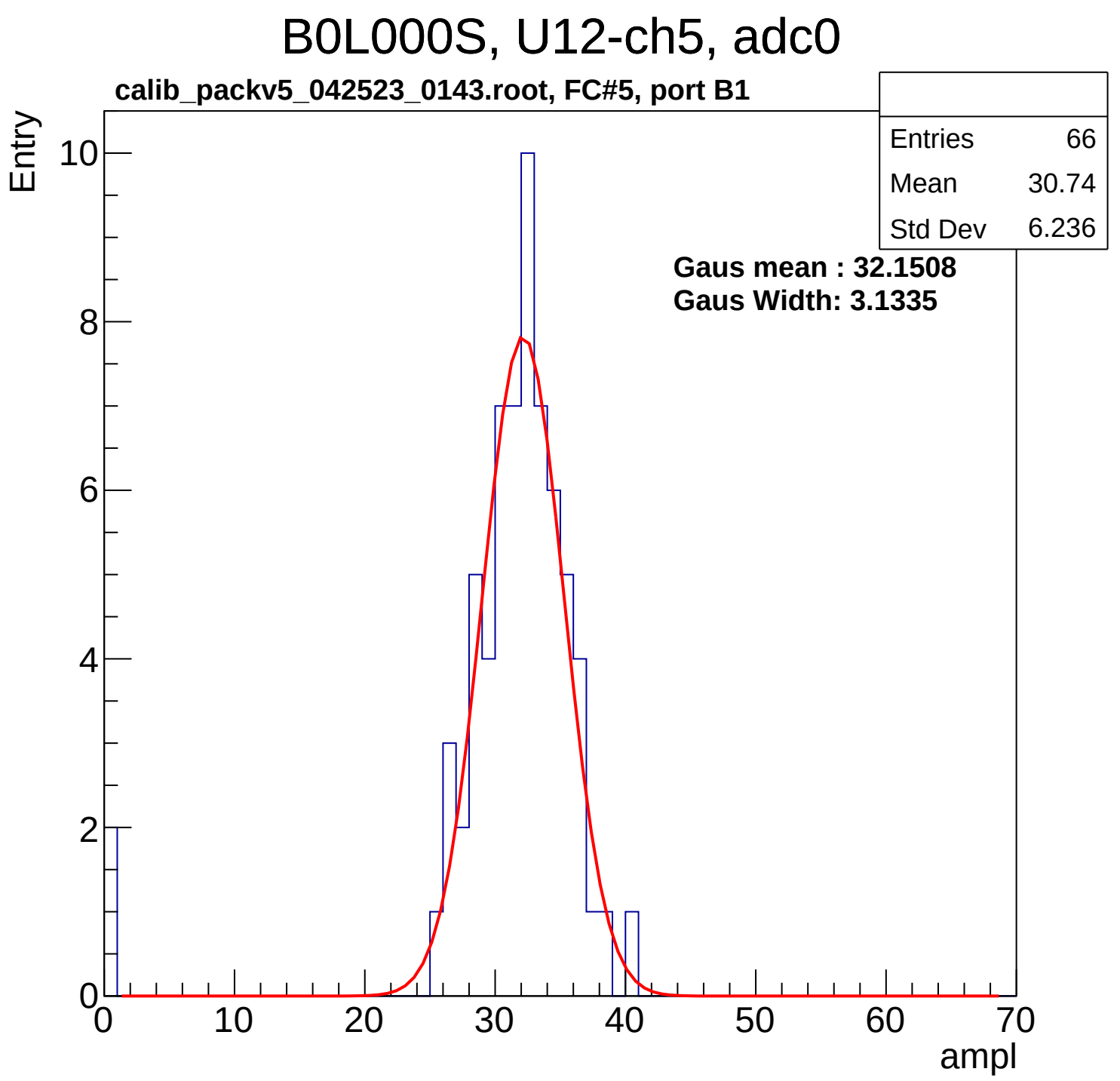
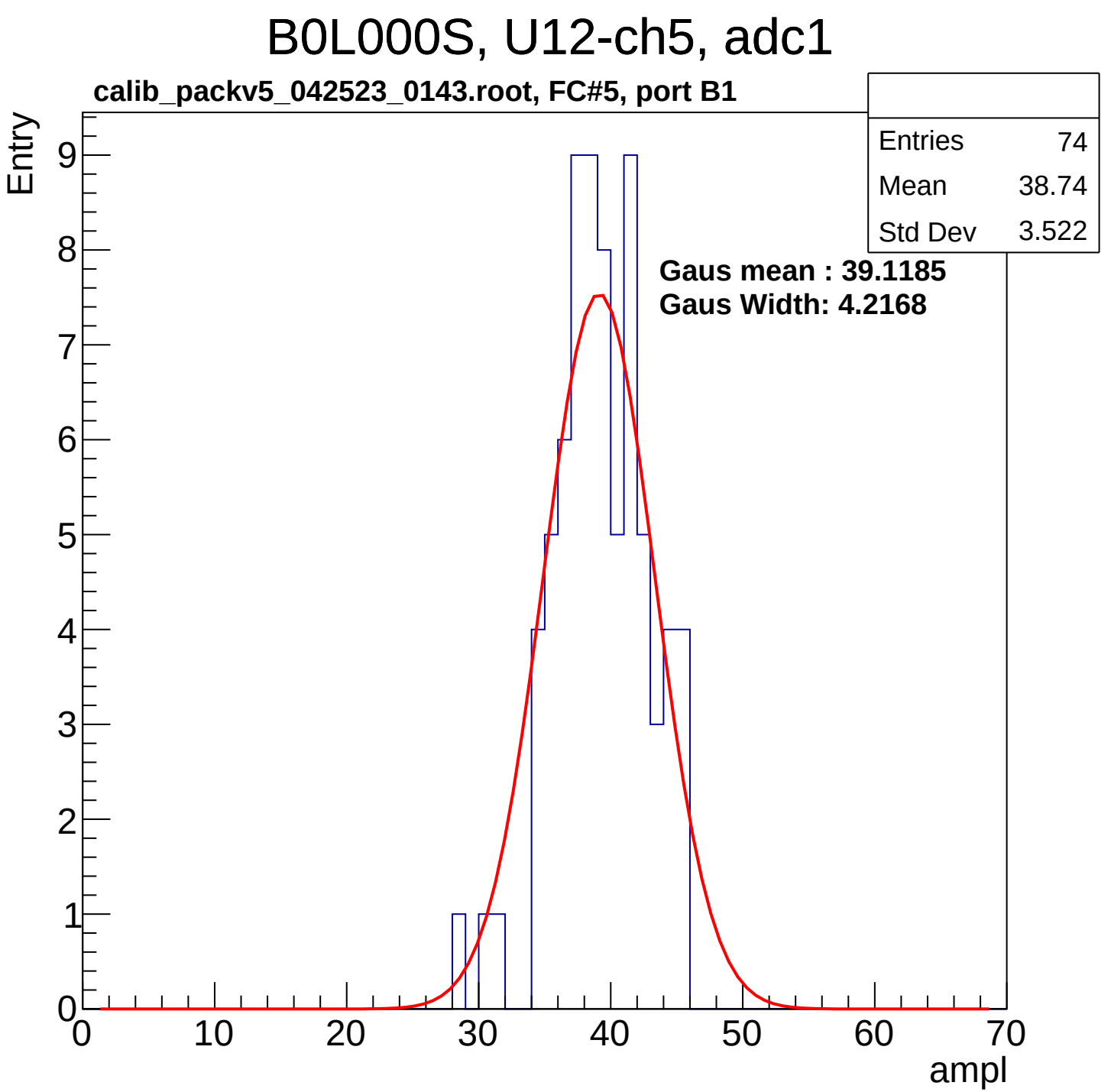
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.74
Std Dev 6.236
Gaus mean : 32.1508
Gaus Width: 3.1335
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 38.74
Std Dev 3.522
Gaus mean : 39.1185
Gaus Width: 4.2168
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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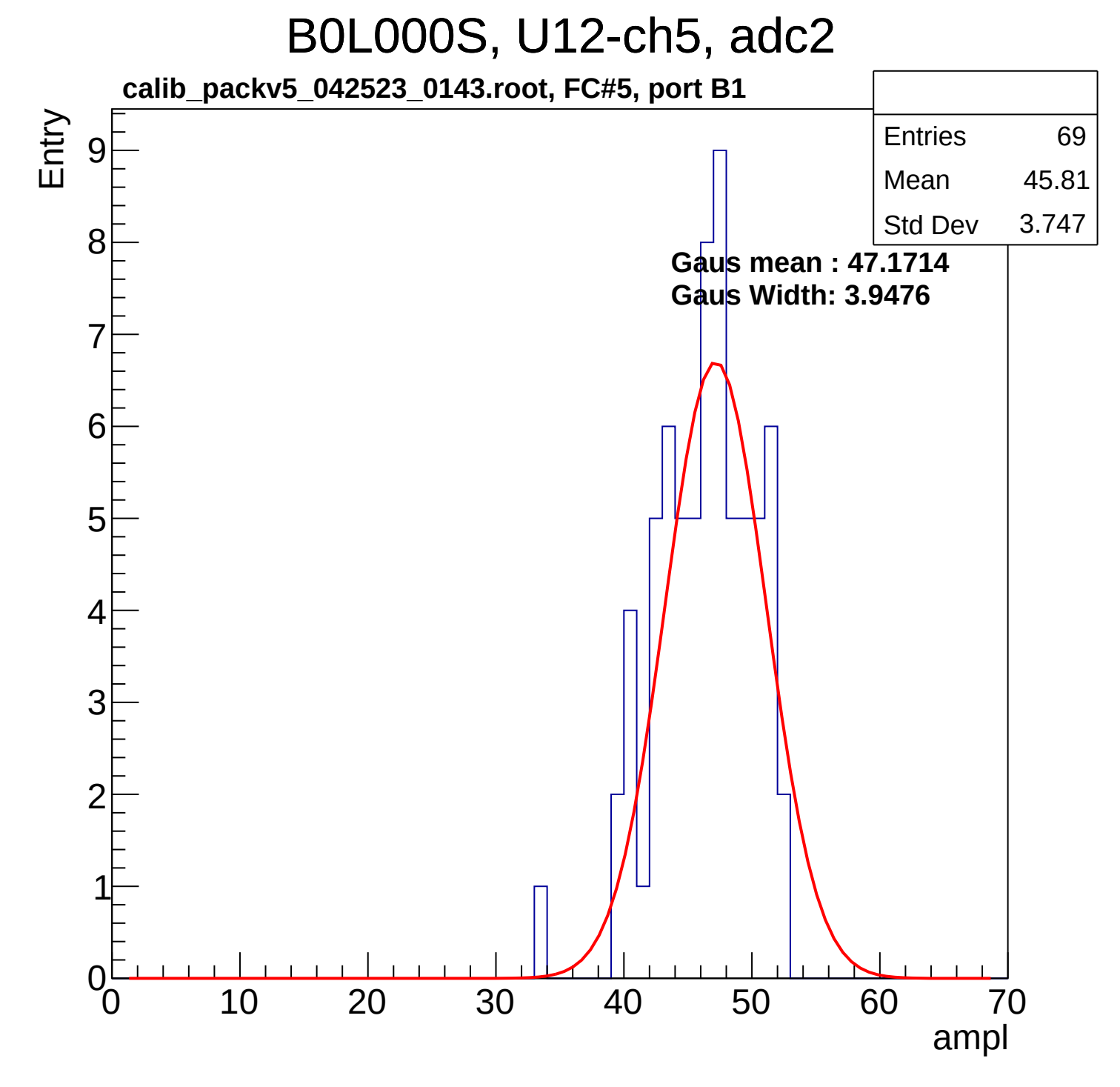
B0L000S, U12-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.81
Std Dev 3.747
Gaus mean : 47.1714
Gaus Width: 3.9476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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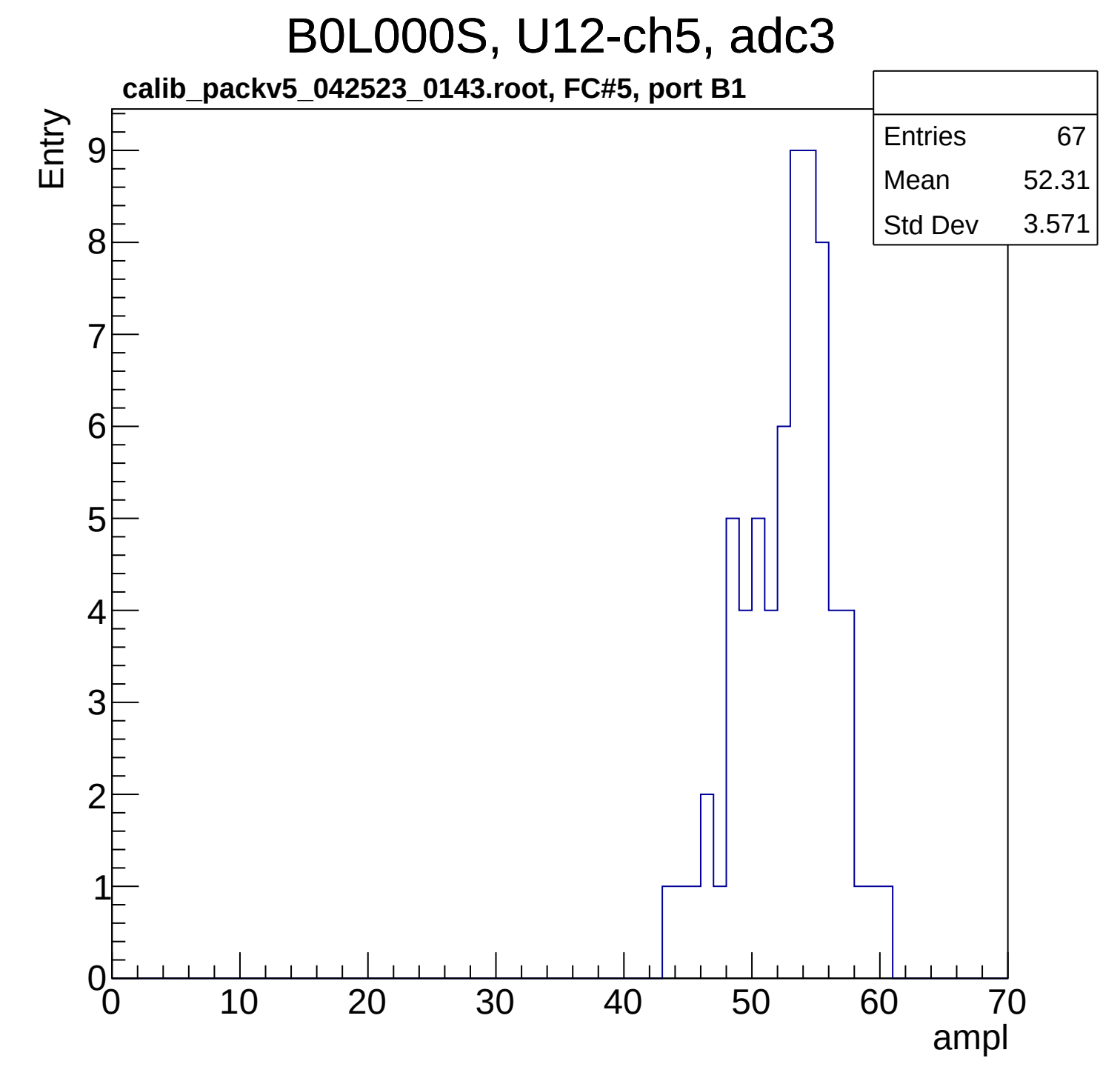
B0L000S, U12-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 52.31
Std Dev 3.571
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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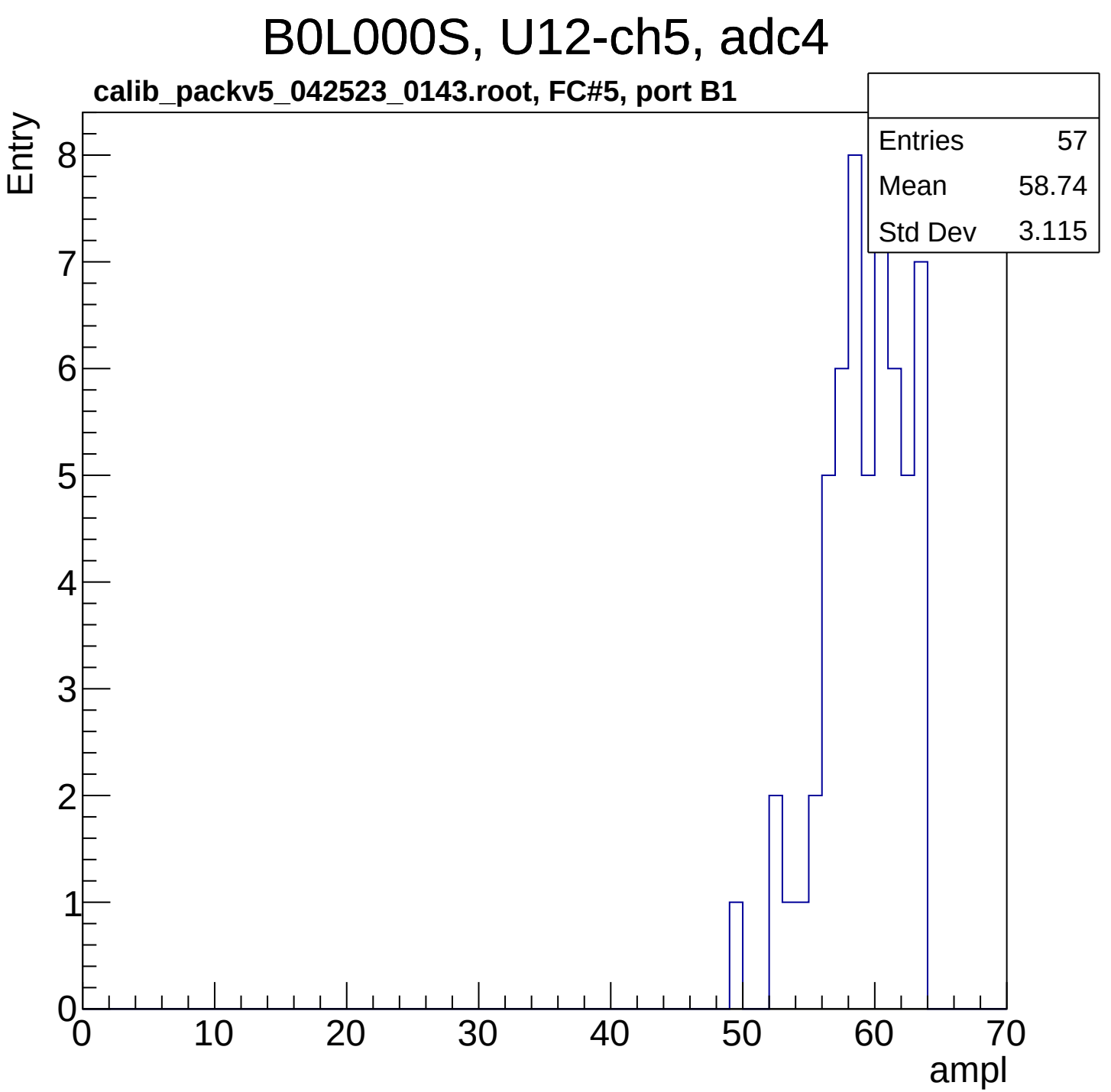
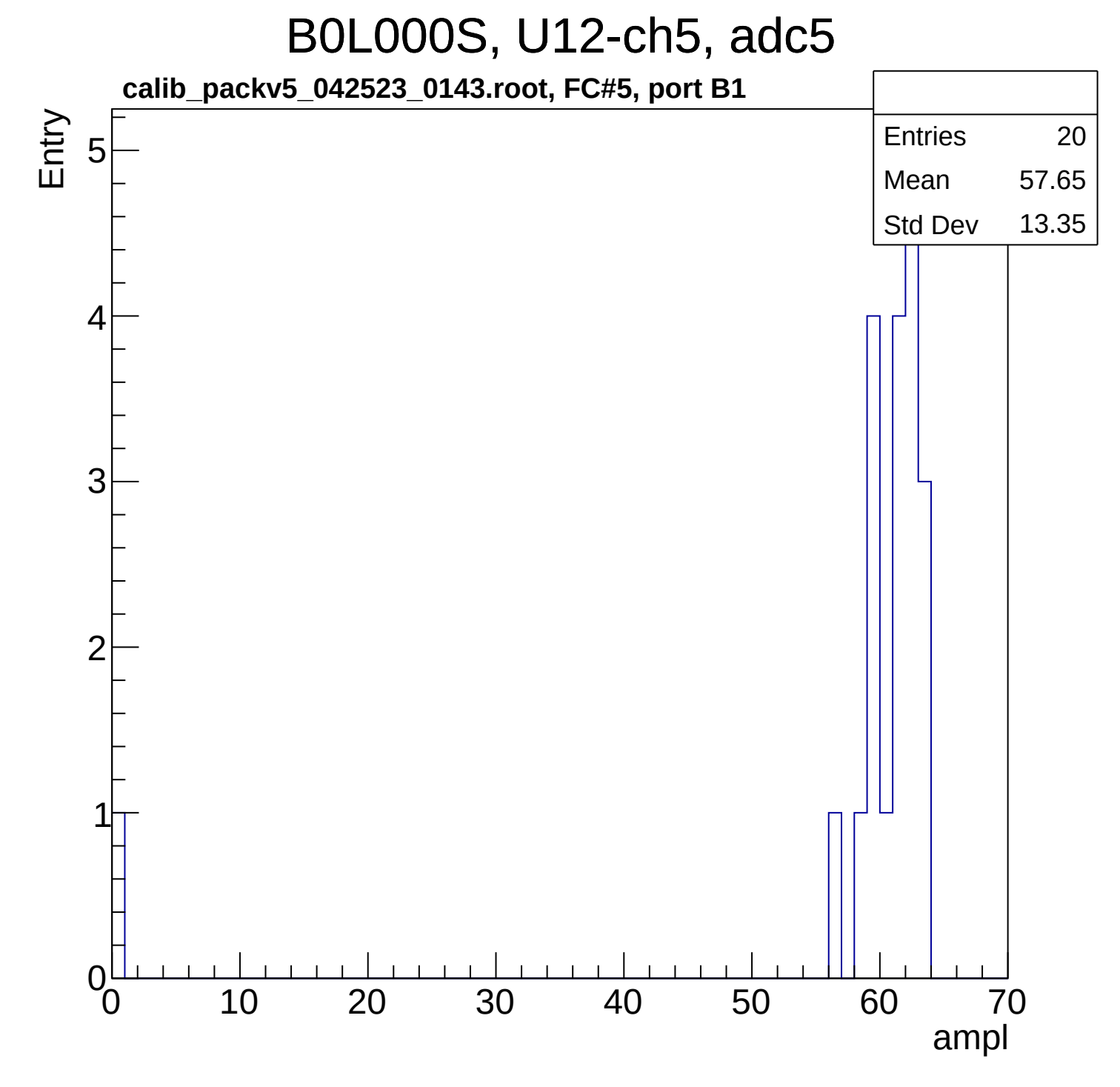
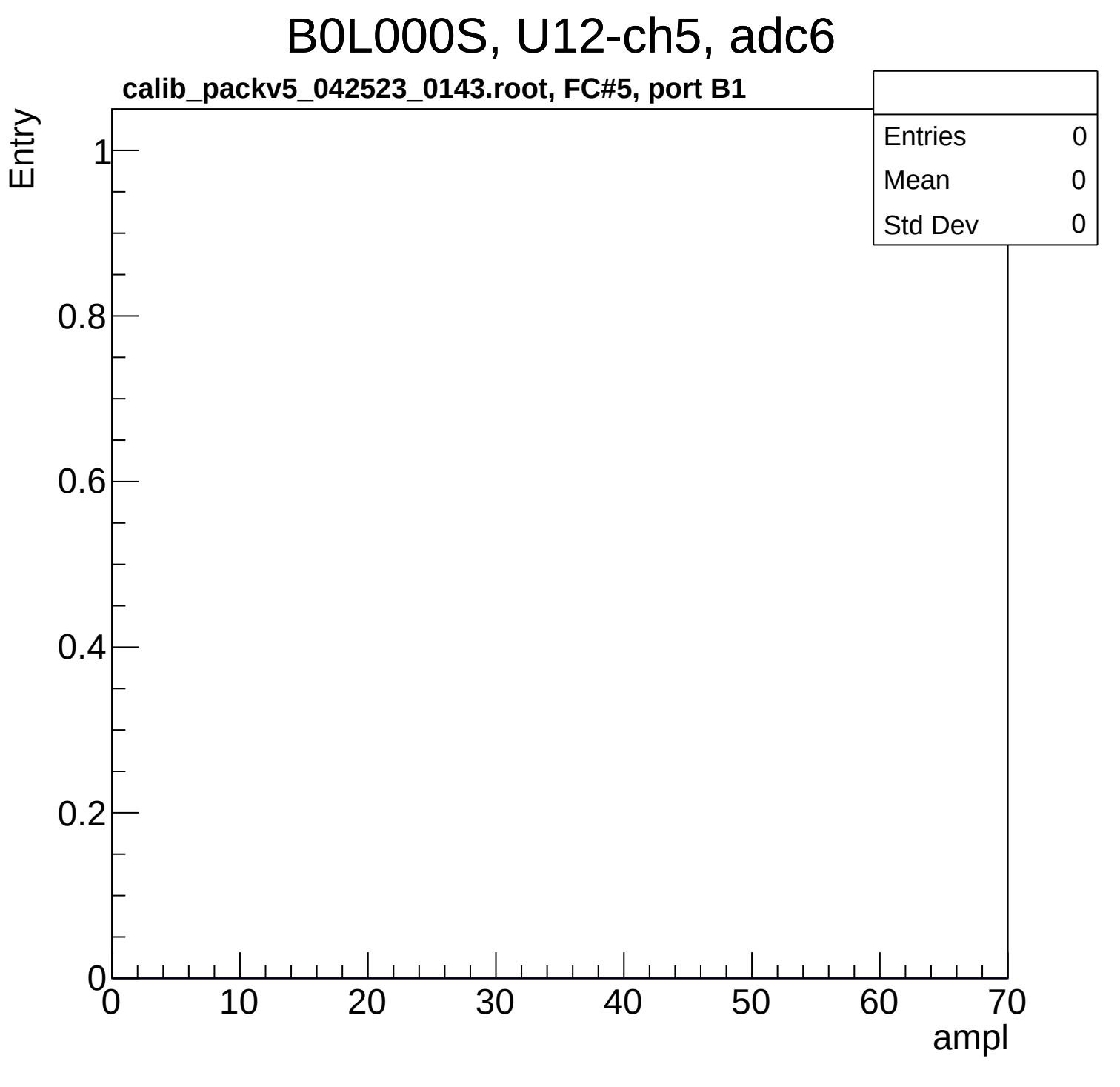

B0L000S, U12-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.74
Std Dev 3.115
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 57.65
Std Dev 13.35
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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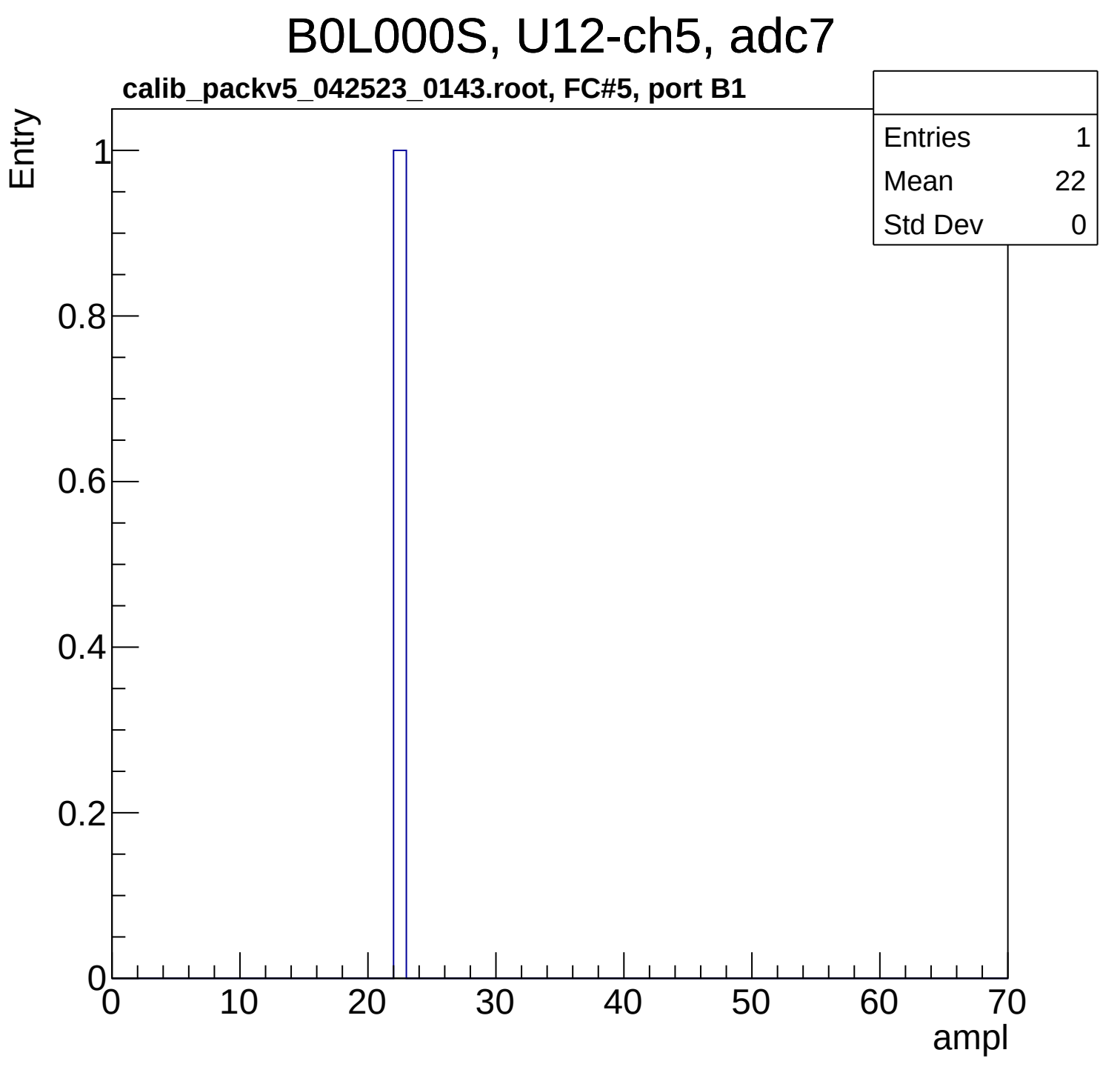
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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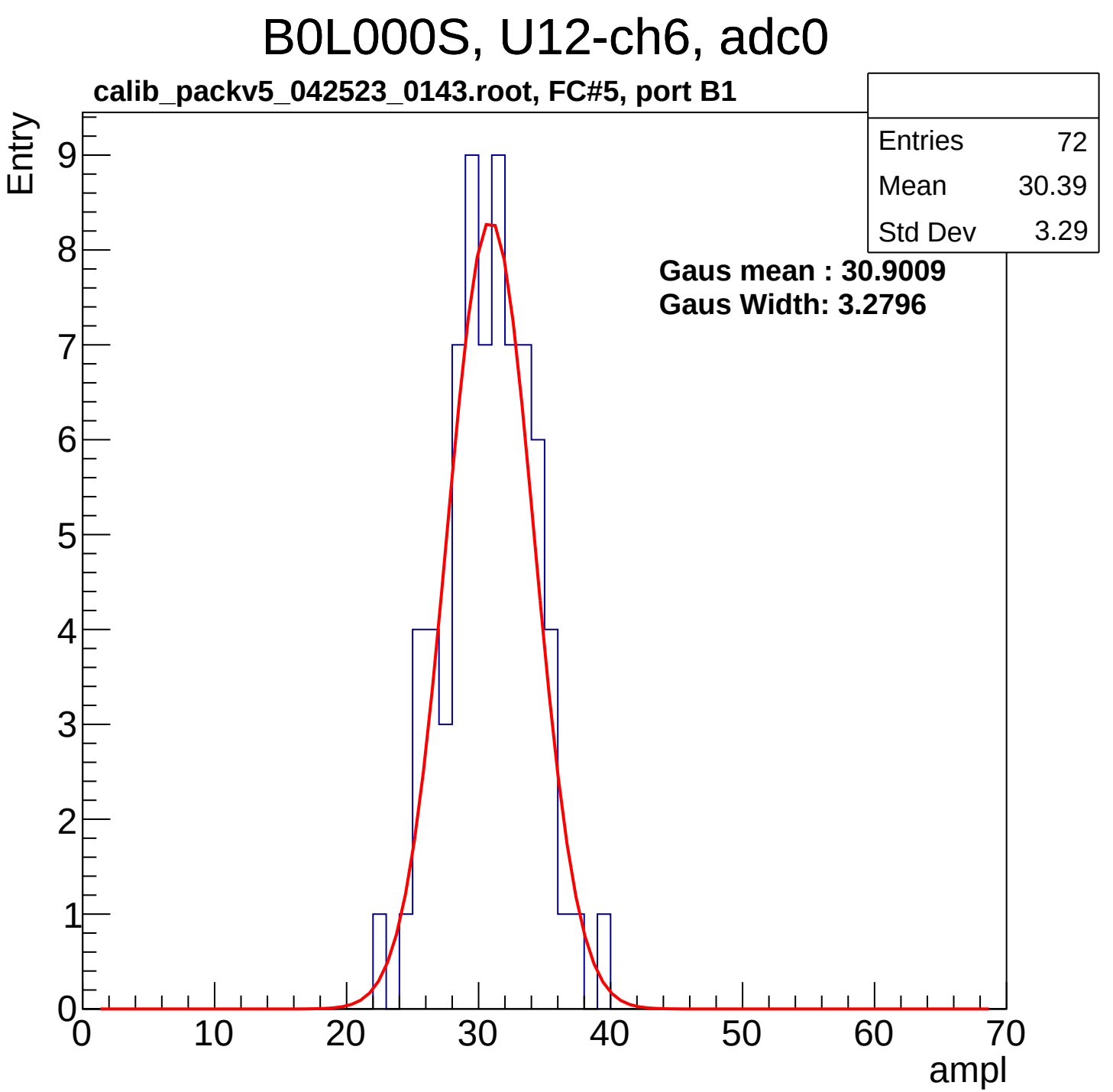
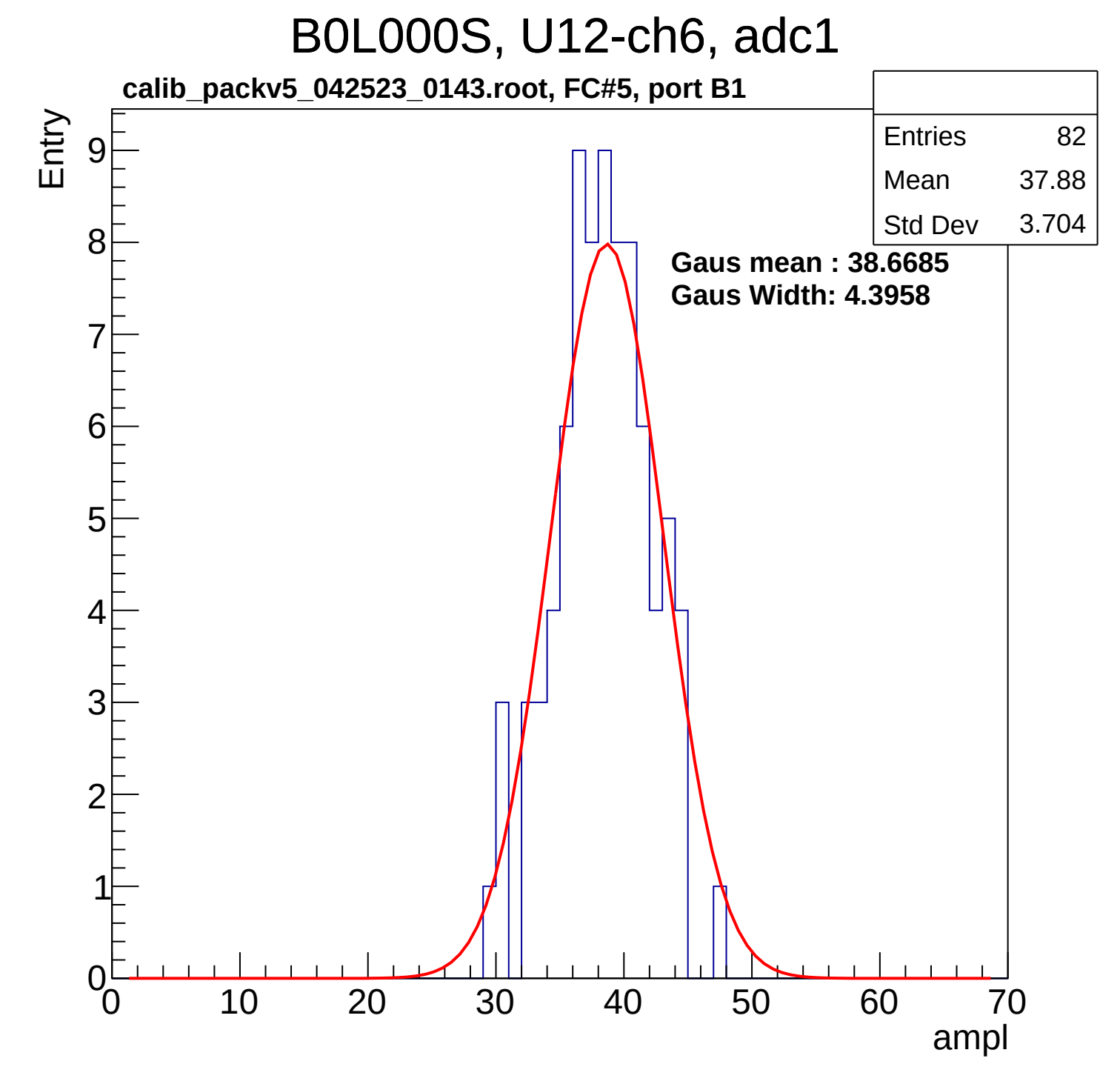
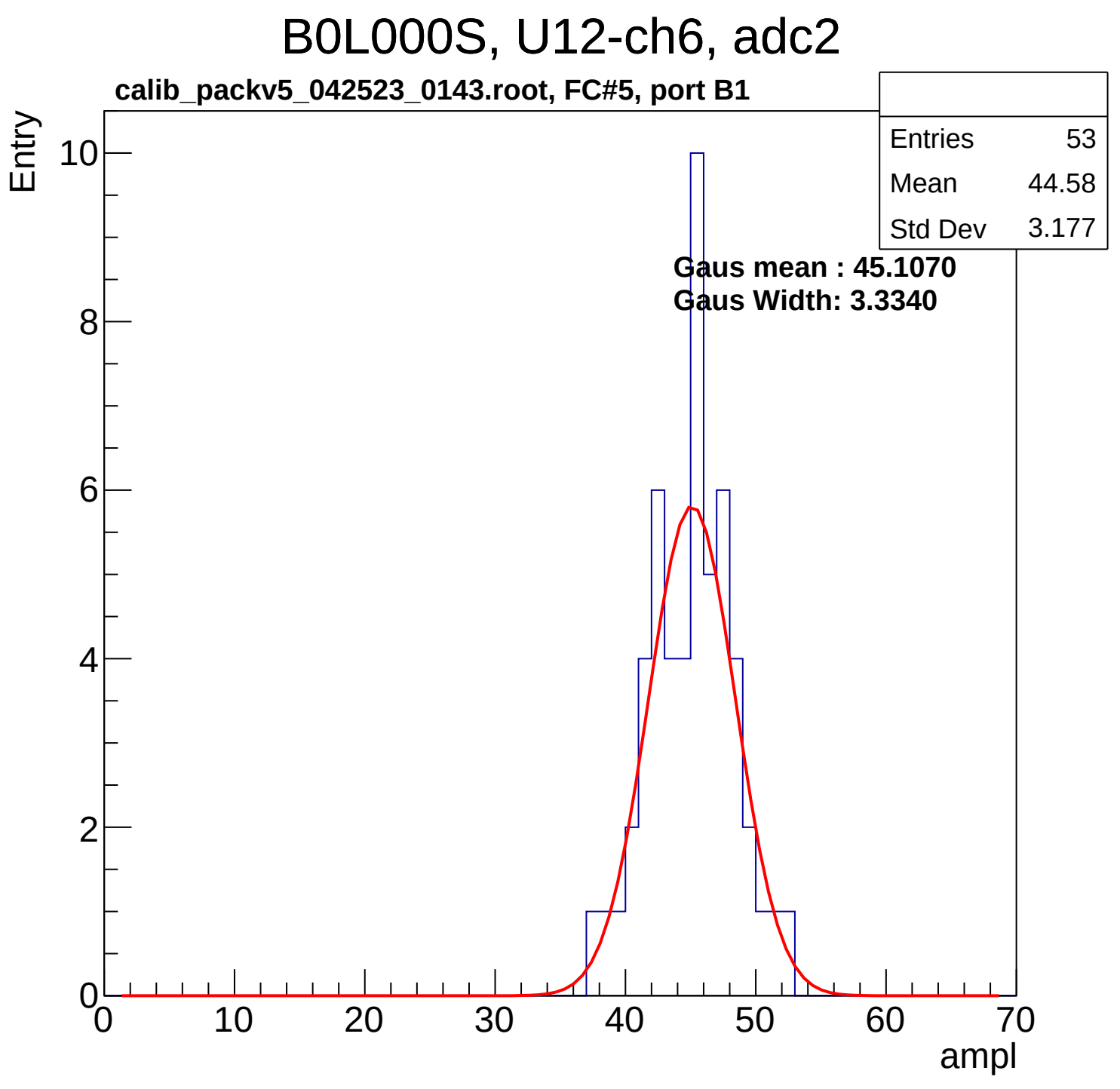
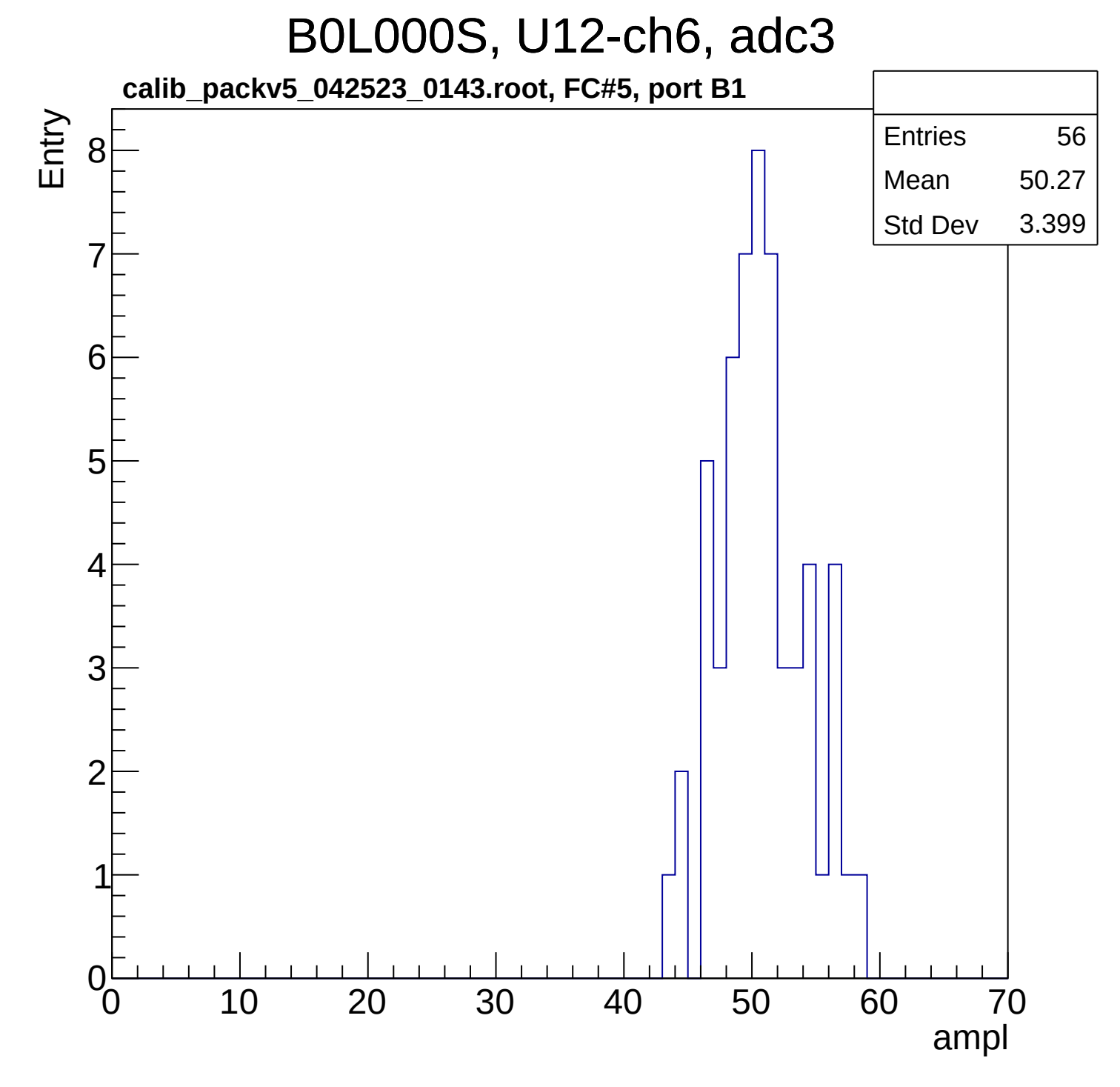
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.39
Std Dev 3.29
Gaus mean : 30.9009
Gaus Width: 3.2796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 37.88
Std Dev 3.704
Gaus mean : 38.6685
Gaus Width: 4.3958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 44.58
Std Dev 3.177
Gaus mean : 45.1070
Gaus Width: 3.3340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 50.27
Std Dev 3.399
Entry
ampl
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3
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5
6
7
8
0
10
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50
60
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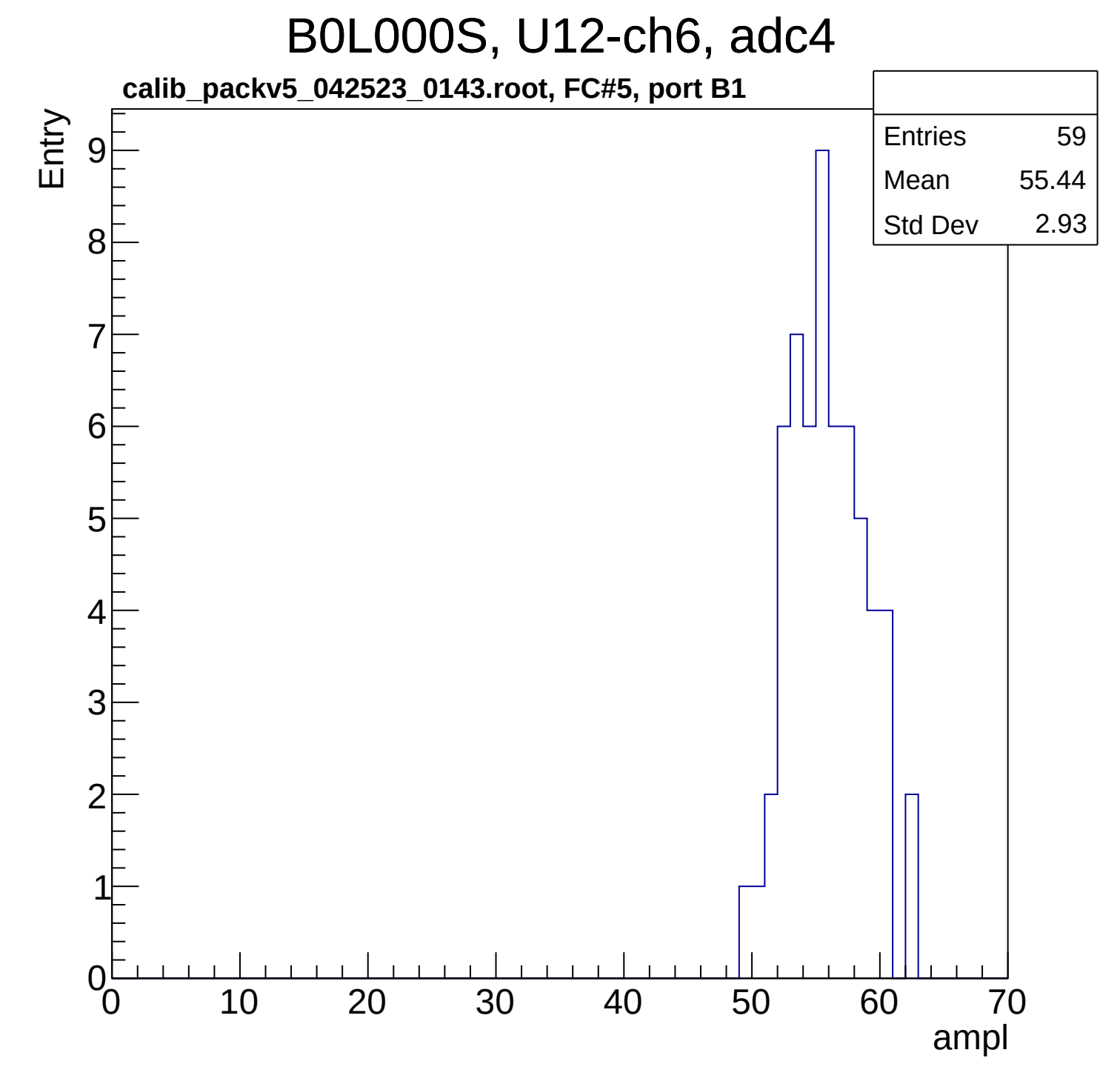
B0L000S, U12-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.44
Std Dev 2.93
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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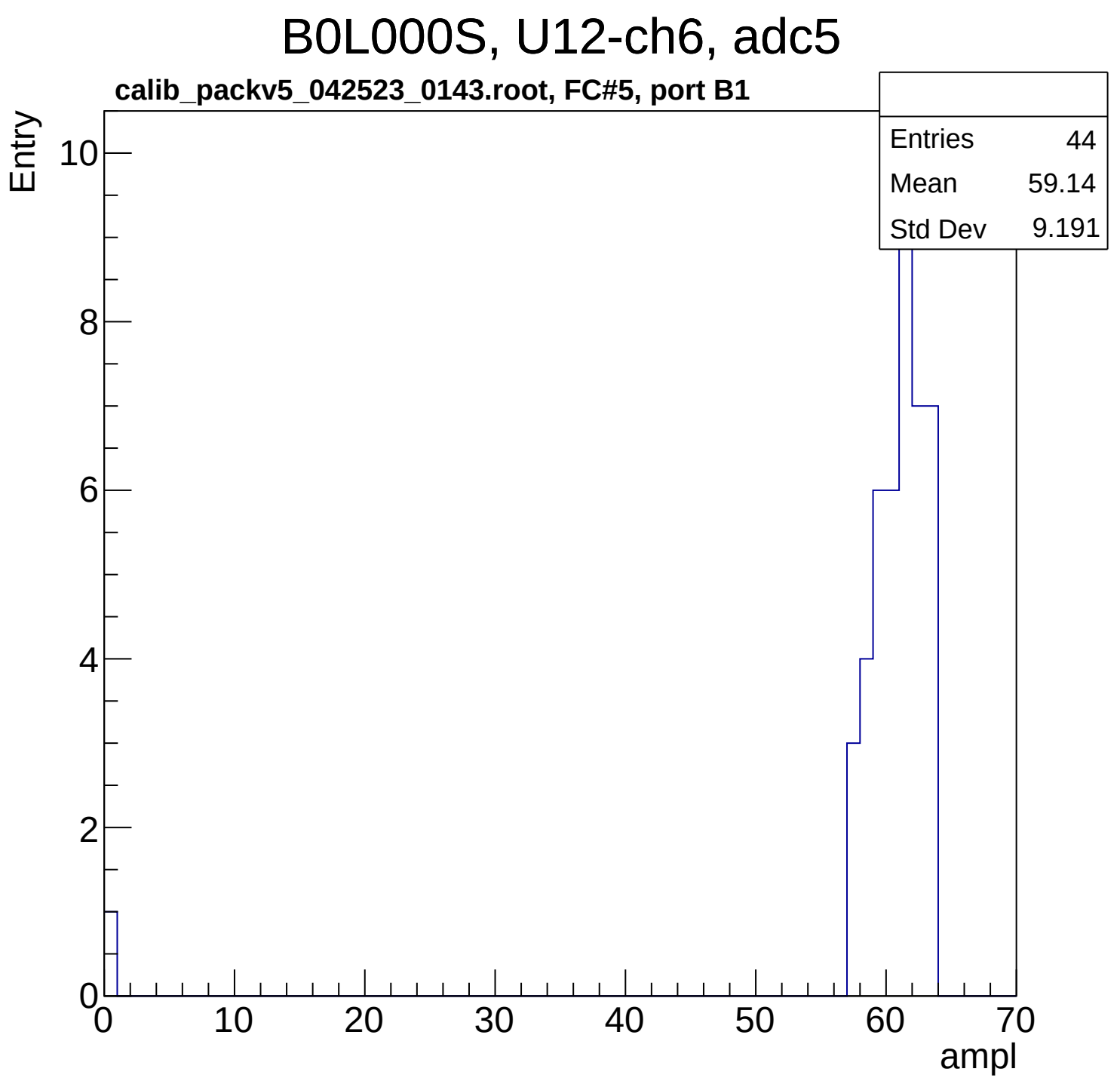
B0L000S, U12-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.14
Std Dev 9.191
Entry
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2
4
6
8
10
0
10
20
30
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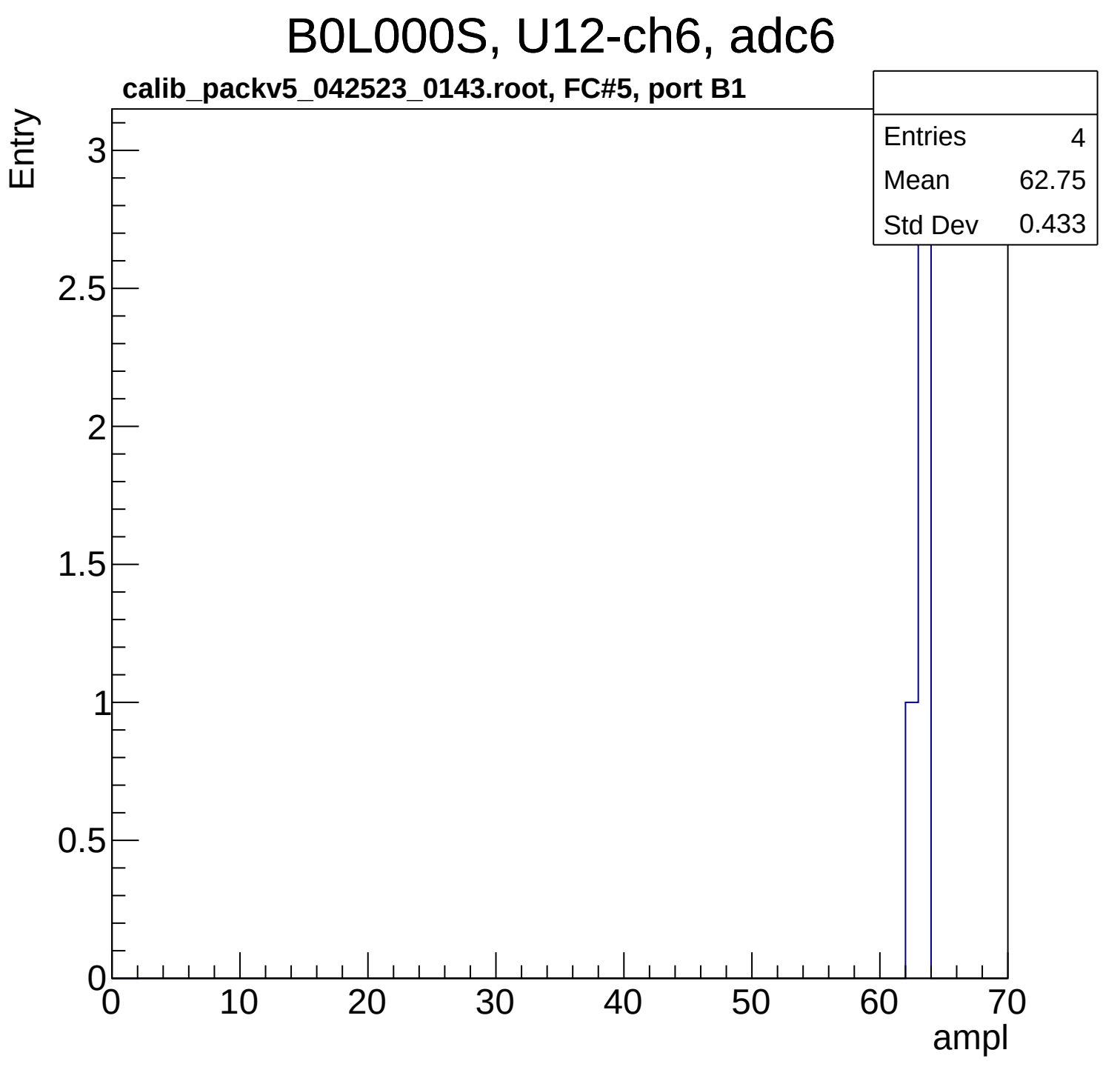
50
60
70
ampl


B0L000S, U12-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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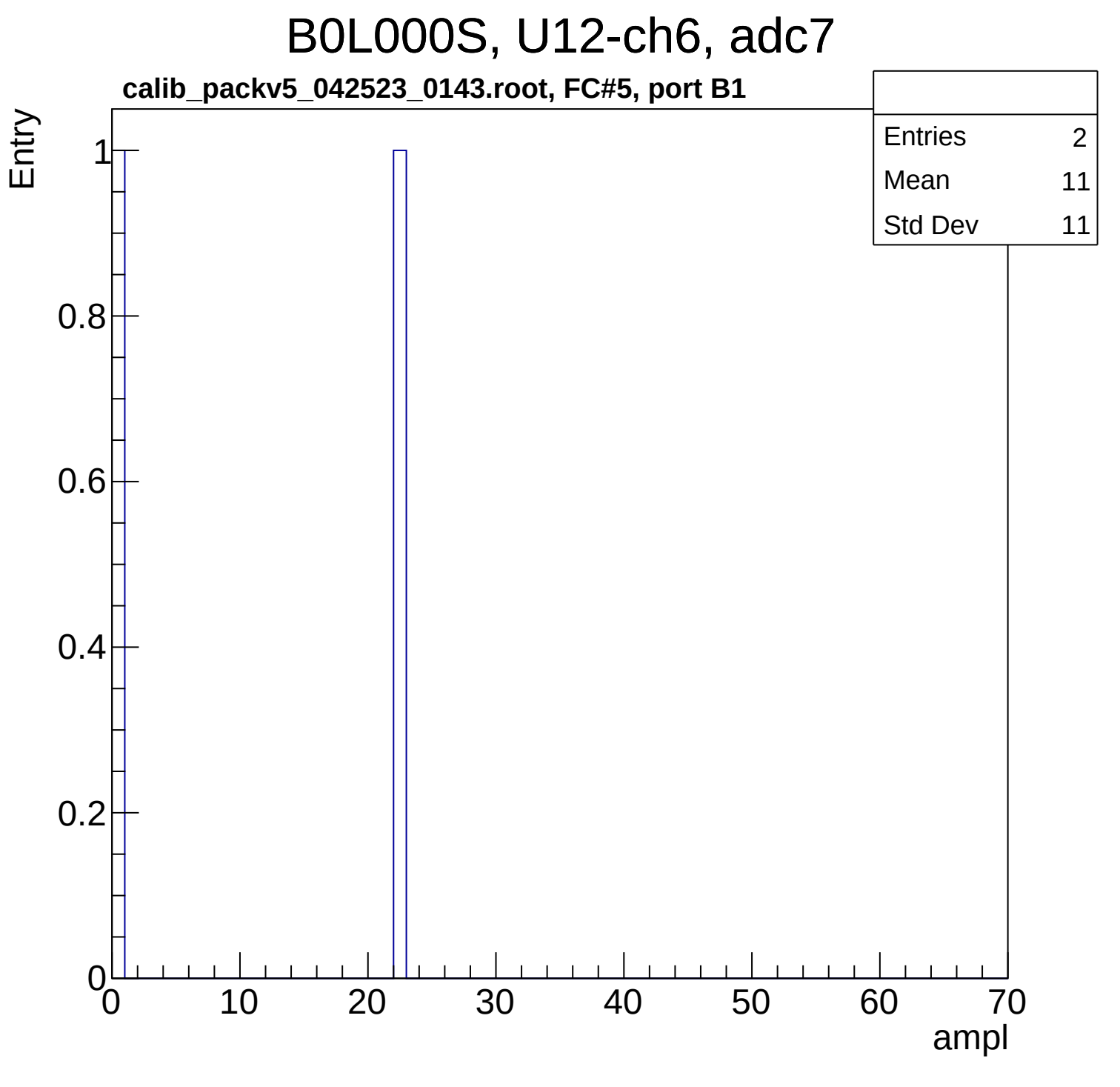
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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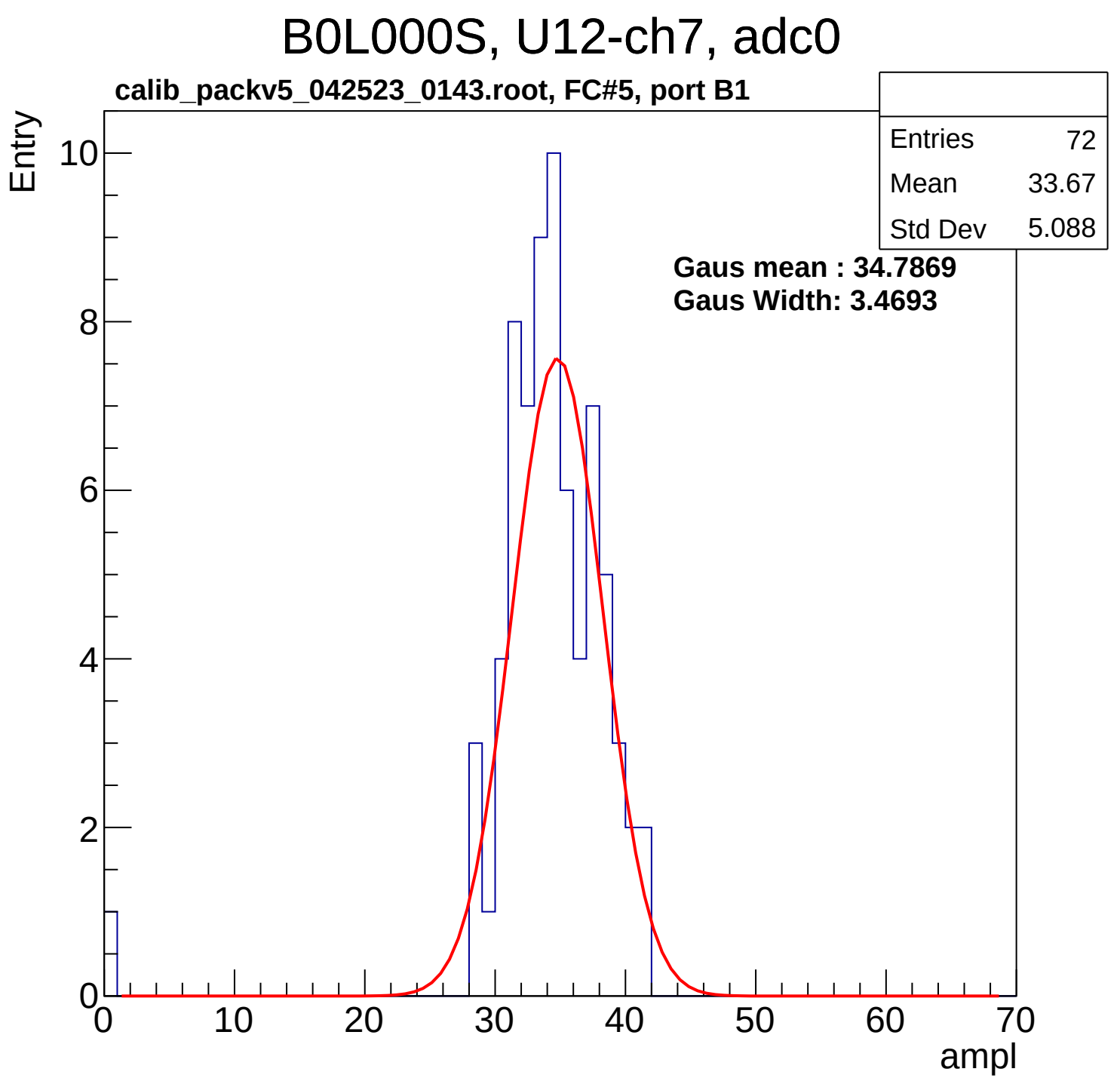
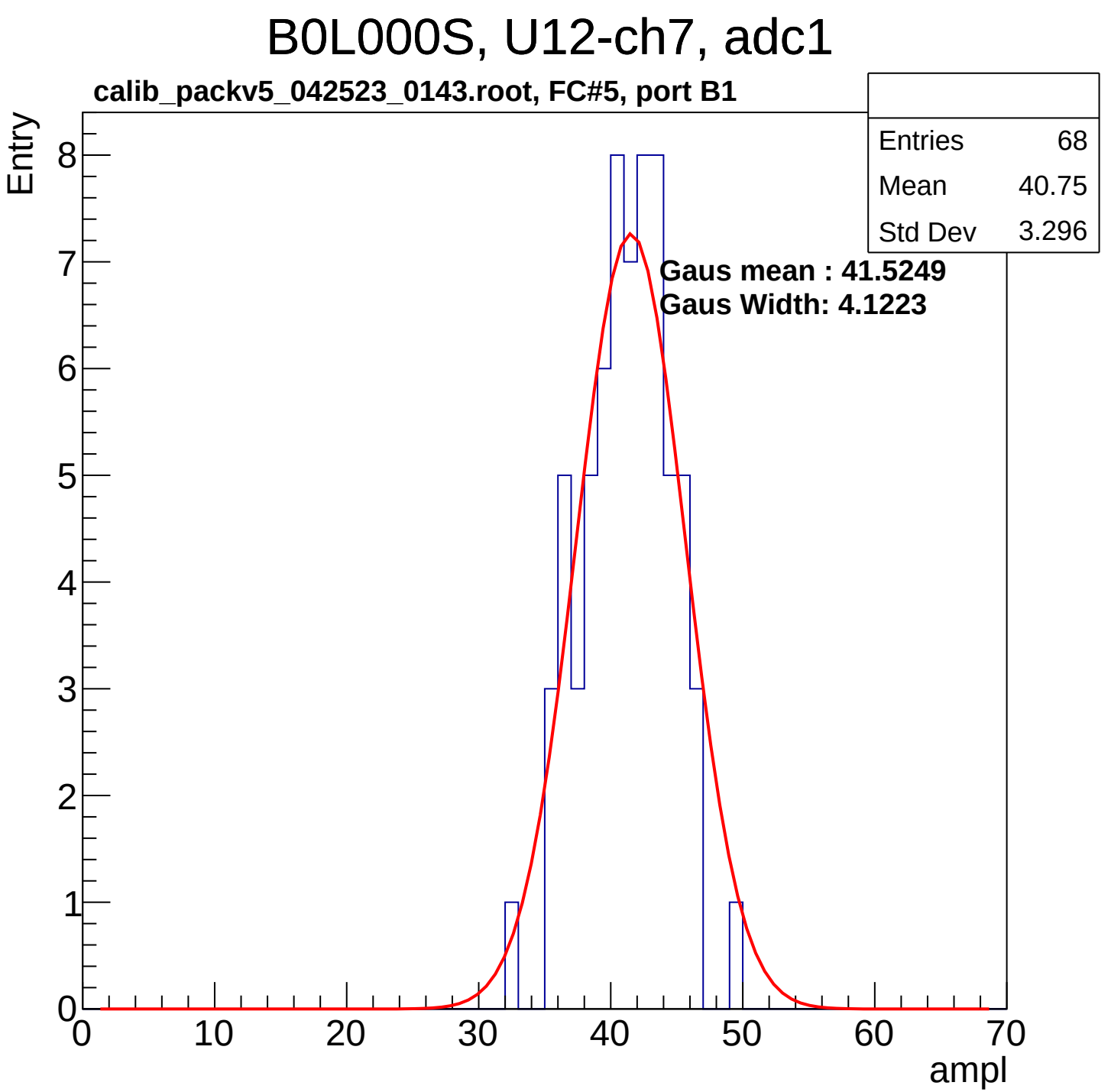
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 33.67
Std Dev 5.088
Gaus mean : 34.7869
Gaus Width: 3.4693
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 40.75
Std Dev 3.296
Gaus mean : 41.5249
Gaus Width: 4.1223
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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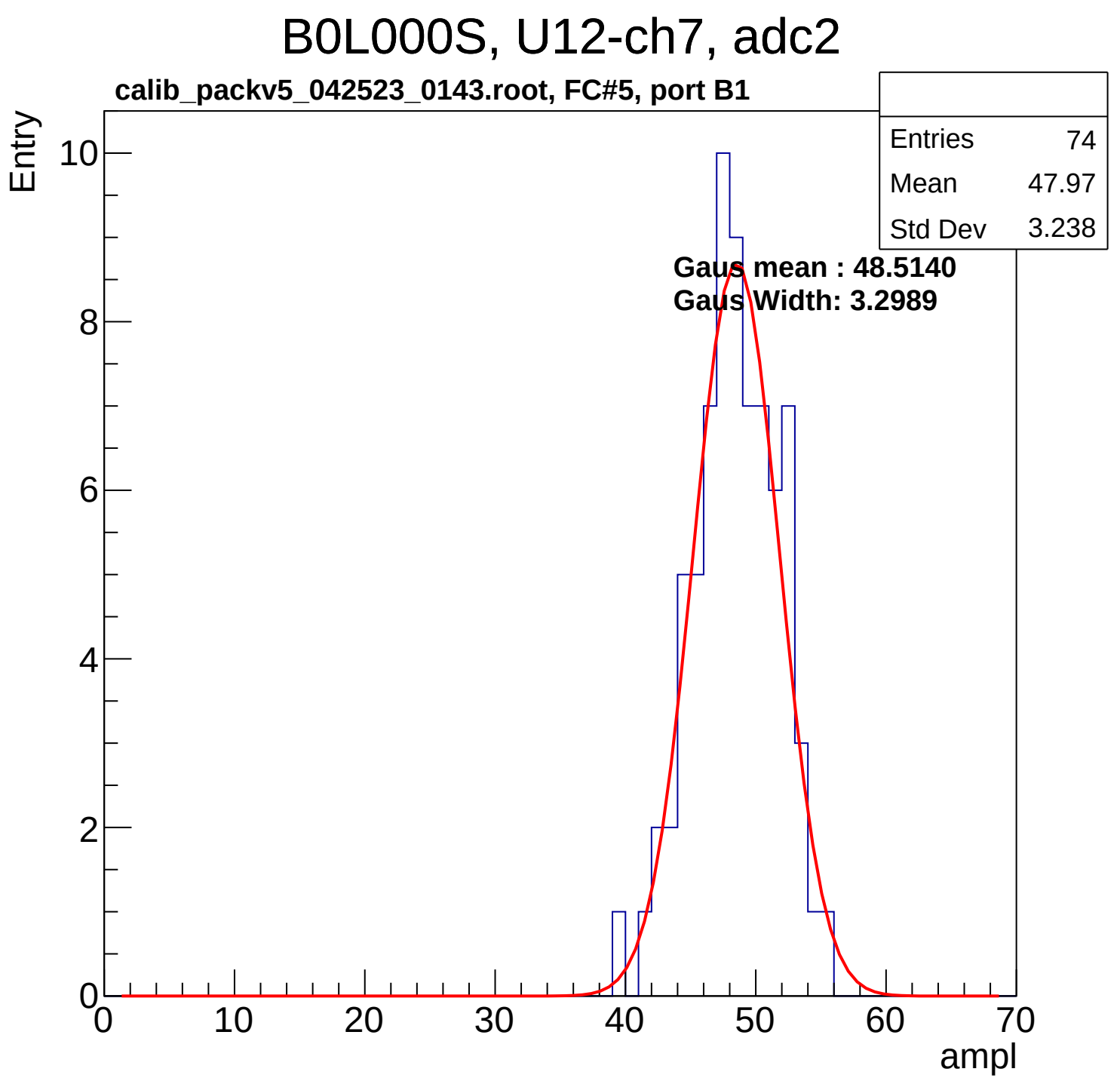
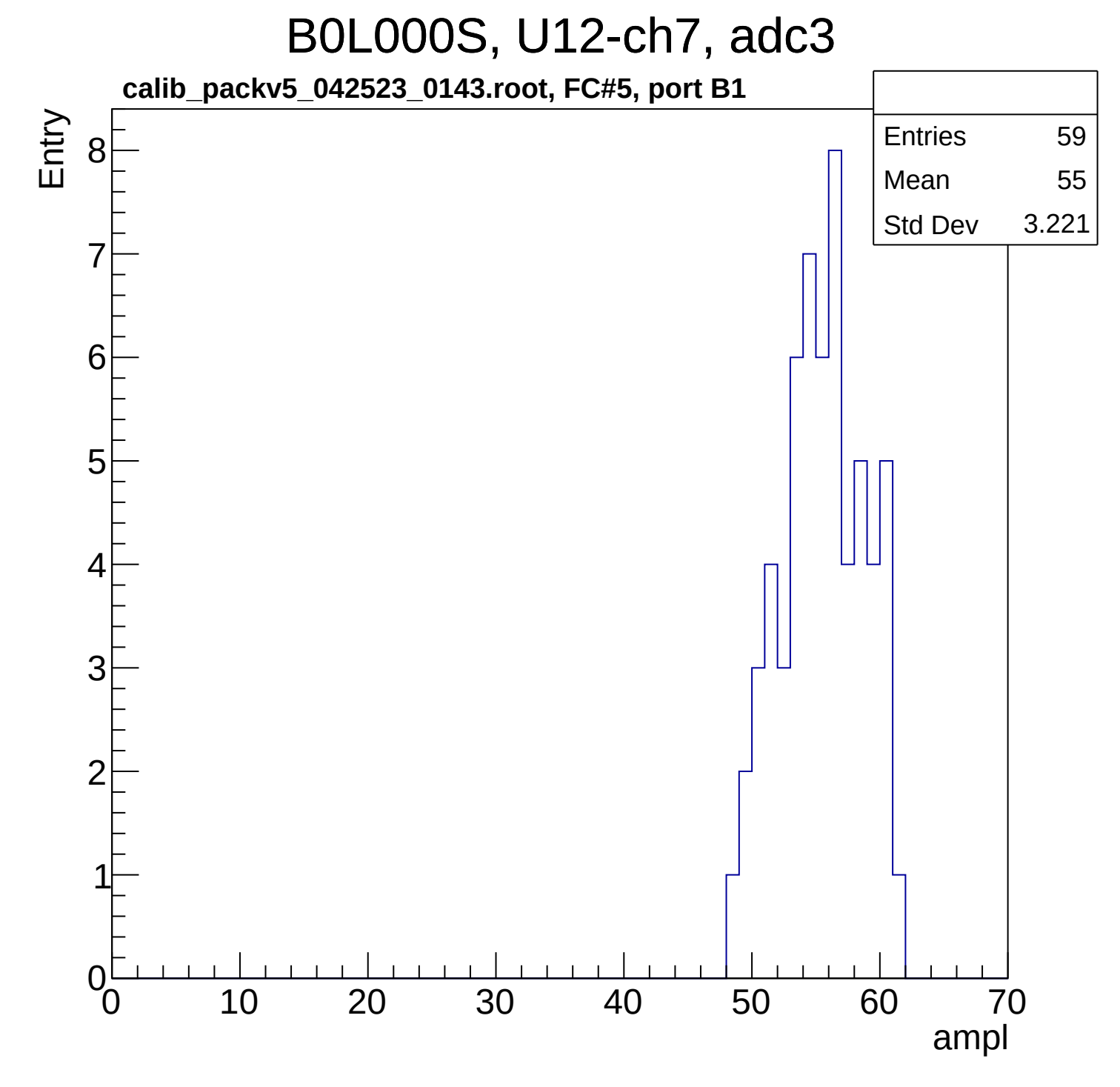

B0L000S, U12-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 47.97
Std Dev 3.238
Gaus mean : 48.5140
Gaus Width: 3.2989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55
Std Dev 3.221
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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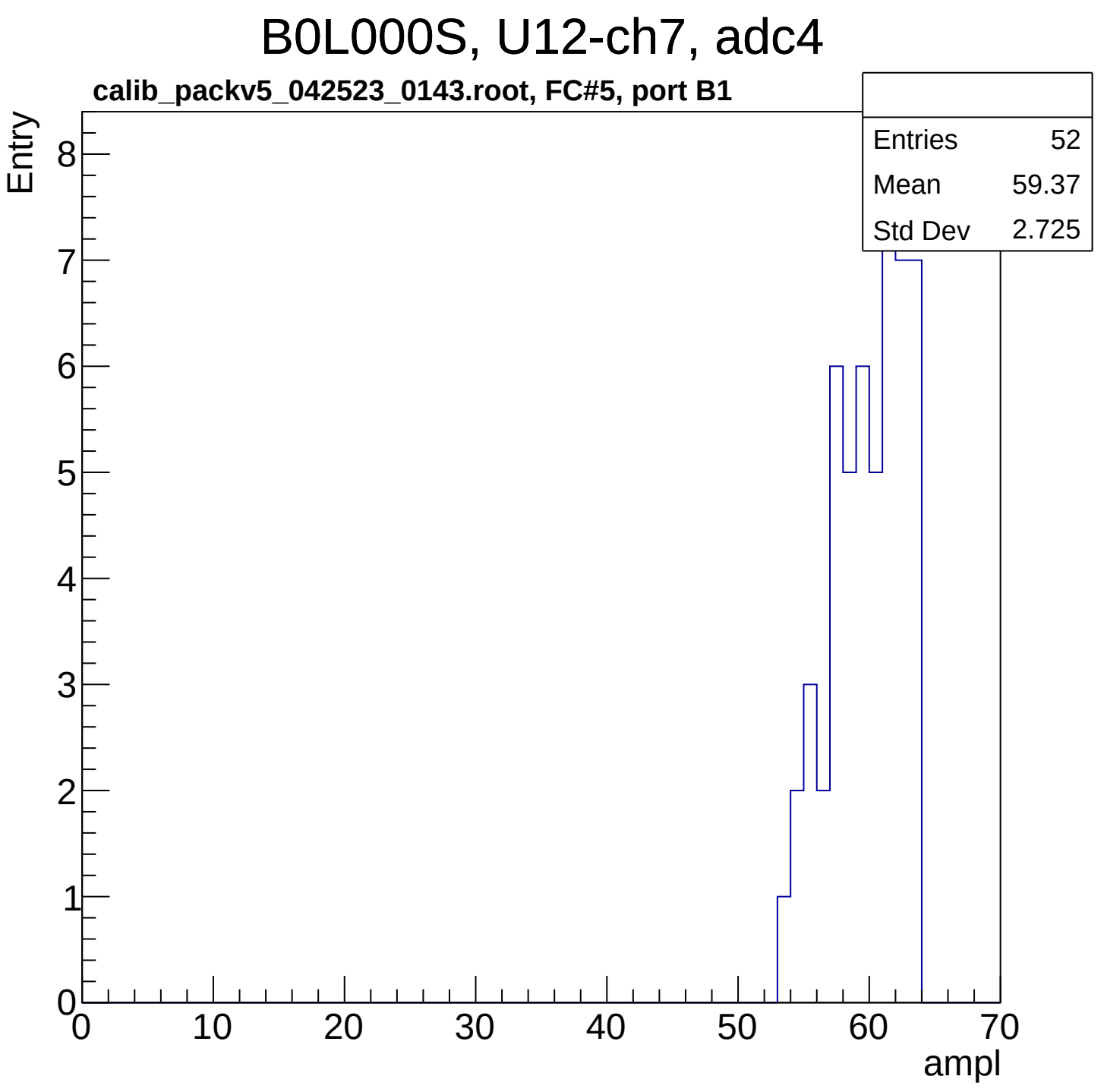
B0L000S, U12-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.37
Std Dev 2.725
Entry
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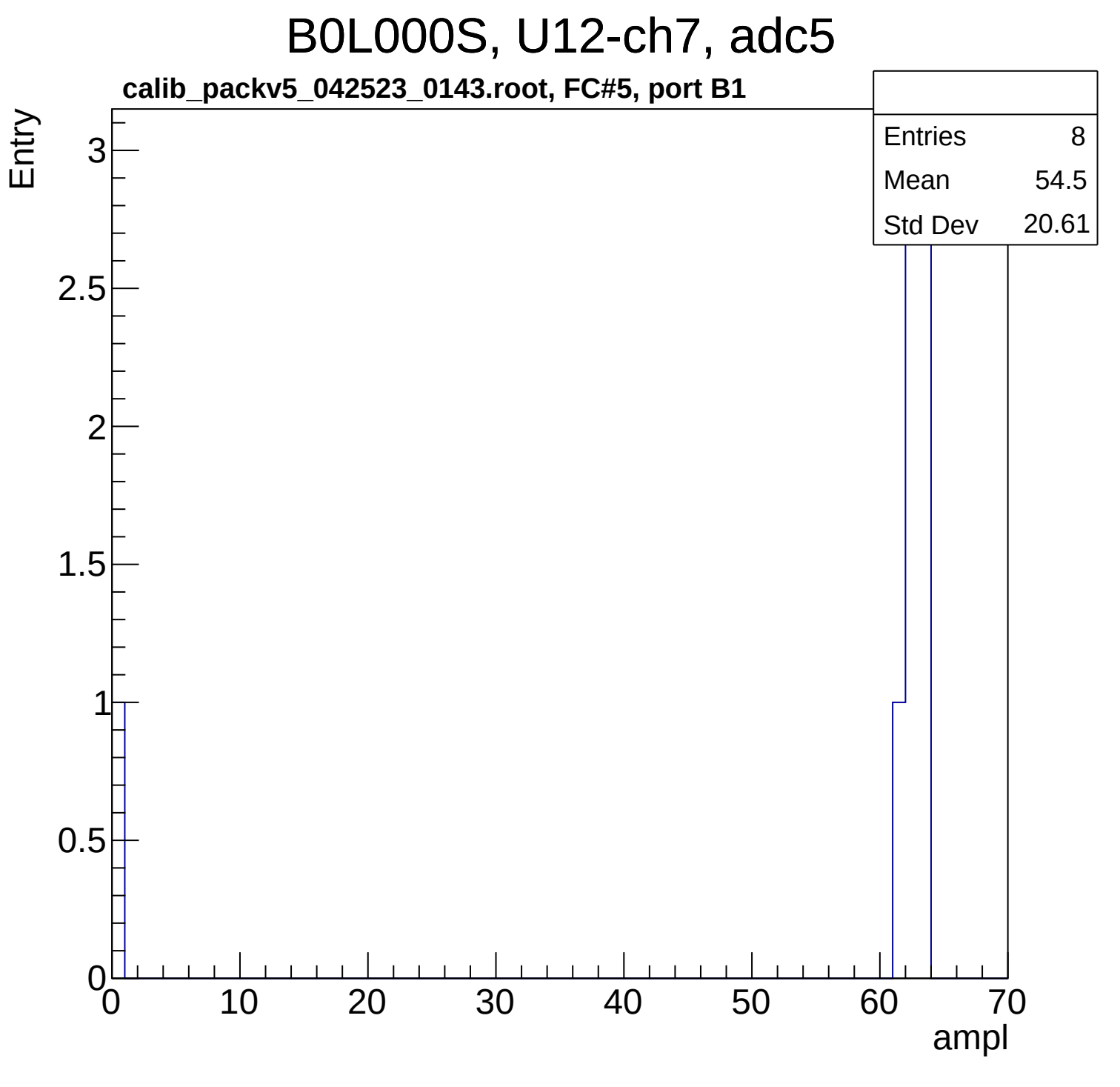
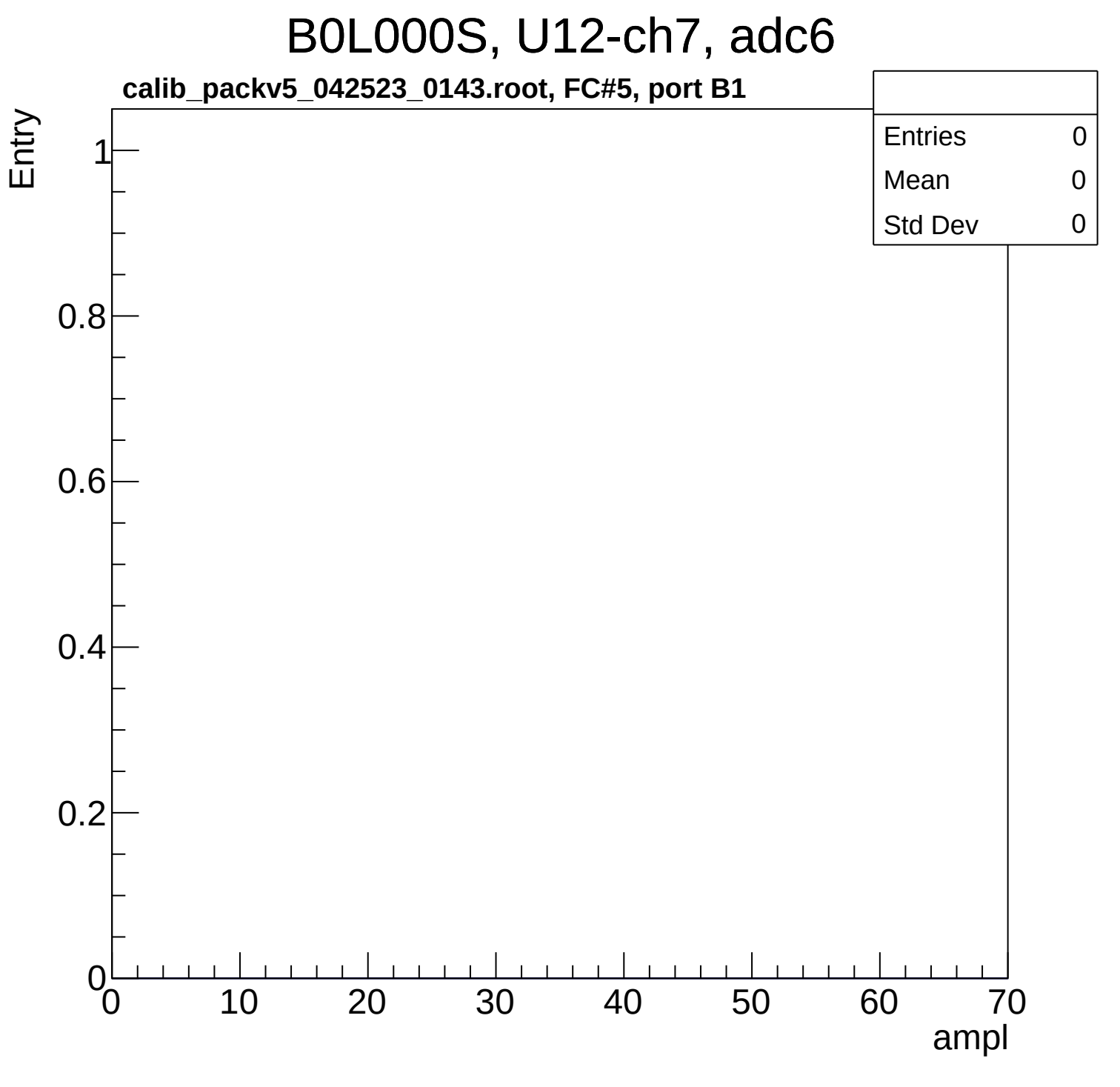
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.5
Std Dev 20.61
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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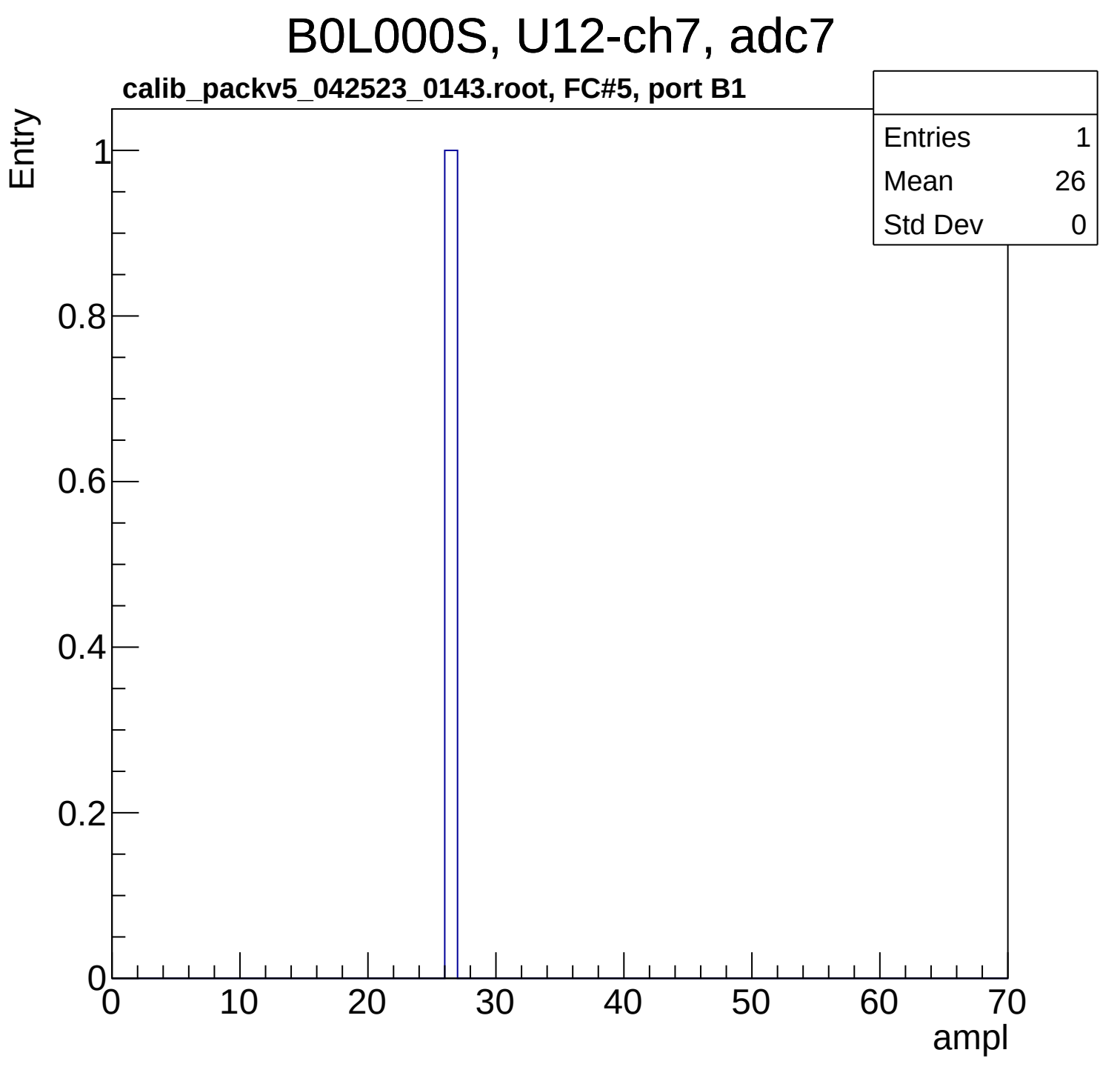
70
ampl


B0L000S, U12-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 26
Std Dev 0
Entry
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0.6
0.4
0.2
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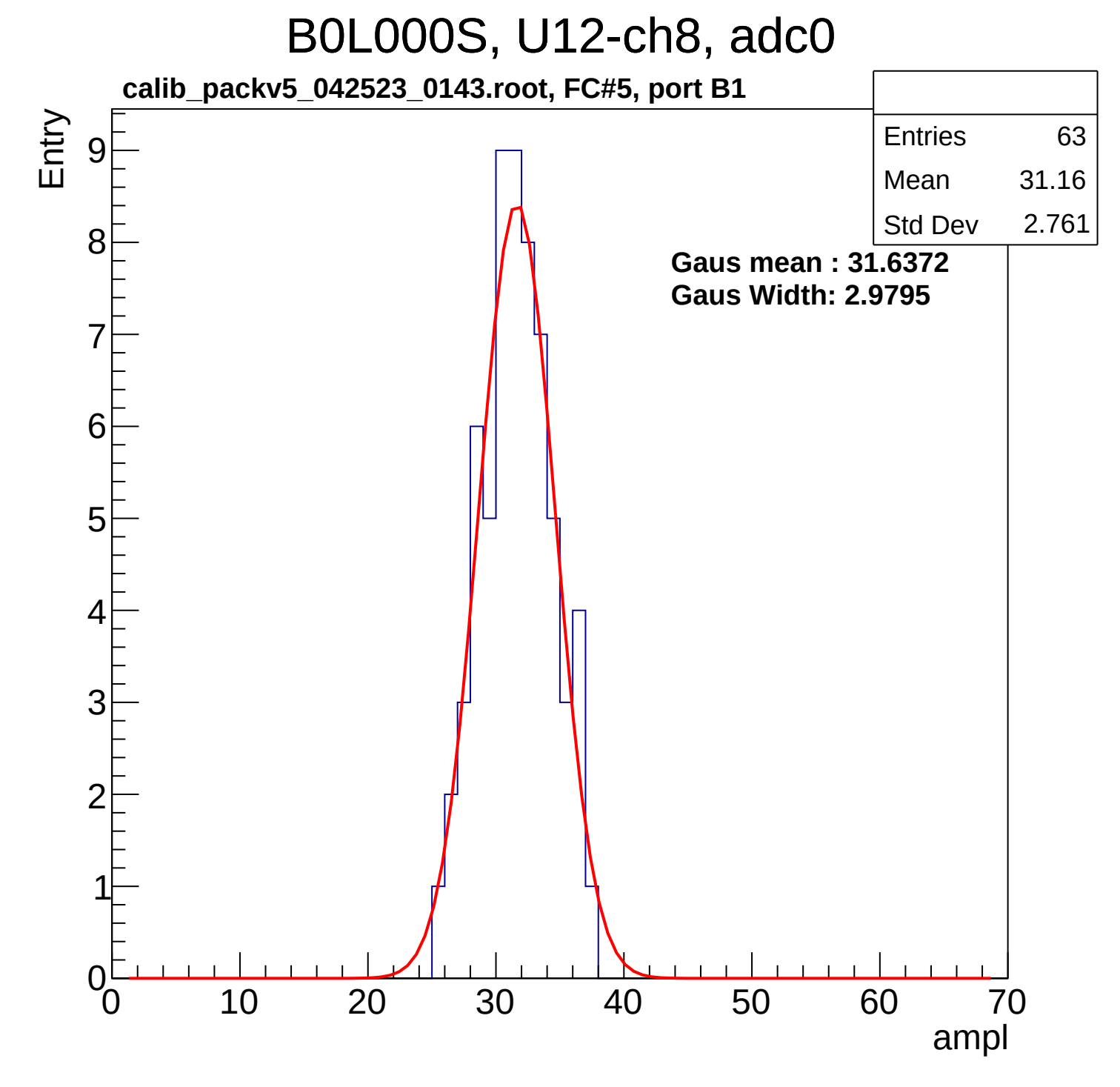
ampl


B0L000S, U12-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 31.16
Std Dev 2.761
Gaus mean : 31.6372
Gaus Width: 2.9795
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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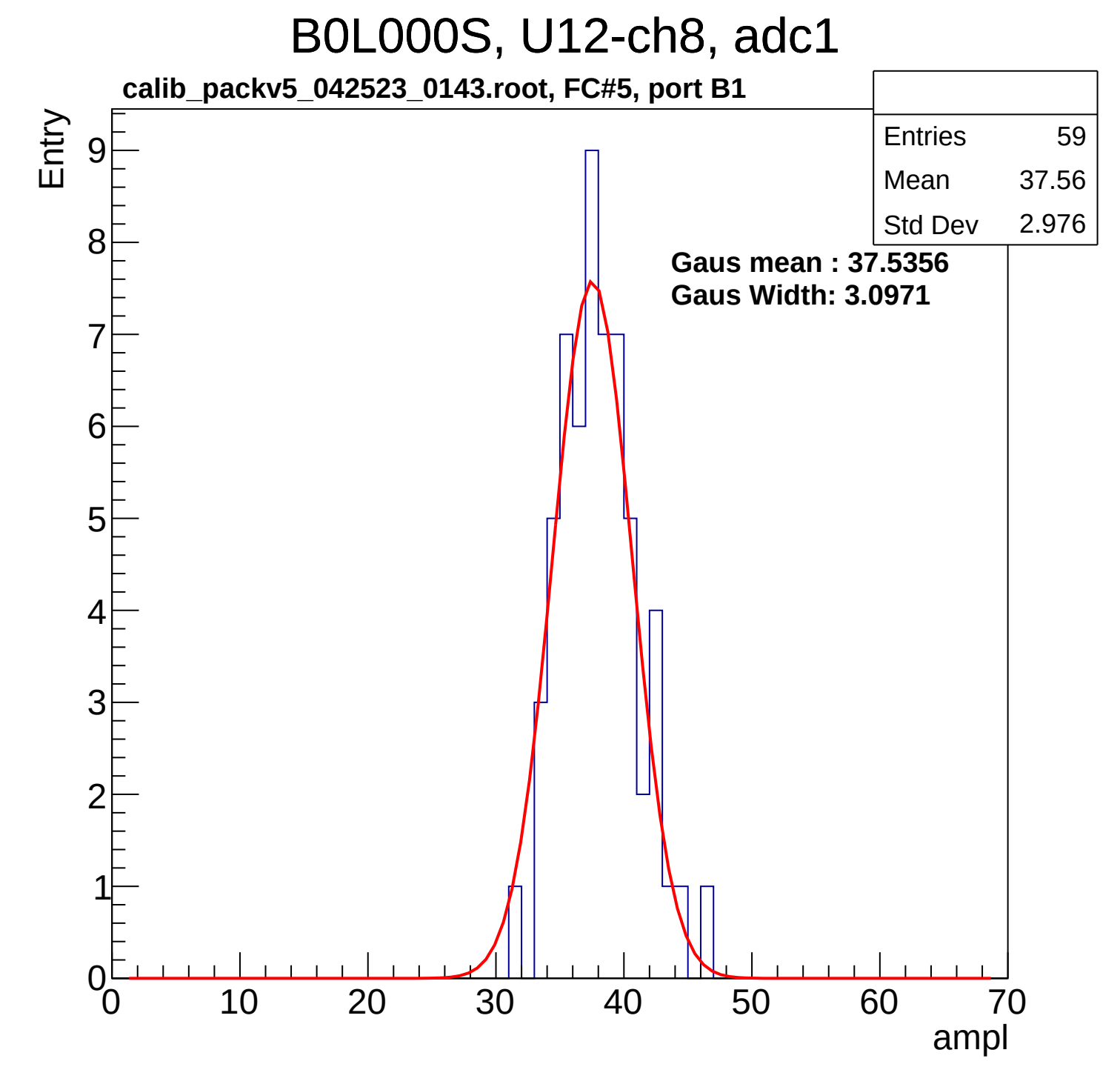
B0L000S, U12-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.56
Std Dev 2.976
Gaus mean : 37.5356
Gaus Width: 3.0971
Entry
ampl
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4
5
6
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0
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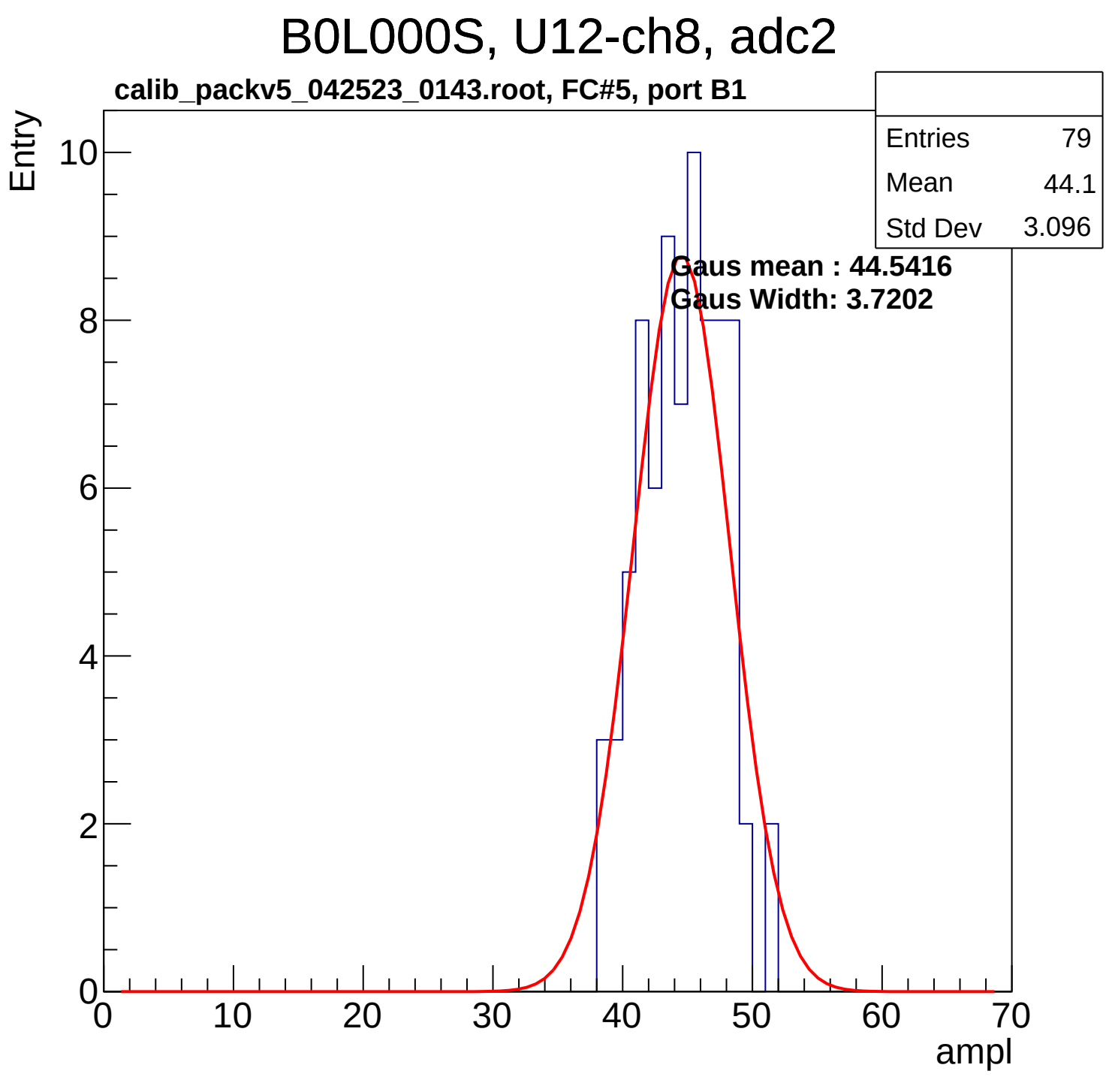
B0L000S, U12-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 44.1
Std Dev 3.096
Gaus mean : 44.5416
Gaus Width: 3.7202
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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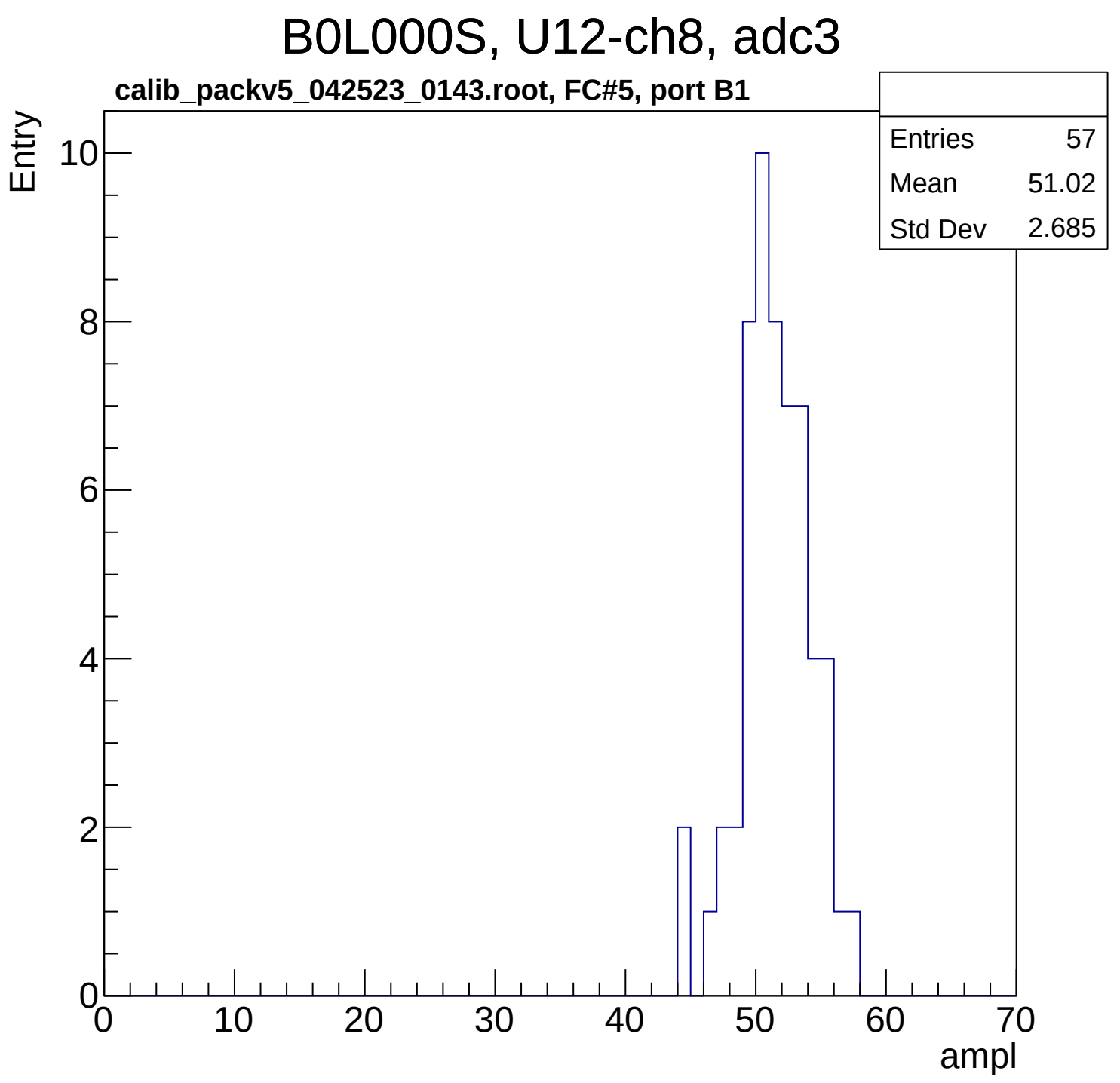
B0L000S, U12-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 51.02
Std Dev 2.685
Entry
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2
4
6
8
10
0
10
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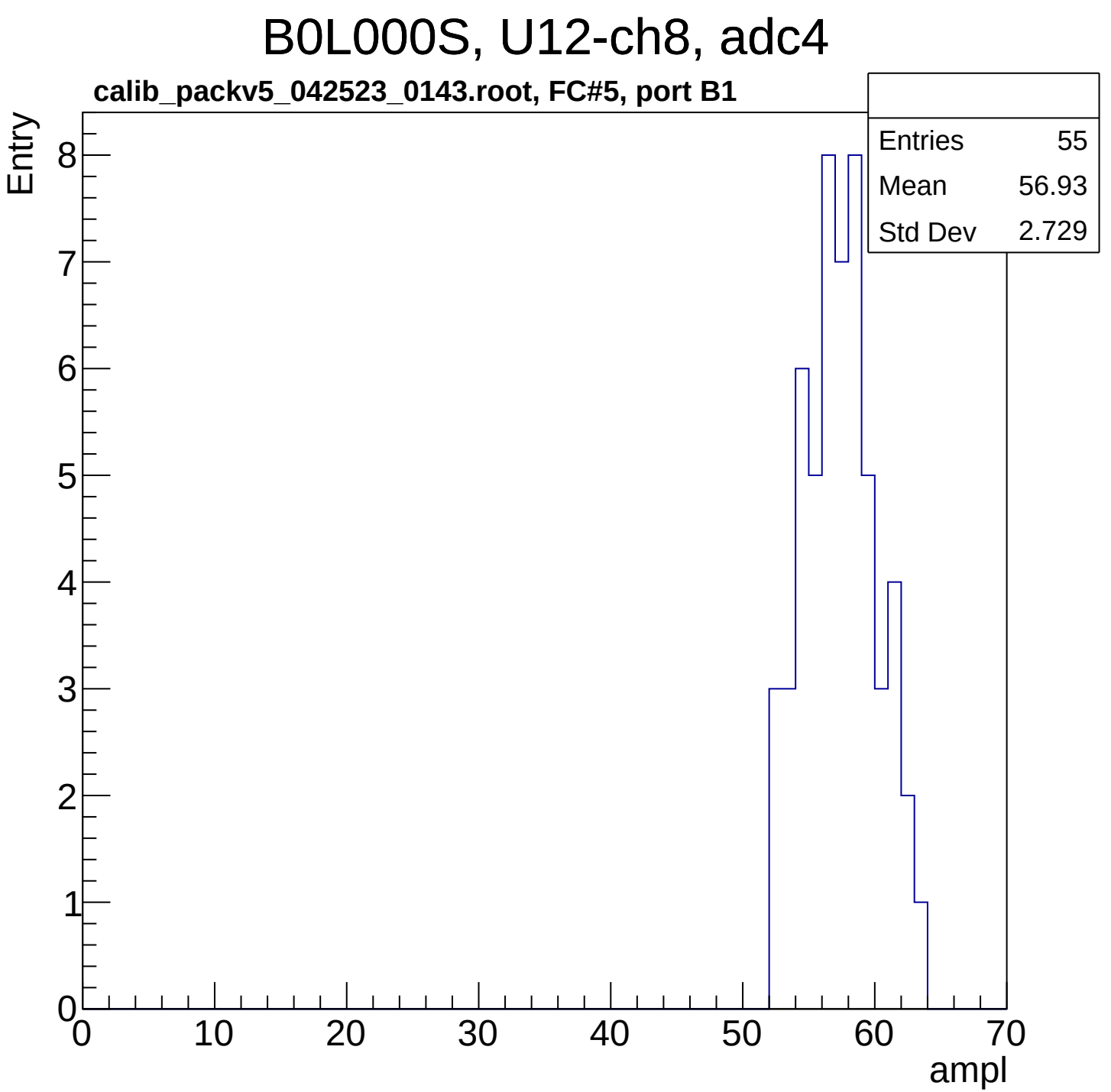
70
ampl


B0L000S, U12-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.93
Std Dev 2.729
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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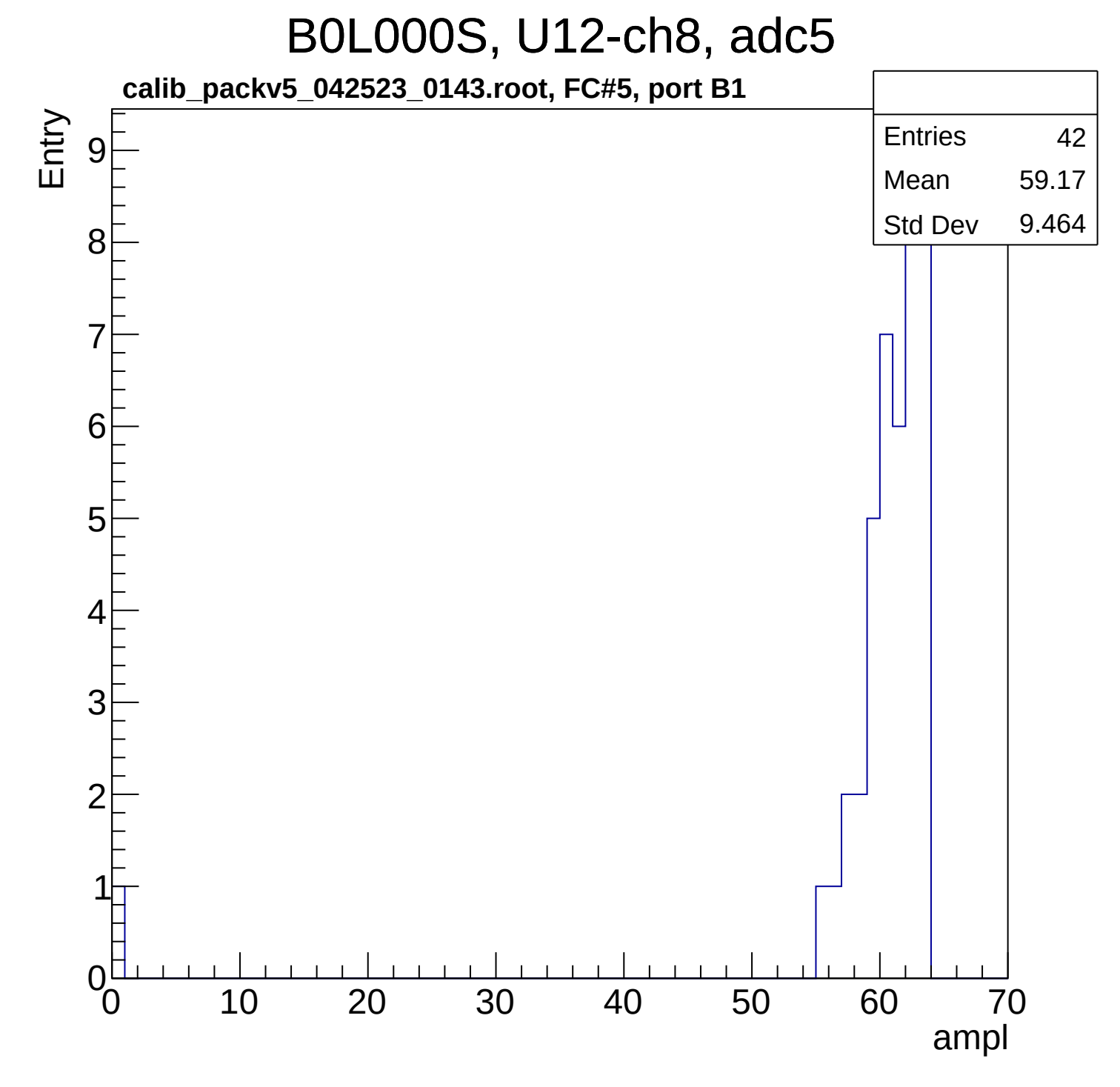
B0L000S, U12-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.17
Std Dev 9.464
Entry
ampl
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2
3
4
5
6
7
8
9
0
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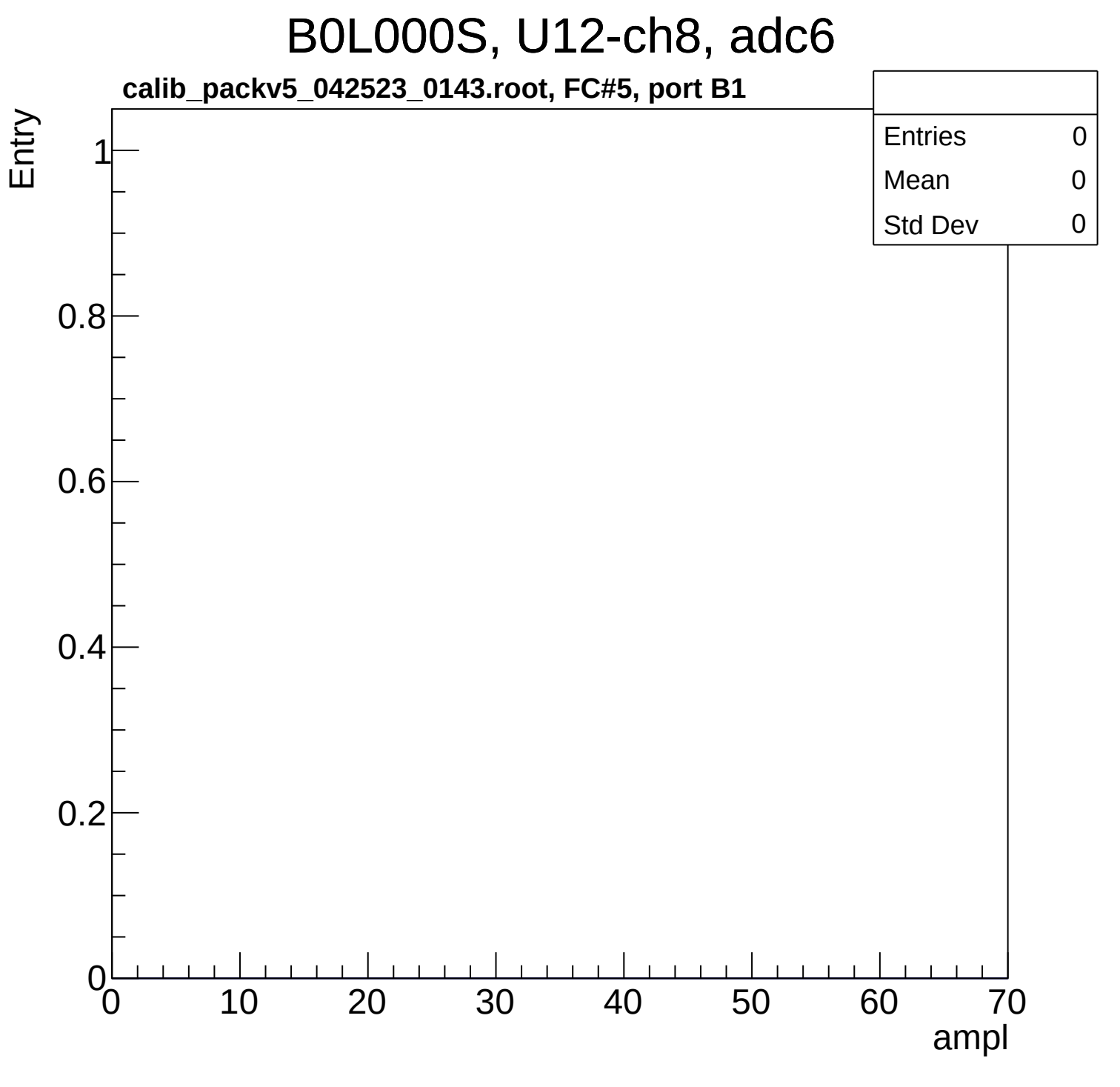
B0L000S, U12-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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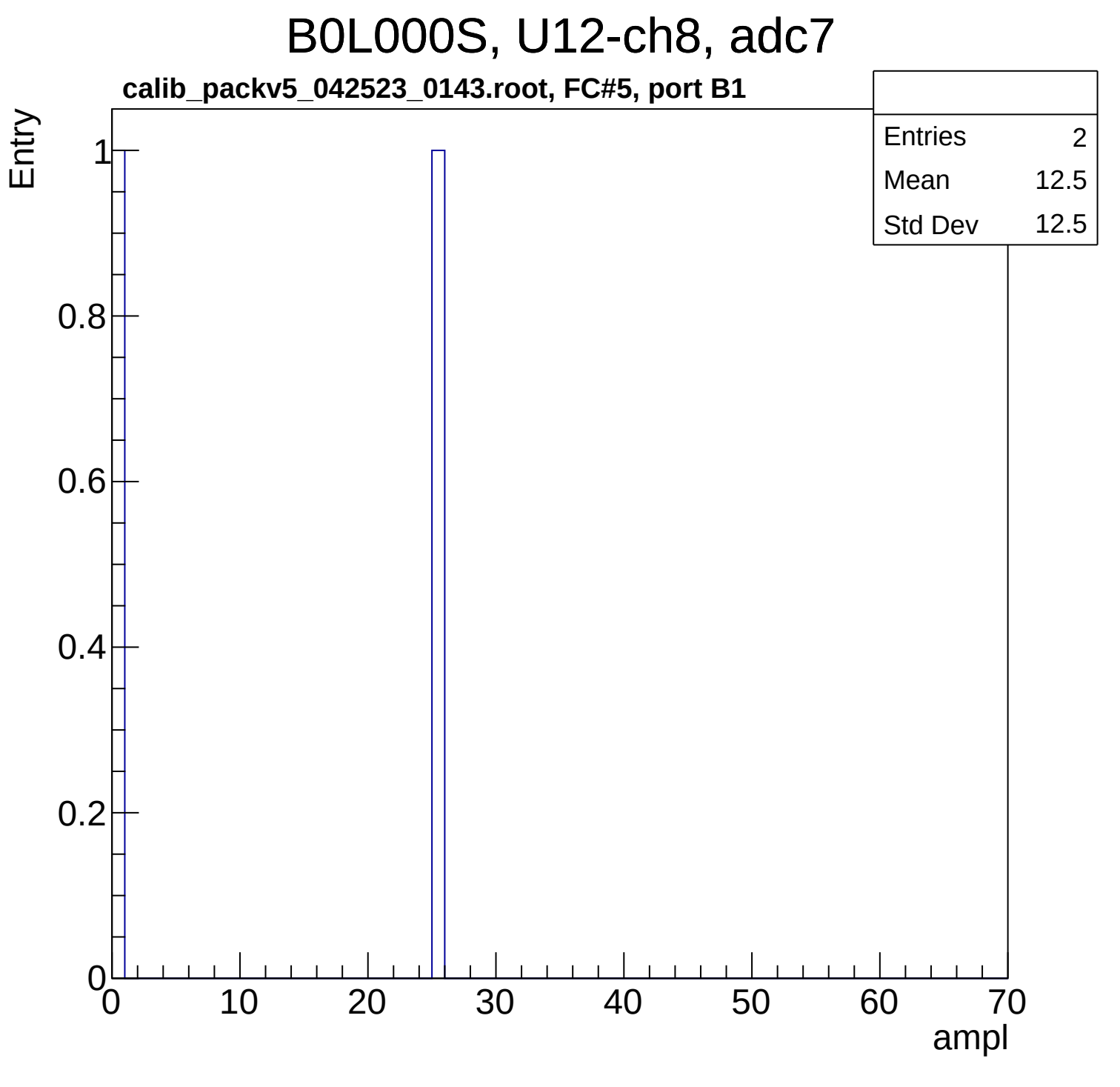
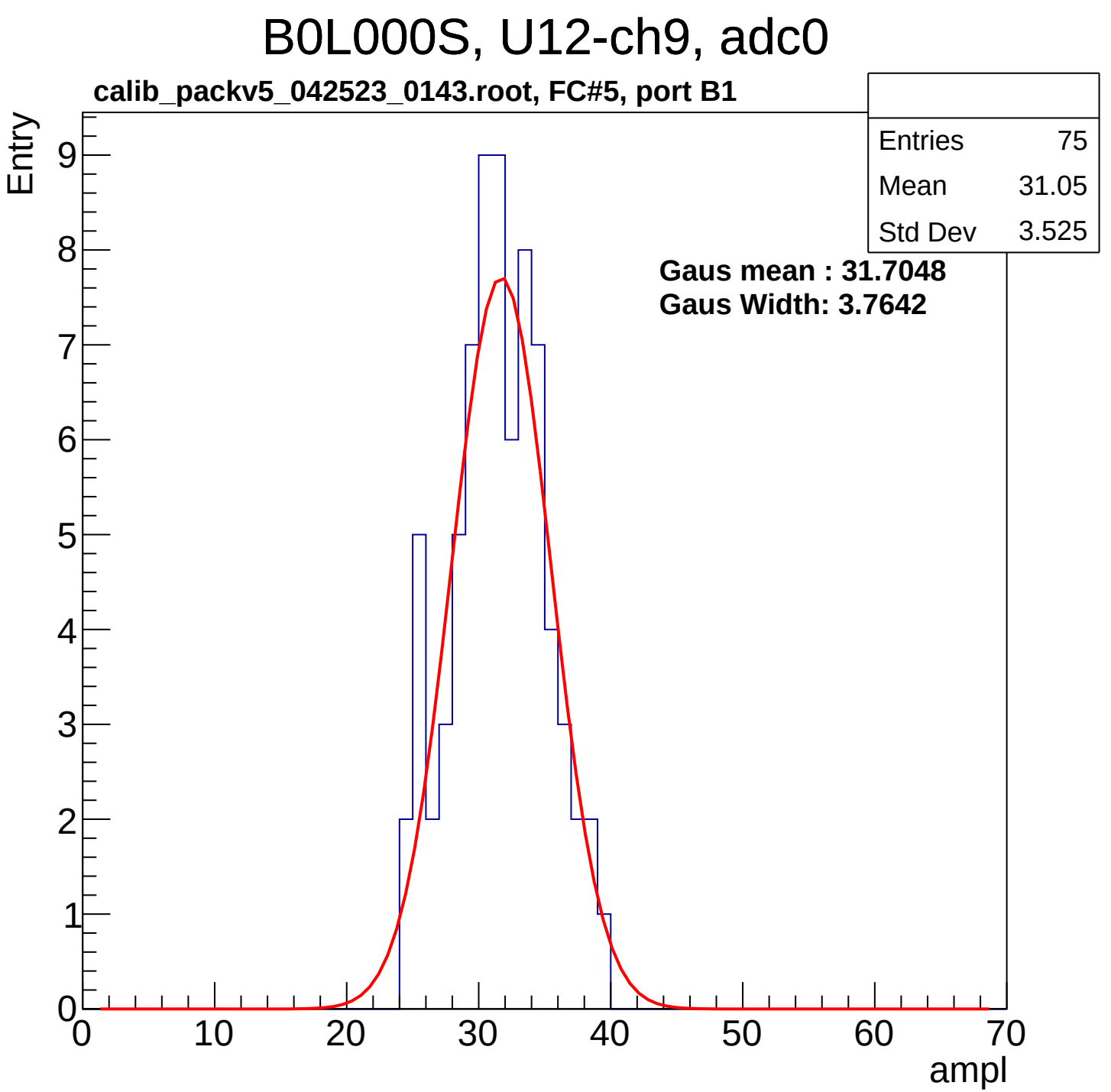
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 31.05
Std Dev 3.525
Gaus mean : 31.7048
Gaus Width: 3.7642
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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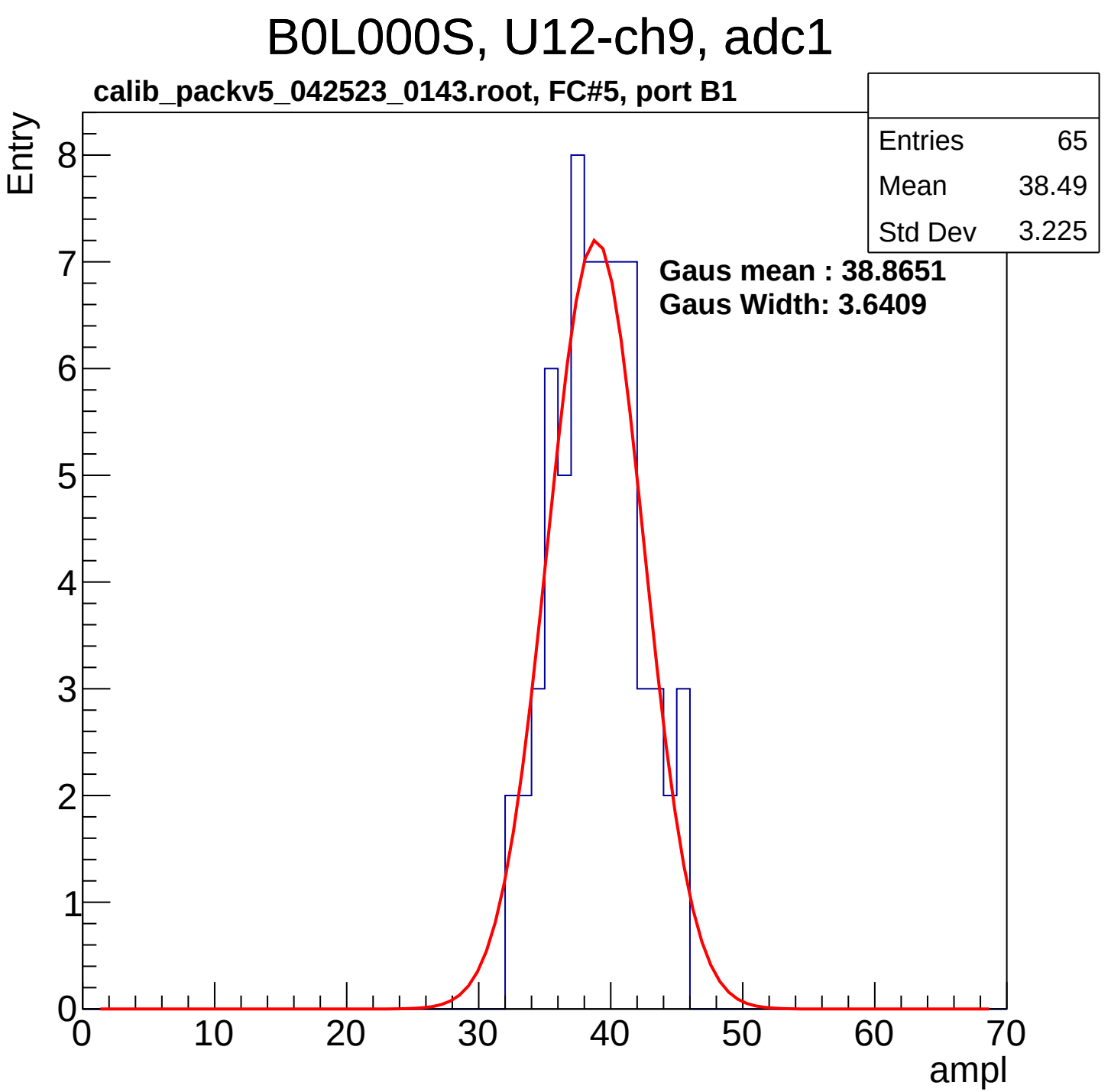
B0L000S, U12-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.49
Std Dev 3.225
Gaus mean : 38.8651
Gaus Width: 3.6409
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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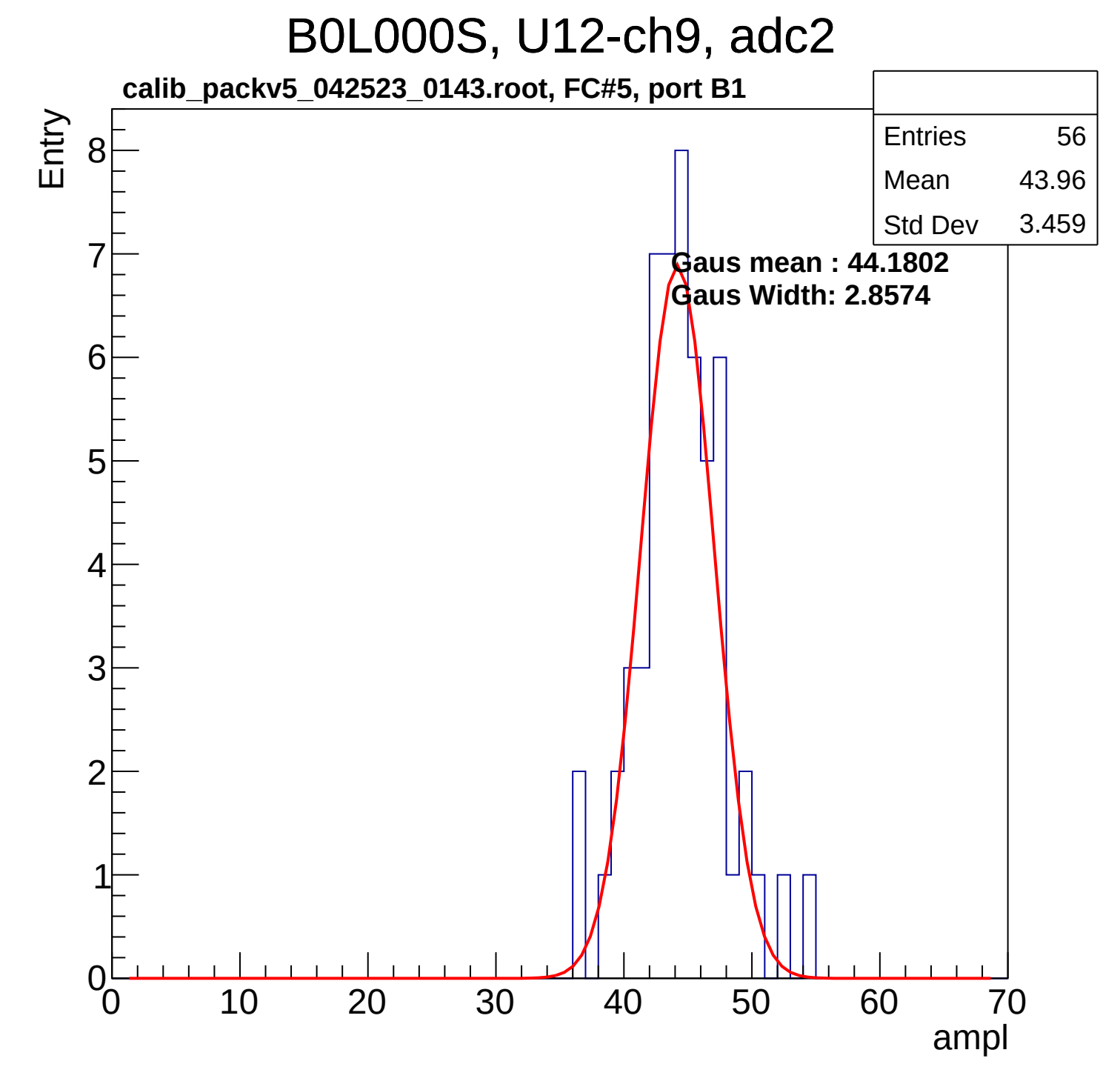
B0L000S, U12-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.96
Std Dev 3.459
Gaus mean : 44.1802
Gaus Width: 2.8574
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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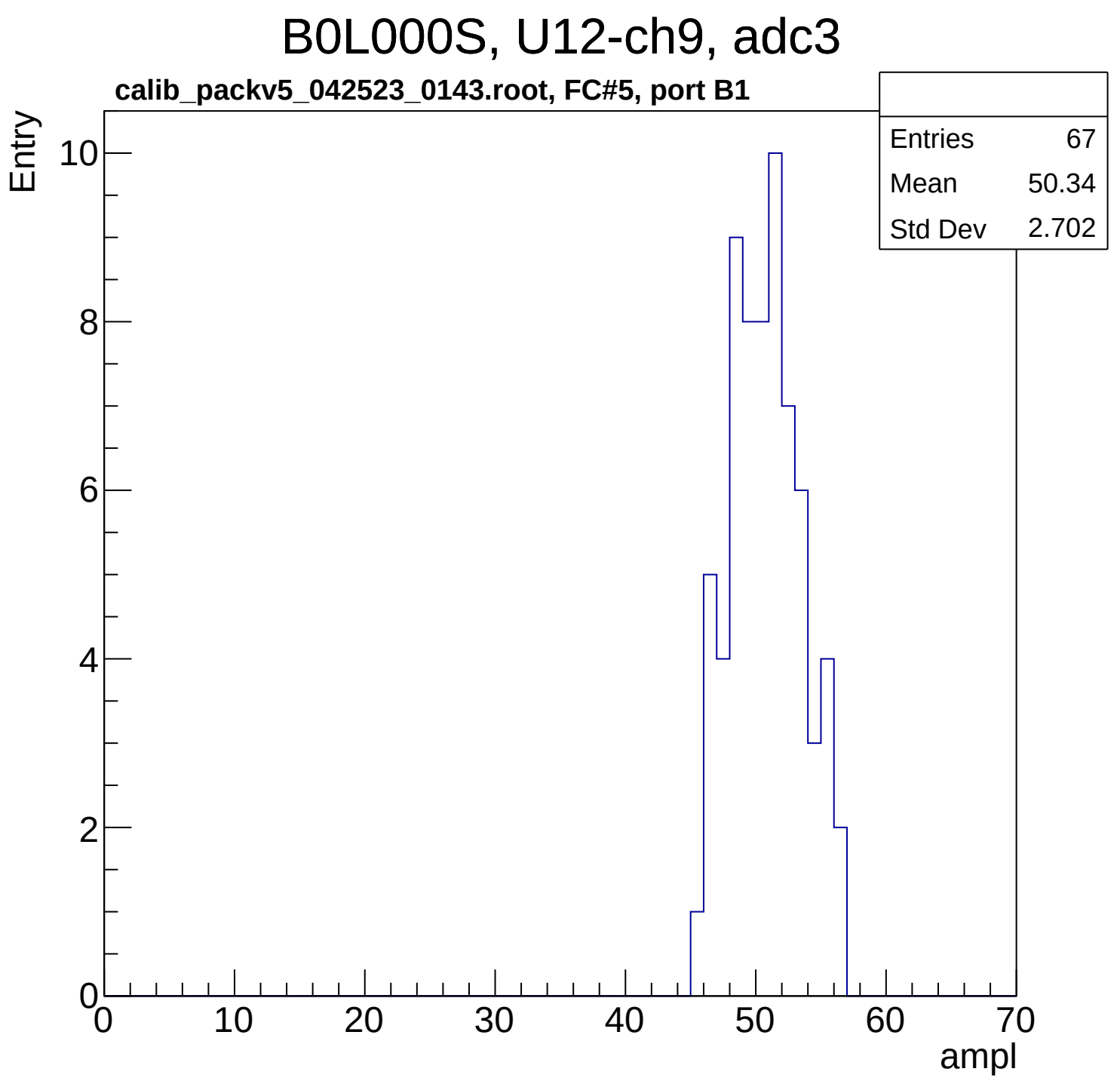
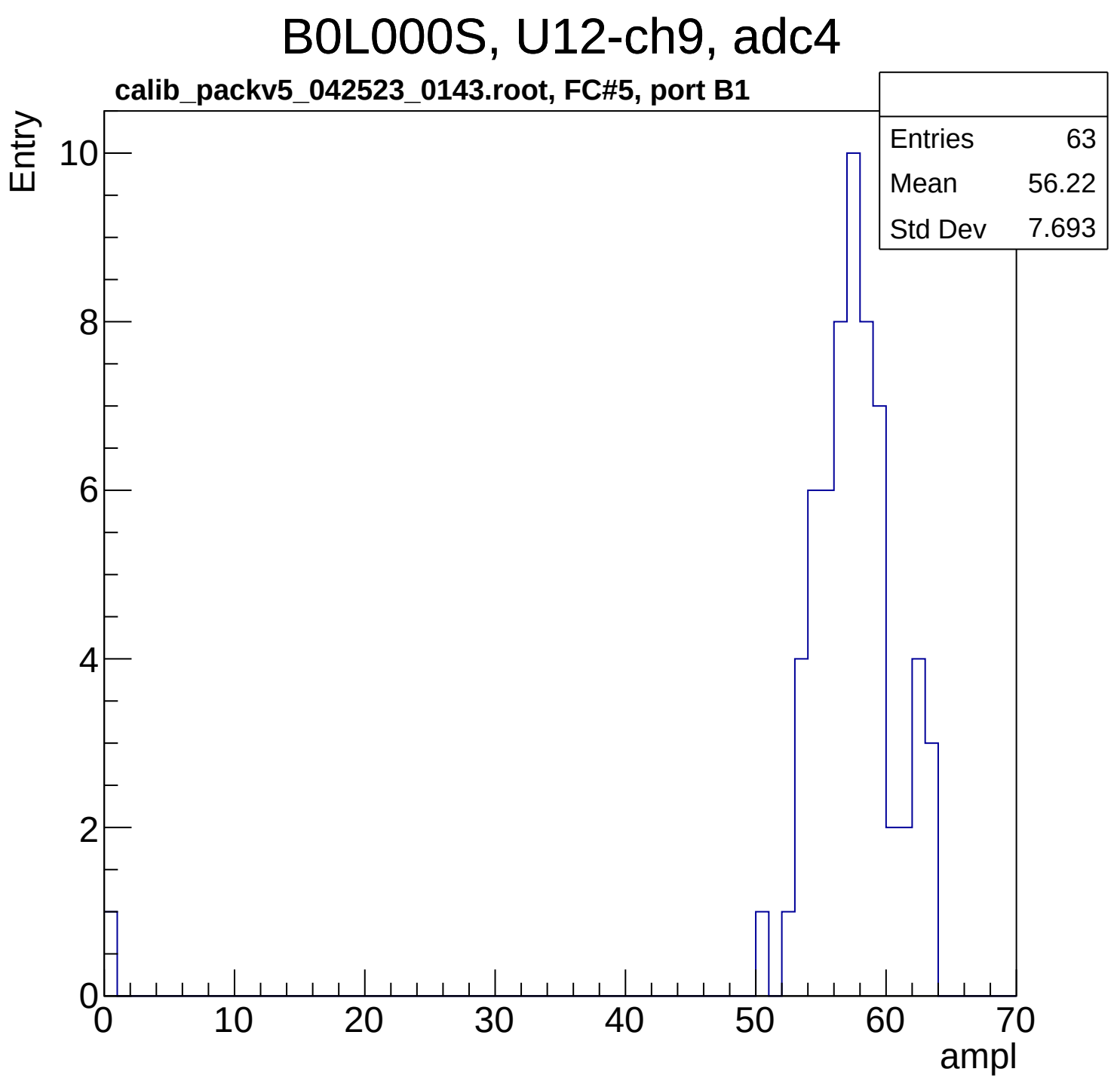

B0L000S, U12-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.34
Std Dev 2.702
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.22
Std Dev 7.693
Entry
ampl
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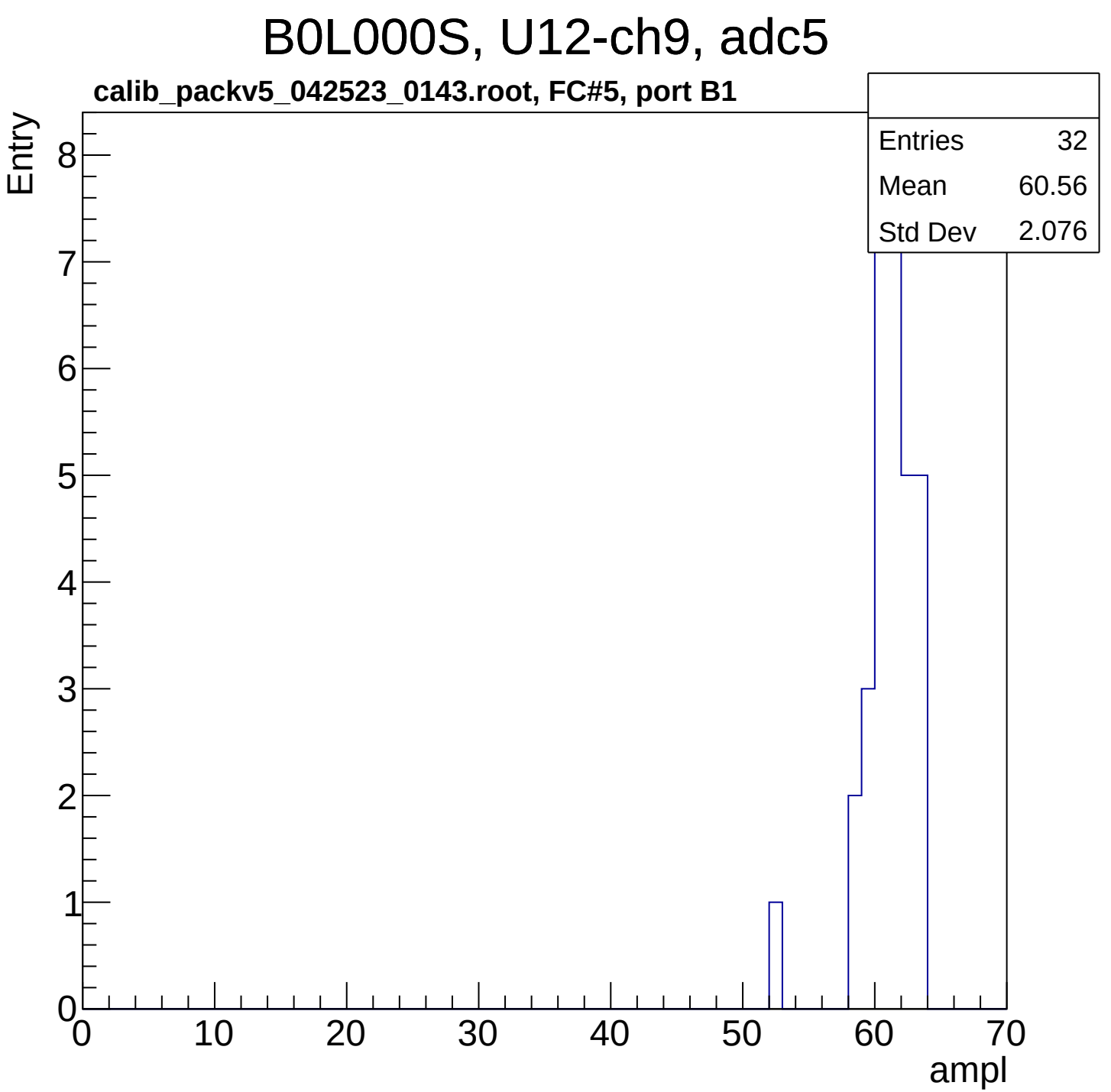
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.56
Std Dev 2.076
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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30
40
50
60
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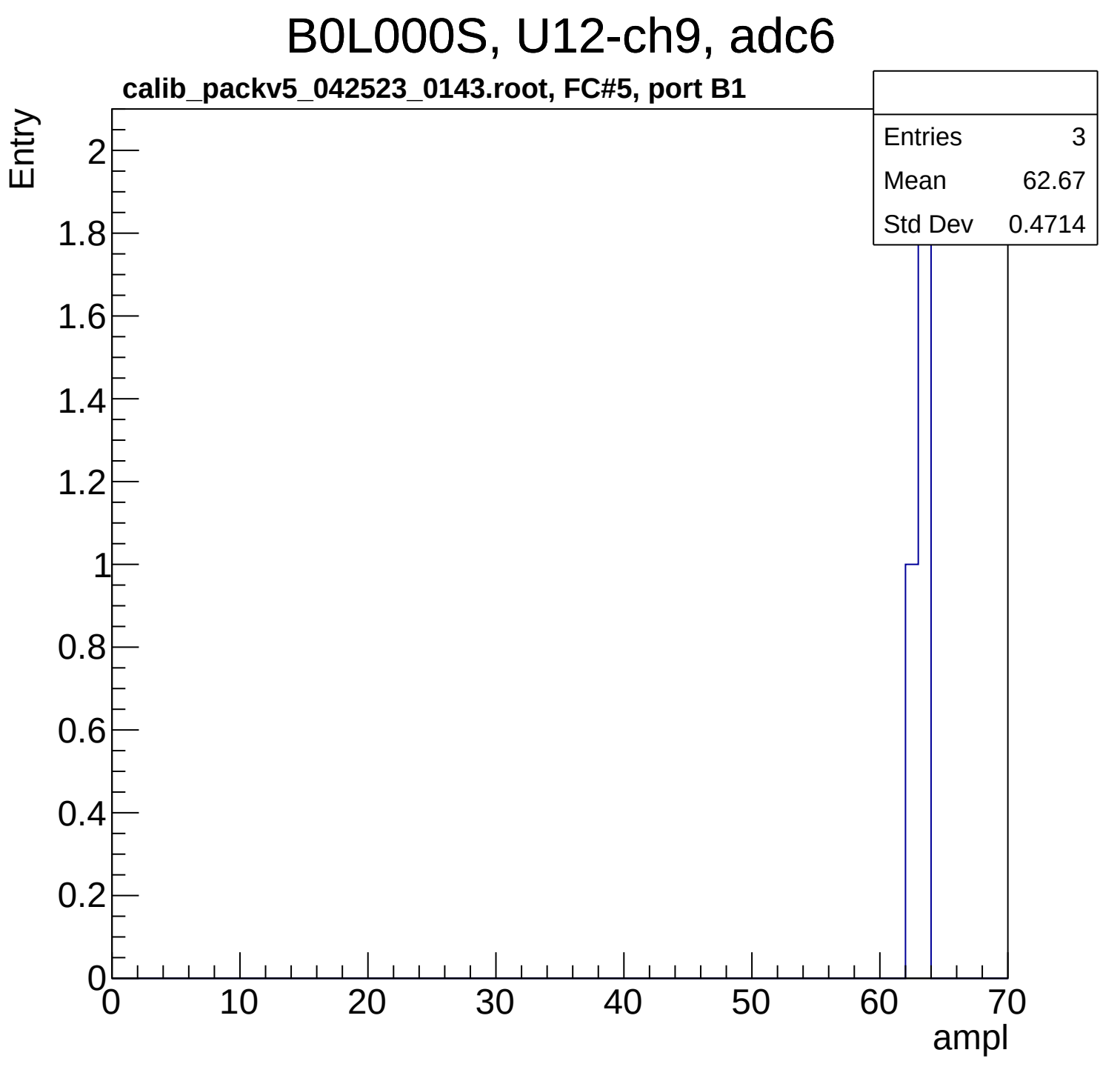
B0L000S, U12-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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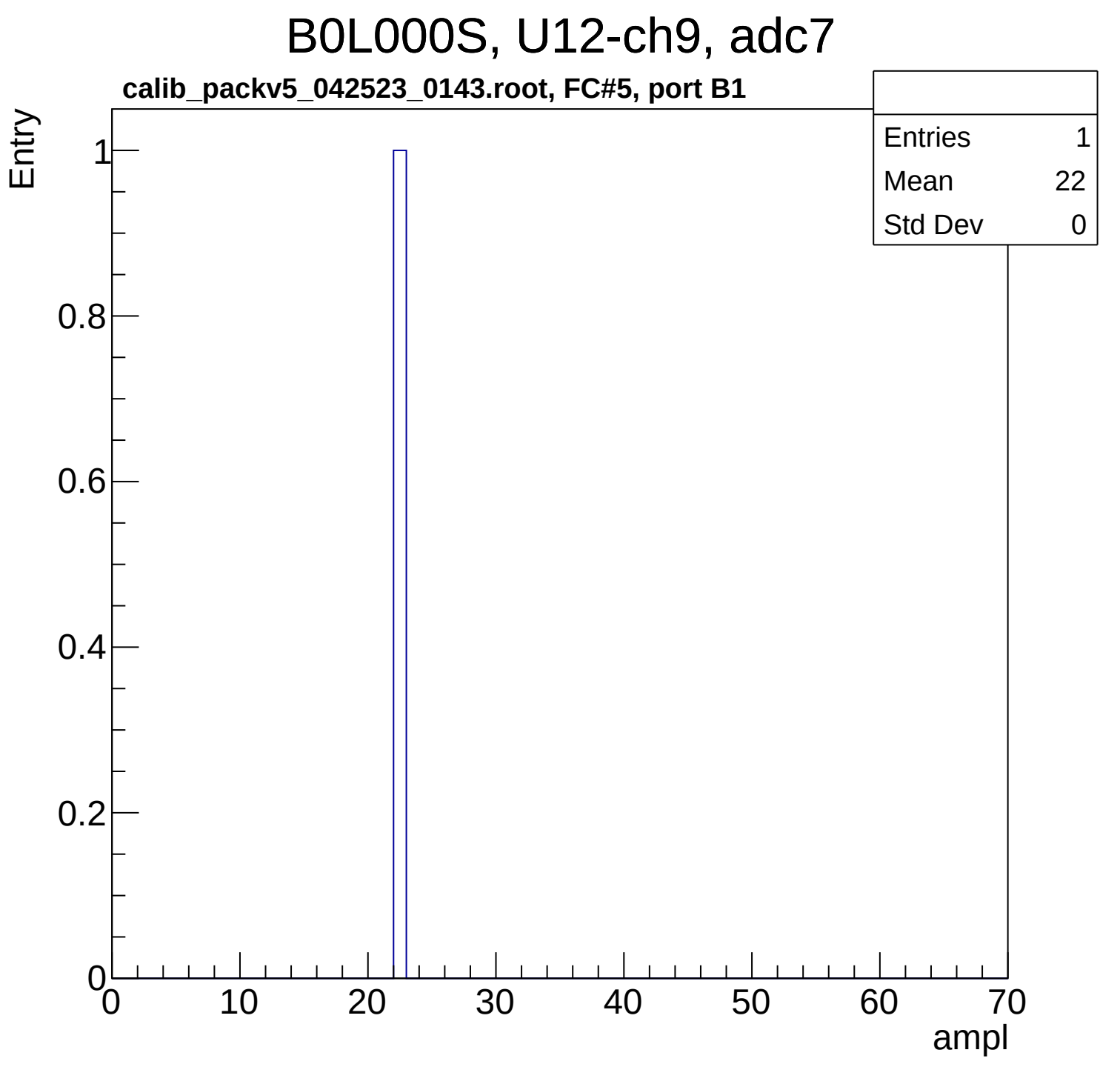
40
50
60
70


B0L000S, U12-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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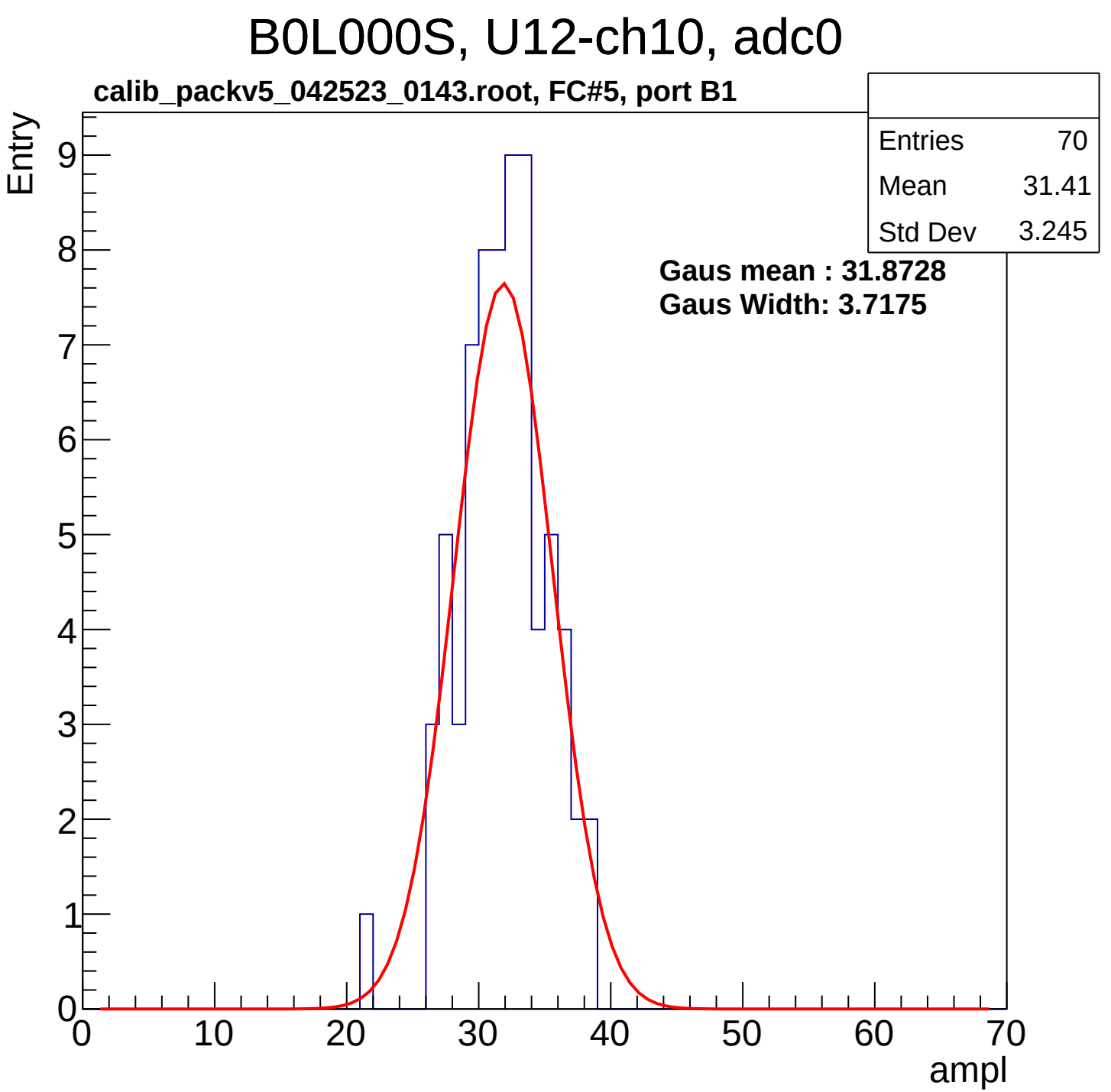
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 31.41
Std Dev 3.245
Gaus mean : 31.8728
Gaus Width: 3.7175
Entry
ampl
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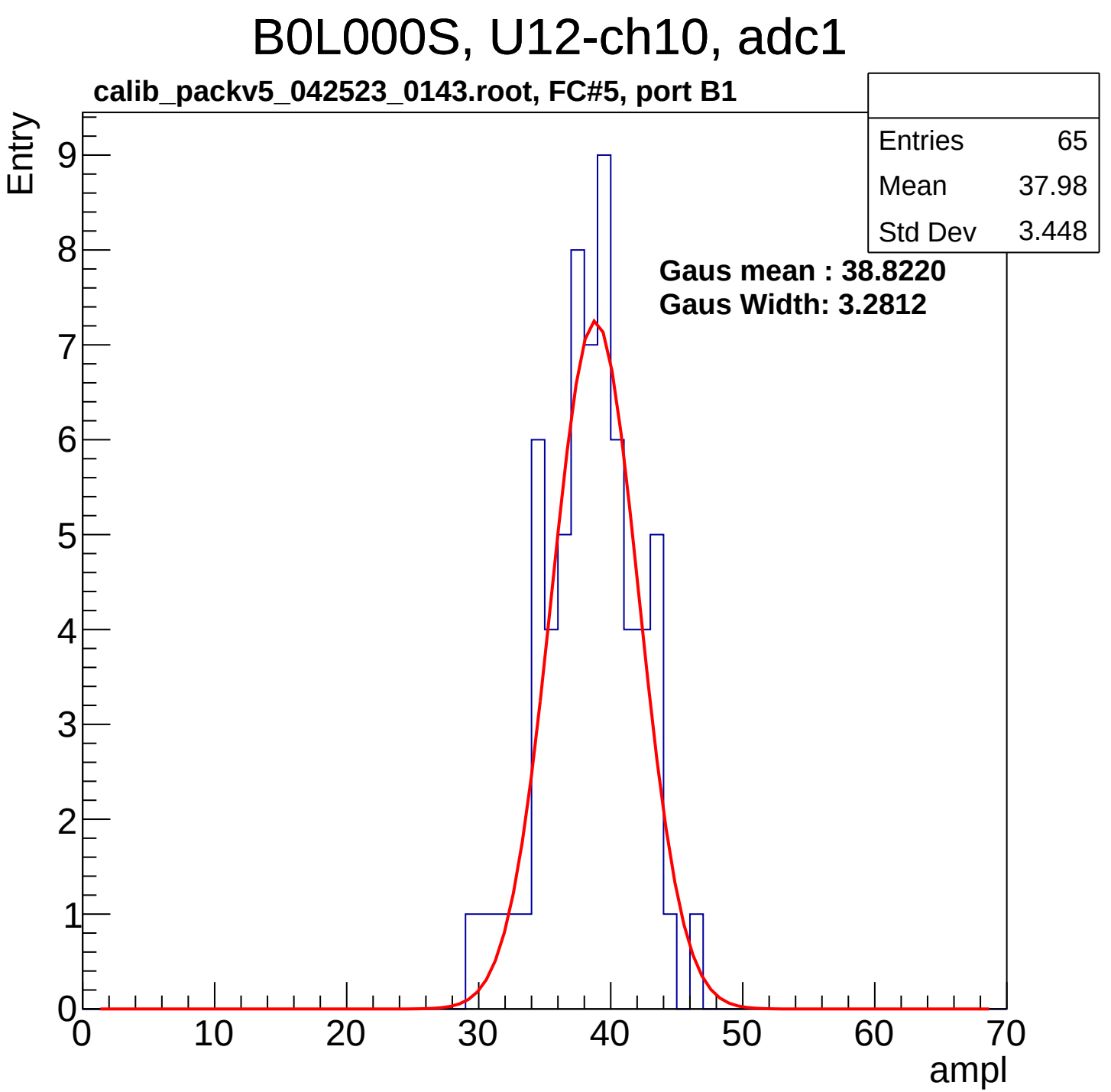
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.98
Std Dev 3.448
Gaus mean : 38.8220
Gaus Width: 3.2812
Entry
ampl
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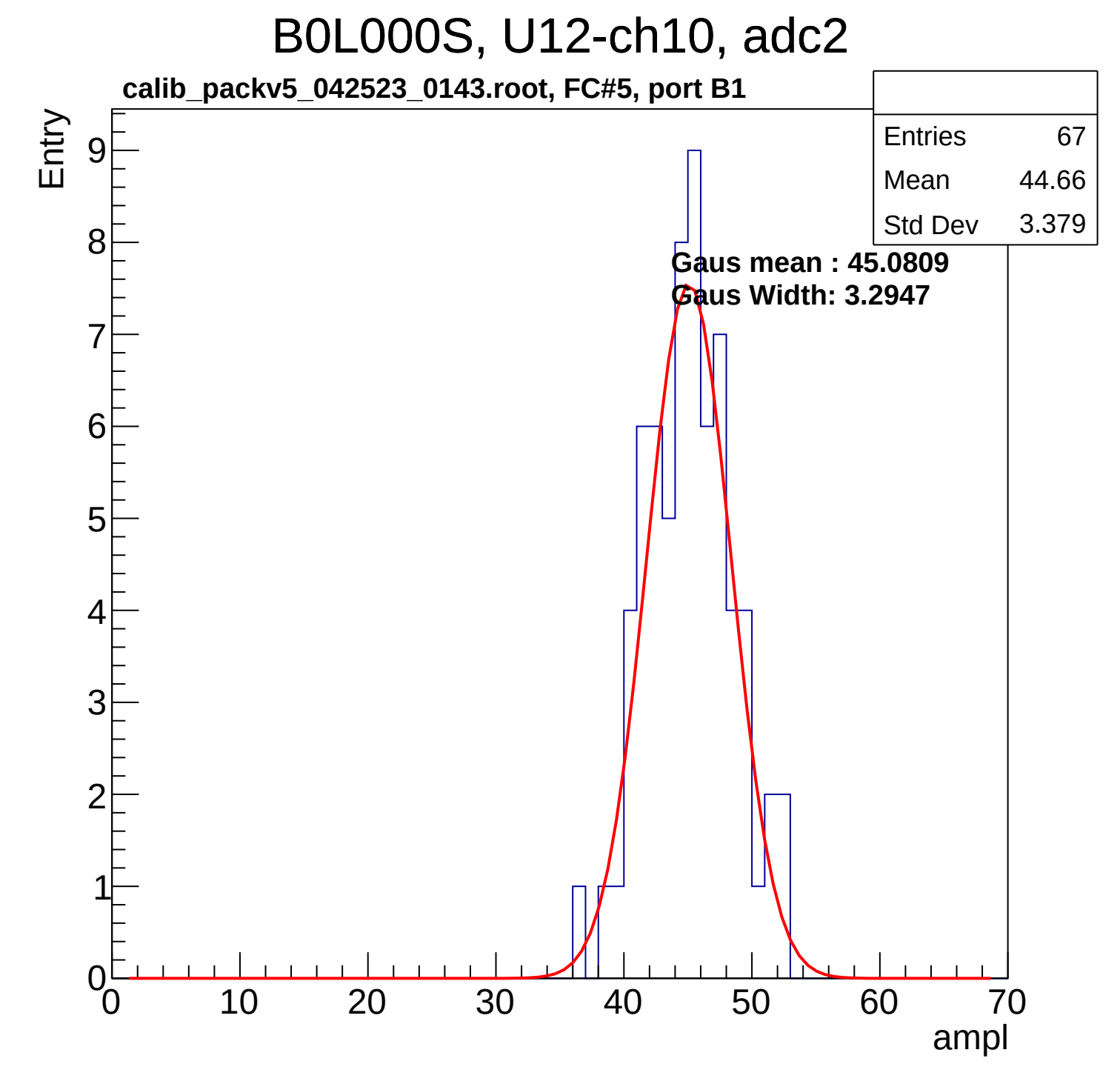
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.66
Std Dev 3.379
Gaus mean : 45.0809
Gaus Width: 3.2947
Entry
ampl
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3
4
5
6
7
8
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0
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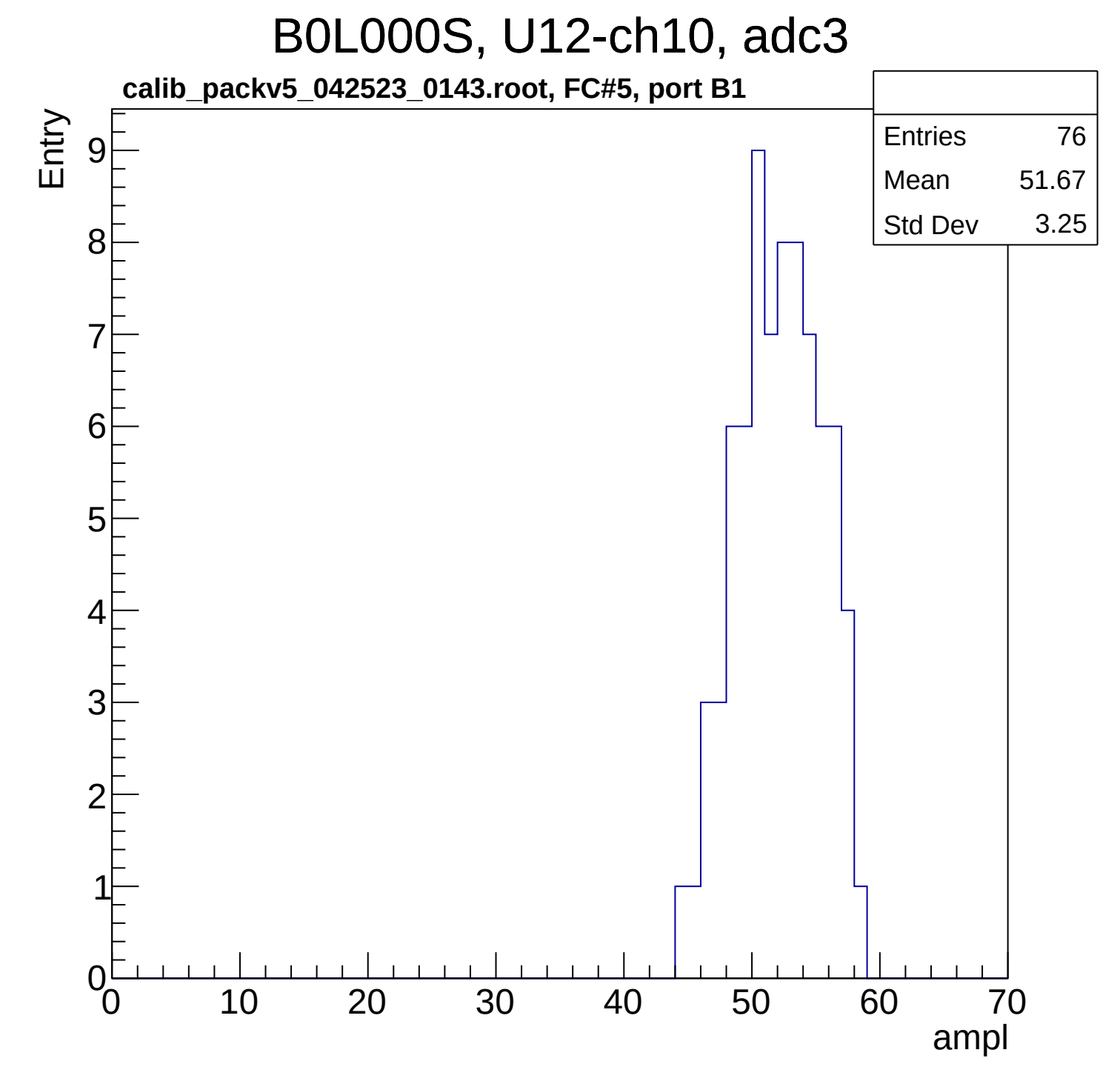
B0L000S, U12-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 51.67
Std Dev 3.25
Entry
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3
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5
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7
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9
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10
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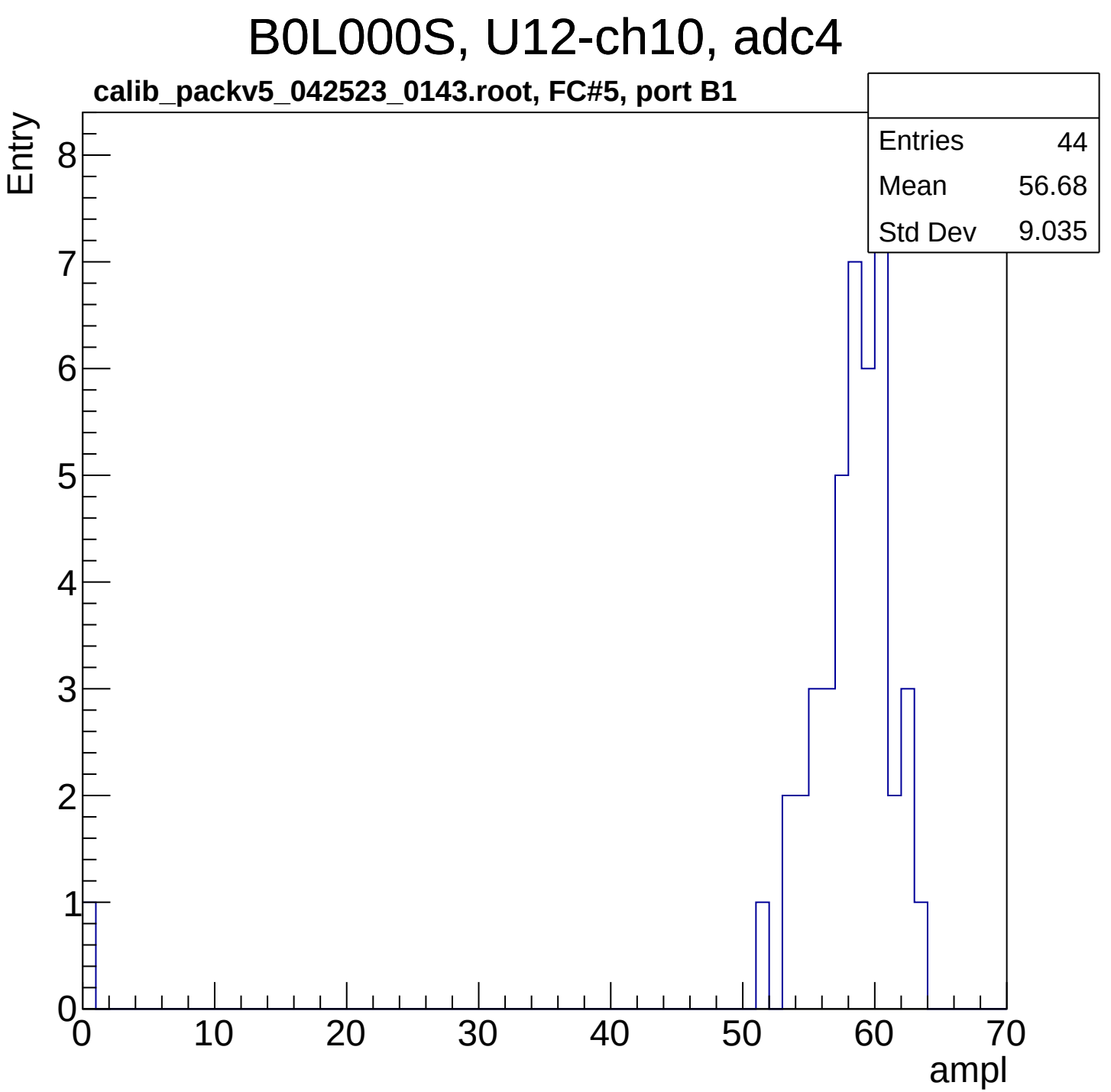
60
70
ampl


B0L000S, U12-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 56.68
Std Dev 9.035
Entry
ampl
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2
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0
10
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60
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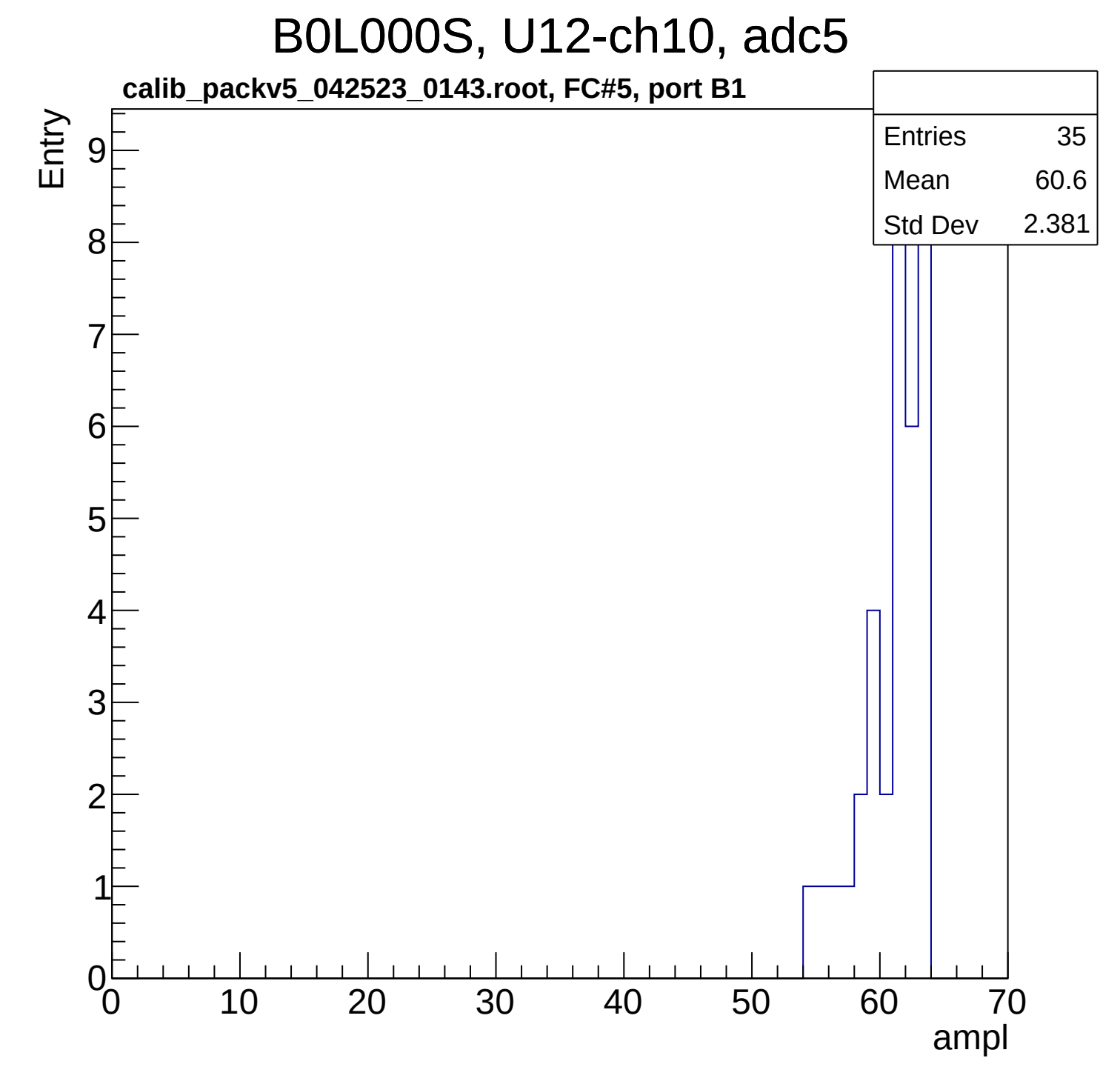
B0L000S, U12-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.6
Std Dev 2.381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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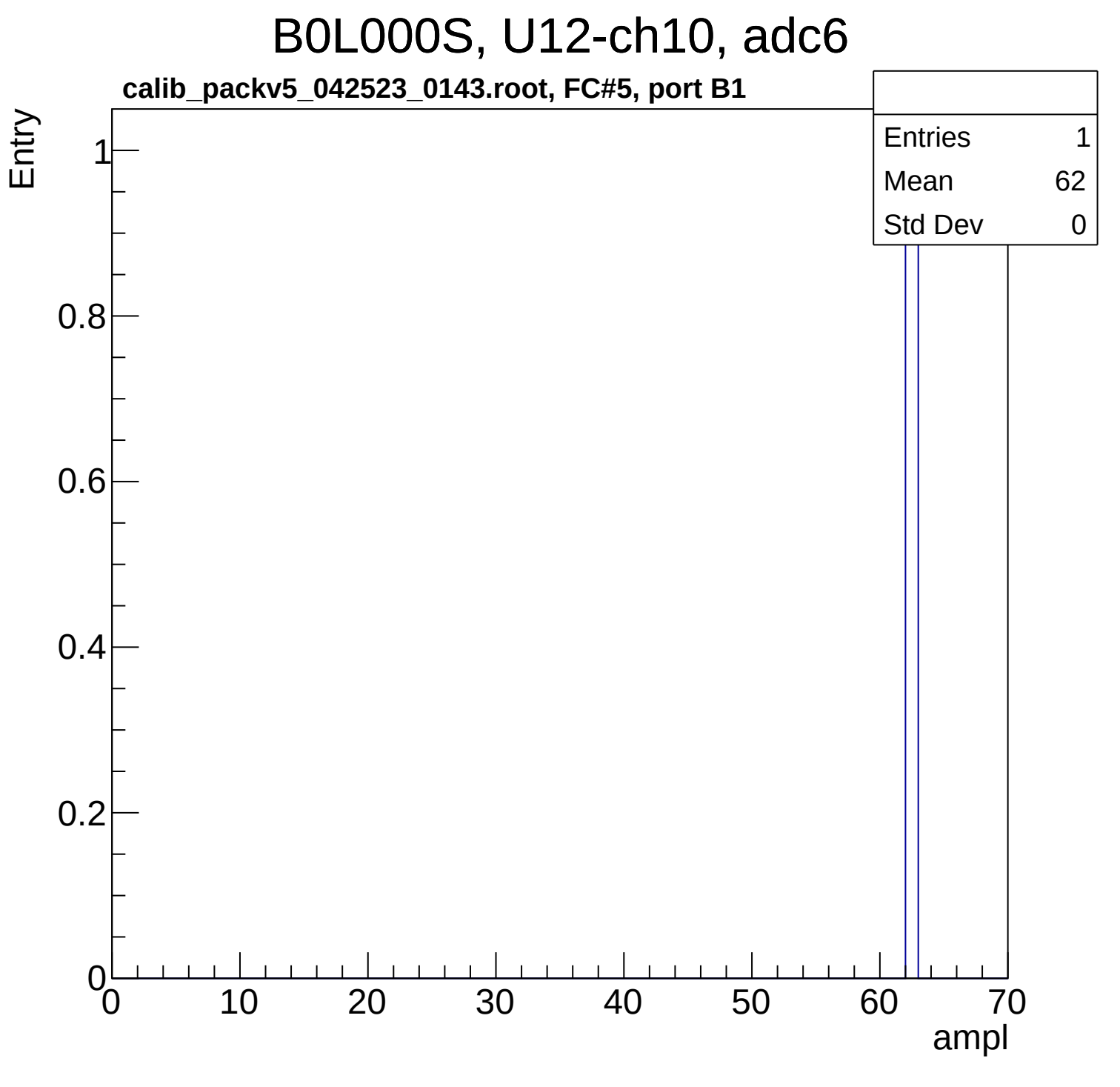
B0L000S, U12-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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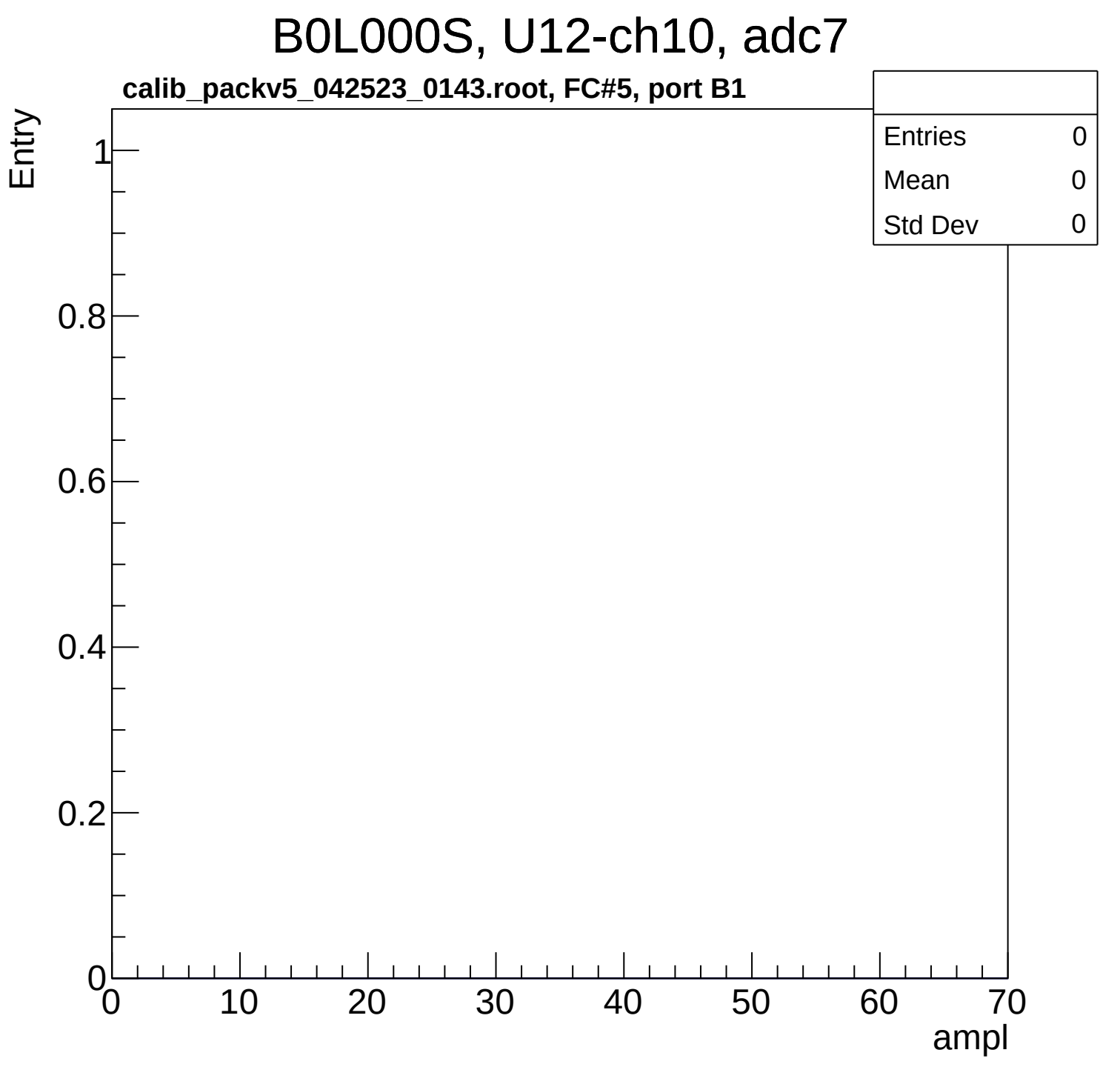
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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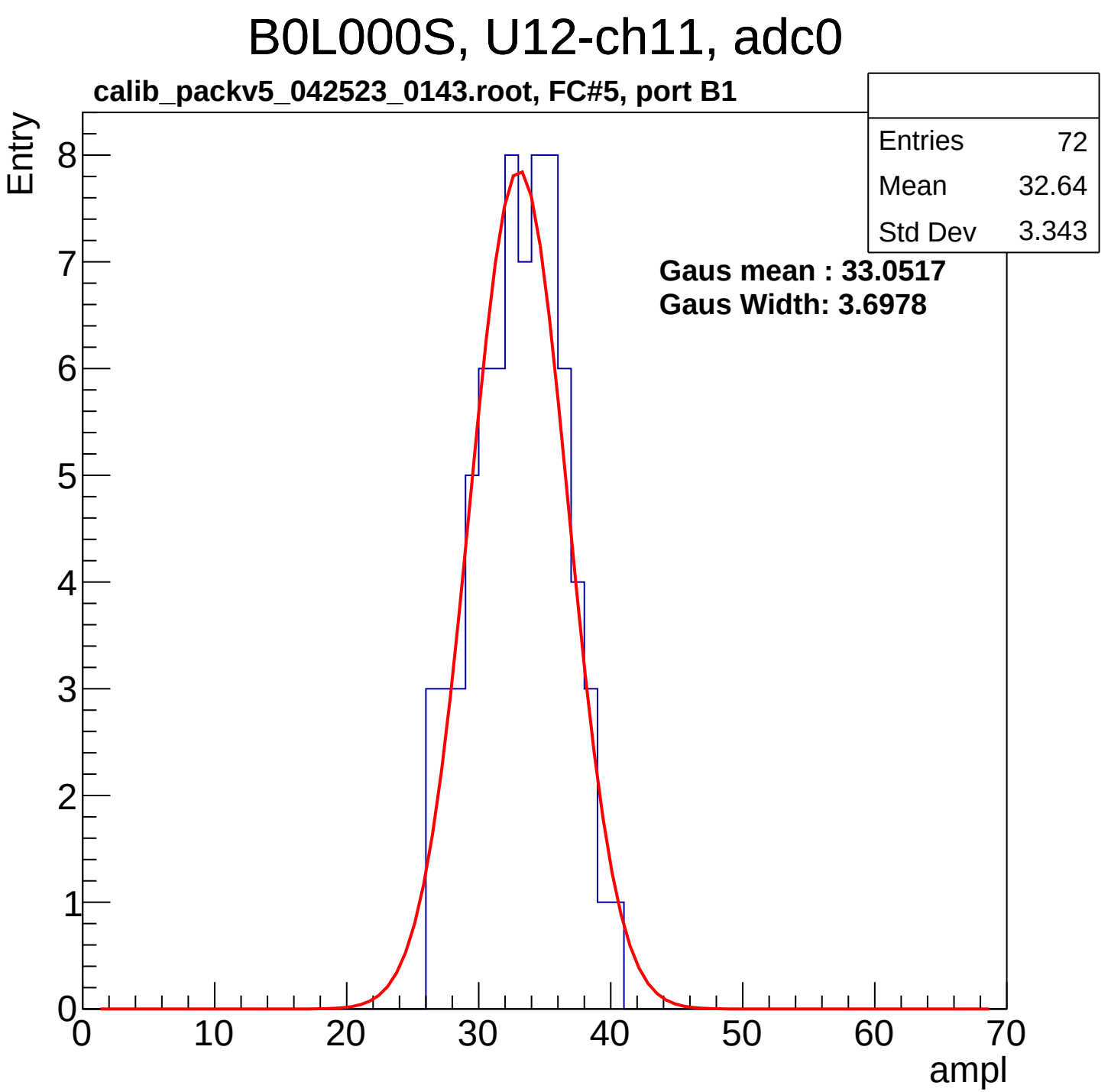
50
60
70
ampl


B0L000S, U12-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 32.64
Std Dev 3.343
Gaus mean : 33.0517
Gaus Width: 3.6978
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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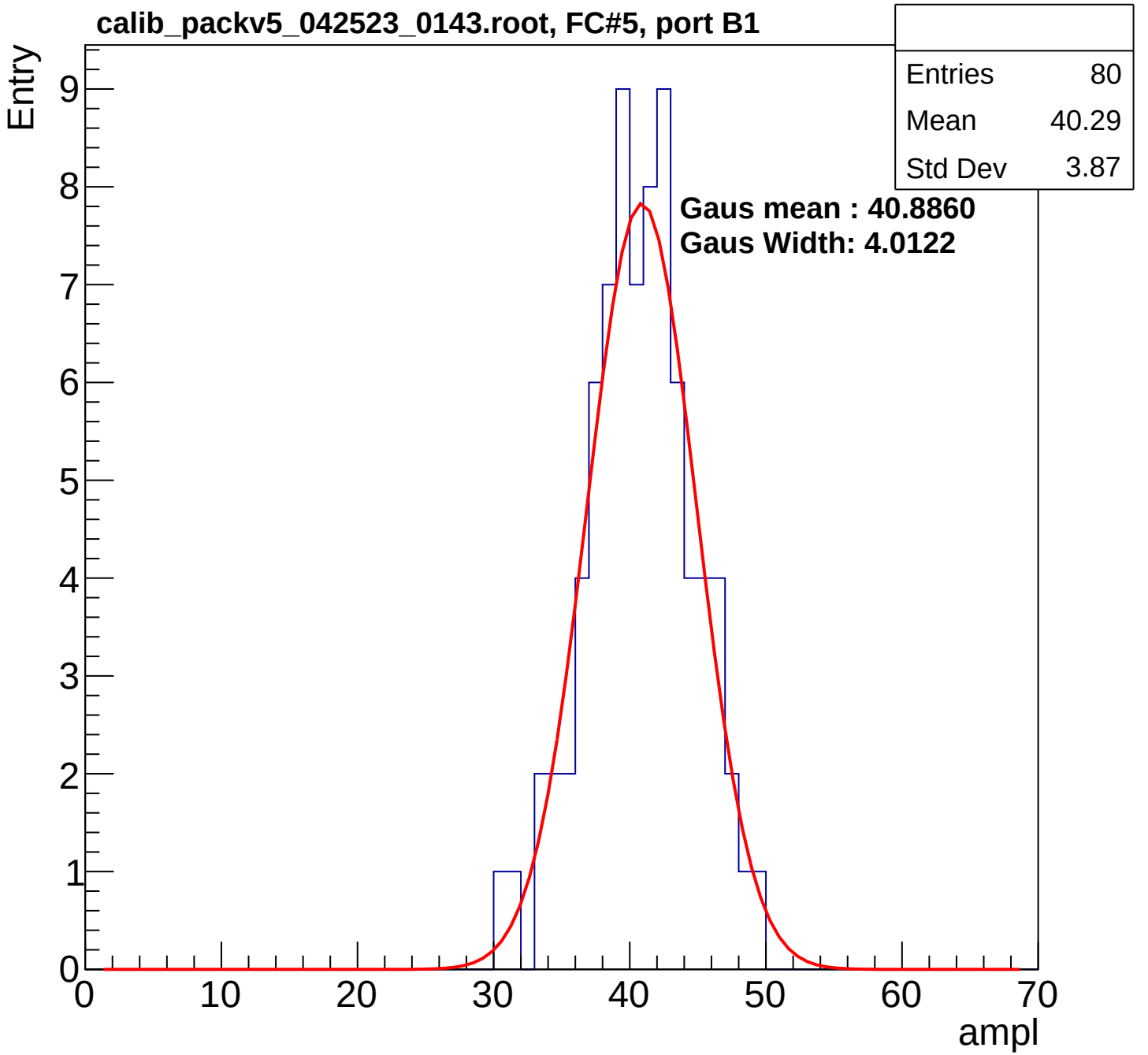
B0L000S, U12-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 40.29
Std Dev 3.87
Gaus mean : 40.8860
Gaus Width: 4.0122
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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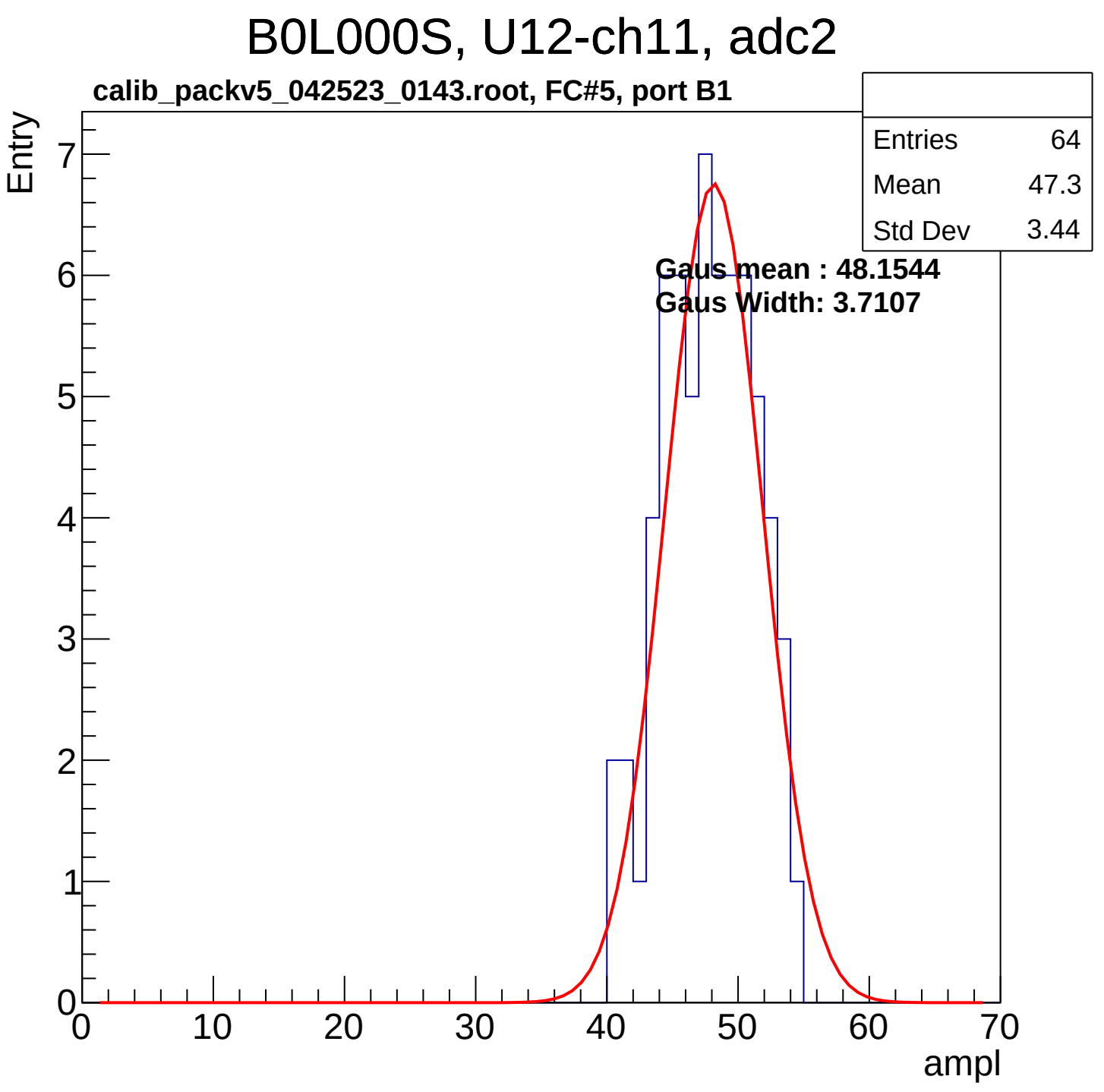
B0L000S, U12-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.3
Std Dev 3.44
Gaus mean : 48.1544
Gaus Width: 3.7107
Entry
ampl
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1
2
3
4
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6
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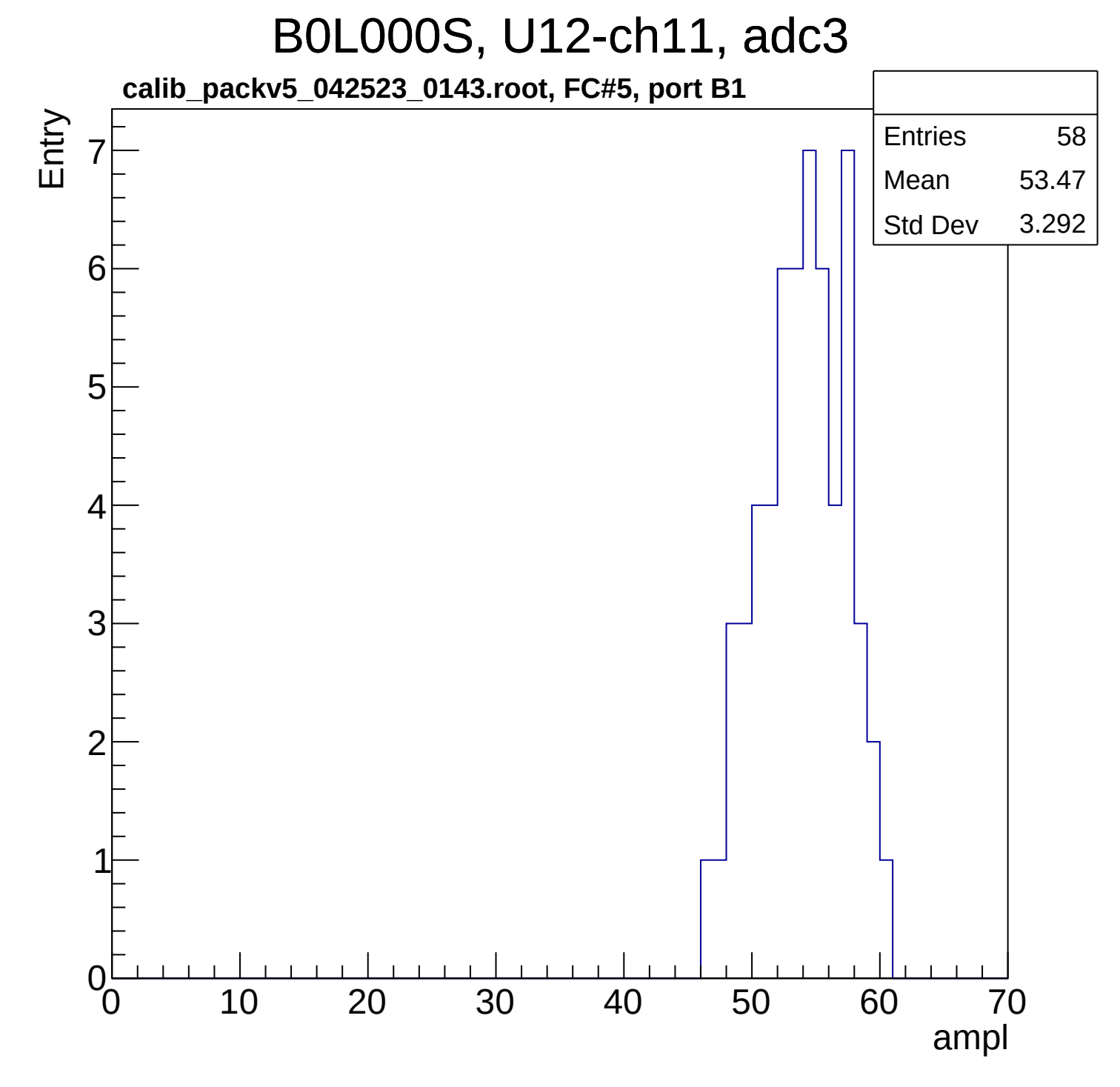
B0L000S, U12-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.47
Std Dev 3.292
Entry
ampl
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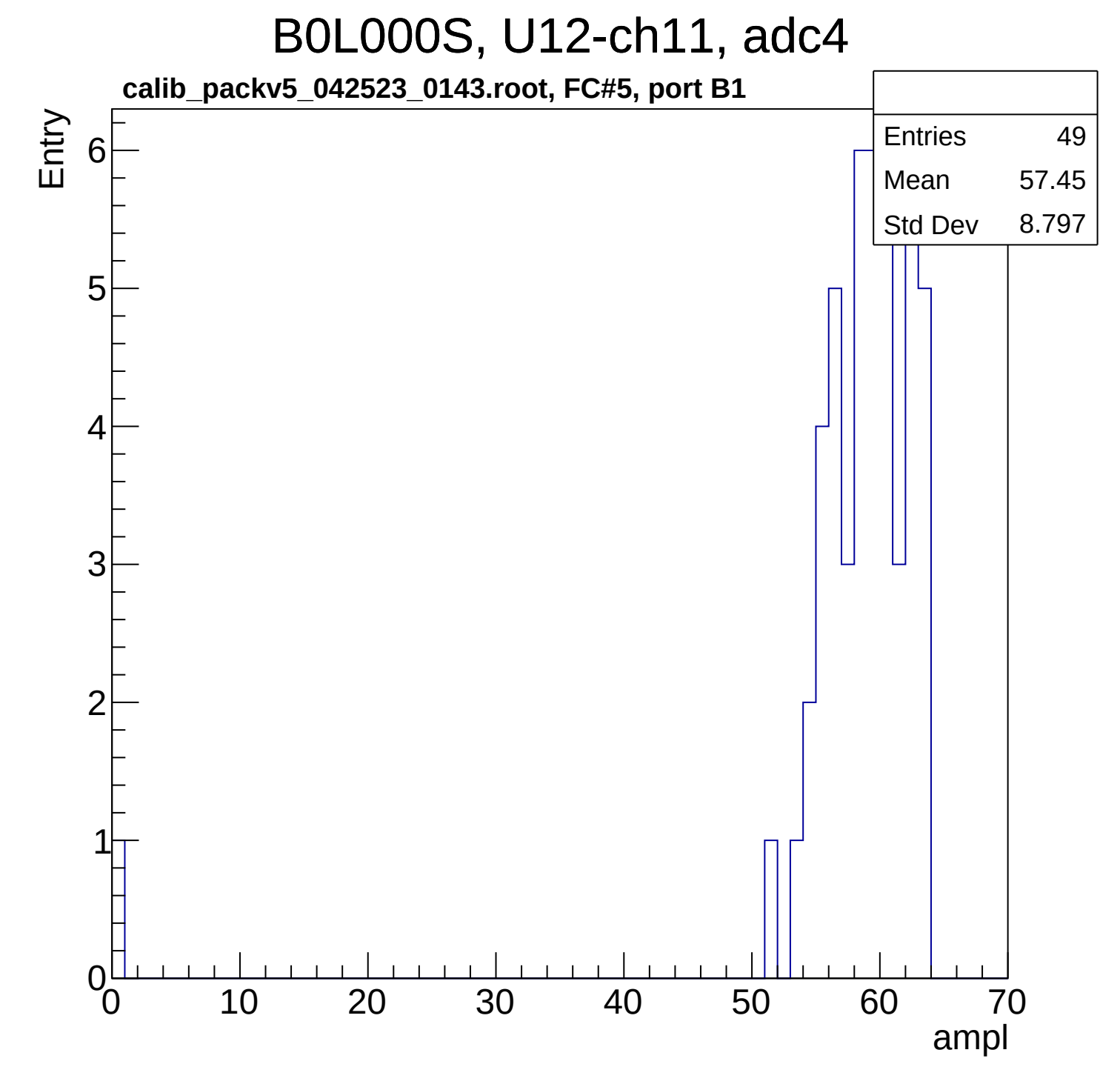
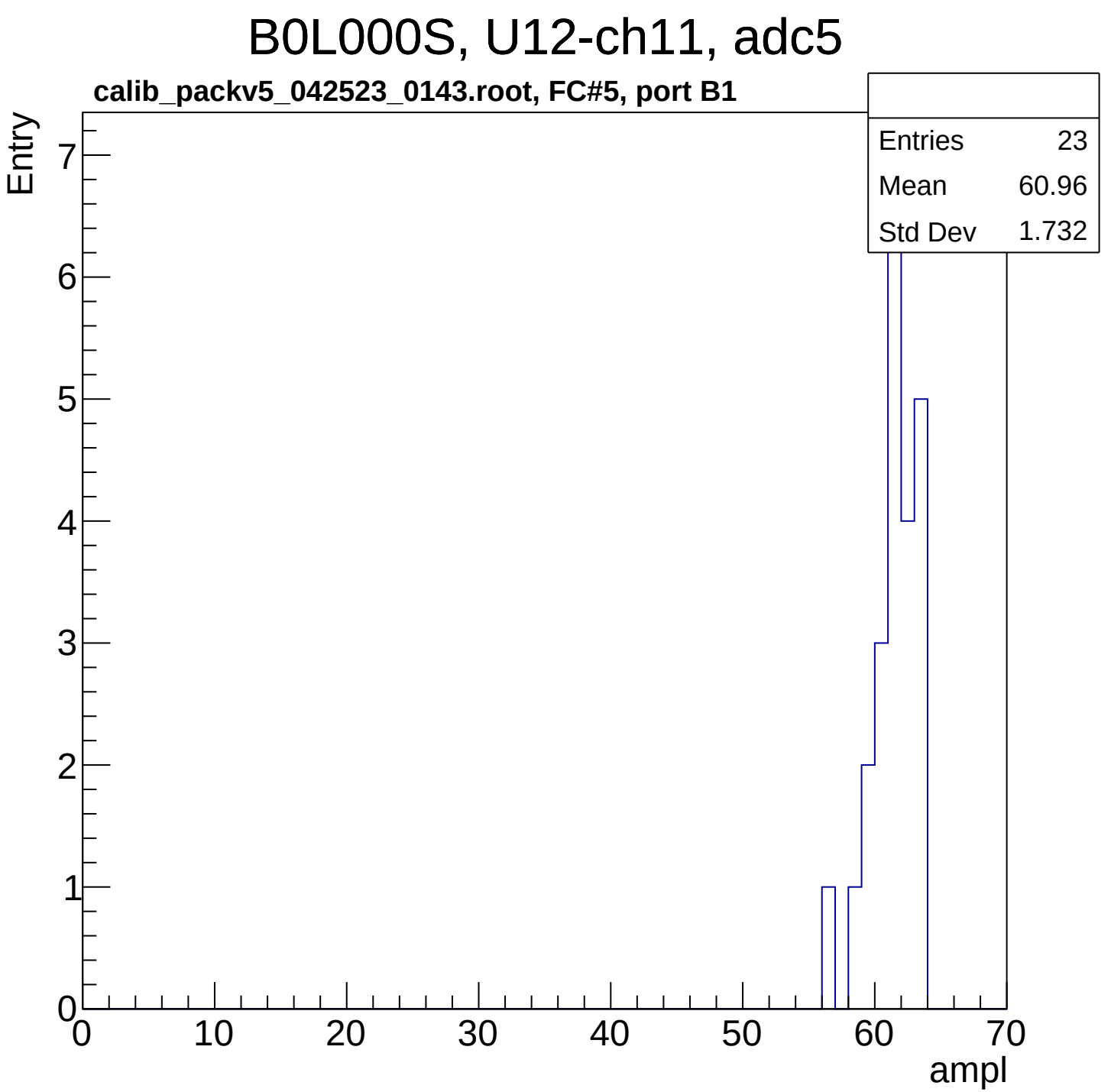

B0L000S, U12-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.45
Std Dev 8.797
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U12-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 60.96
Std Dev 1.732
Entry
ampl
0
1
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3
4
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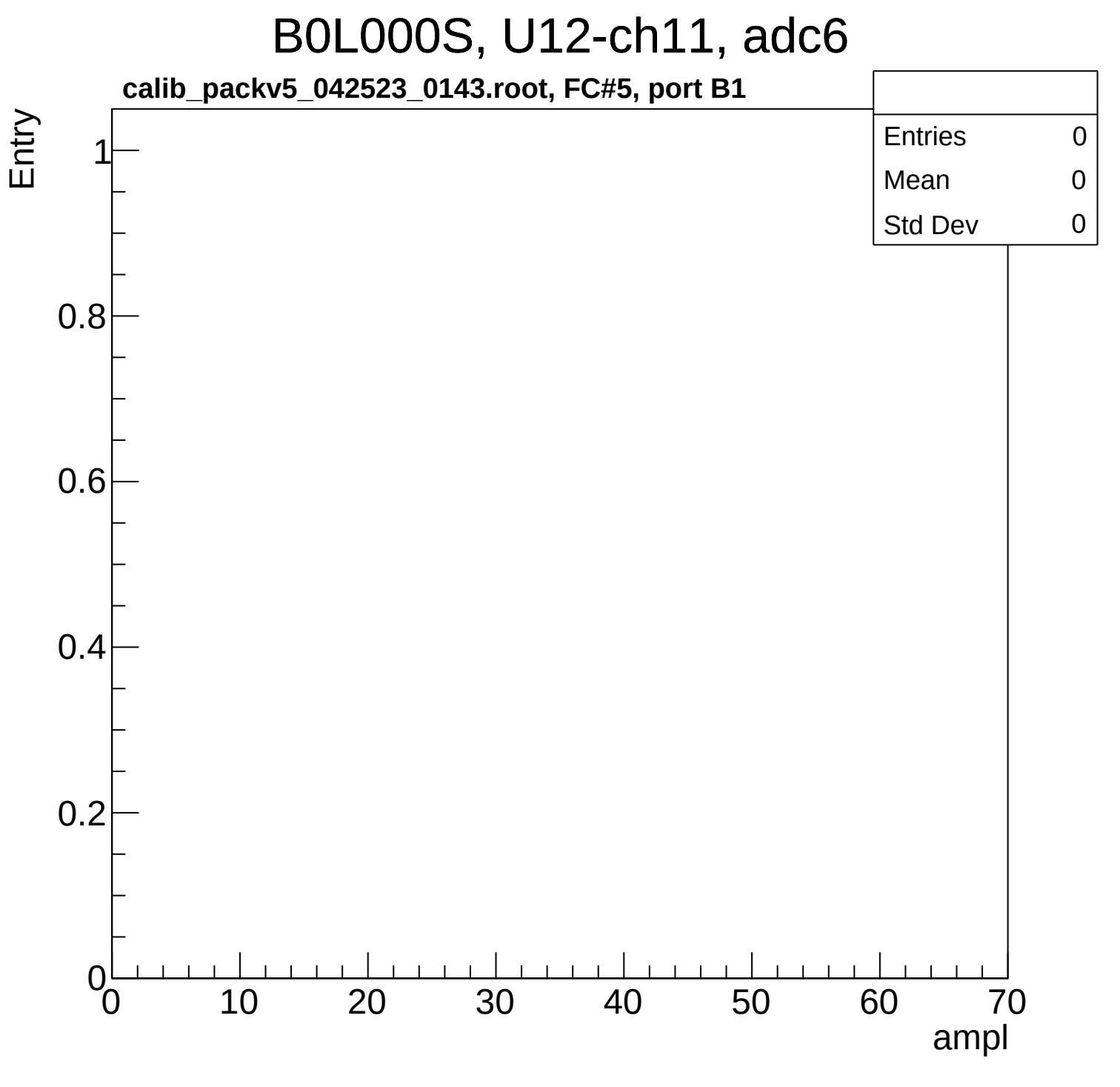
B0L000S, U12-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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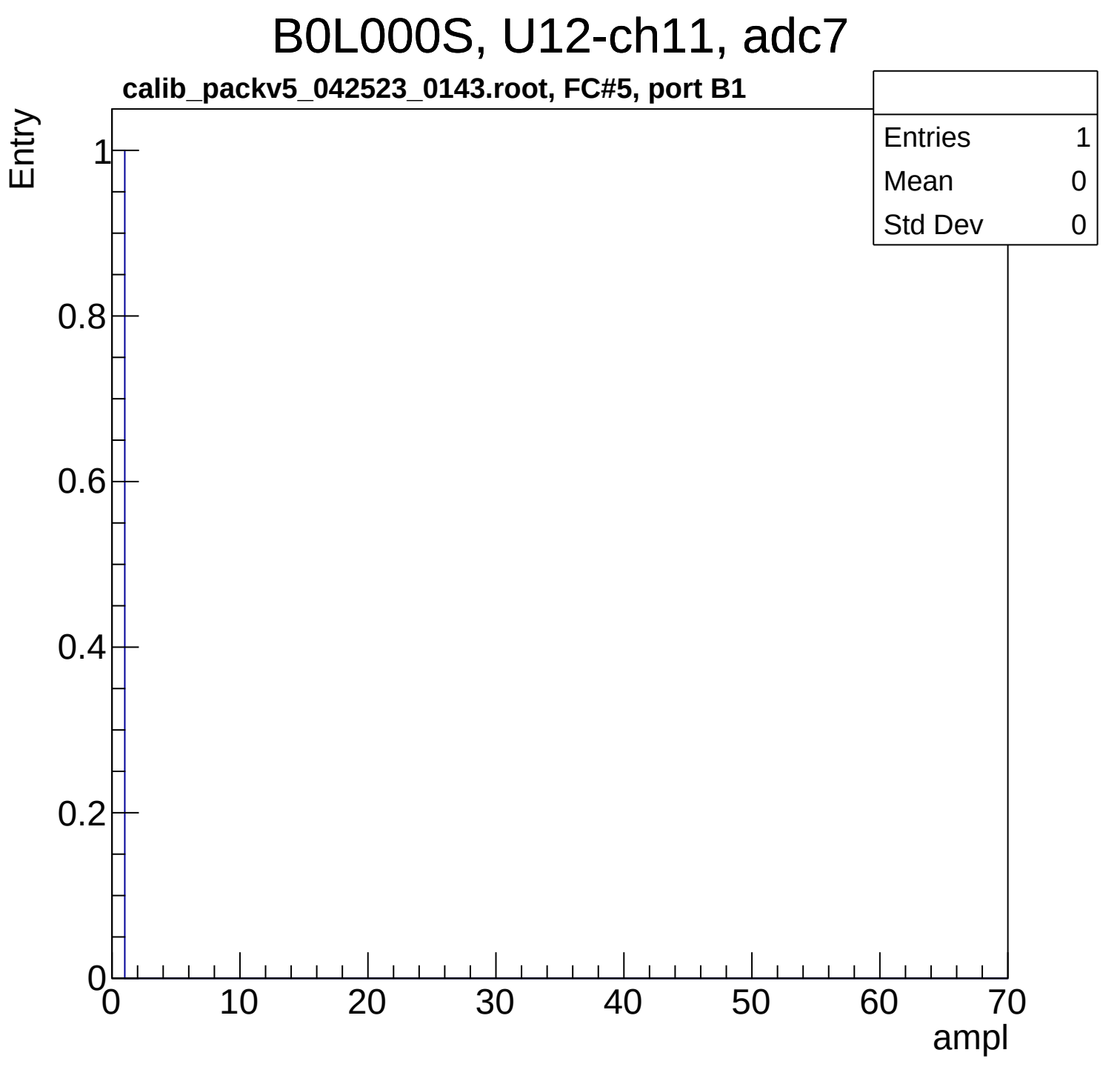
50
60
70
ampl


B0L000S, U12-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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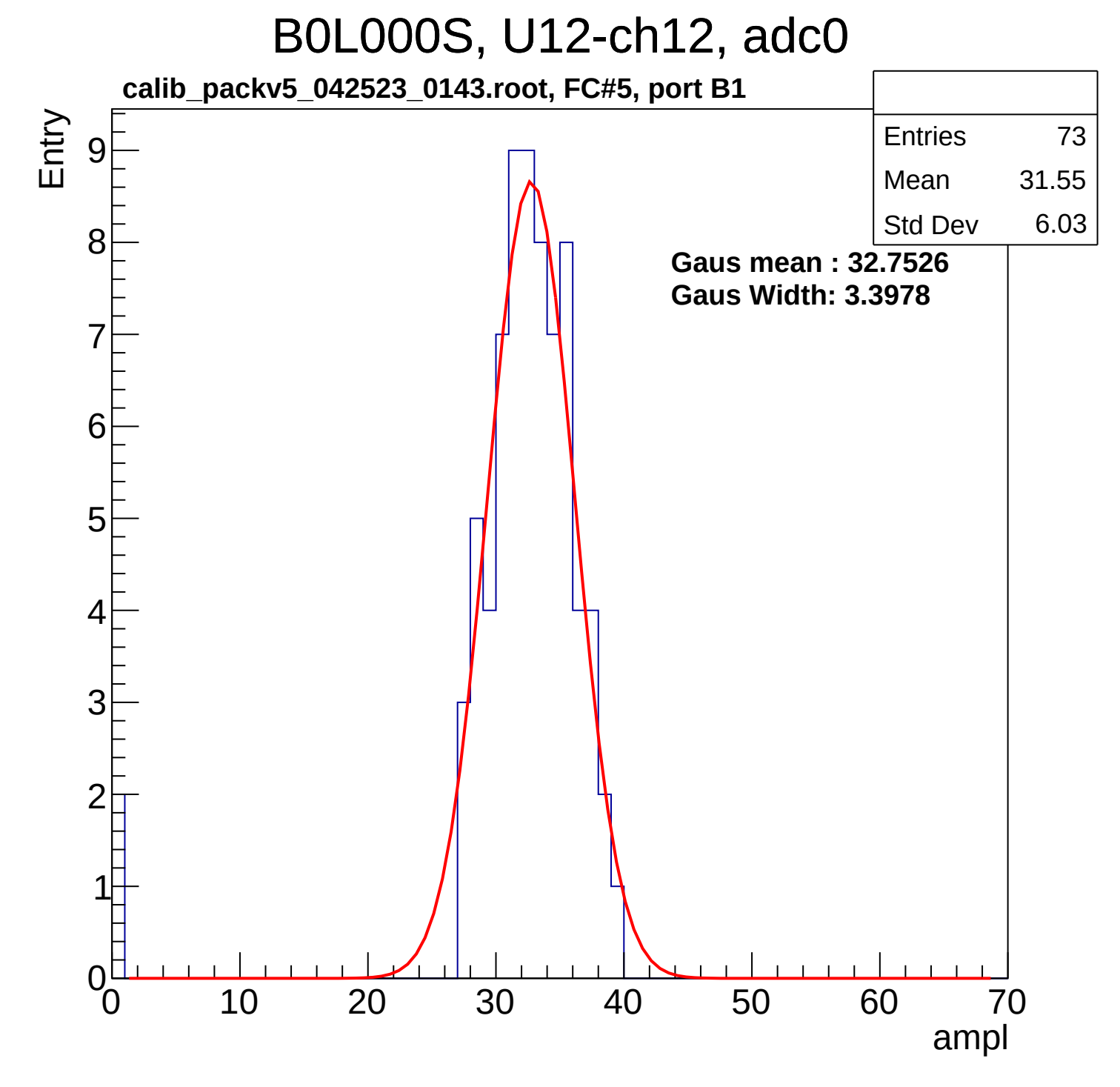
50
60
70
ampl


B0L000S, U12-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 31.55
Std Dev 6.03
Gaus mean : 32.7526
Gaus Width: 3.3978
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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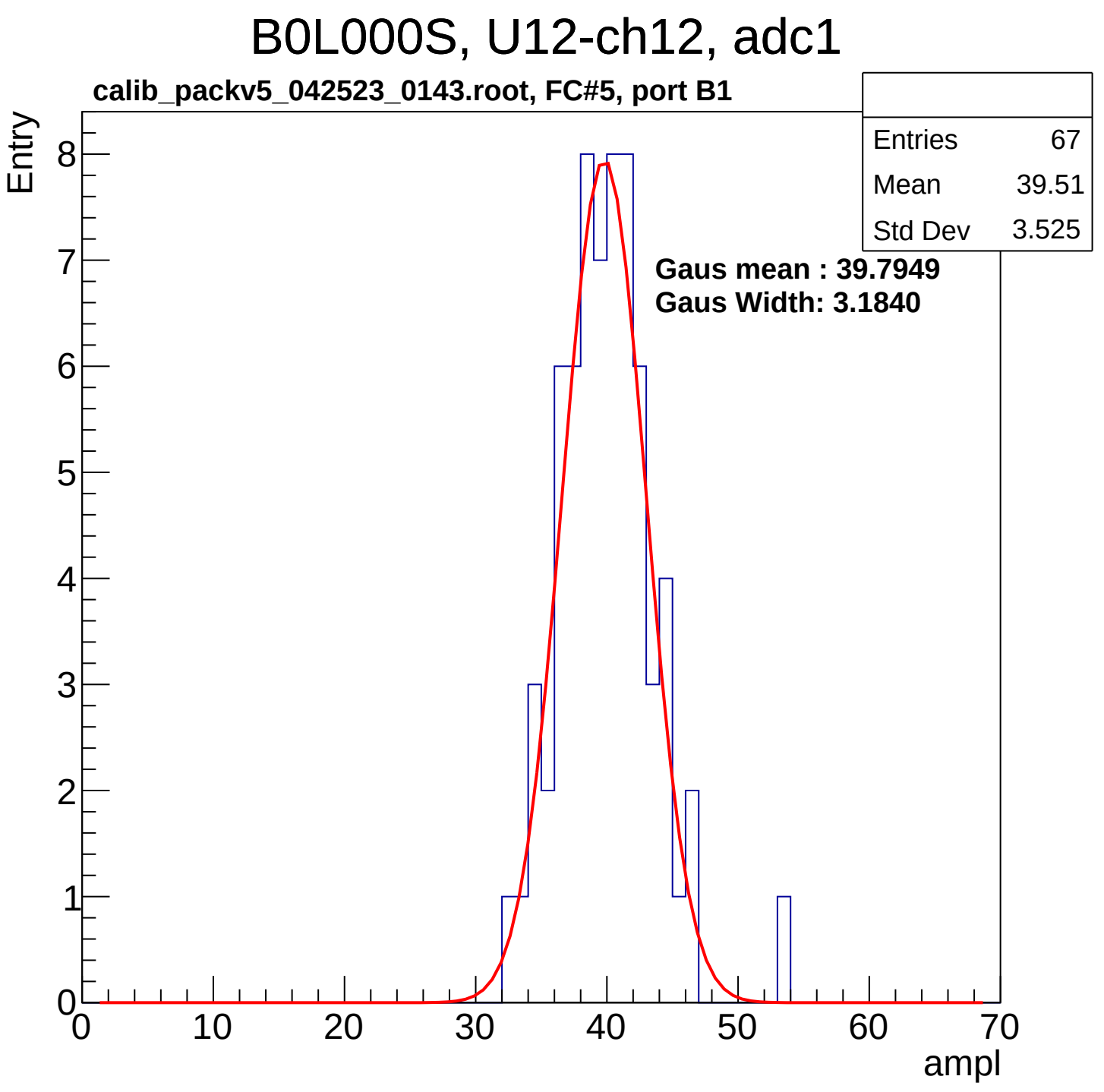
B0L000S, U12-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 39.51
Std Dev 3.525
Gaus mean : 39.7949
Gaus Width: 3.1840
Entry
ampl
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1
2
3
4
5
6
7
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0
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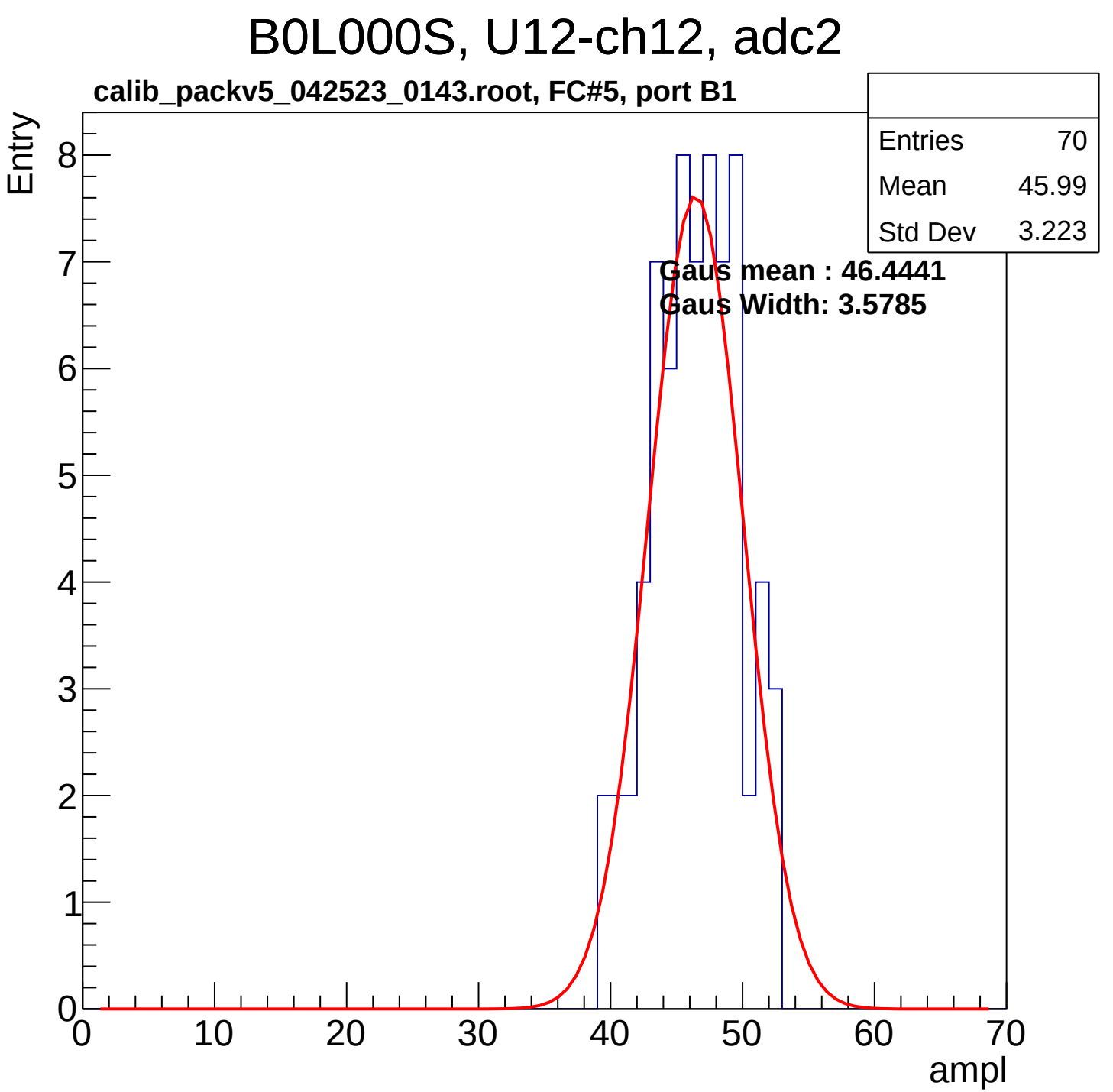
B0L000S, U12-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.99
Std Dev 3.223
Gaus mean : 46.4441
Gaus Width: 3.5785
Entry
ampl
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1
2
3
4
5
6
7
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0
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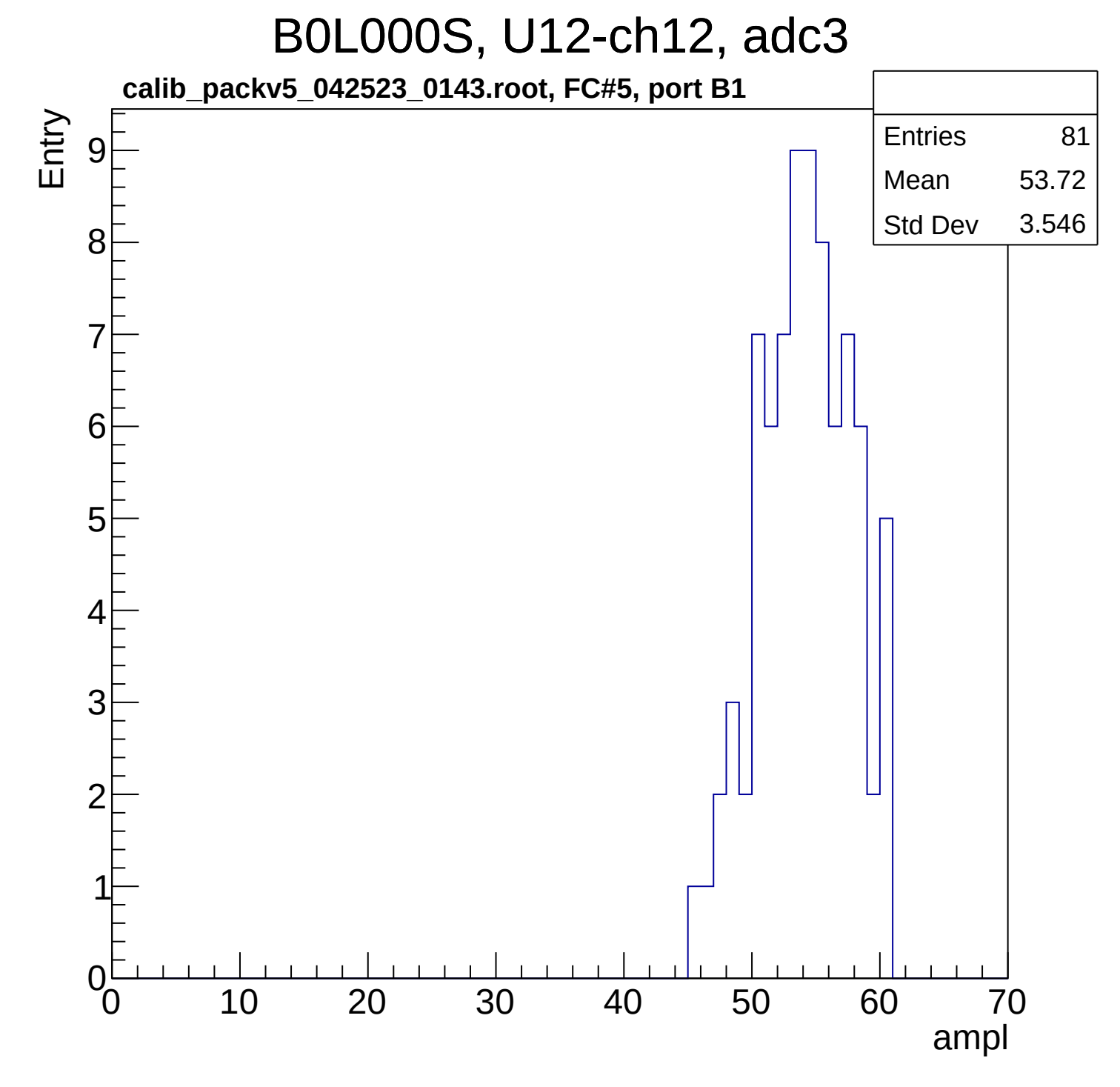
B0L000S, U12-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 53.72
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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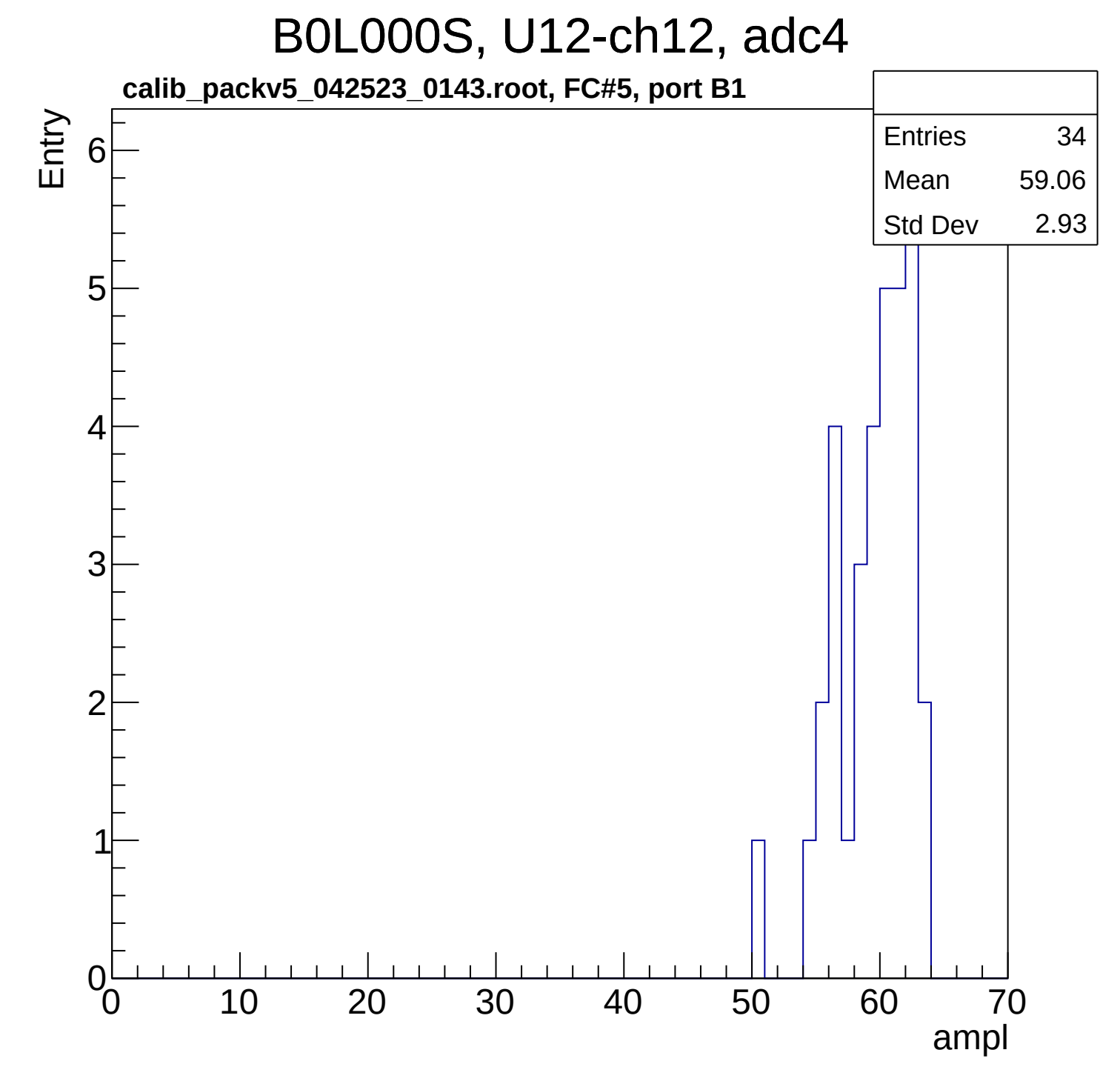
B0L000S, U12-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.06
Std Dev 2.93
Entry
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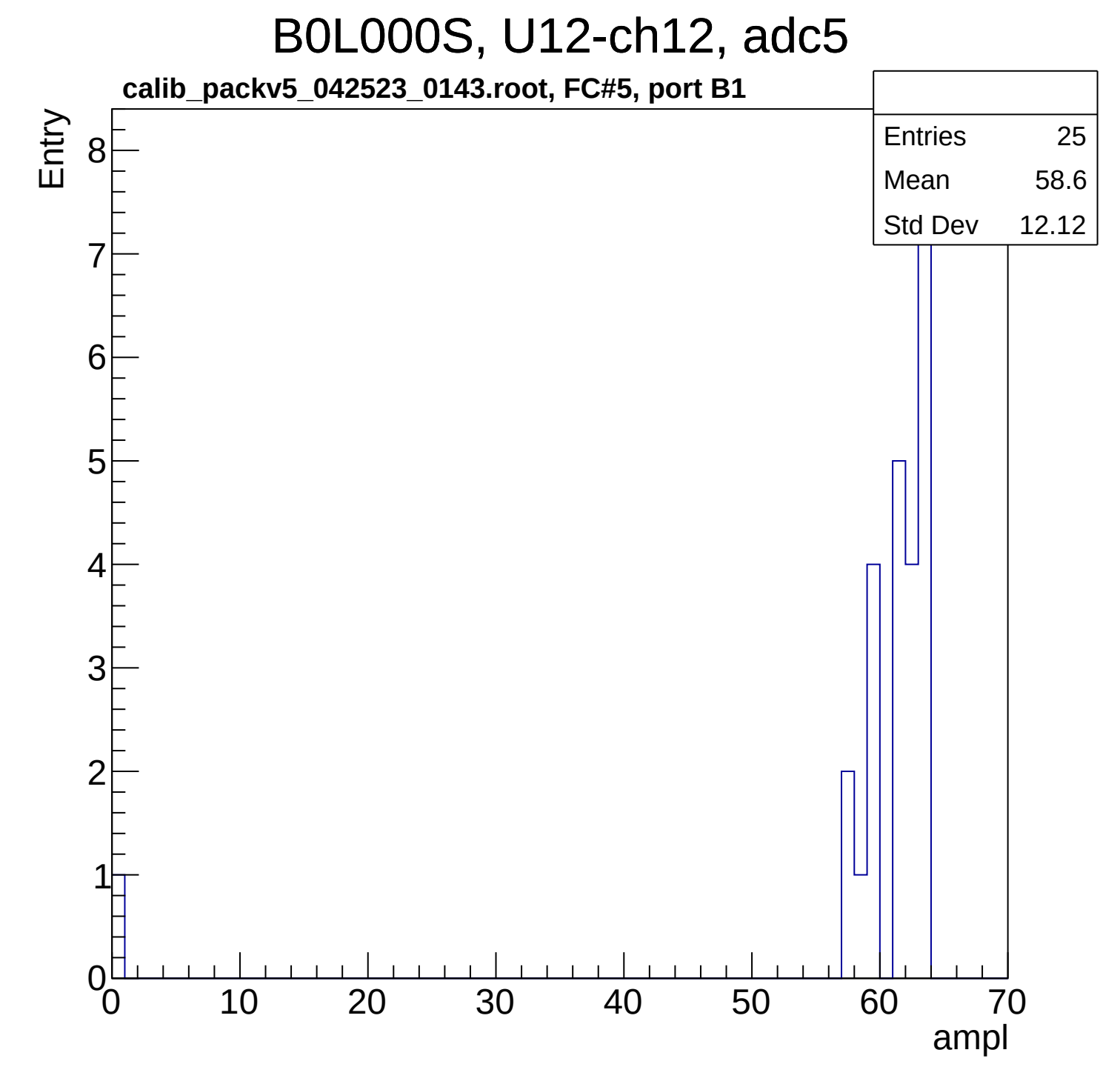
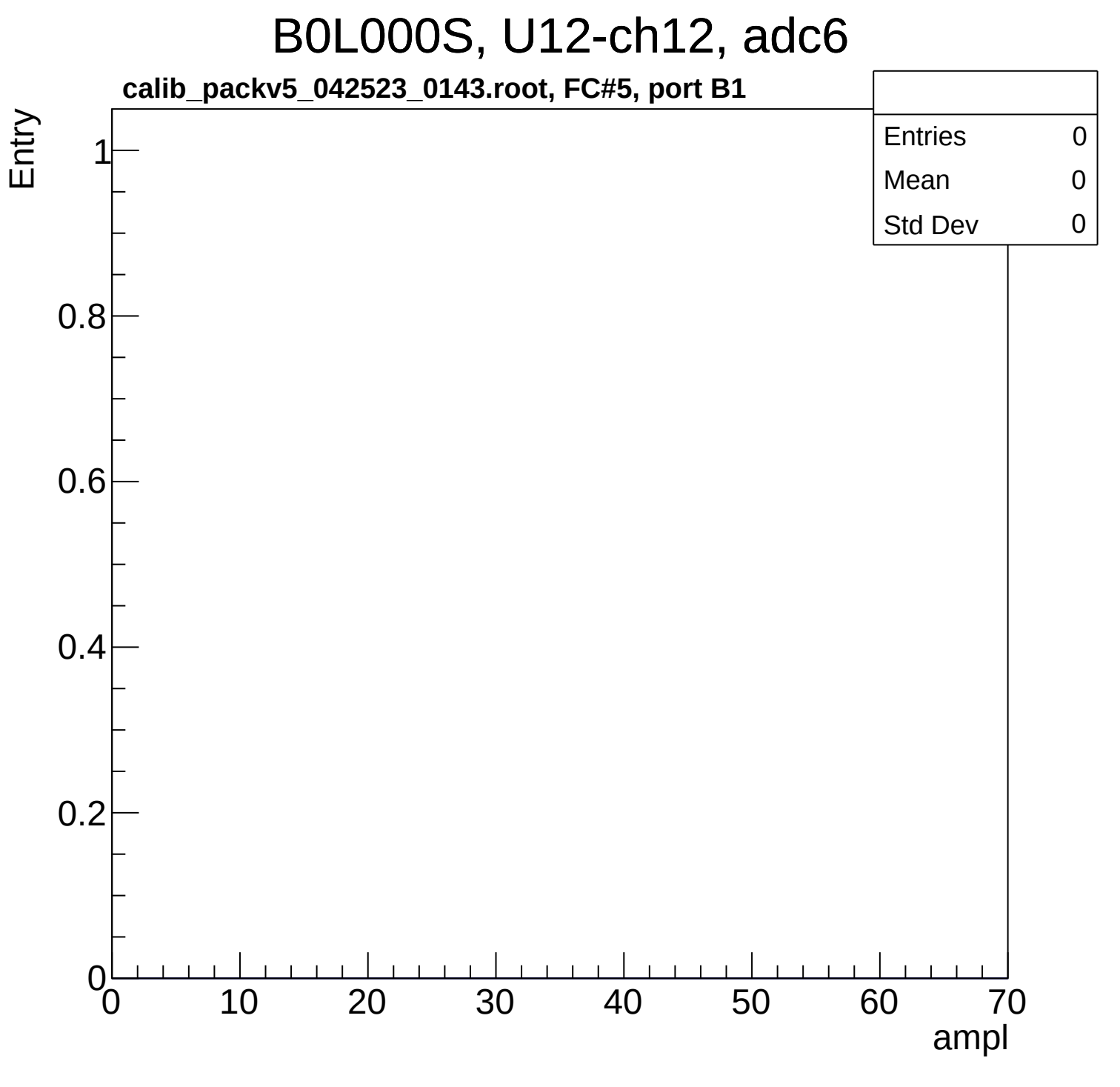
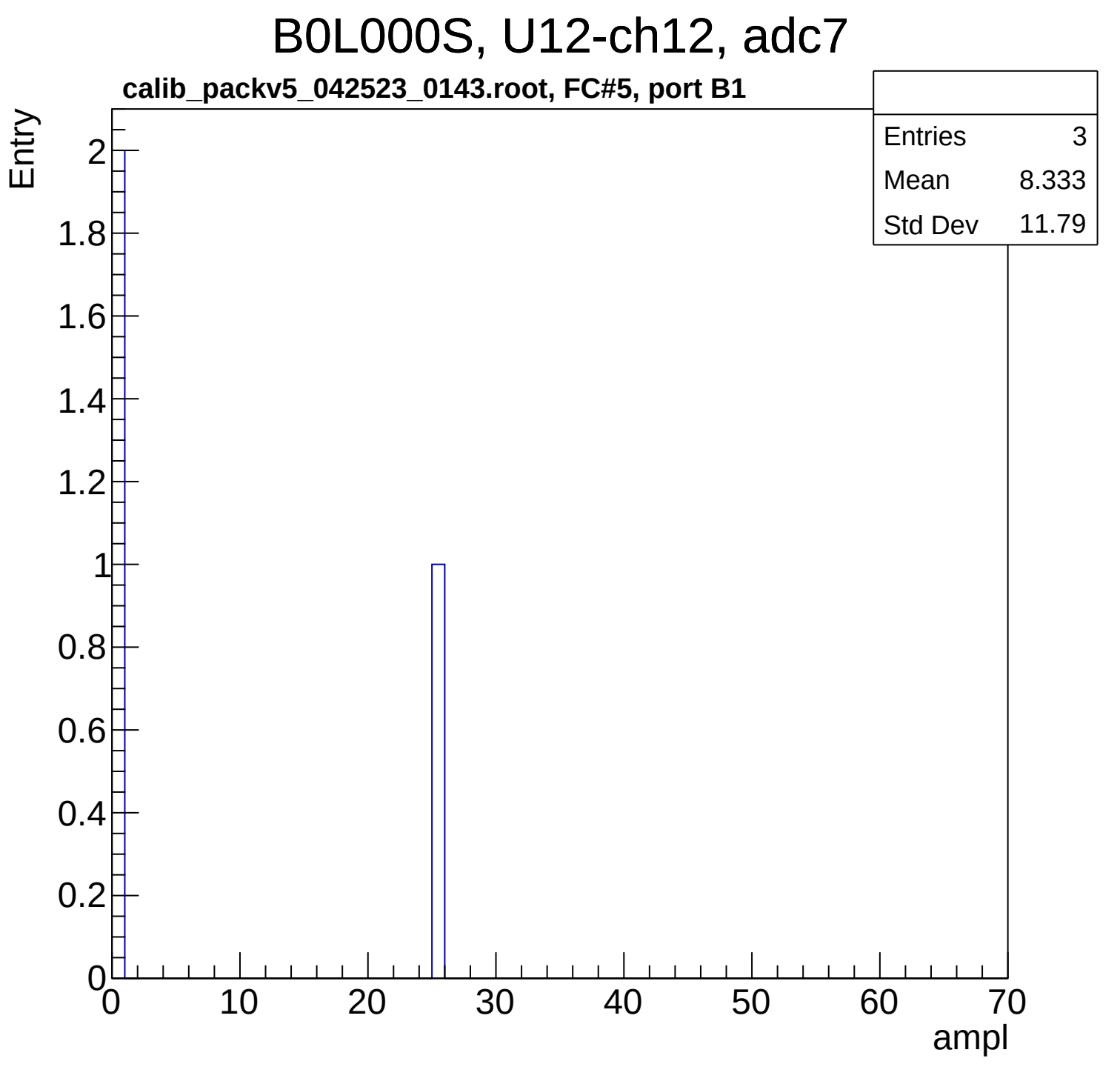
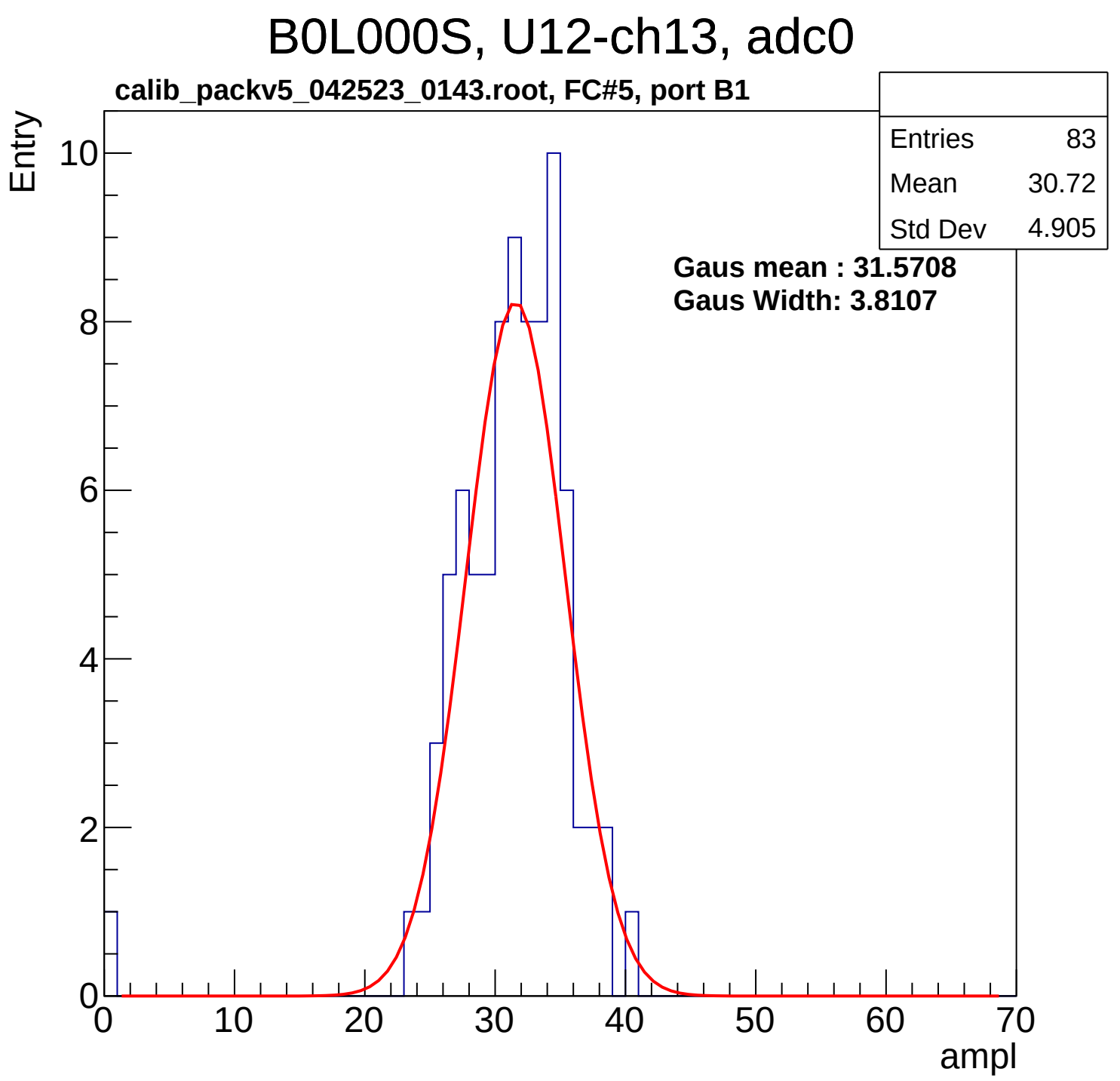
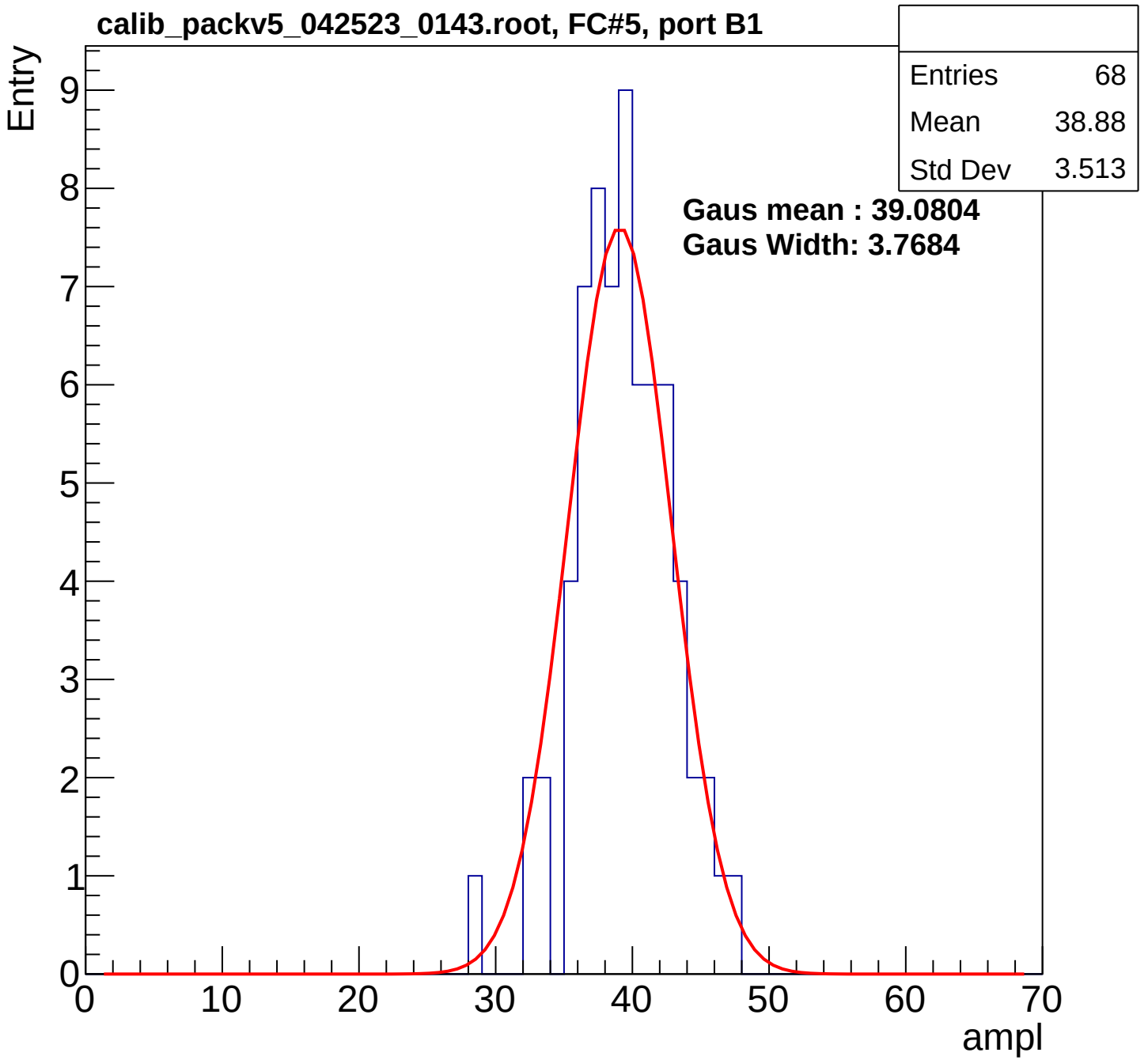
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 58.6
Std Dev 12.12
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.72
Std Dev 4.905
Gaus mean : 31.5708
Gaus Width: 3.8107
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 38.88
Std Dev 3.513
Gaus mean : 39.0804
Gaus Width: 3.7684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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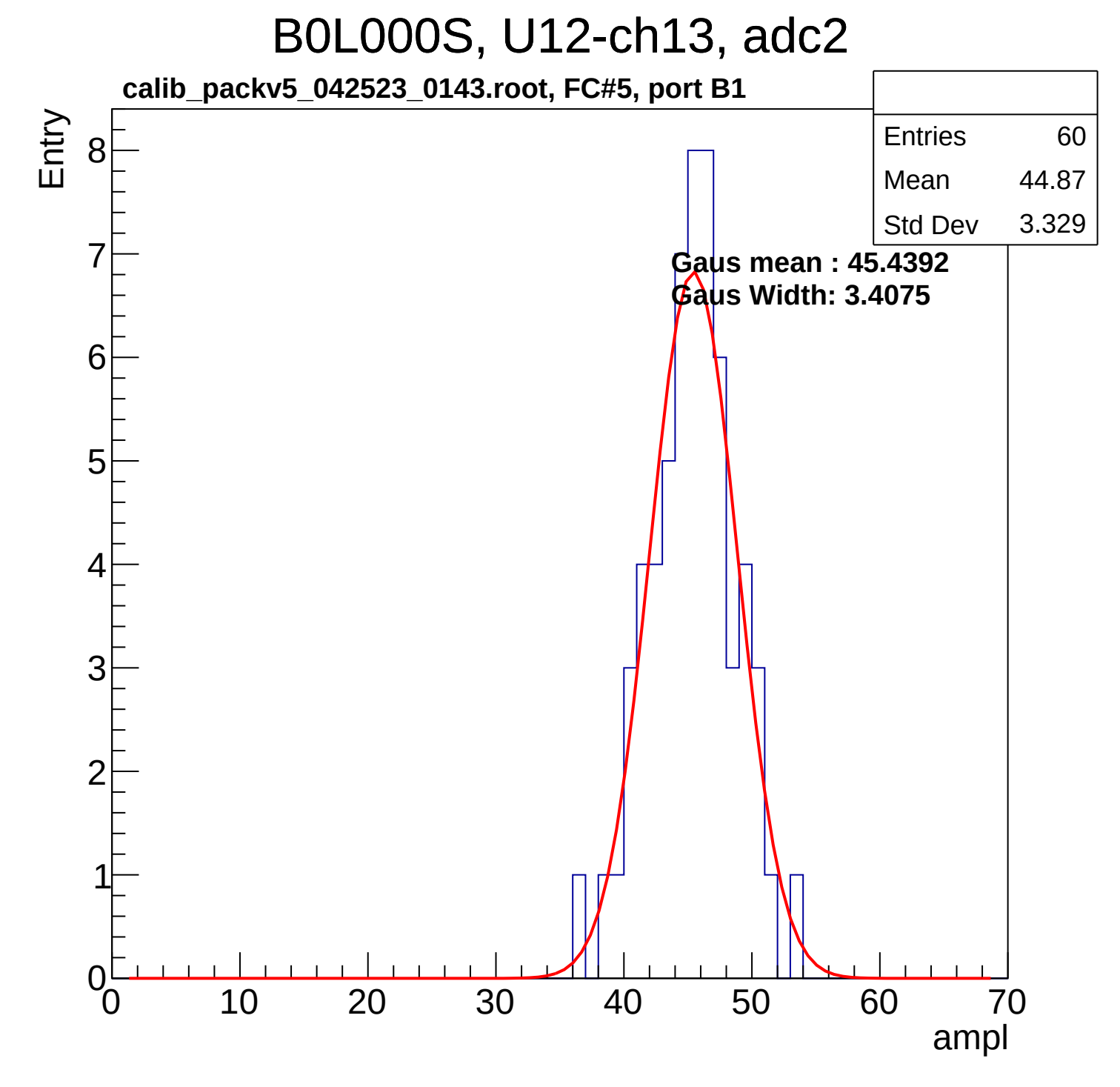
B0L000S, U12-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 44.87
Std Dev 3.329
Gaus mean : 45.4392
Gaus Width: 3.4075
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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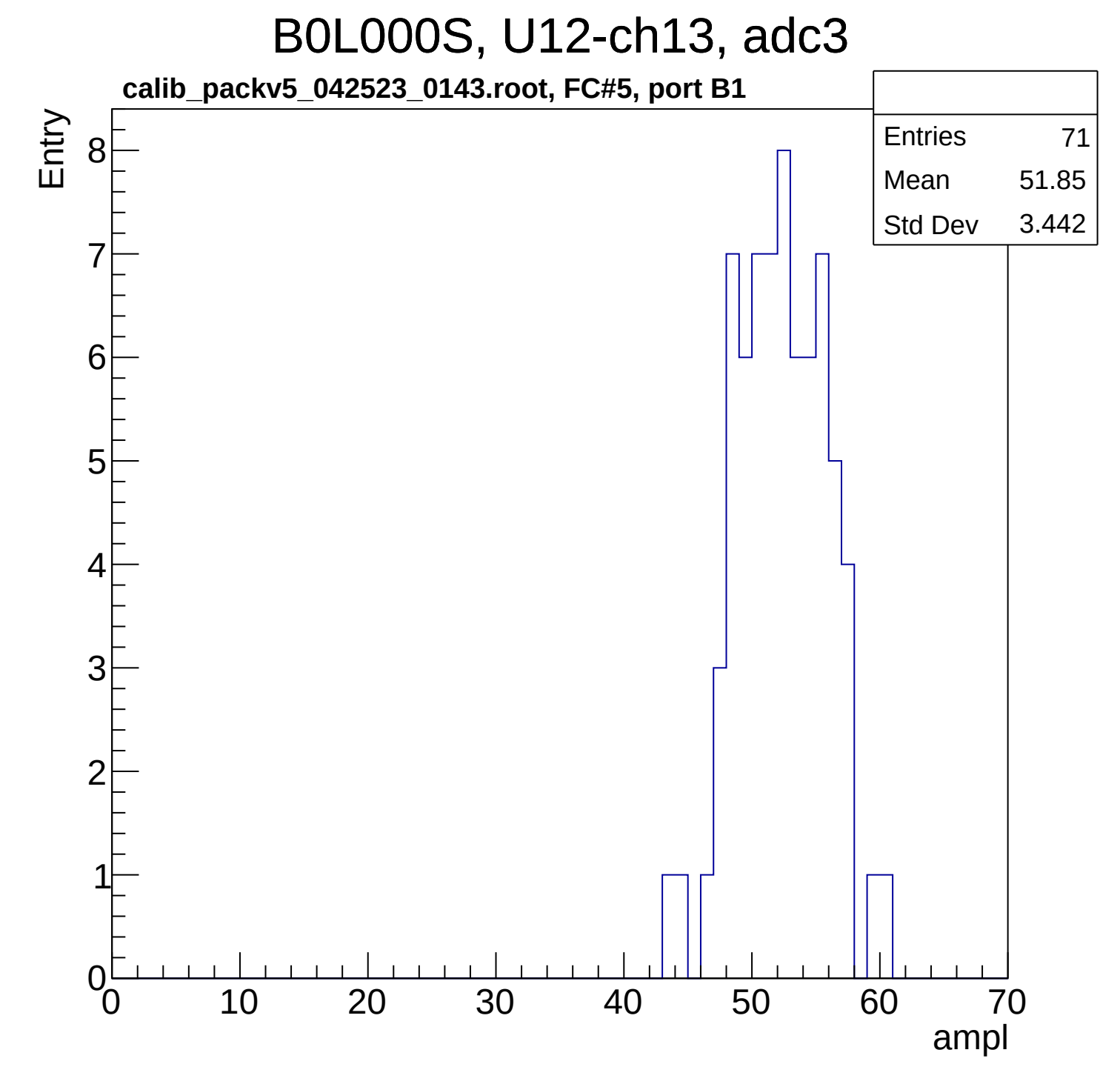
B0L000S, U12-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 51.85
Std Dev 3.442
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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60
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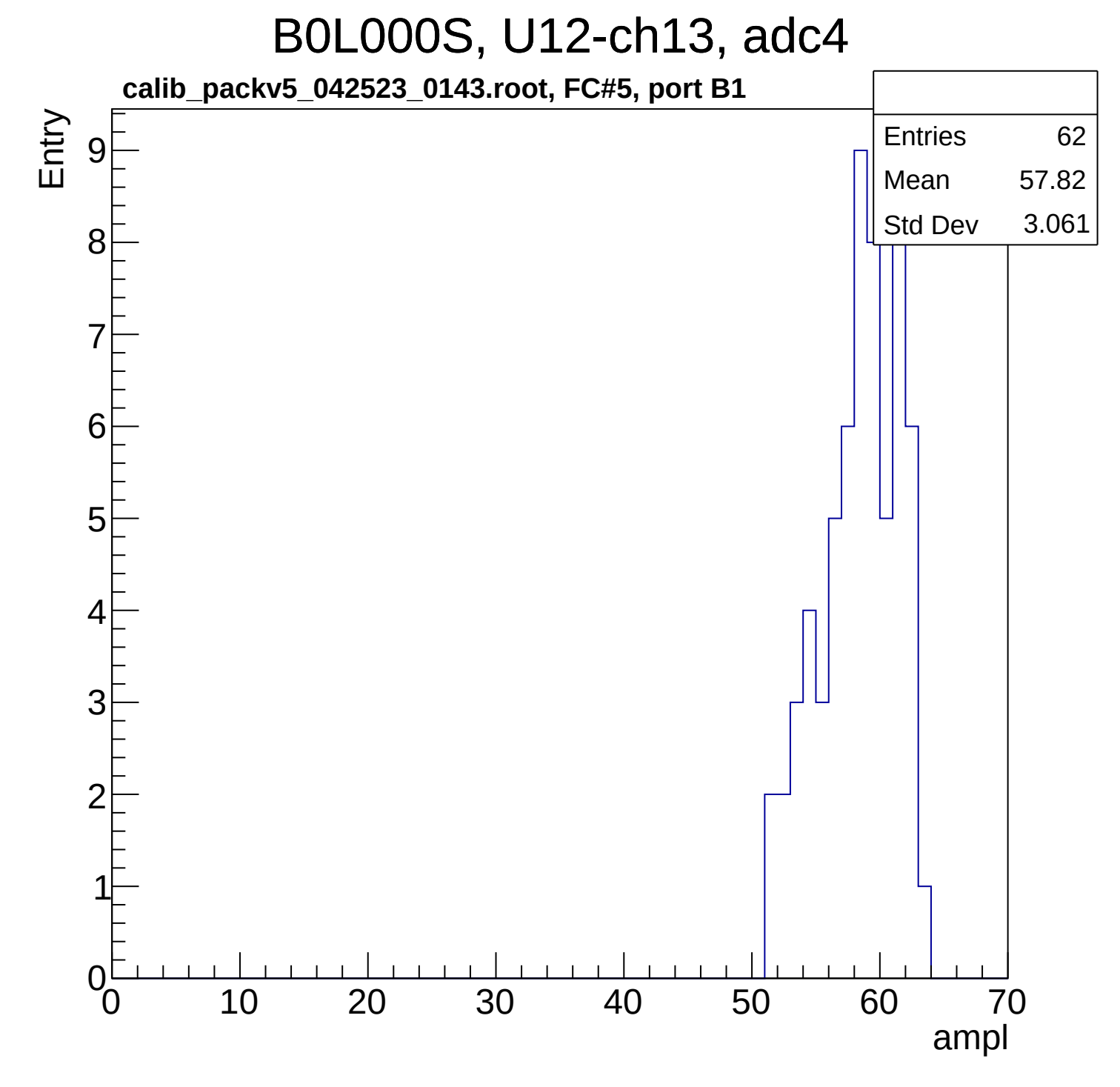
B0L000S, U12-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.82
Std Dev 3.061
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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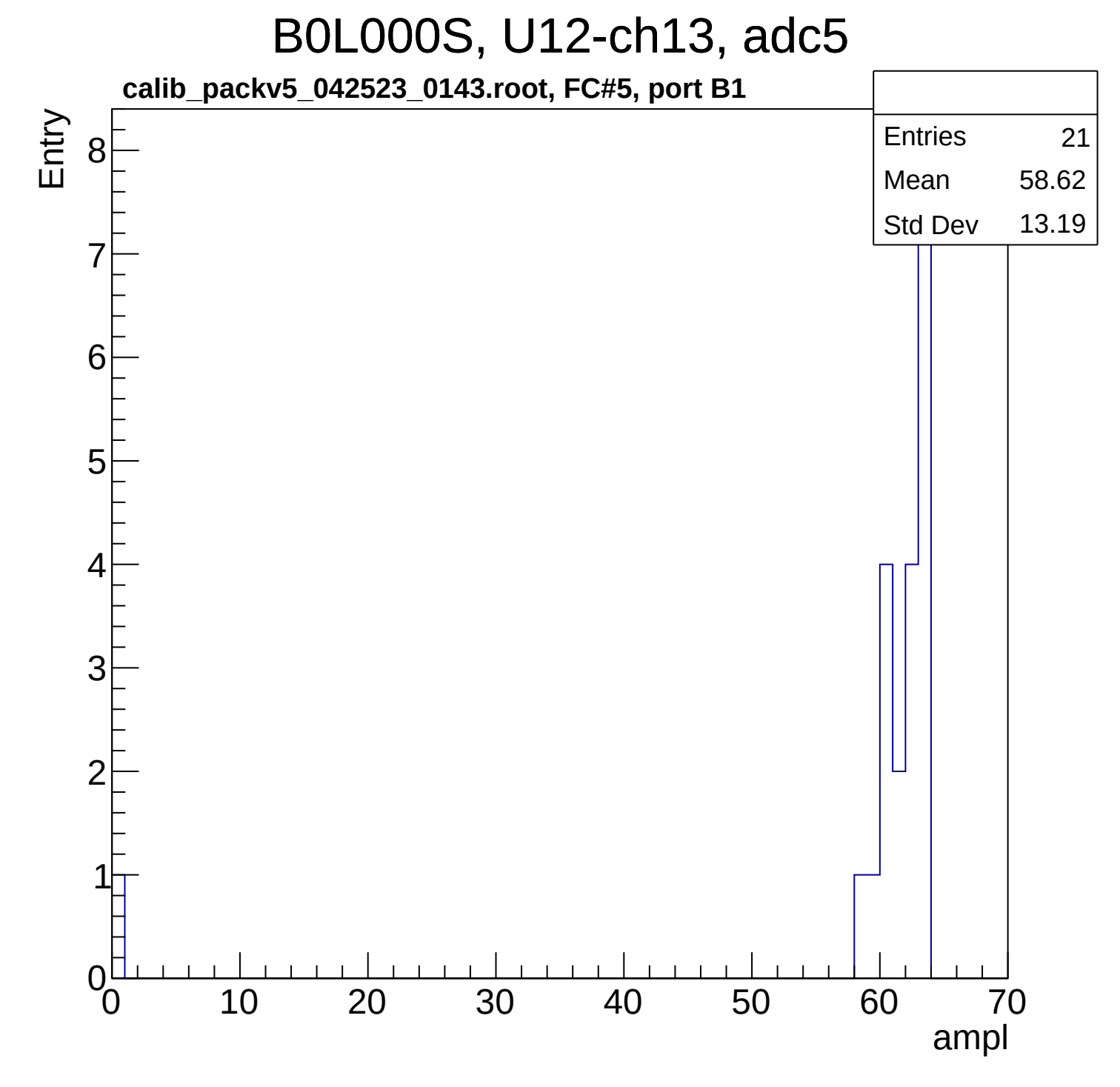
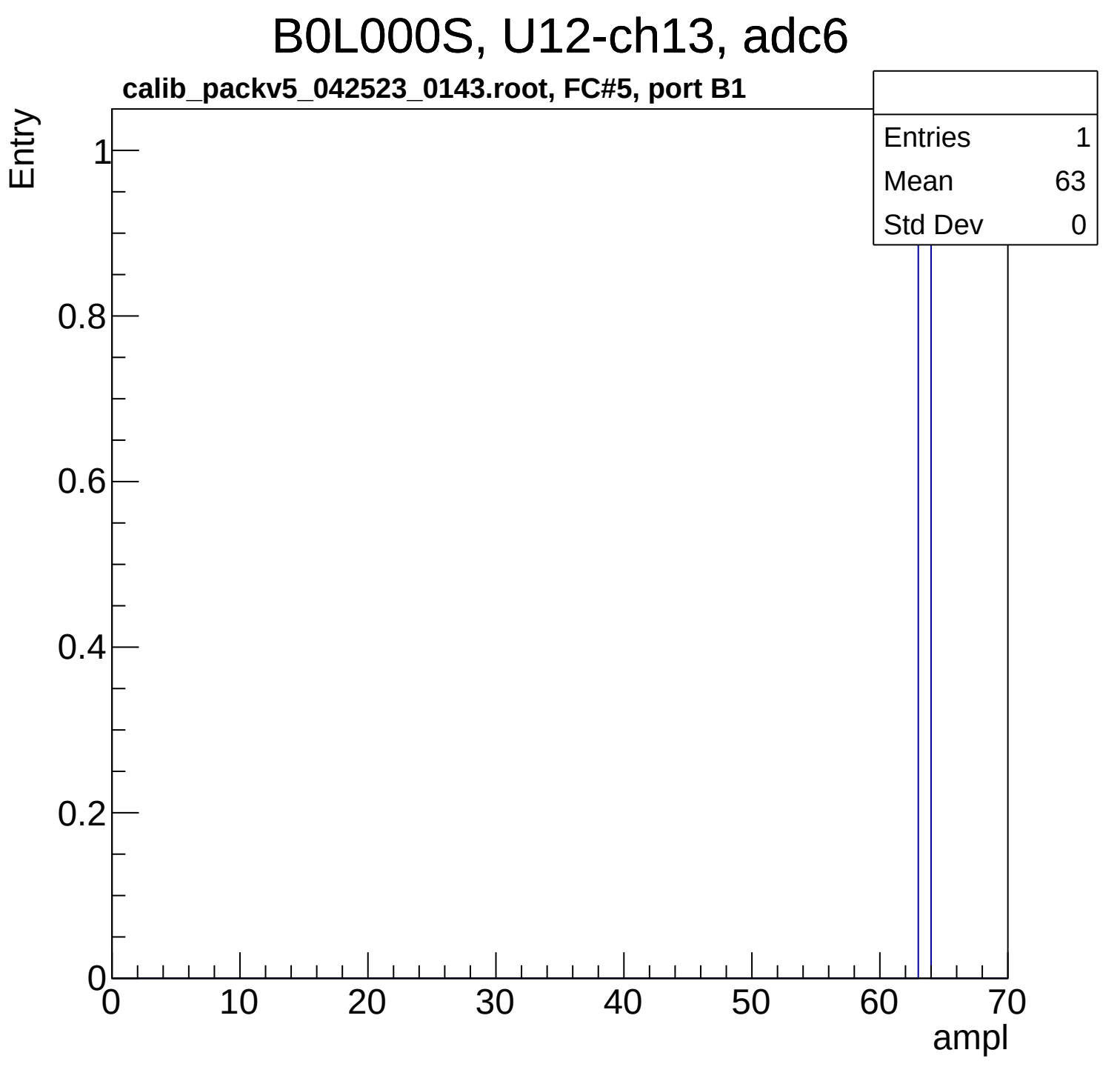

B0L000S, U12-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.62
Std Dev 13.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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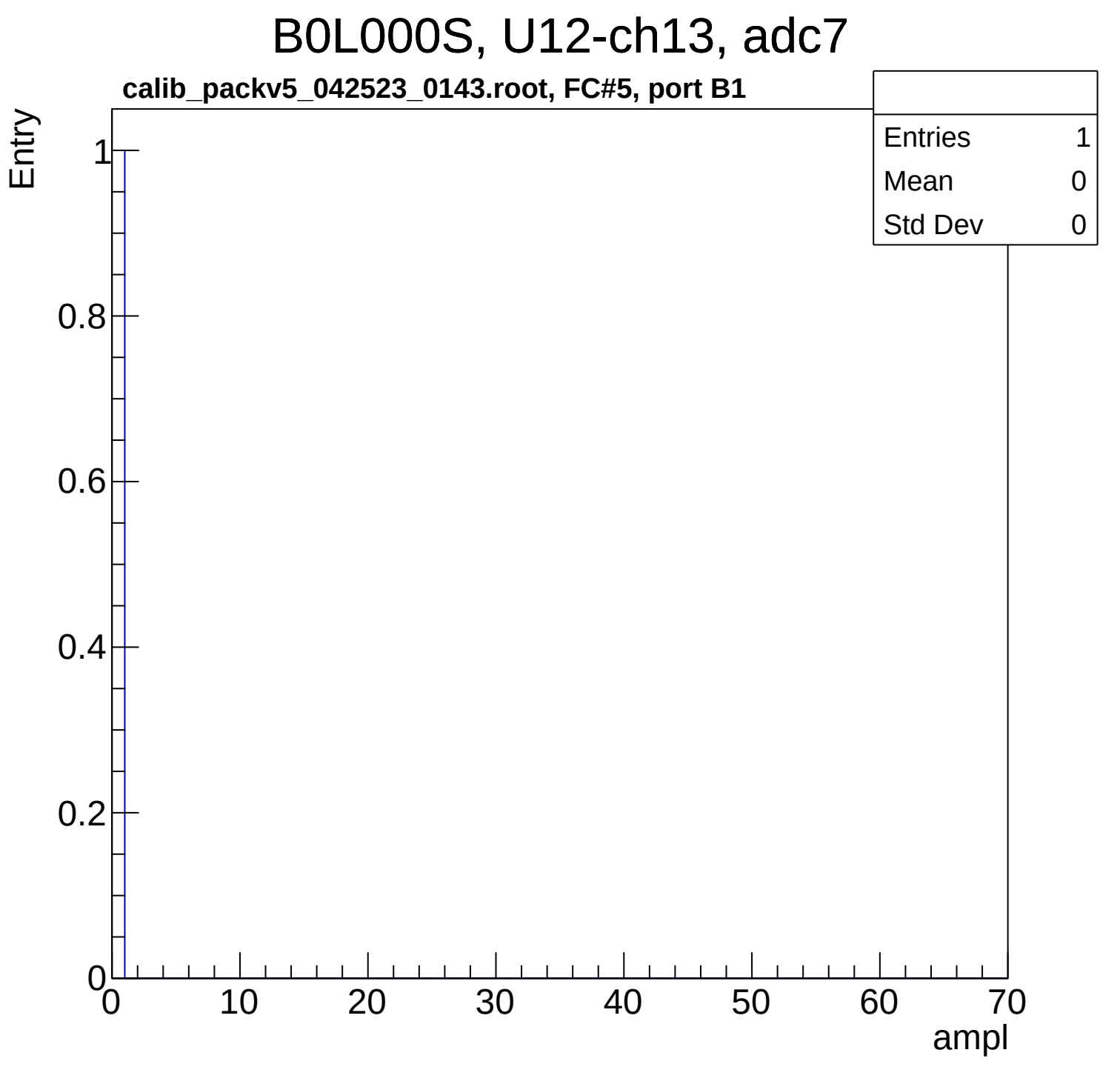
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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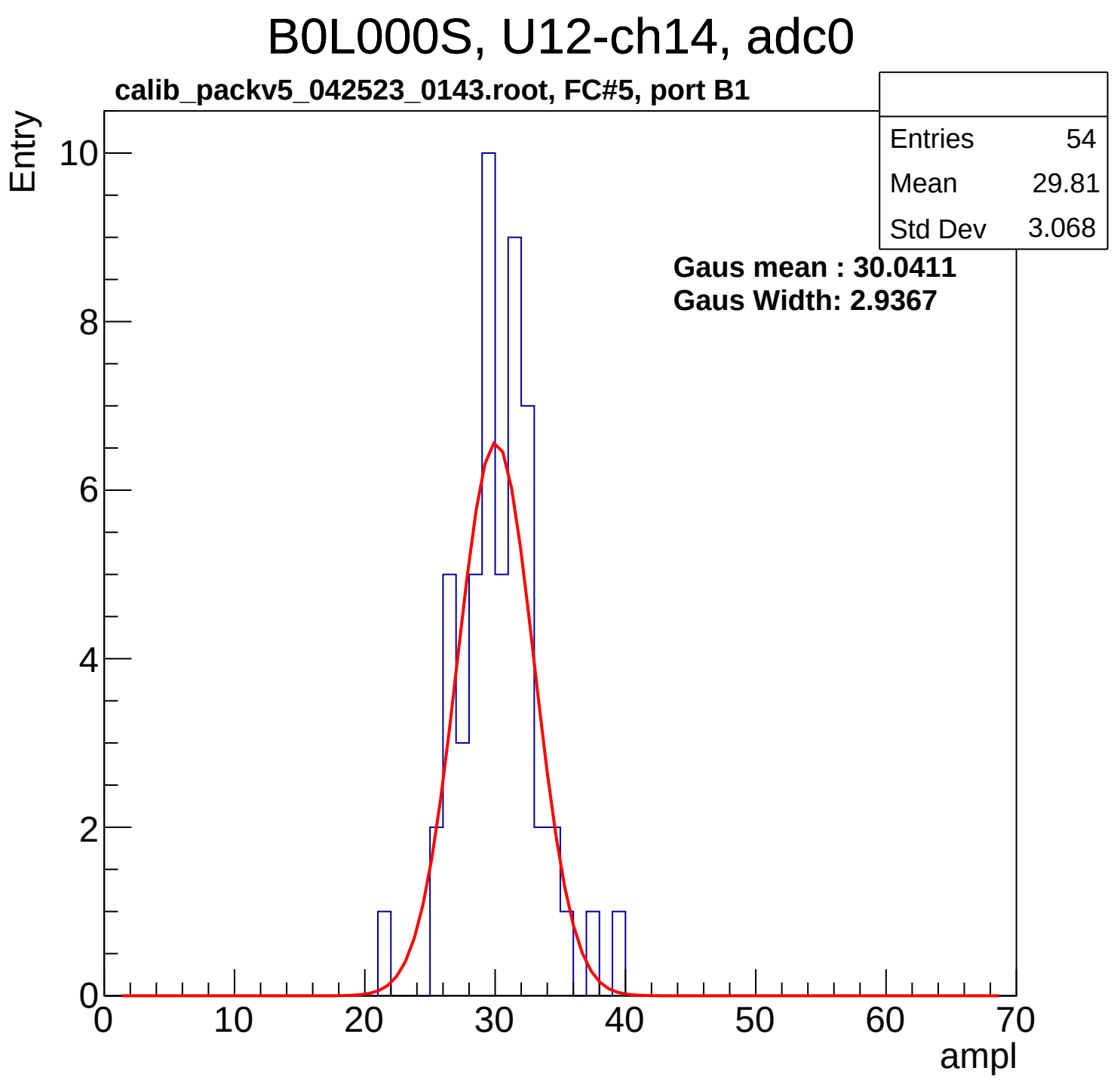
50
60
70
ampl


B0L000S, U12-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.81
Std Dev 3.068
Gaus mean : 30.0411
Gaus Width: 2.9367
Entry
ampl
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6
8
10
0
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20
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60
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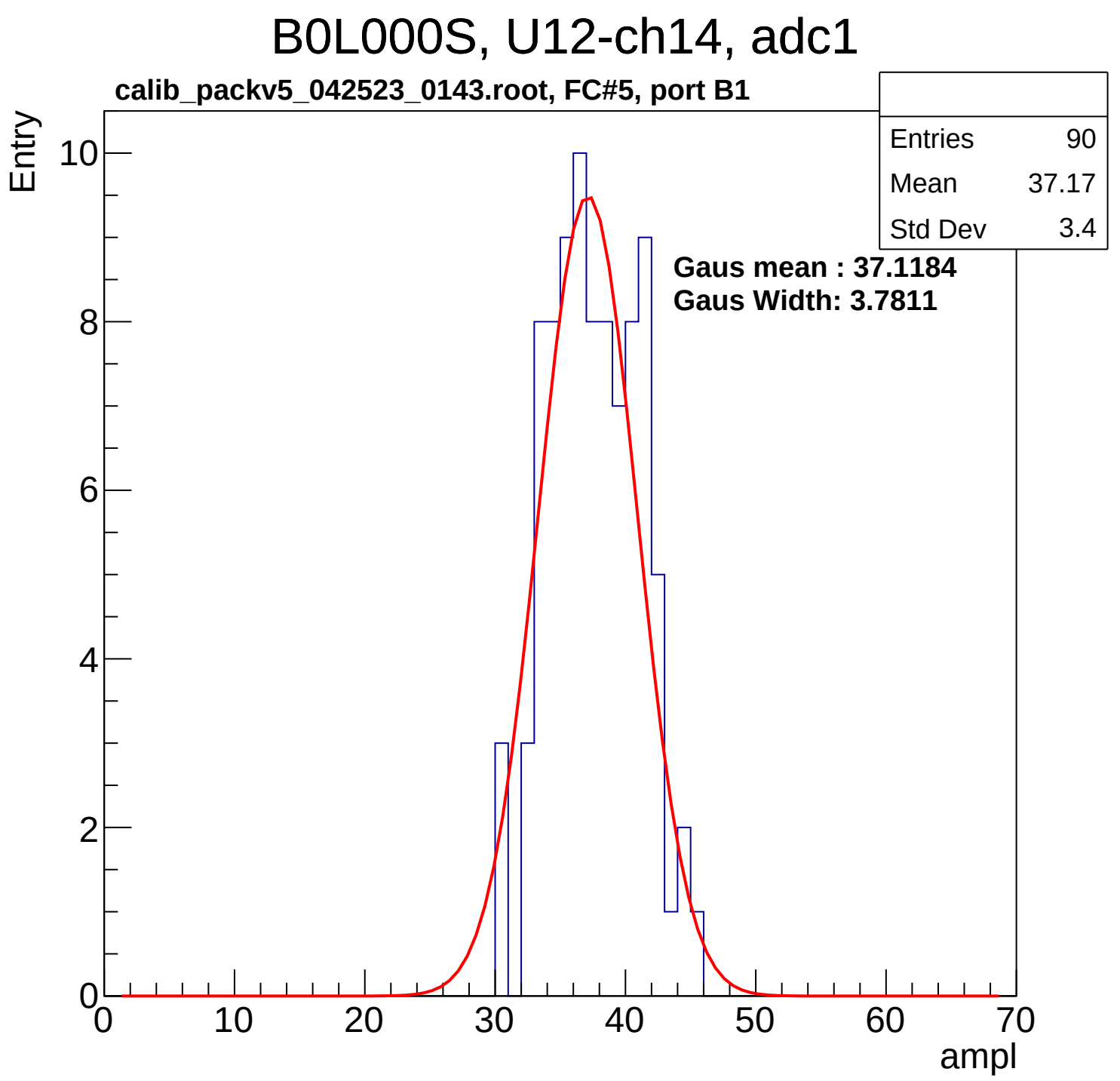
B0L000S, U12-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 37.17
Std Dev 3.4
Gaus mean : 37.1184
Gaus Width: 3.7811
Entry
ampl
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2
4
6
8
10
0
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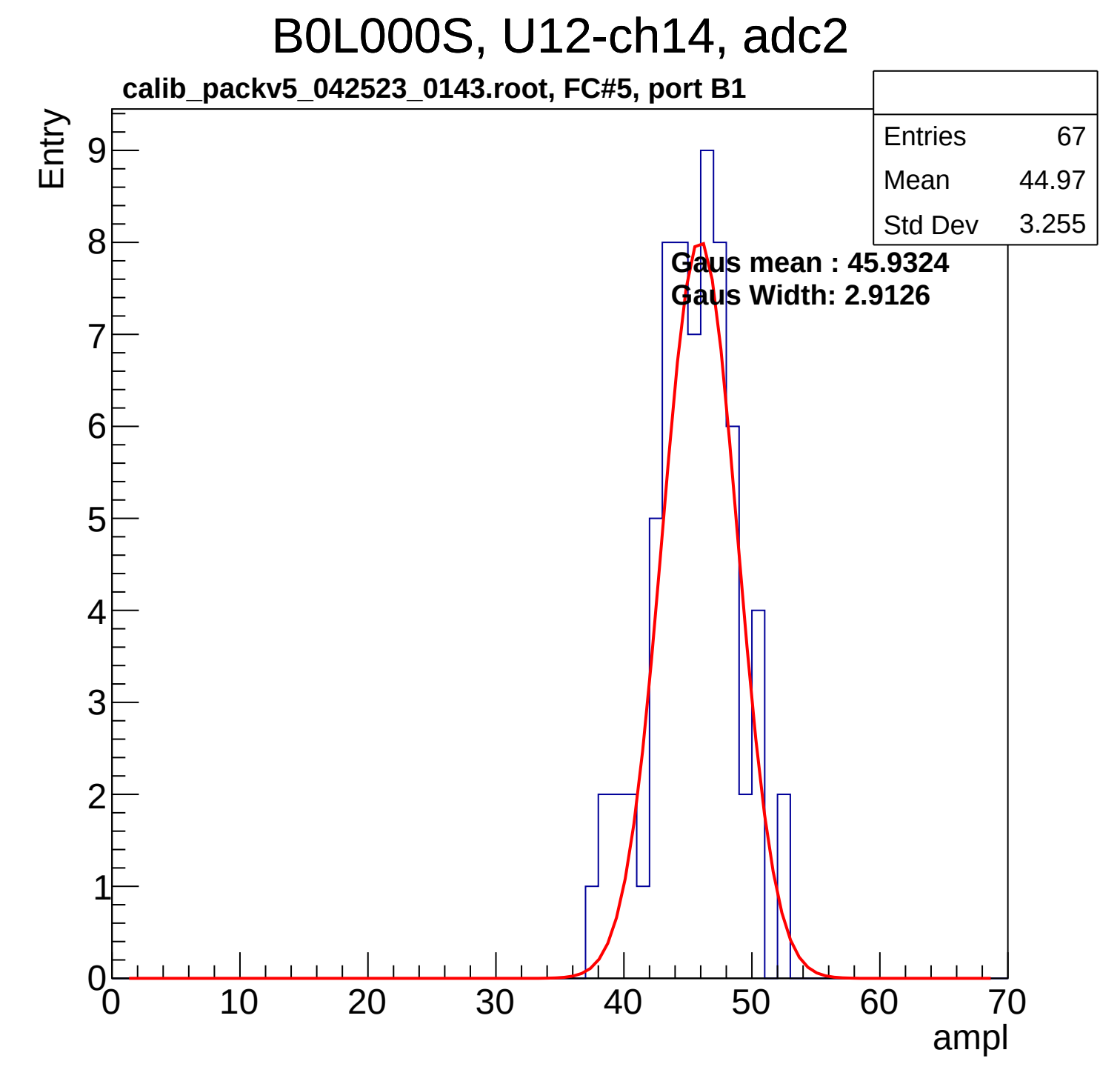
20
30
40
50
60
70


B0L000S, U12-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.97
Std Dev 3.255
Gaus mean : 45.9324
Gaus Width: 2.9126
Entry
ampl
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2
3
4
5
6
7
8
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0
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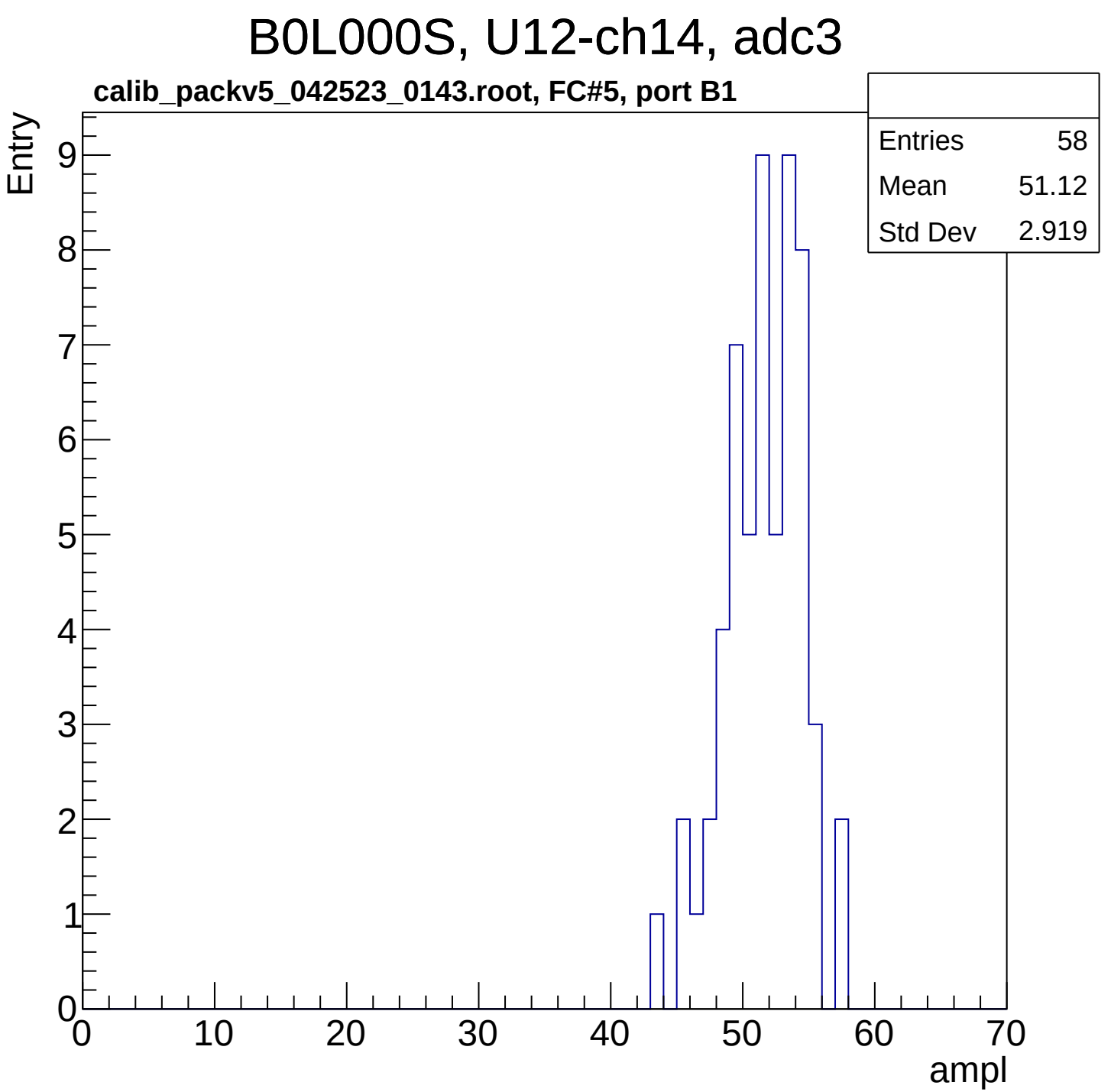
B0L000S, U12-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.12
Std Dev 2.919
Entry
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4
5
6
7
8
9
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10
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30
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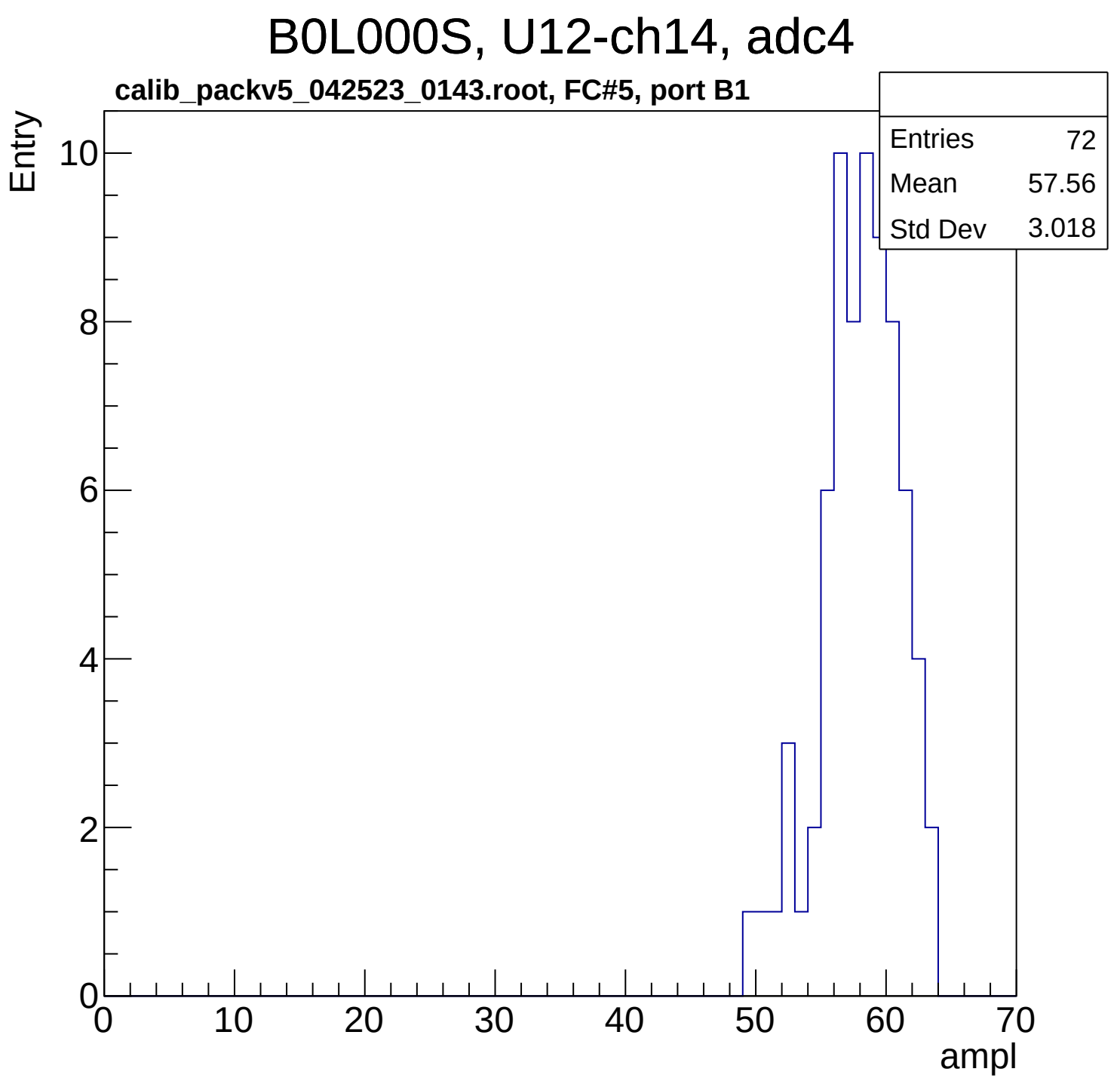
50
60
70
ampl


B0L000S, U12-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 57.56
Std Dev 3.018
Entry
ampl
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8
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0
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60
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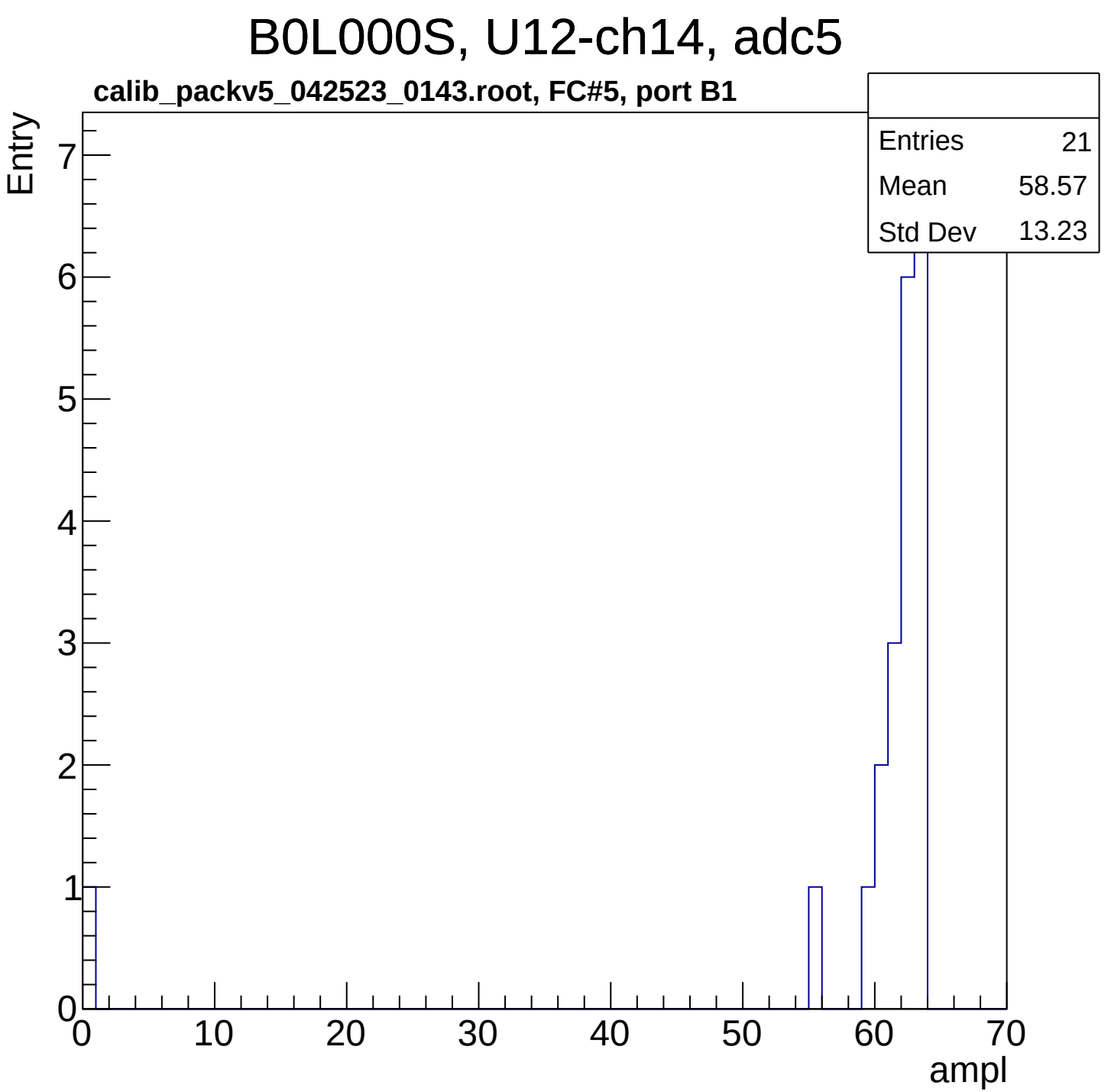
B0L000S, U12-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.57
Std Dev 13.23
Entry
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7
0
10
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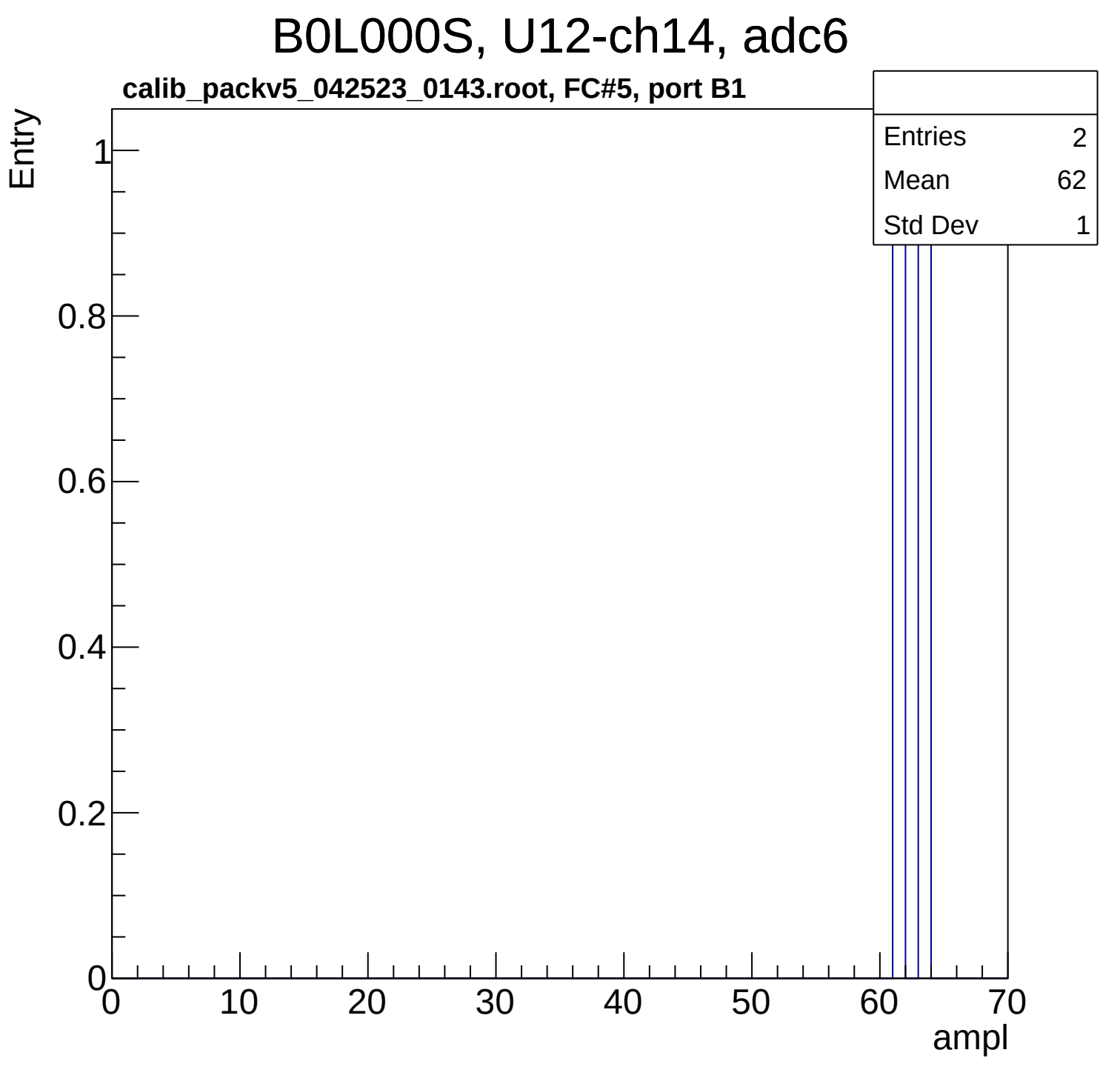
50
60
70
ampl


B0L000S, U12-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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0.6
0.4
0.2
0
0
10
20
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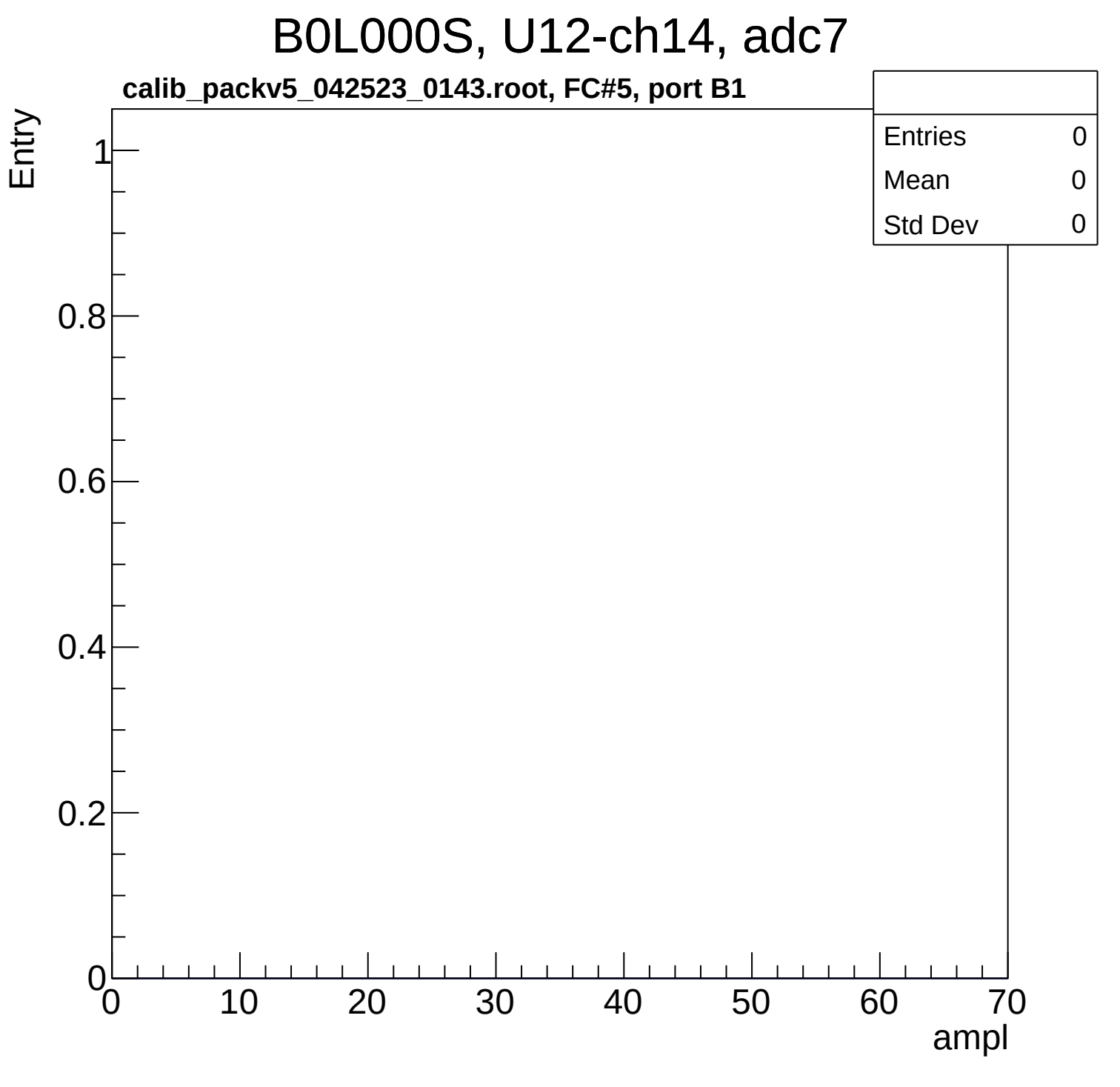
40
50
60
70
ampl


B0L000S, U12-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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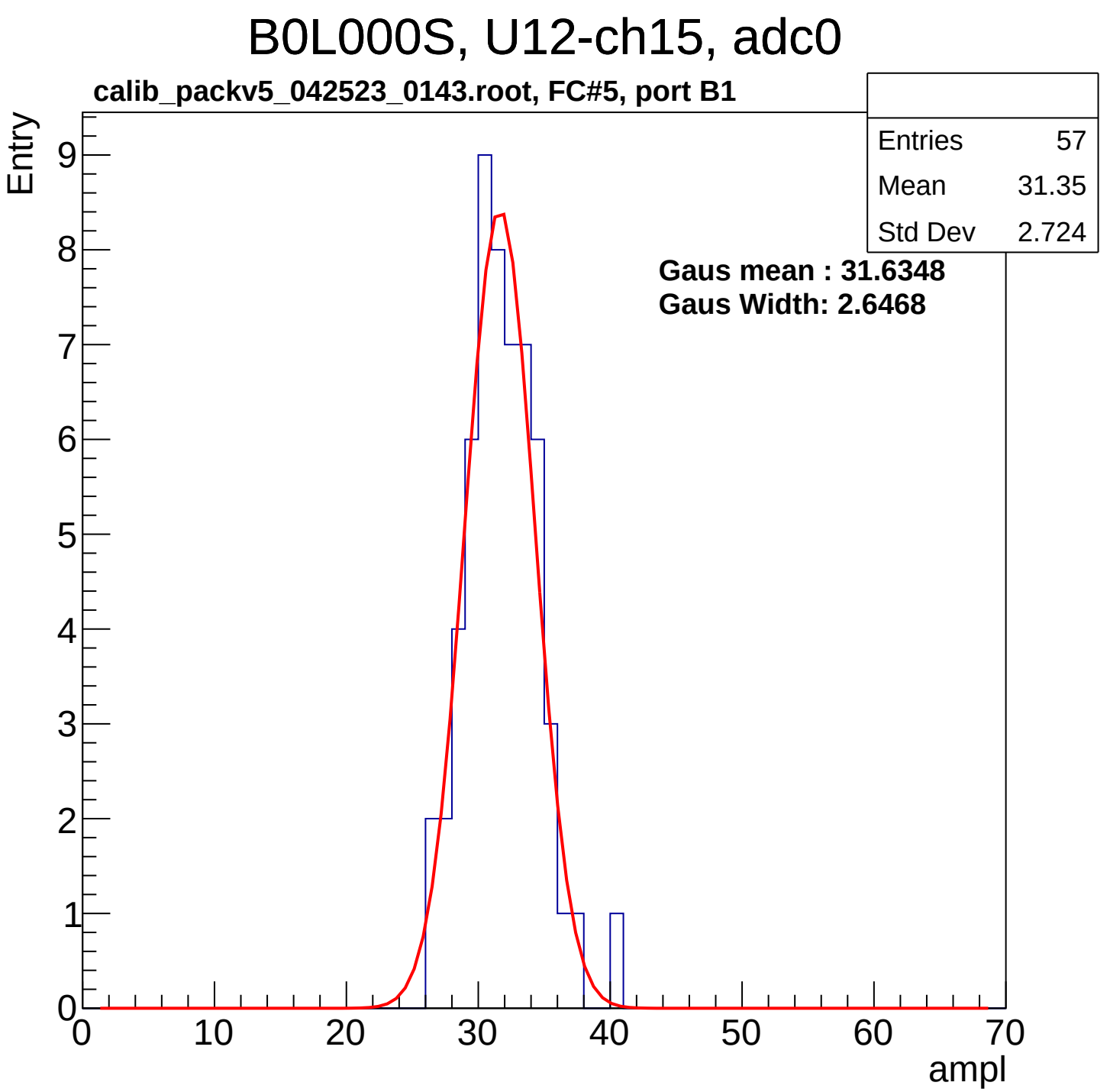
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 31.35
Std Dev 2.724
Gaus mean : 31.6348
Gaus Width: 2.6468
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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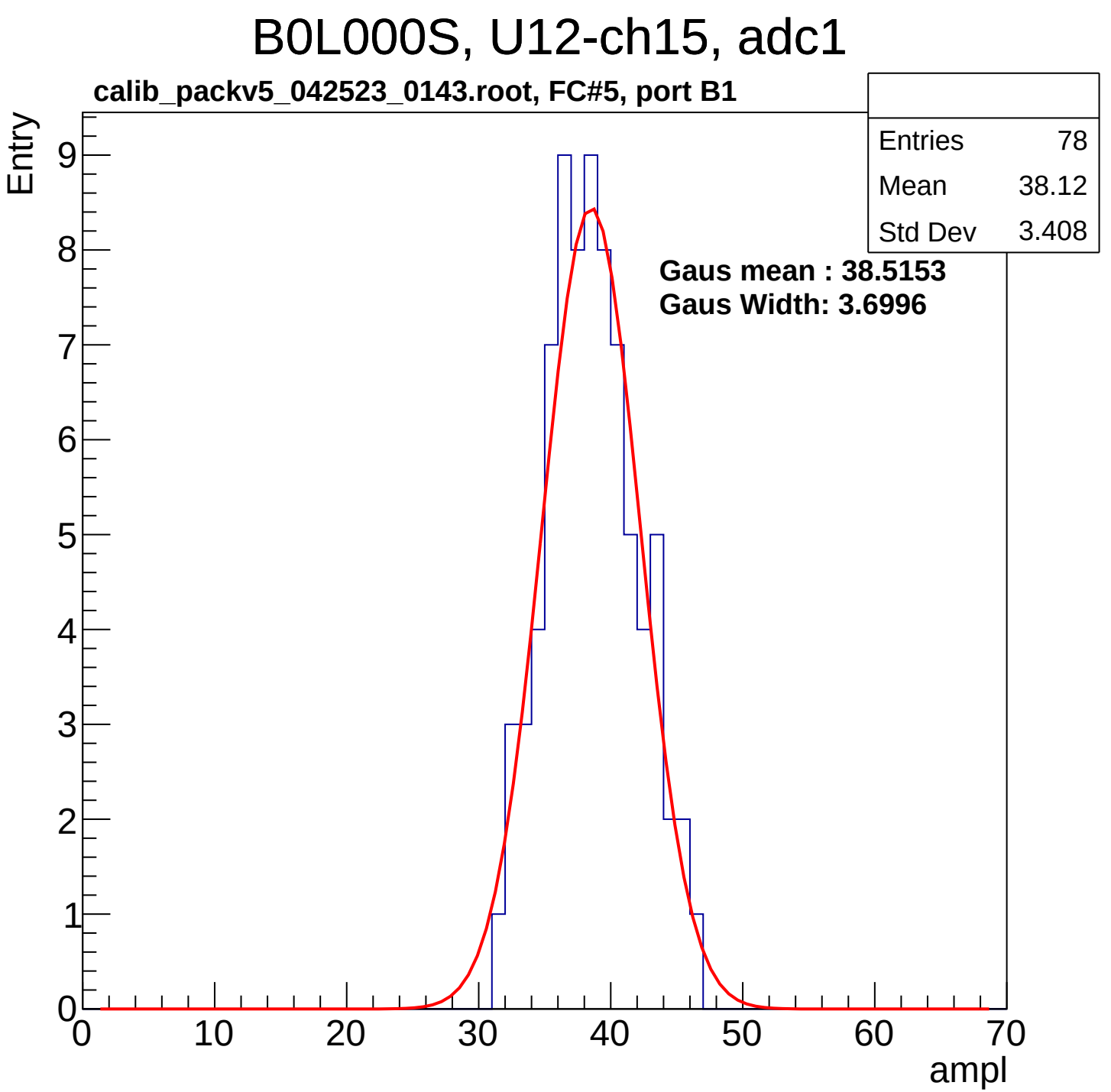
B0L000S, U12-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 38.12
Std Dev 3.408
Gaus mean : 38.5153
Gaus Width: 3.6996
Entry
ampl
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3
4
5
6
7
8
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0
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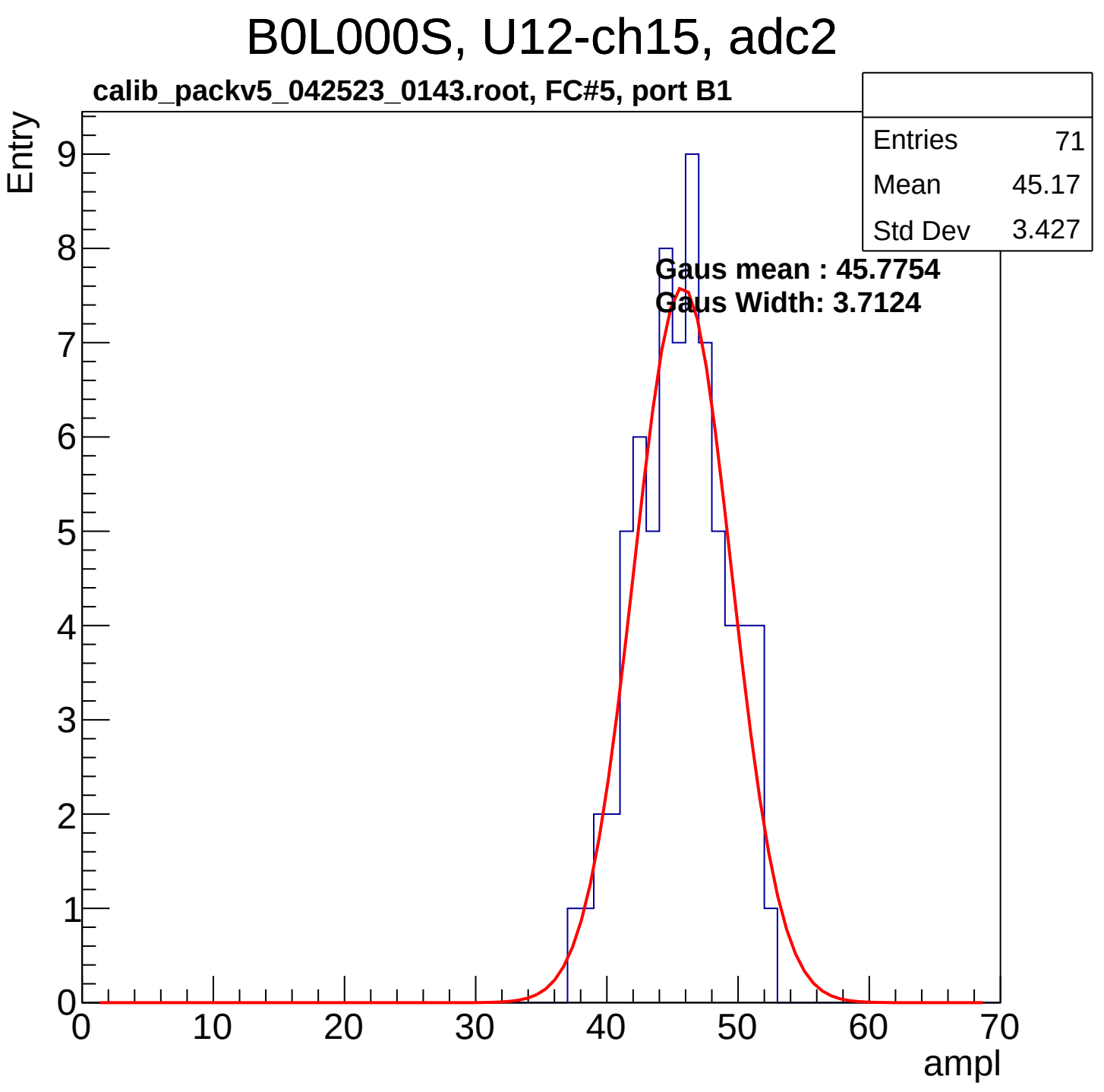
B0L000S, U12-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 45.17
Std Dev 3.427
Gaus mean : 45.7754
Gaus Width: 3.7124
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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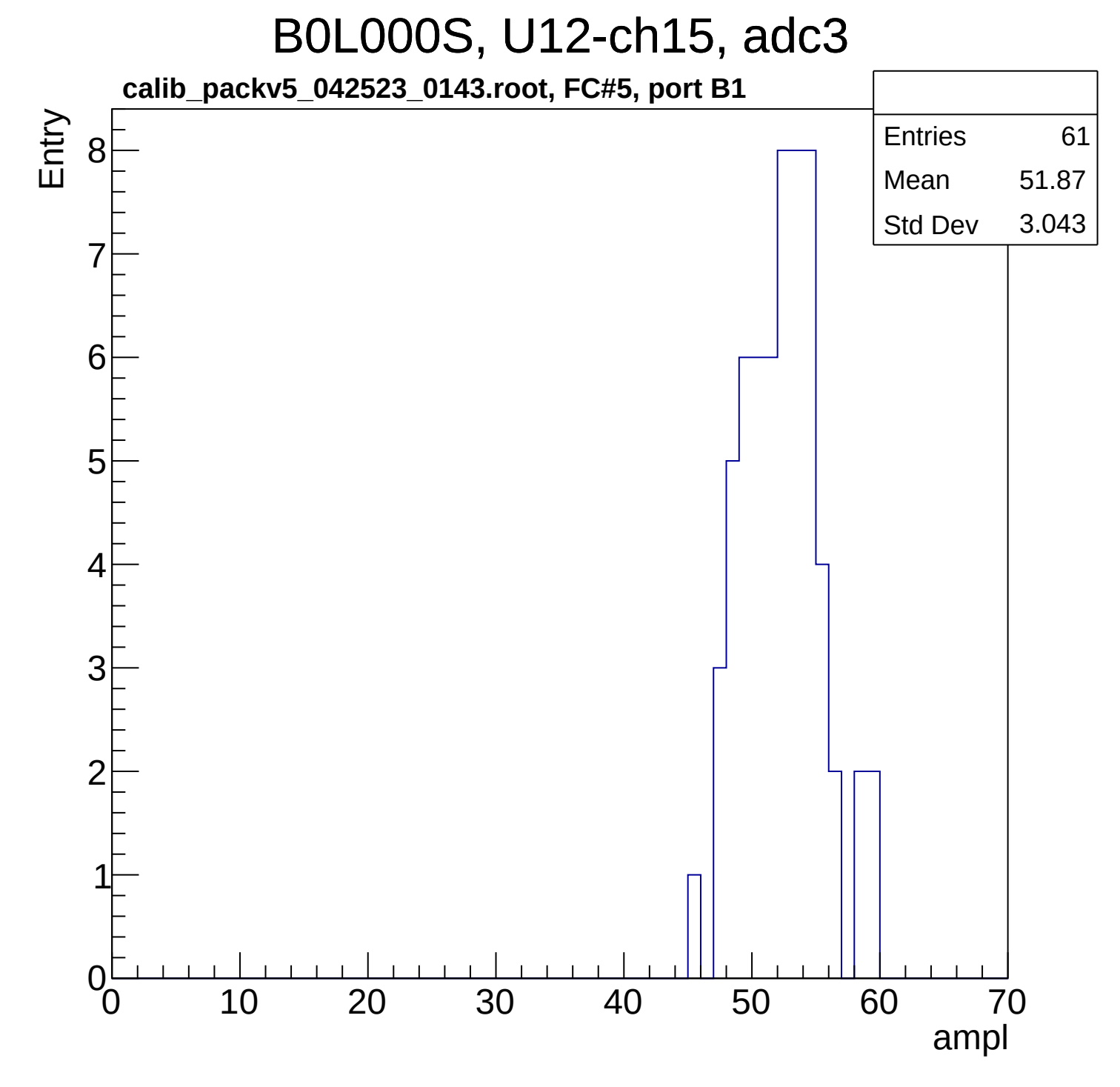
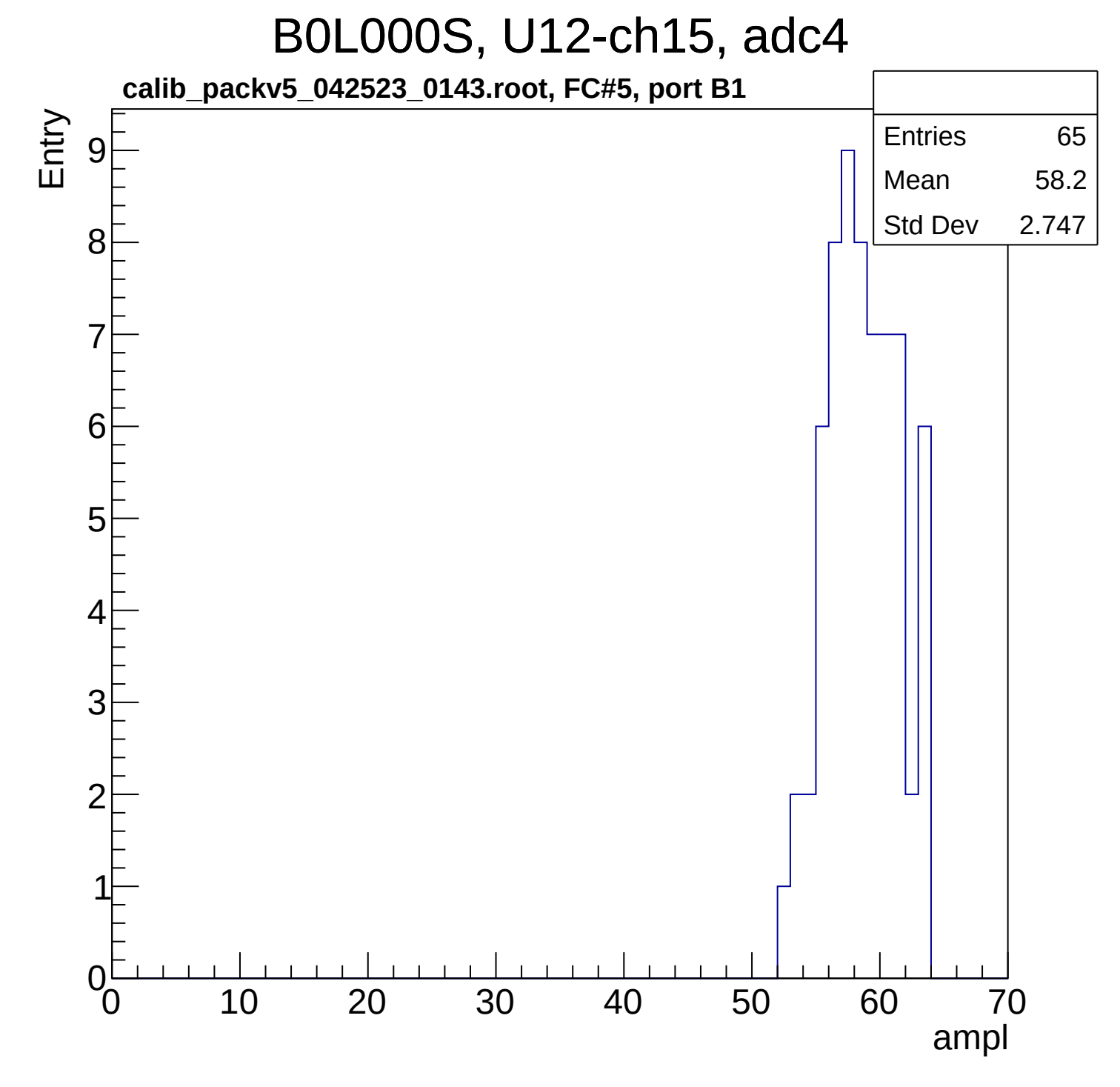

B0L000S, U12-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 51.87
Std Dev 3.043
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 58.2
Std Dev 2.747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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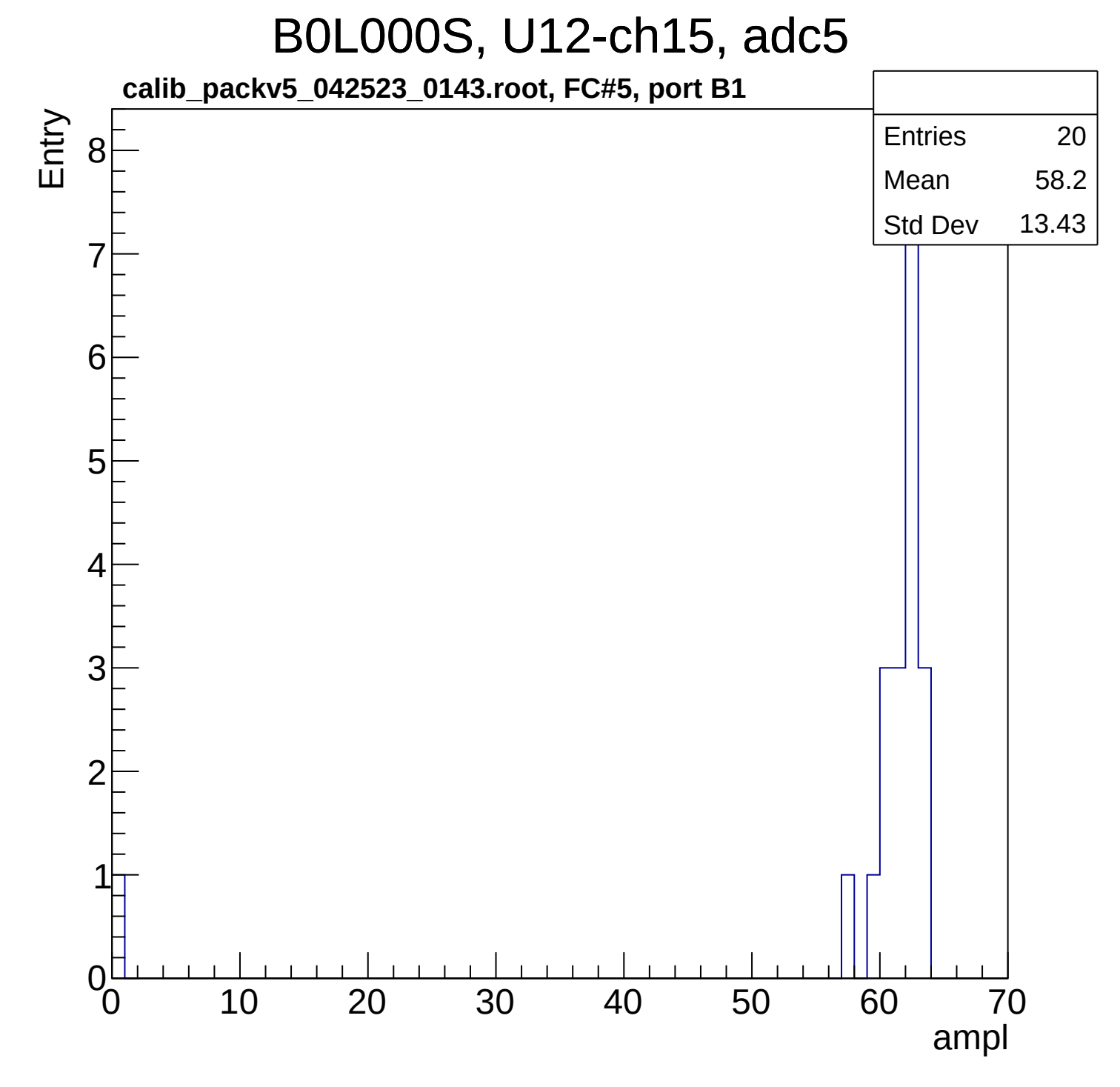
40
50
60
70


B0L000S, U12-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.2
Std Dev 13.43
Entry
ampl
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1
2
3
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5
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8
0
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40
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60
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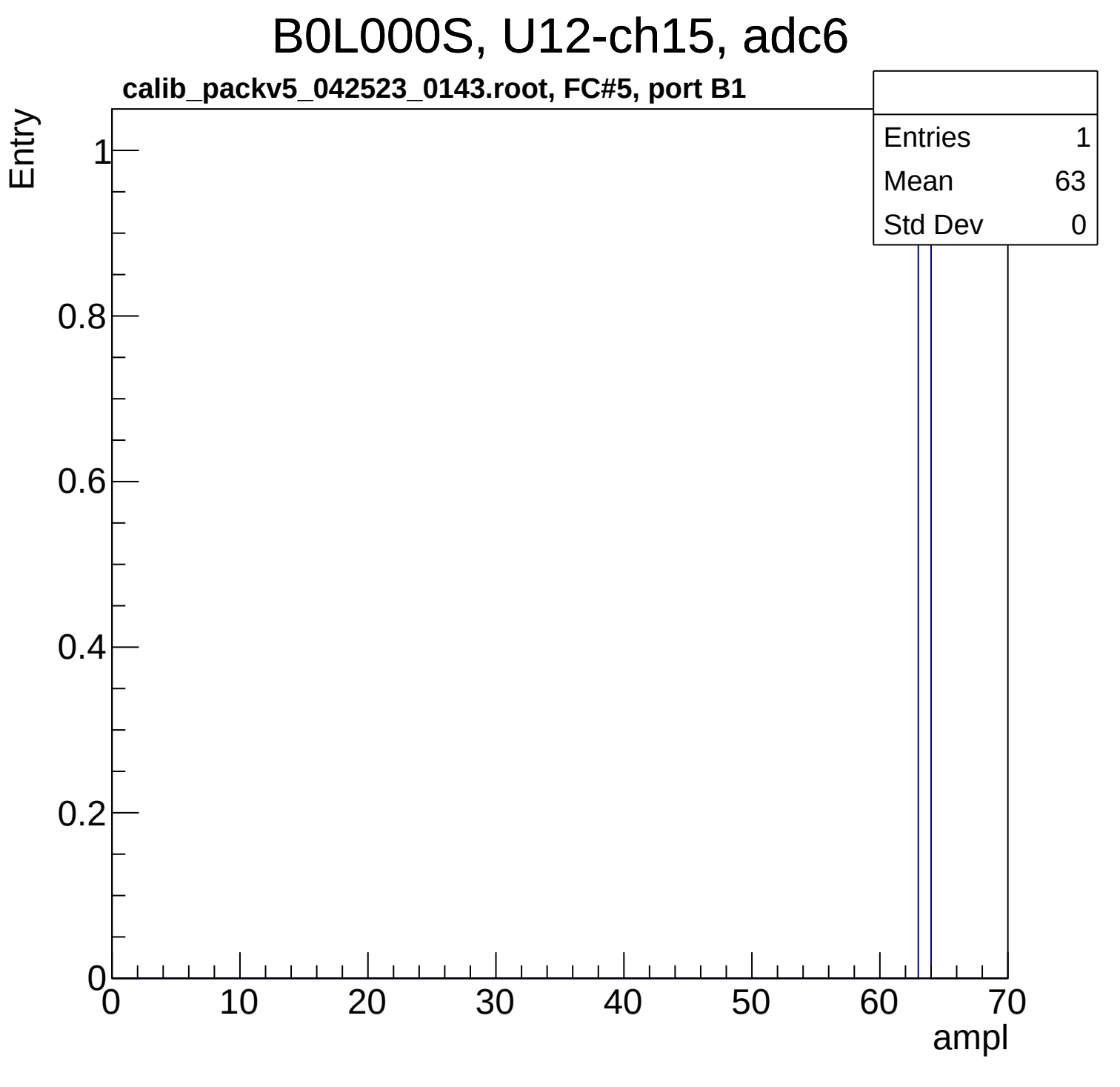
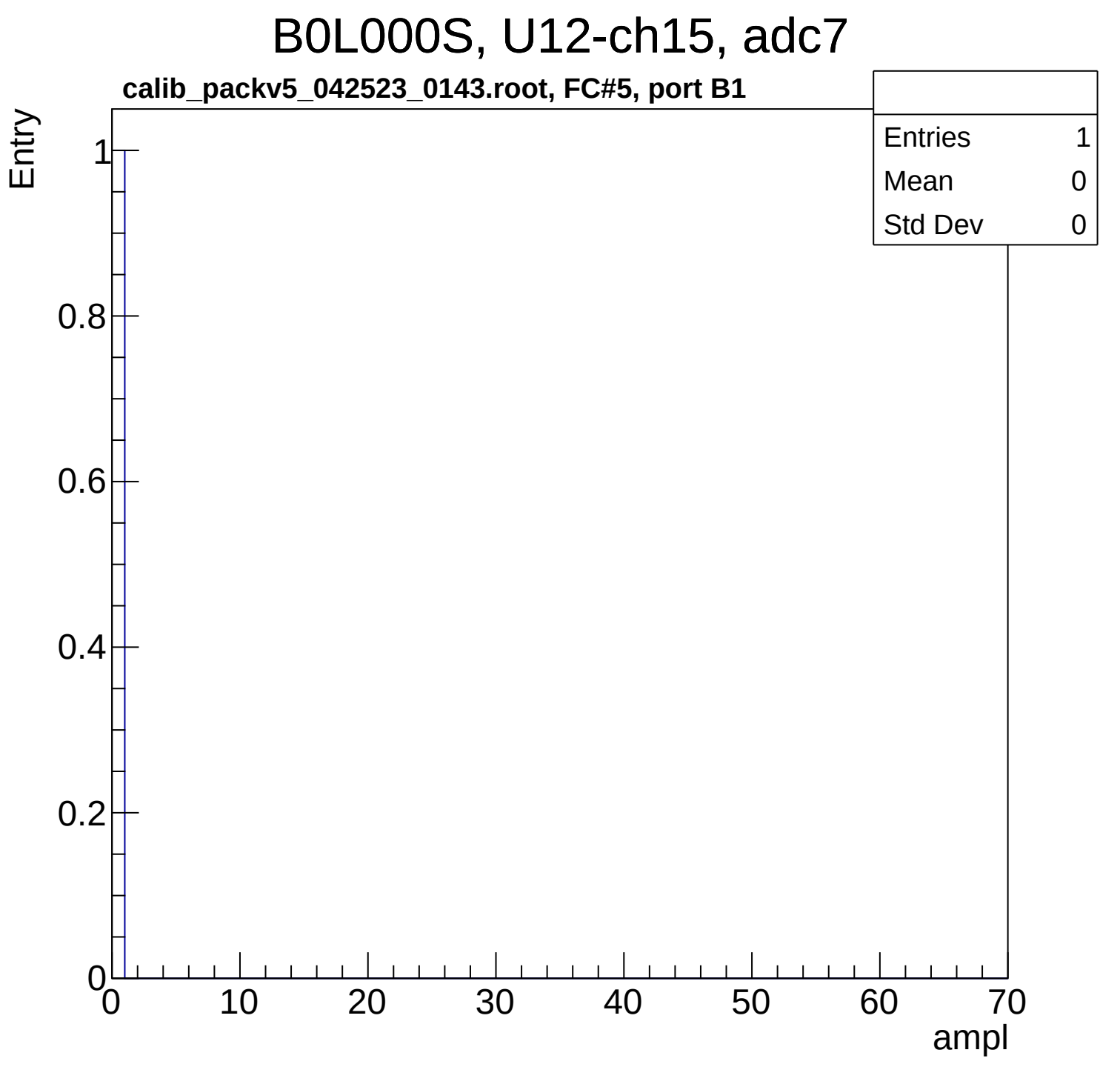
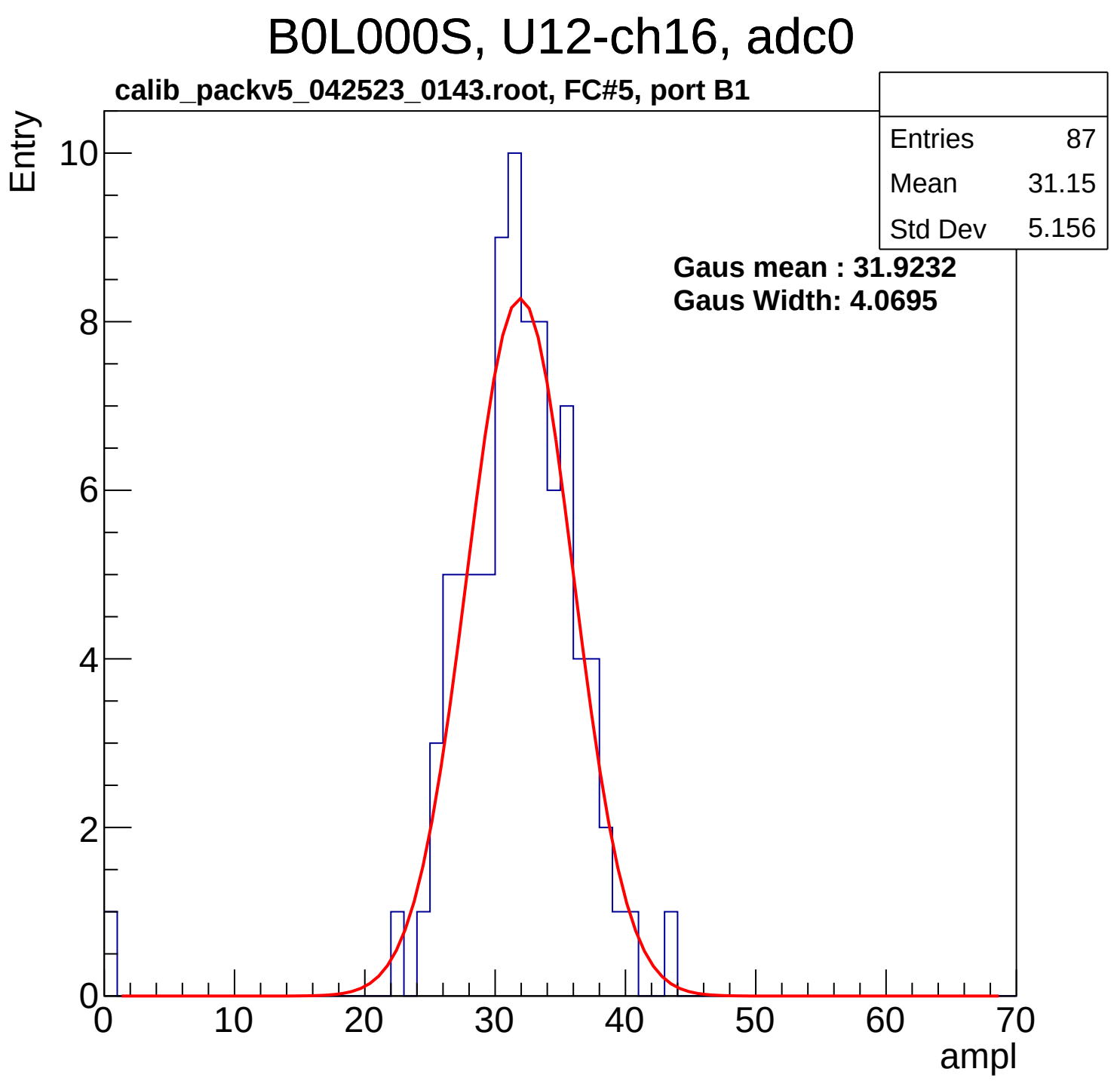

B0L000S, U12-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 31.15
Std Dev 5.156
Gaus mean : 31.9232
Gaus Width: 4.0695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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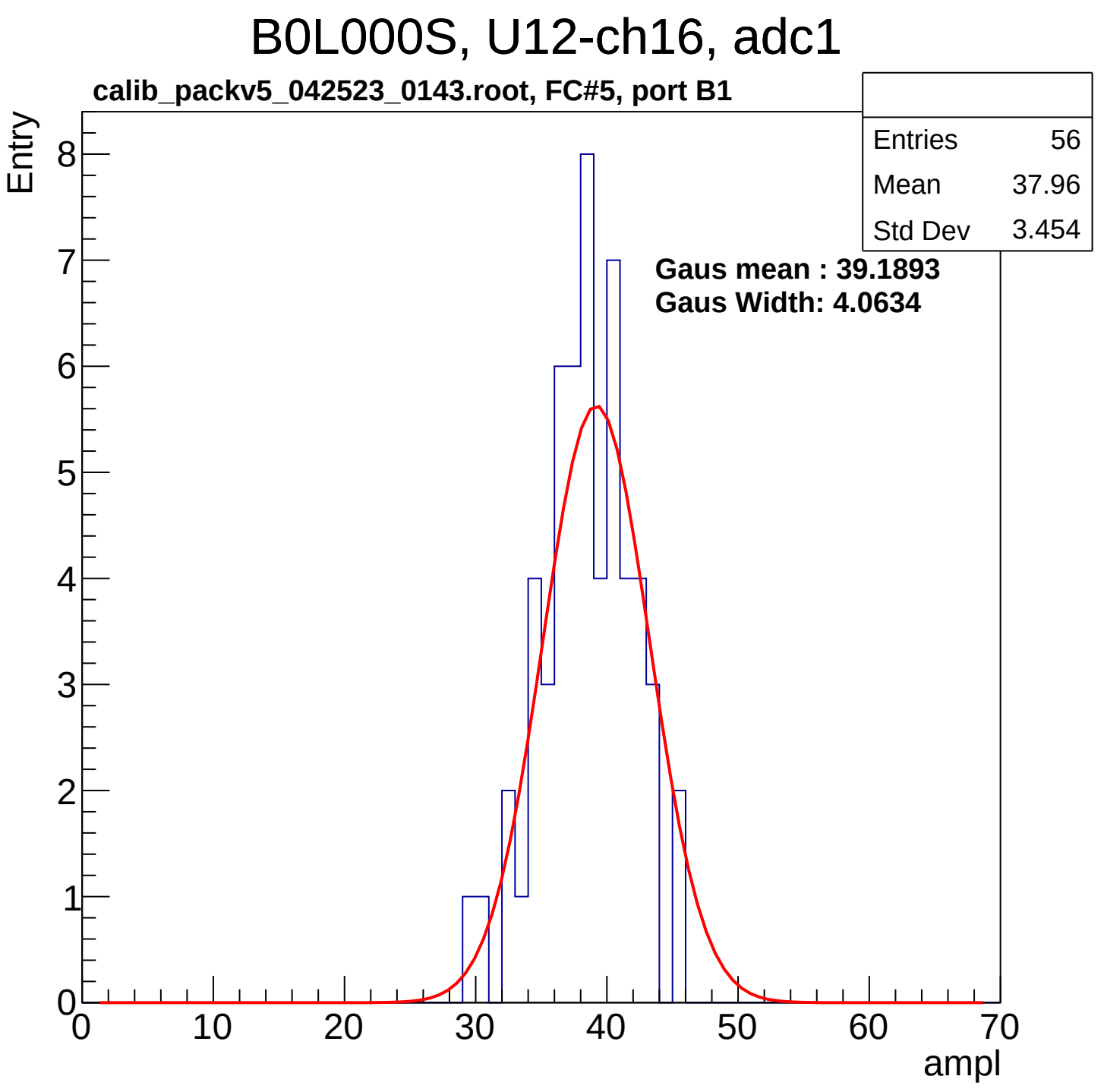
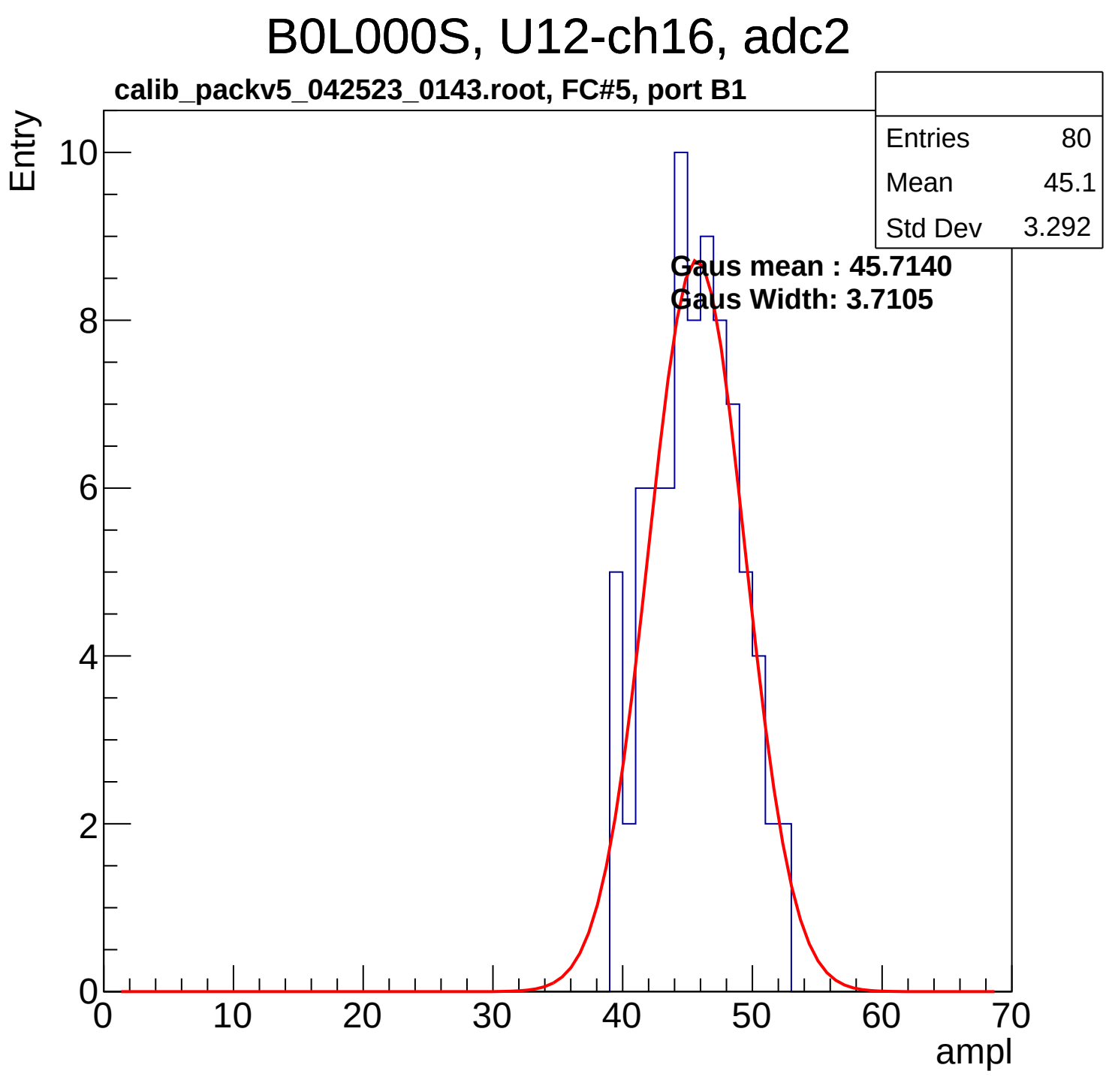

B0L000S, U12-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.96
Std Dev 3.454
Gaus mean : 39.1893
Gaus Width: 4.0634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 45.1
Std Dev 3.292
Gaus mean : 45.7140
Gaus Width: 3.7105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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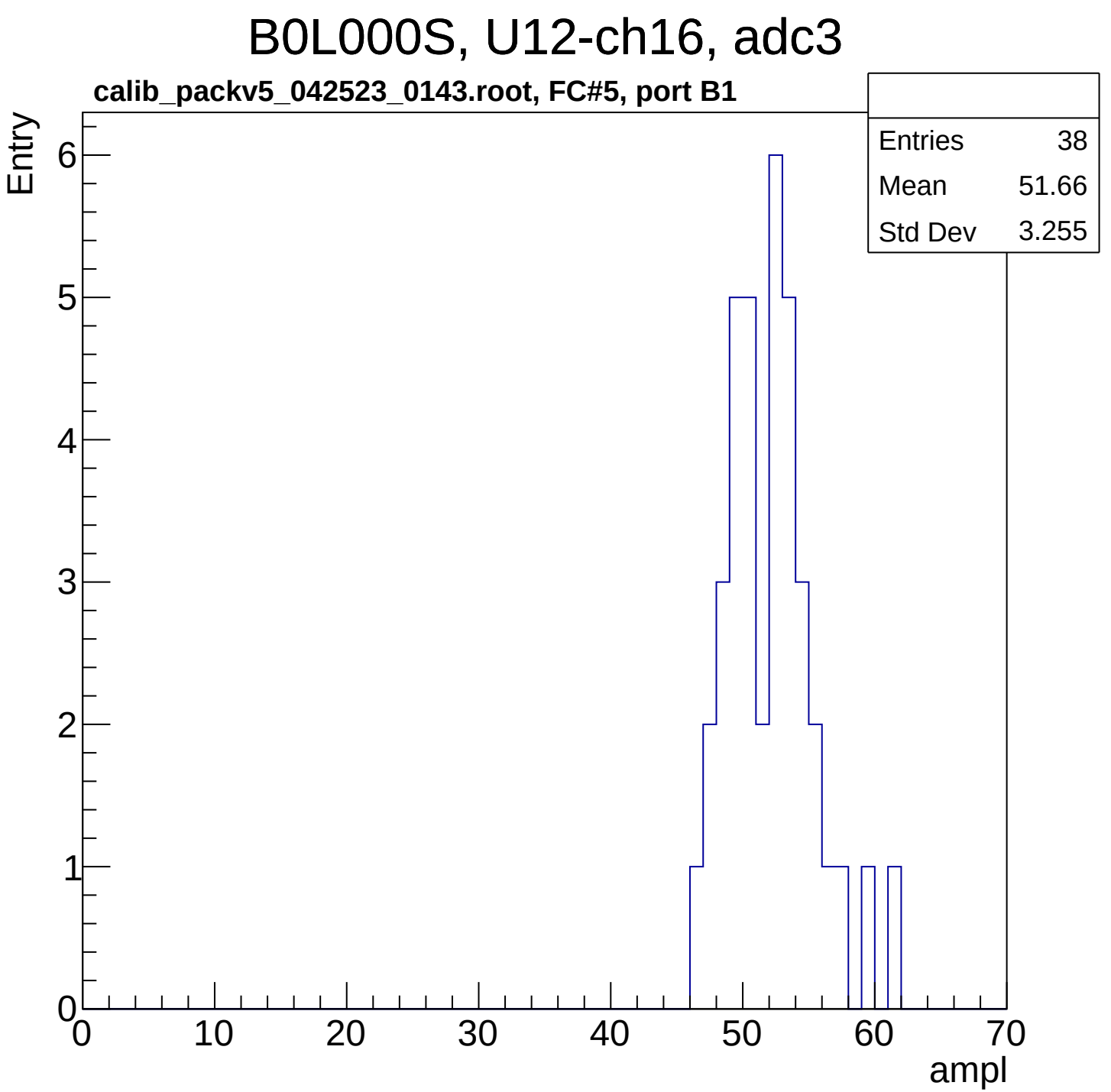
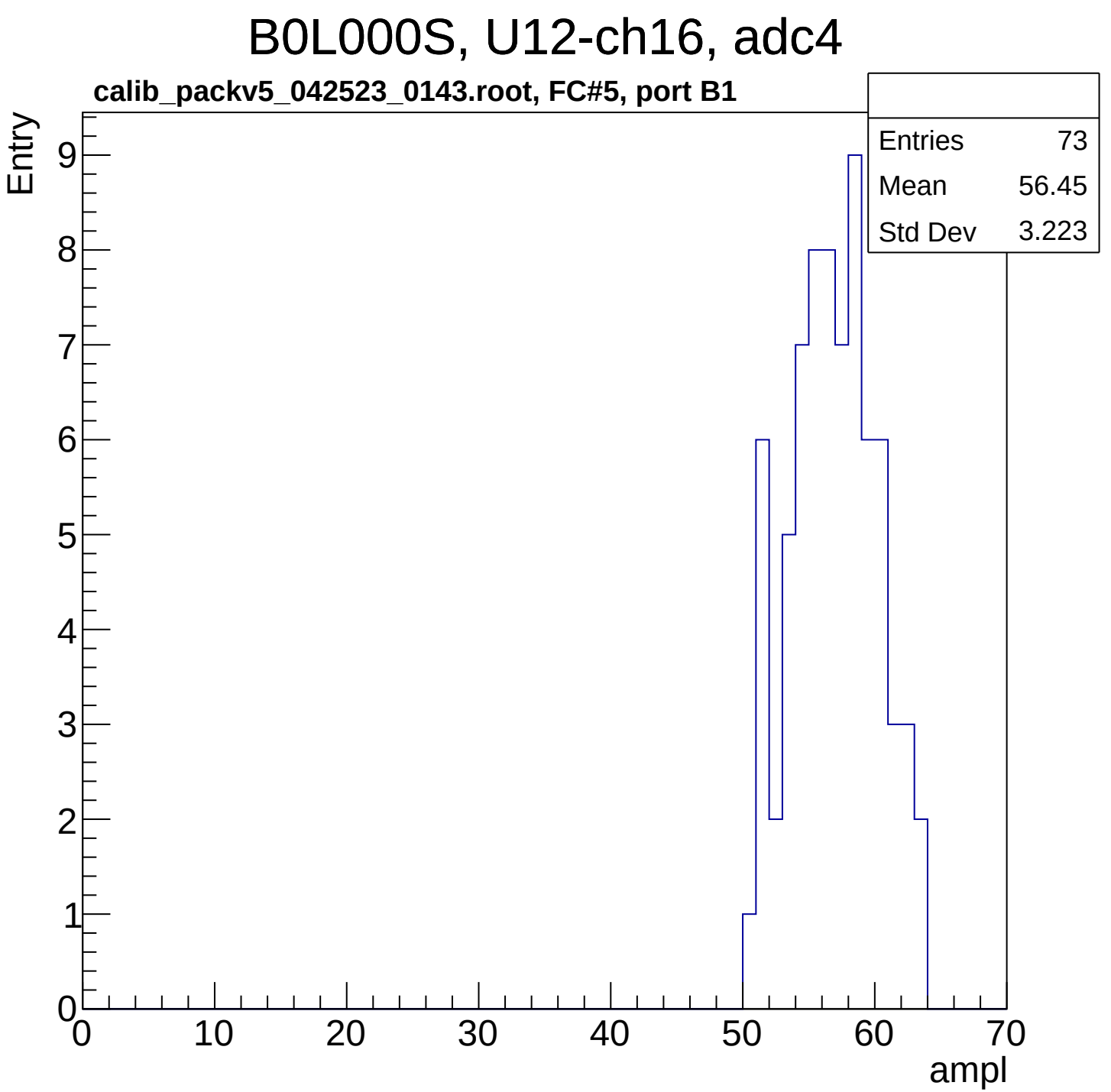
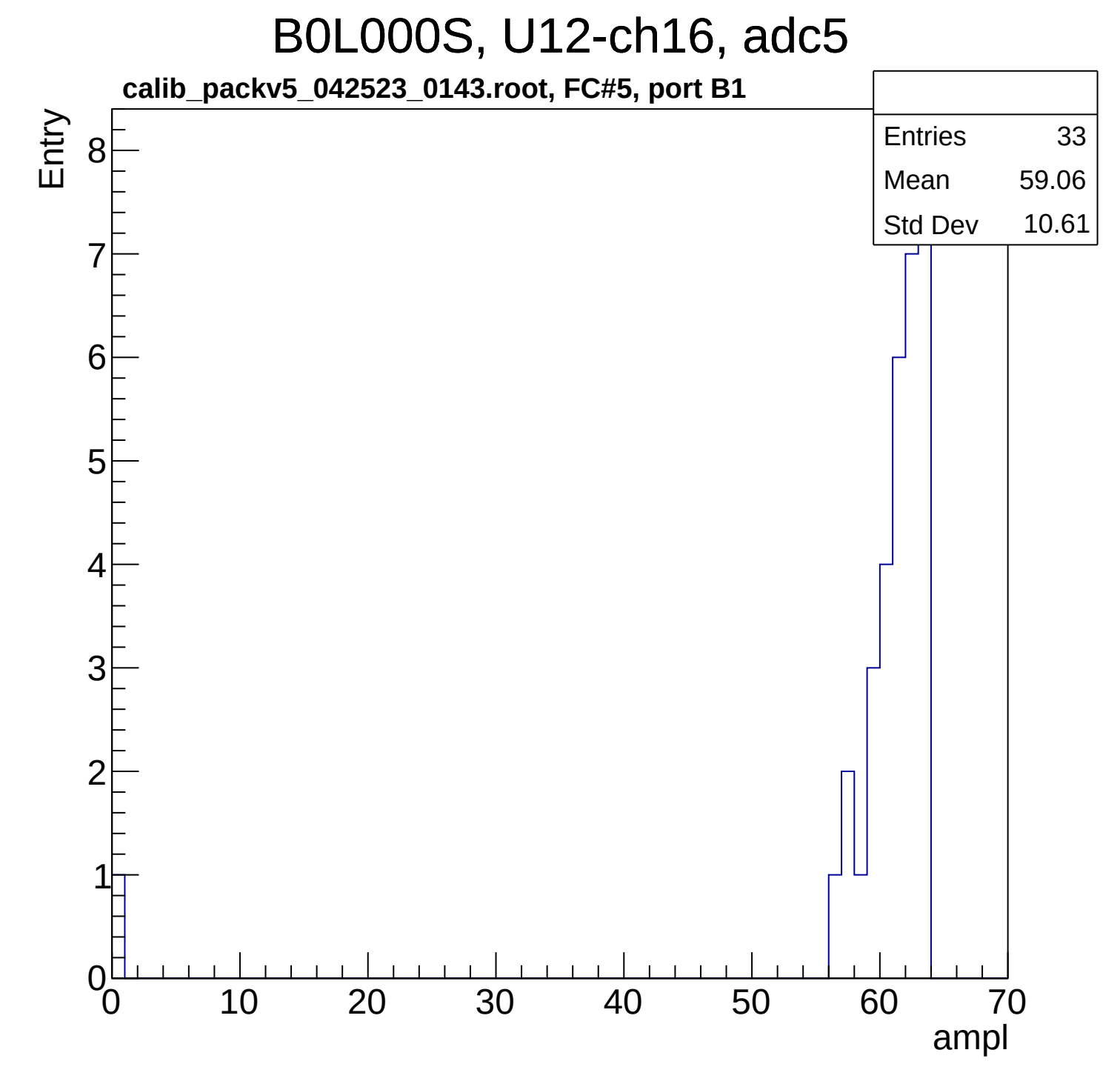
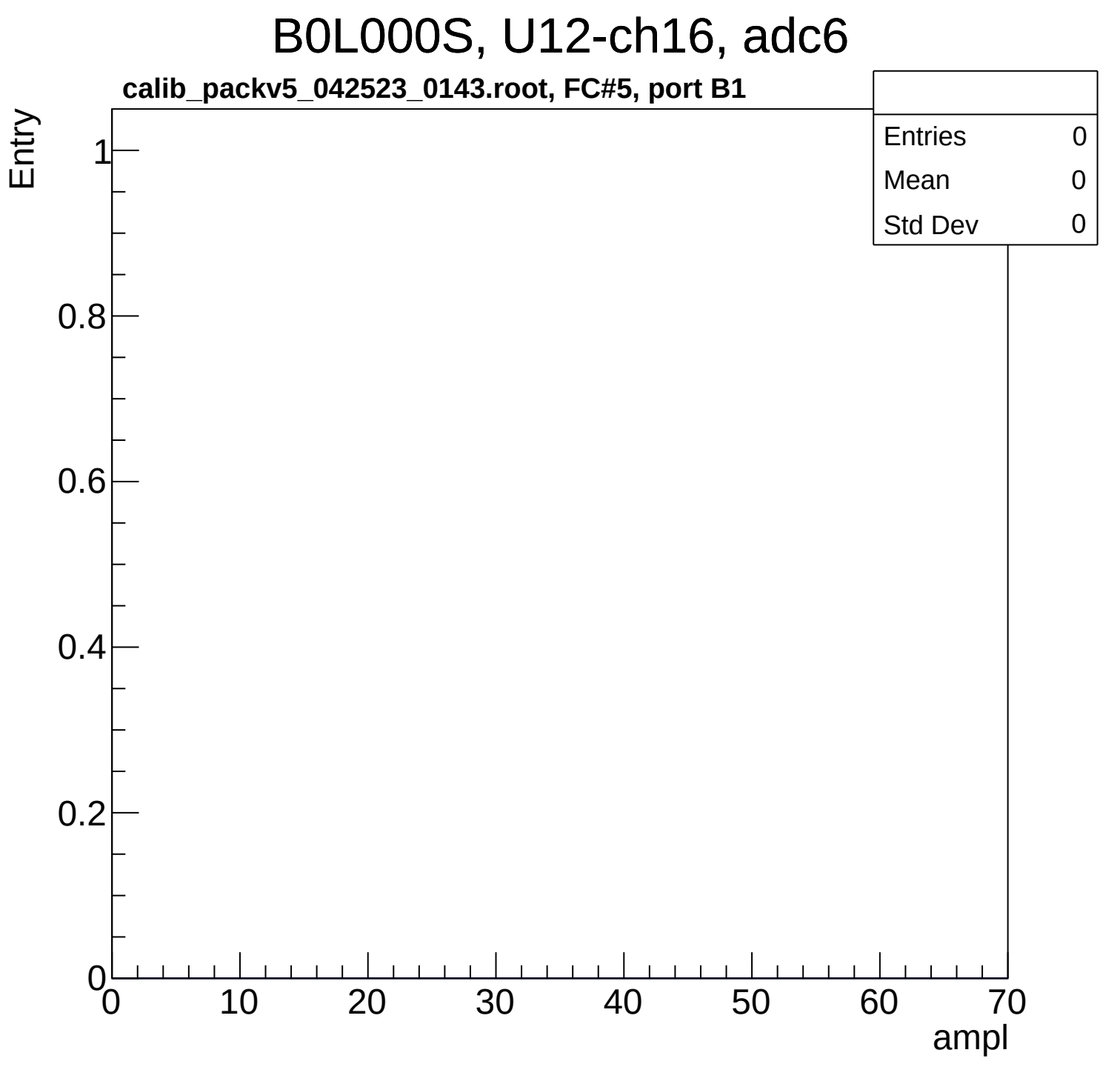
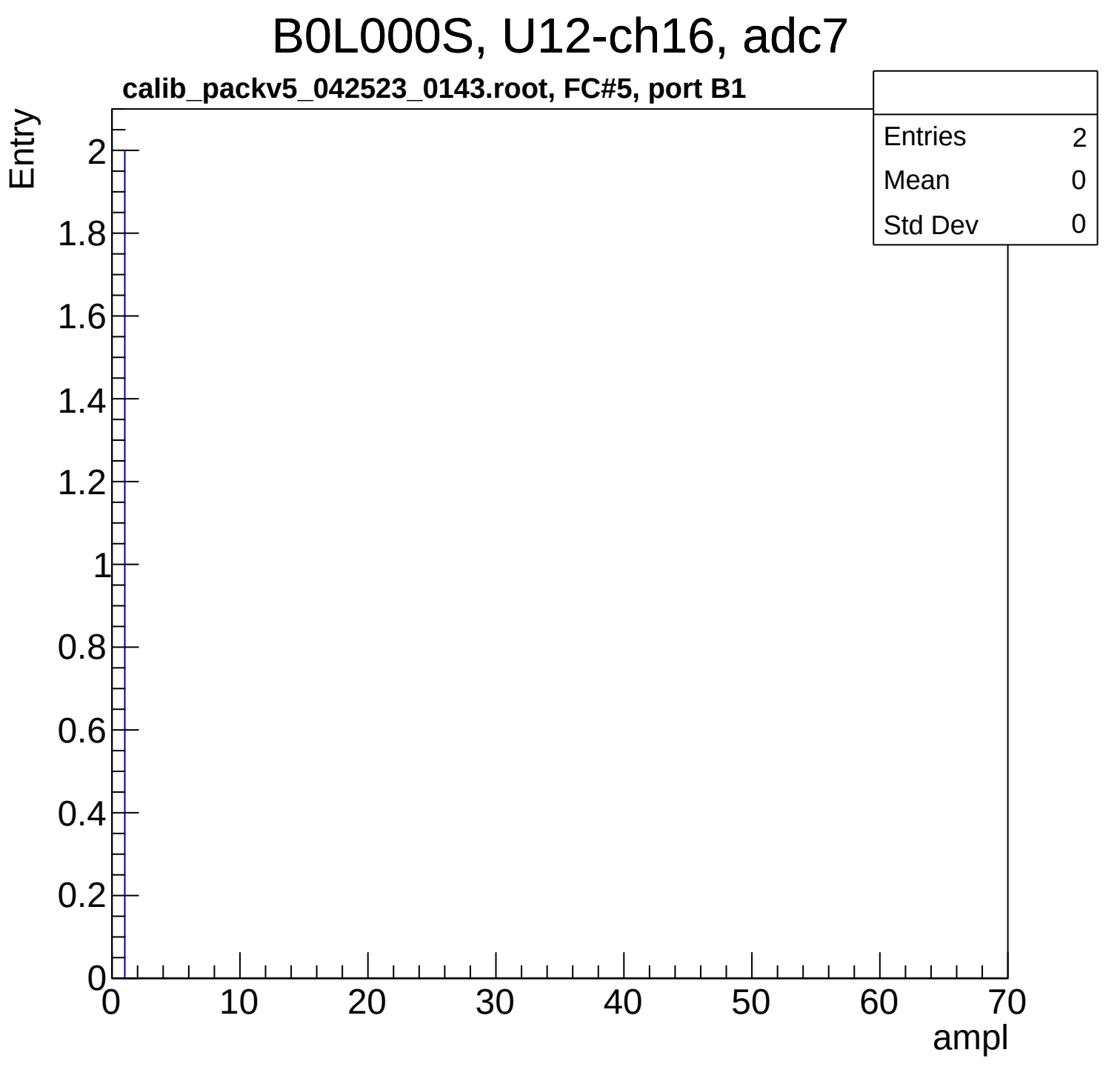

B0L000S, U12-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 51.66
Std Dev 3.255
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 56.45
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 59.06
Std Dev 10.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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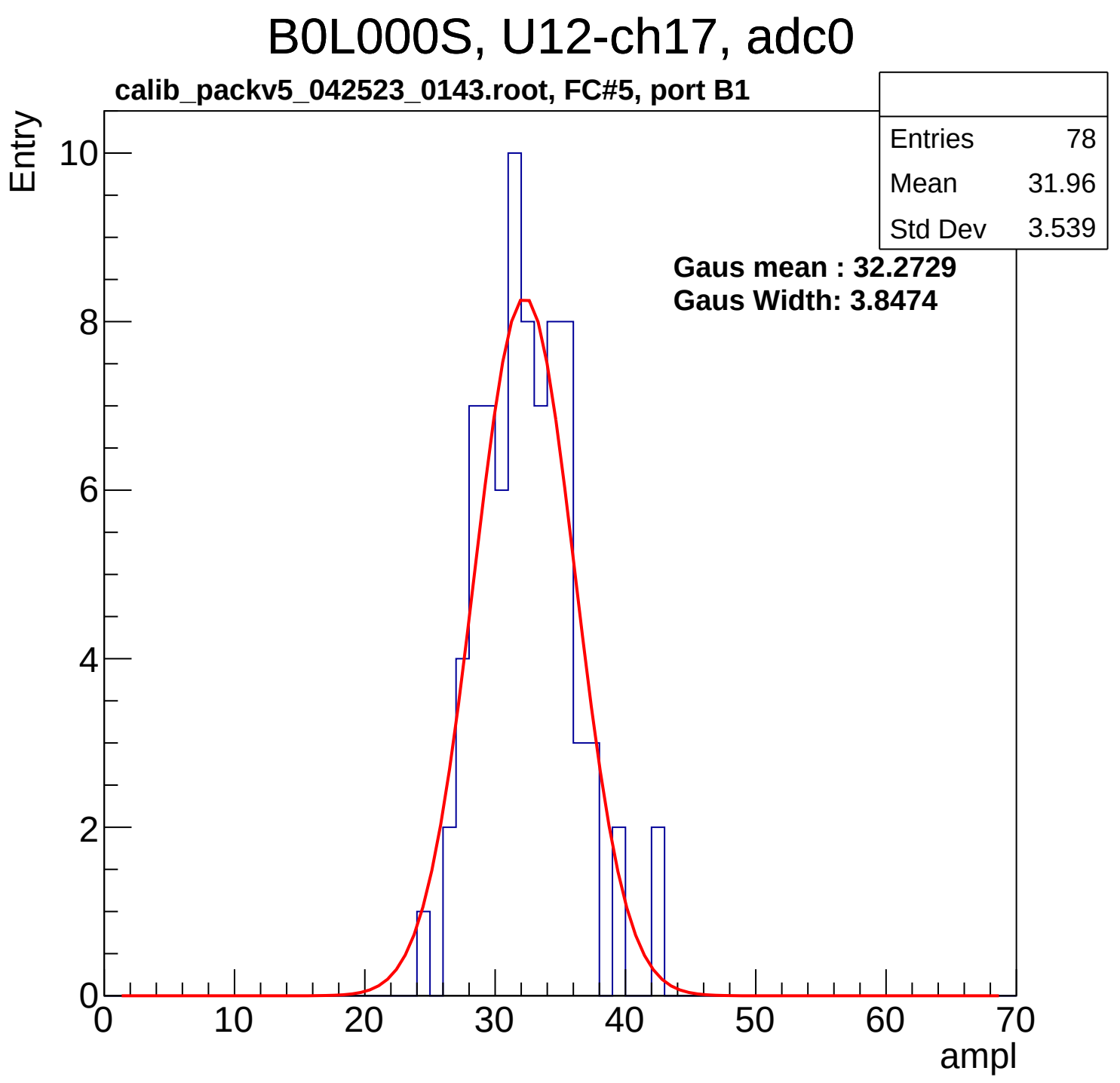
B0L000S, U12-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 31.96
Std Dev 3.539
Gaus mean : 32.2729
Gaus Width: 3.8474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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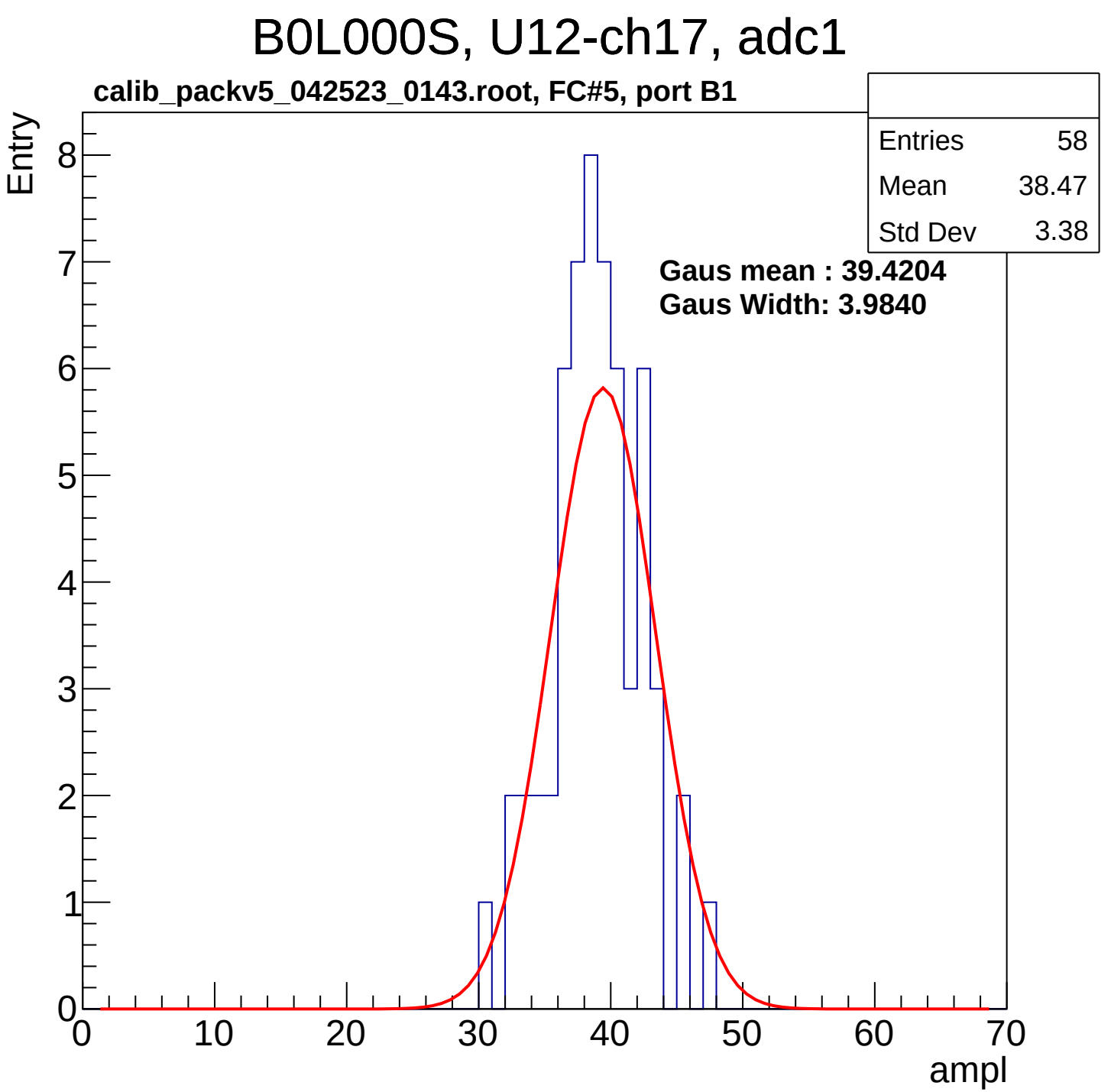
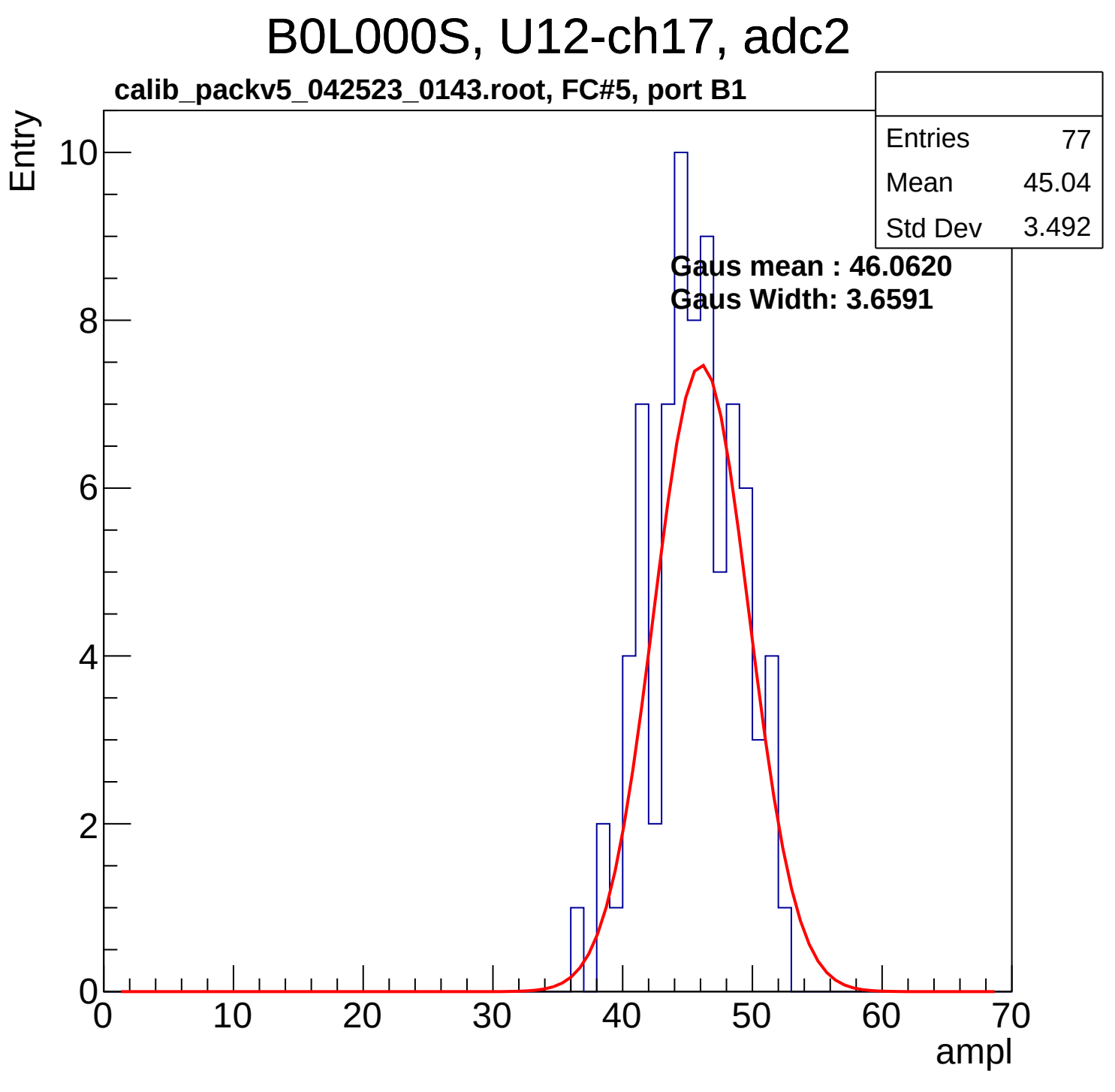
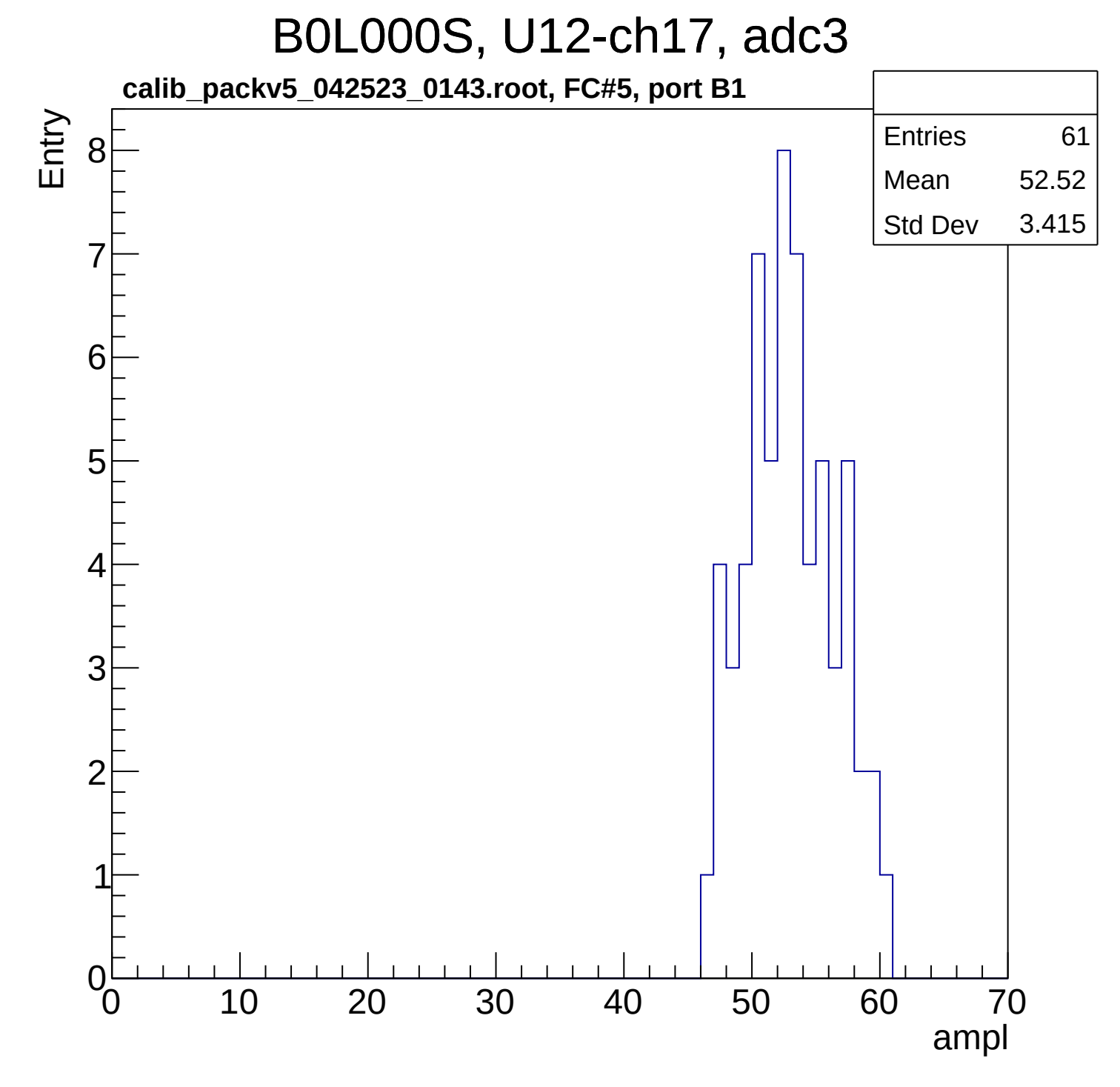
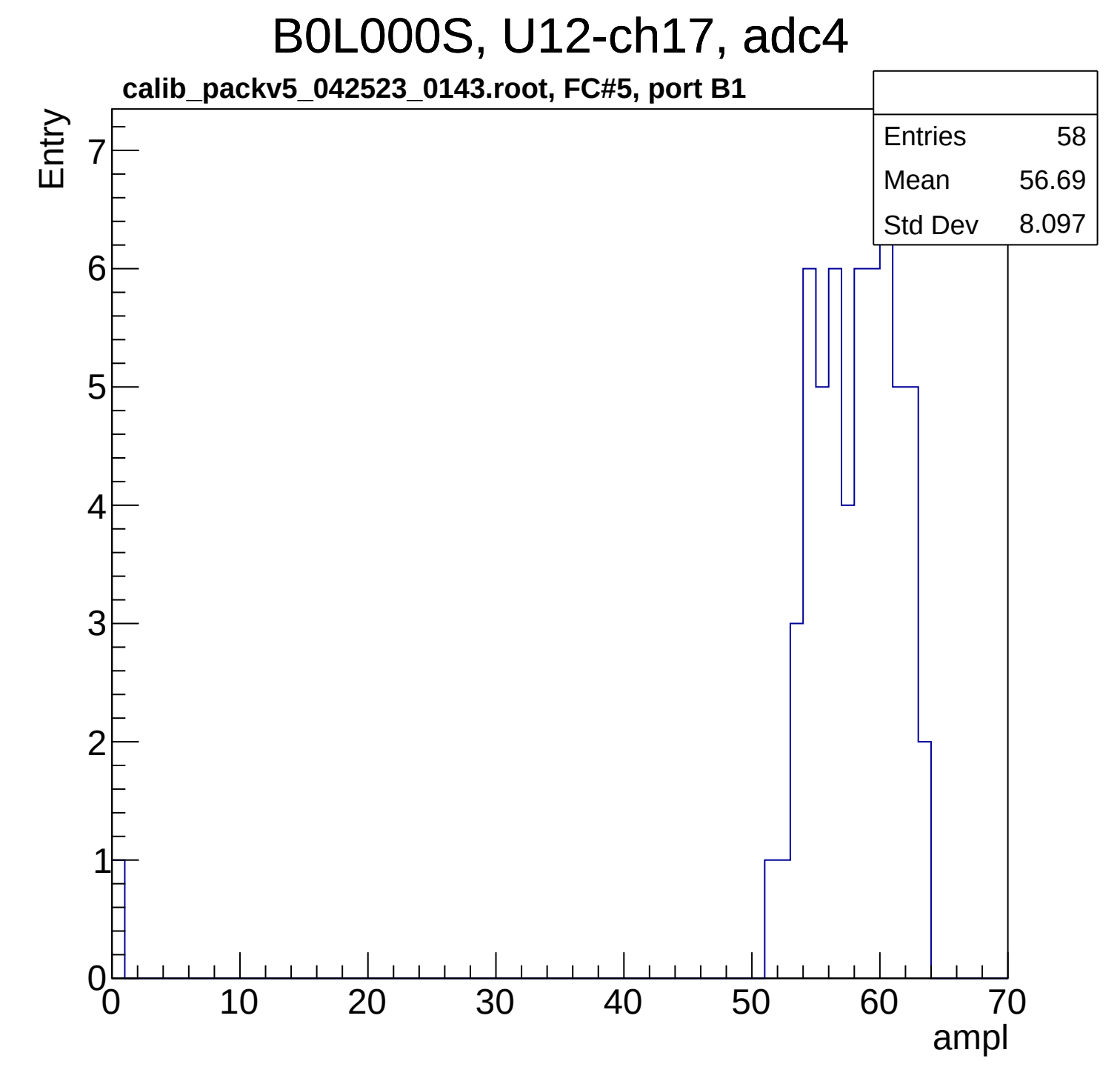
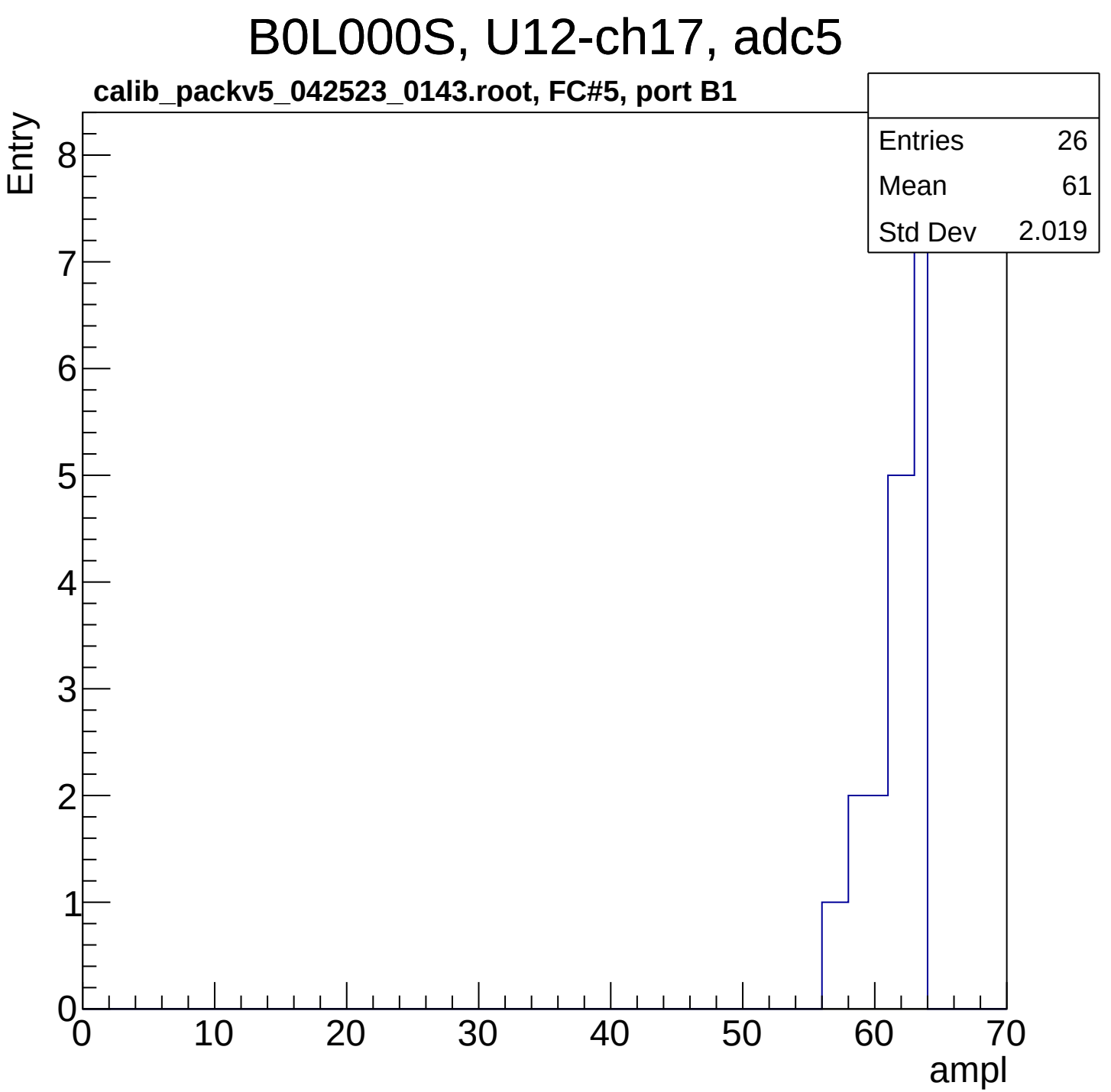

B0L000S, U12-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 38.47
Std Dev 3.38
Gaus mean : 39.4204
Gaus Width: 3.9840
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 45.04
Std Dev 3.492
Gaus mean : 46.0620
Gaus Width: 3.6591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 52.52
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.69
Std Dev 8.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 61
Std Dev 2.019
Entry
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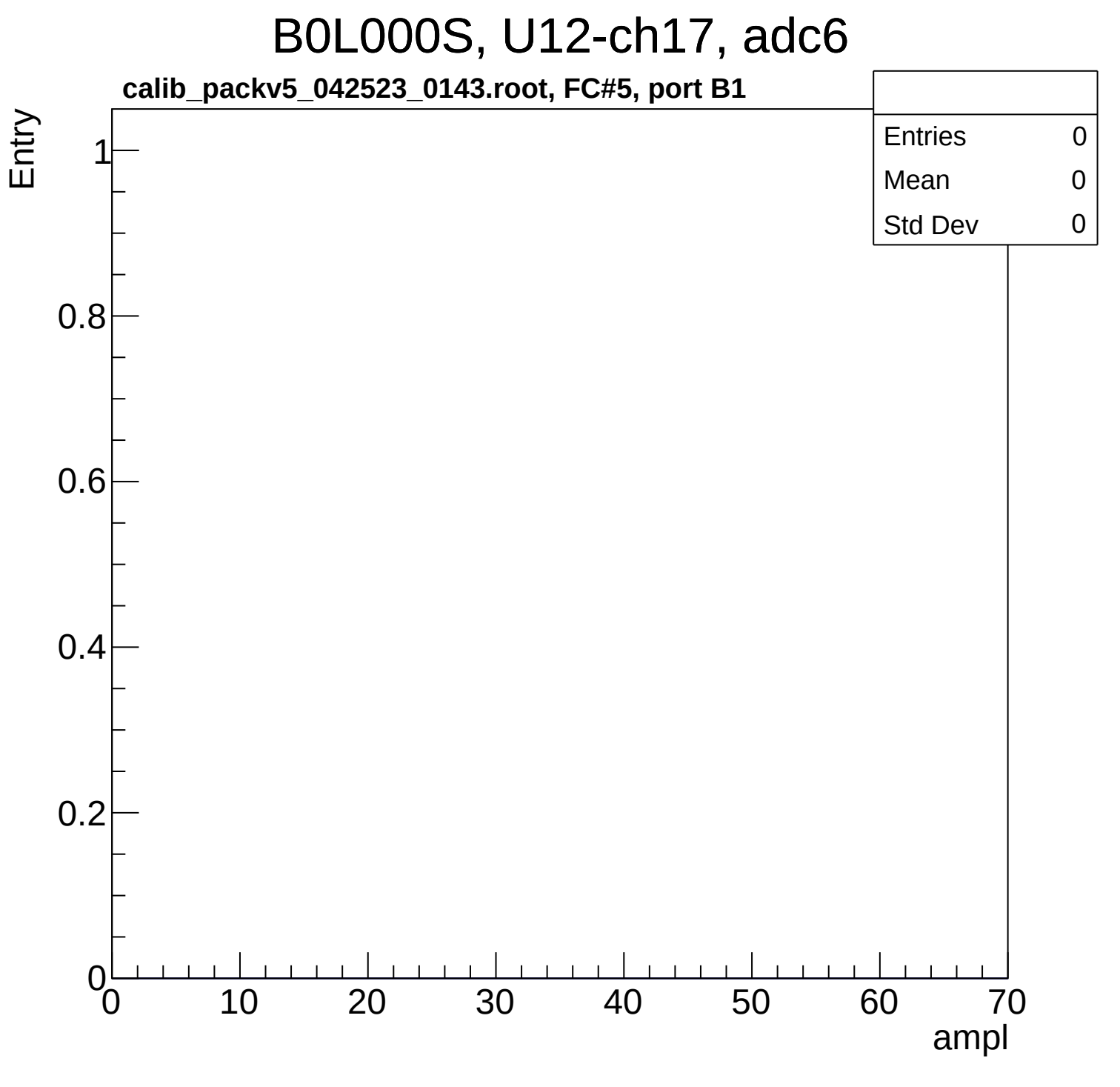
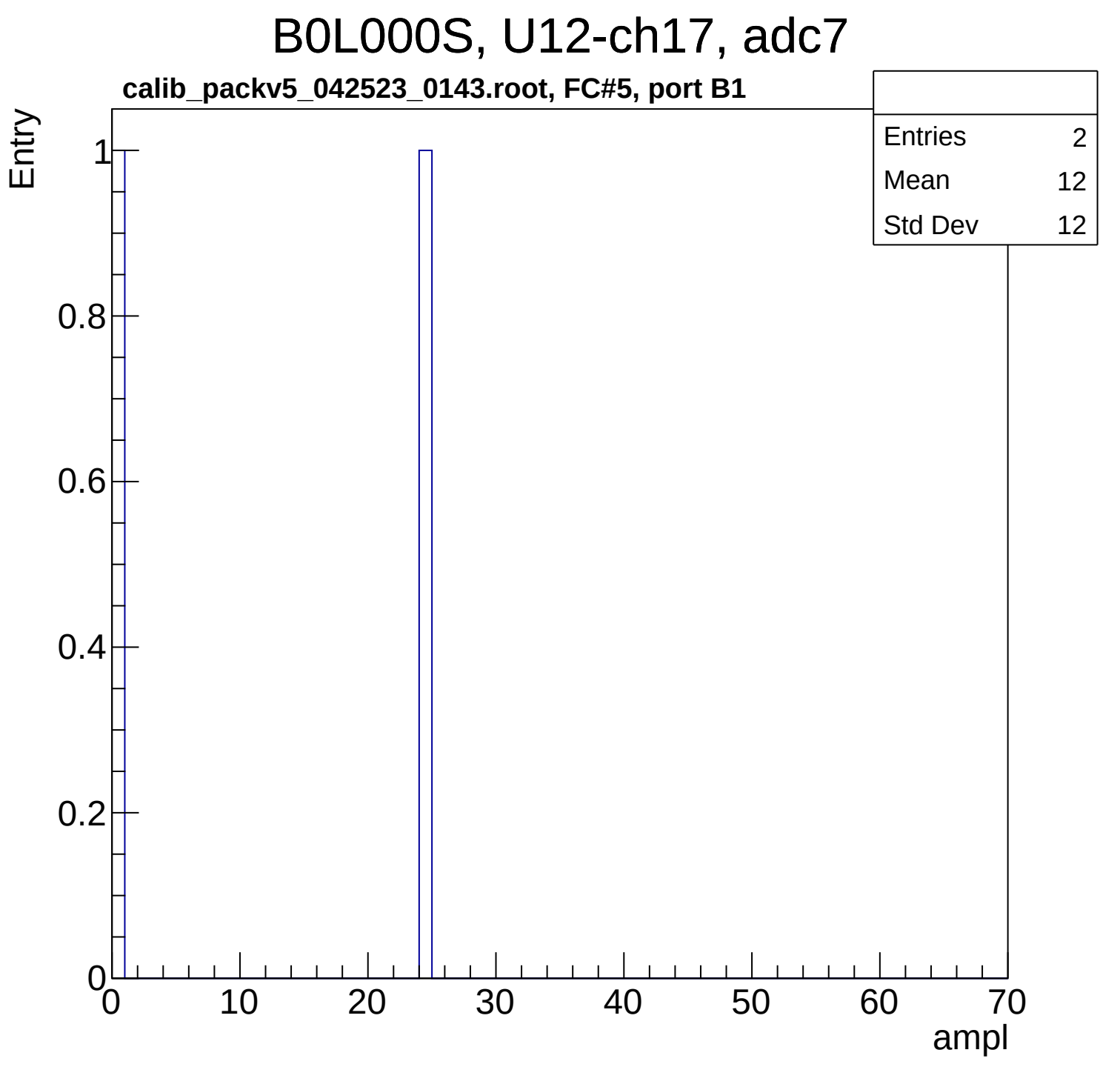
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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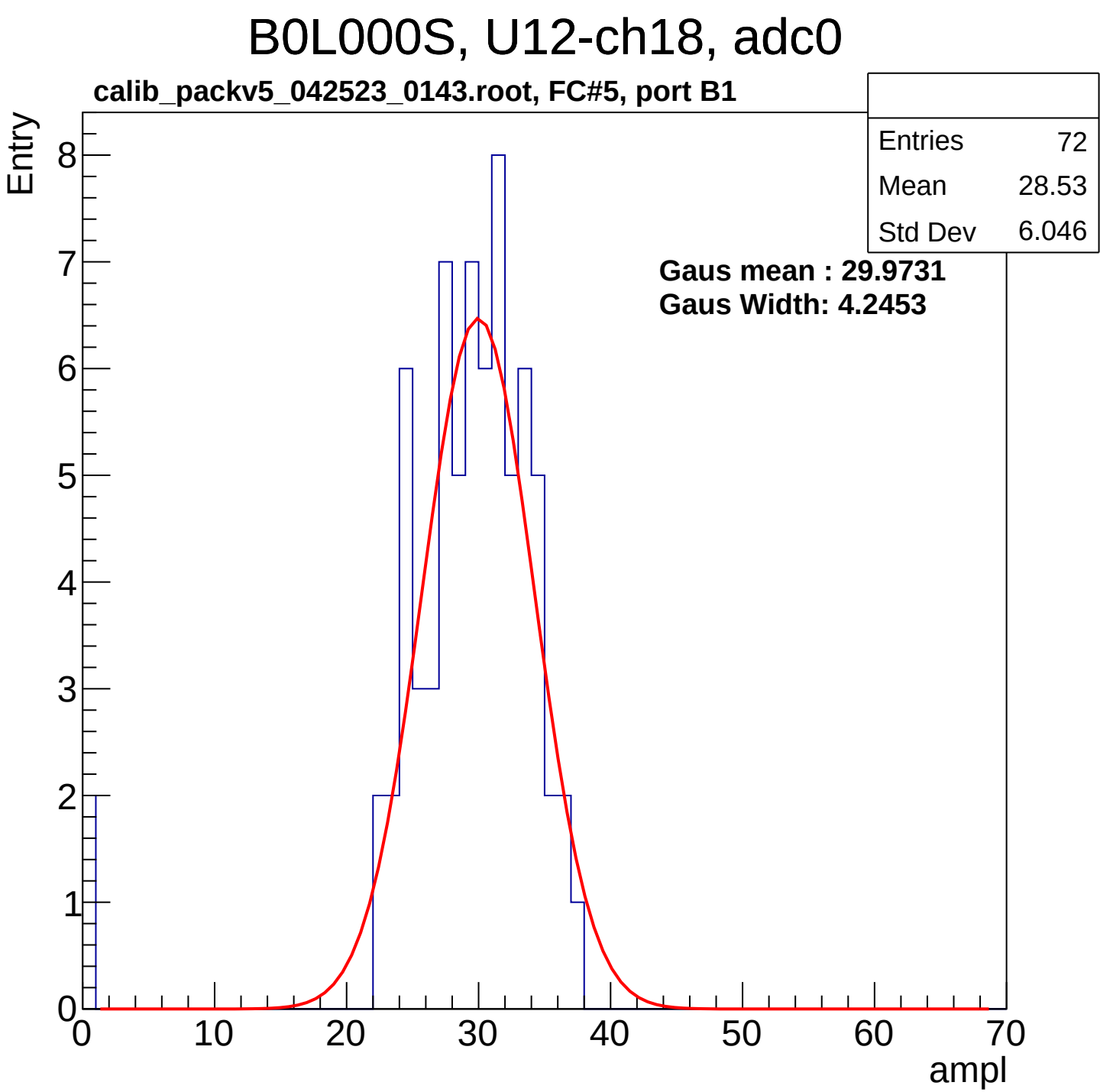
50
60
70
ampl


B0L000S, U12-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.53
Std Dev 6.046
Gaus mean : 29.9731
Gaus Width: 4.2453
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
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40
50
60
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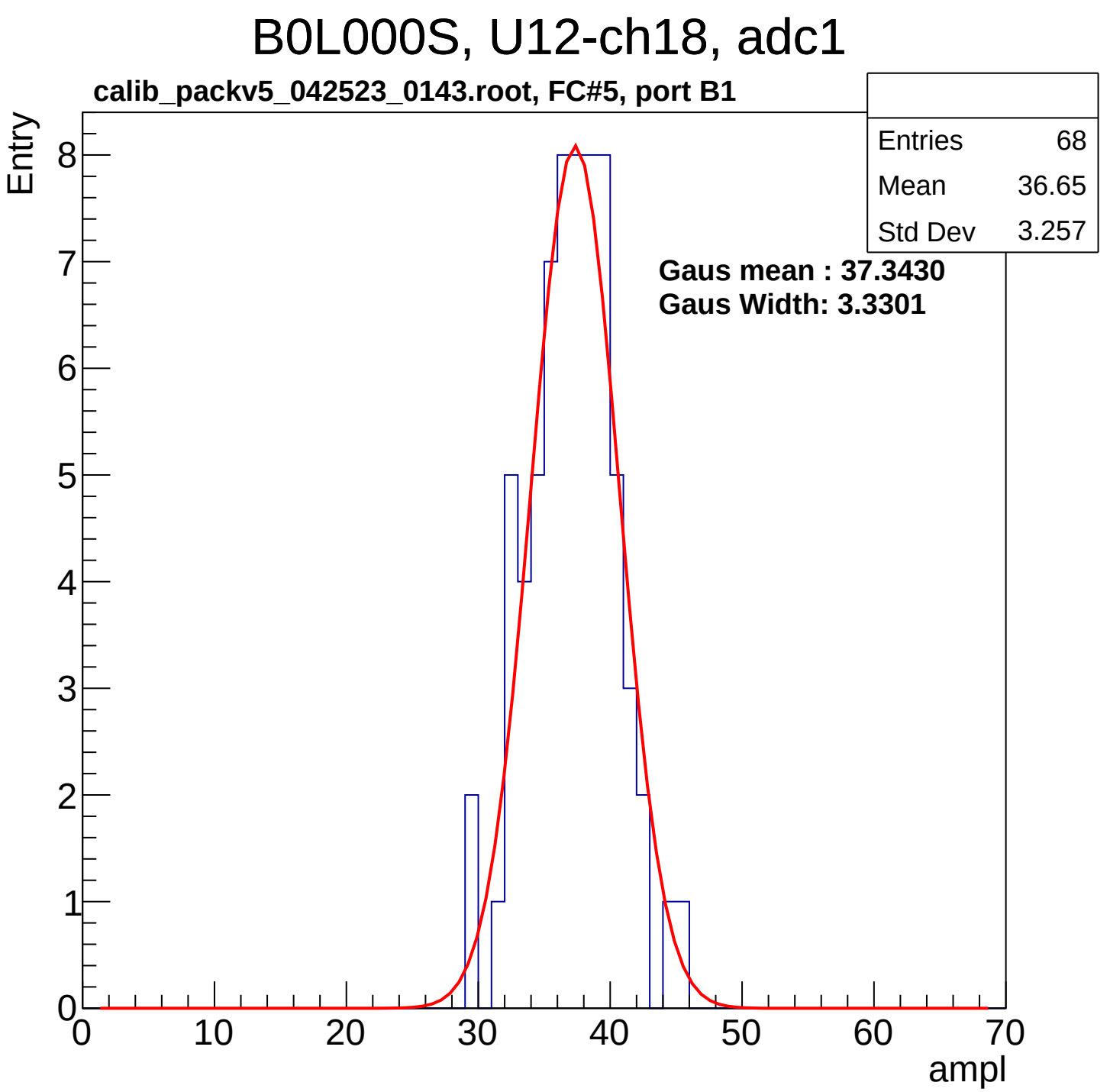
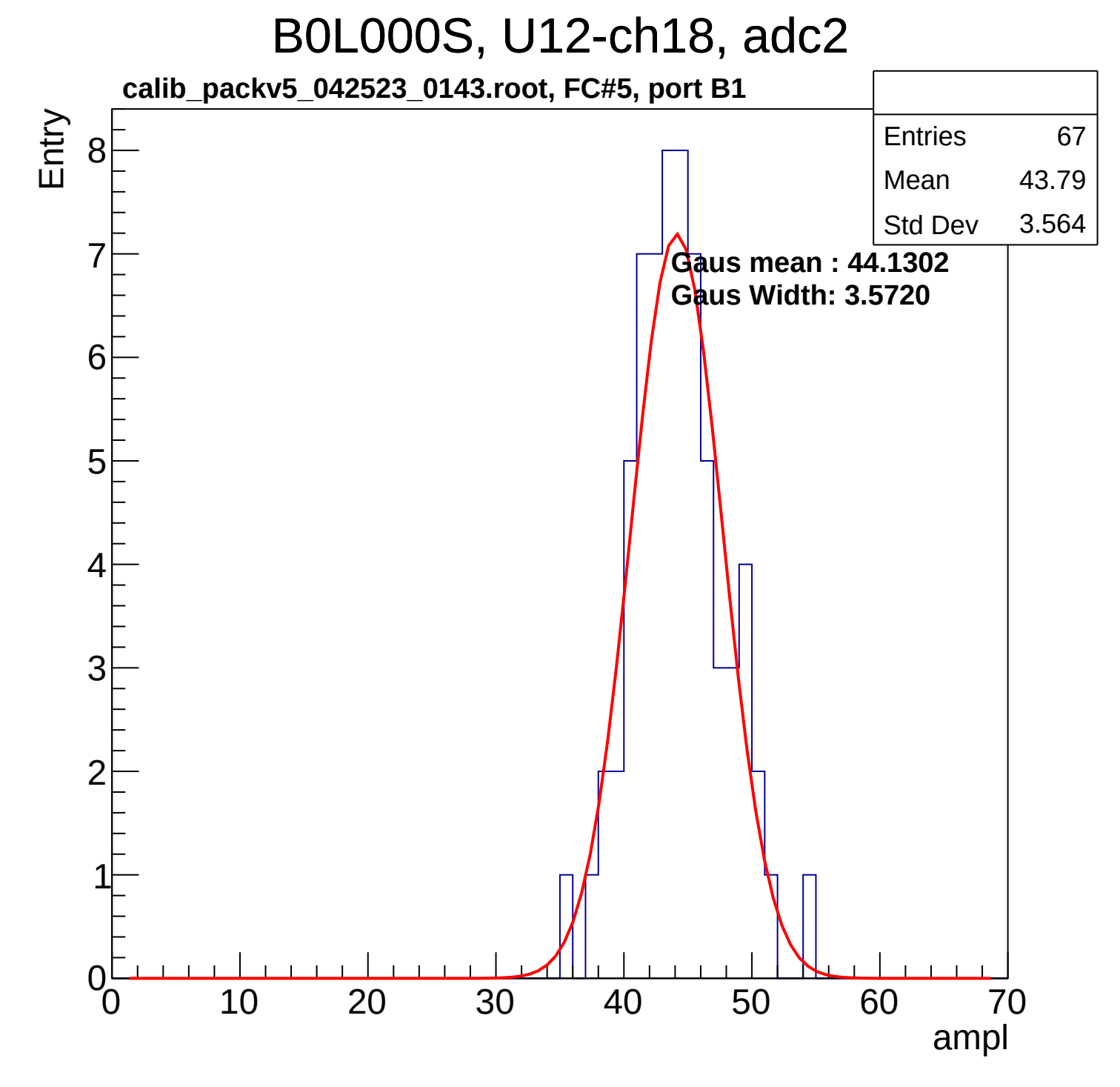

B0L000S, U12-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.65
Std Dev 3.257
Gaus mean : 37.3430
Gaus Width: 3.3301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.79
Std Dev 3.564
Gaus mean : 44.1302
Gaus Width: 3.5720
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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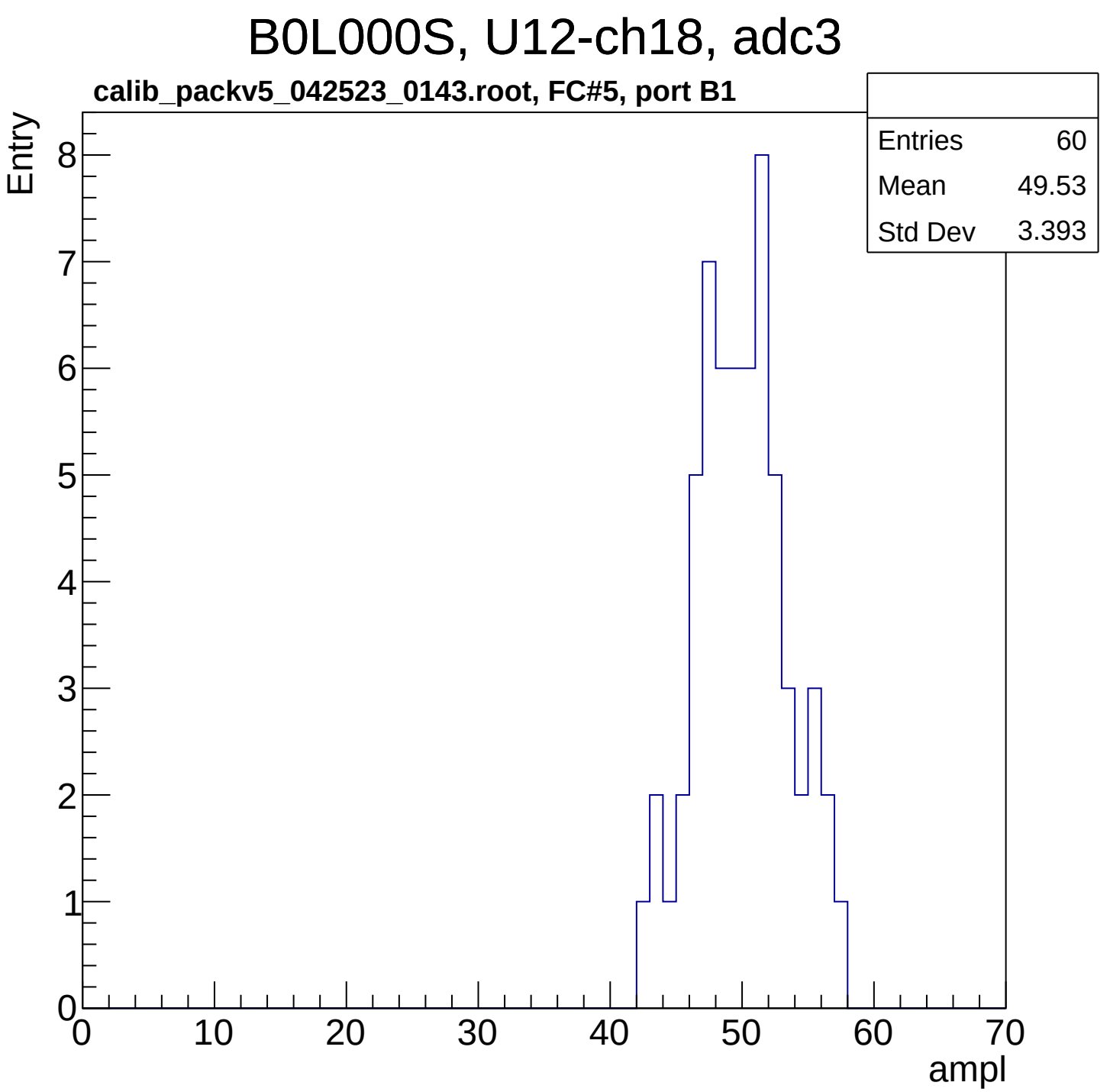
B0L000S, U12-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 49.53
Std Dev 3.393
Entry
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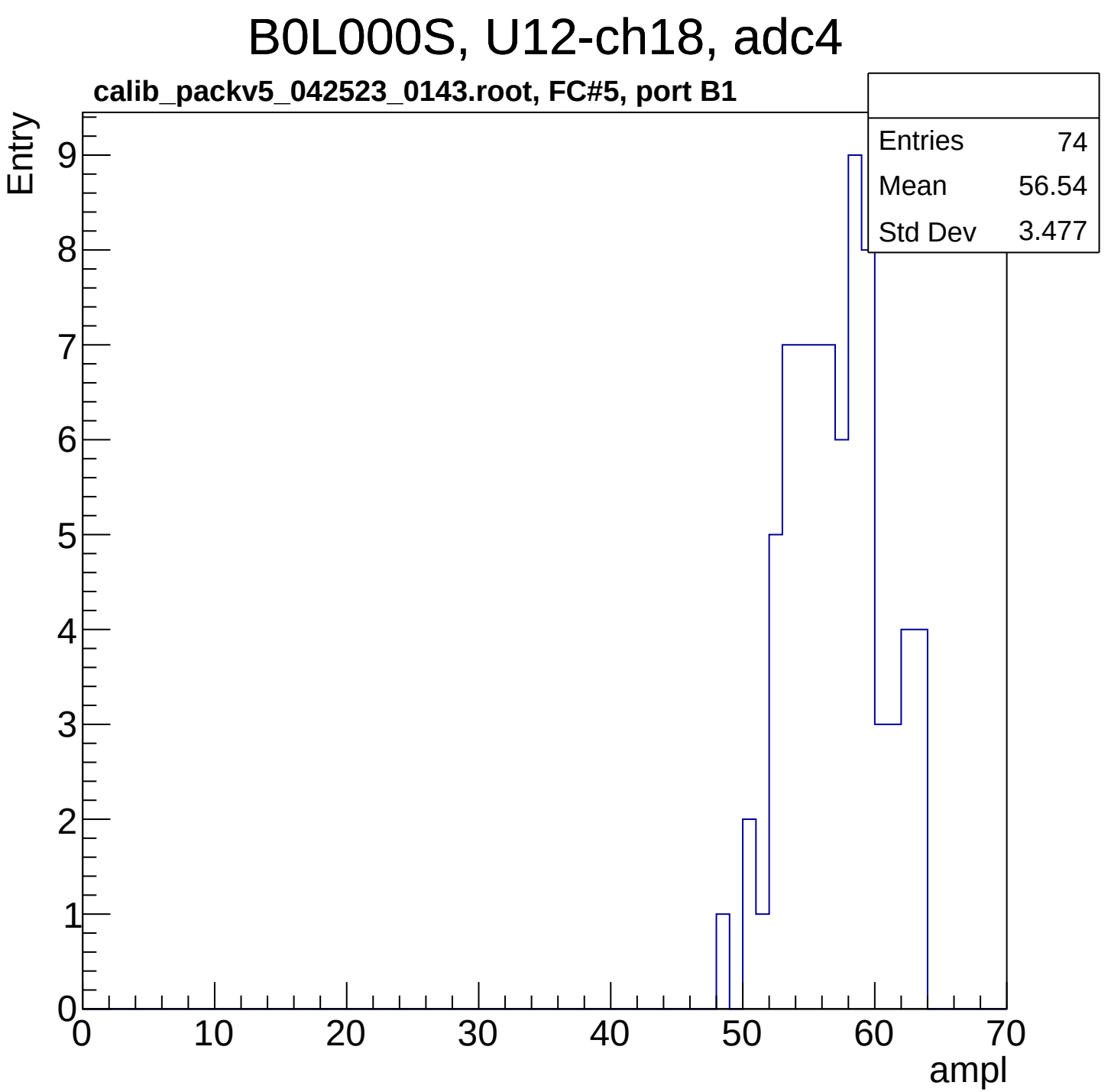
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 56.54
Std Dev 3.477
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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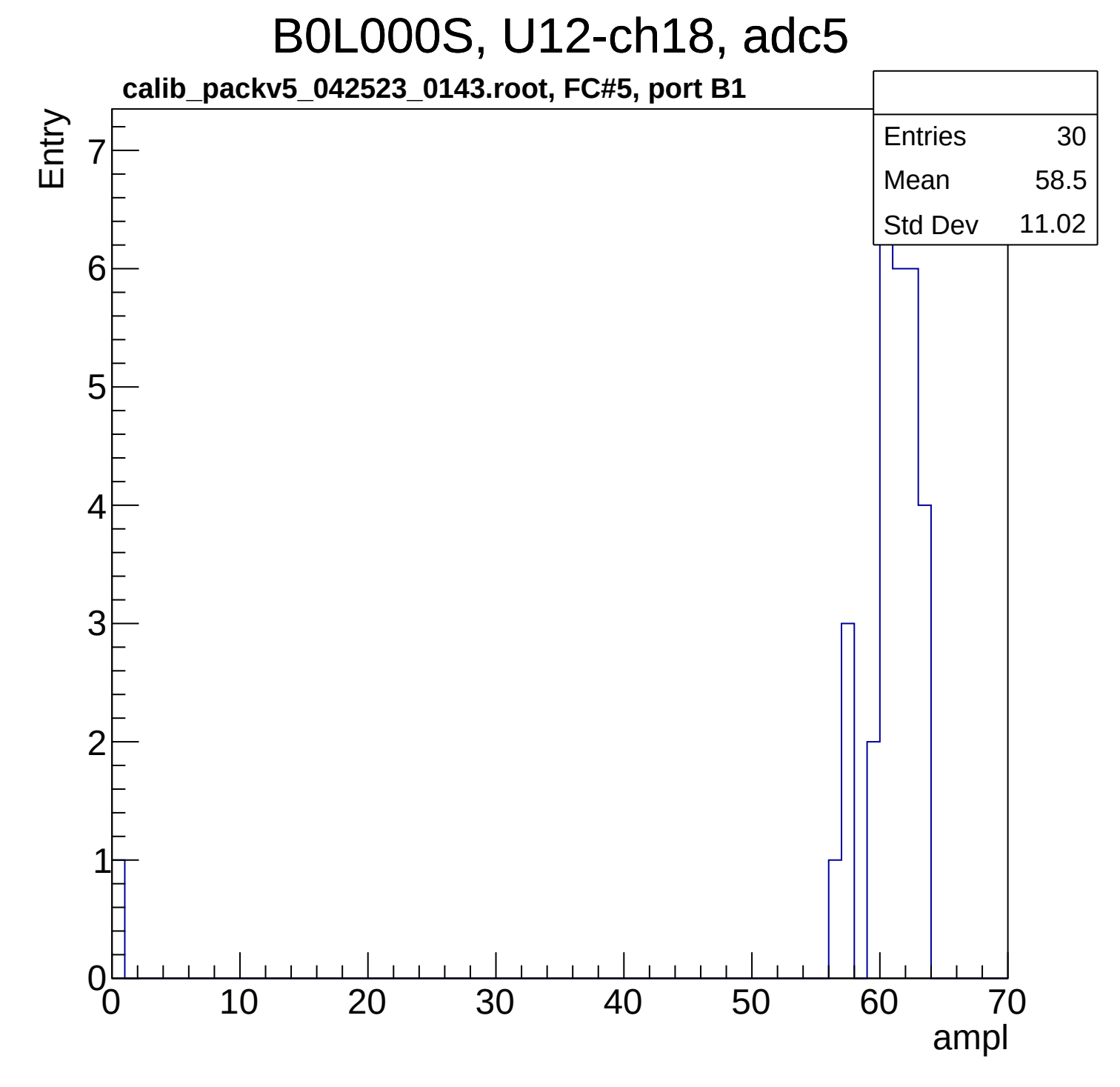
B0L000S, U12-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.5
Std Dev 11.02
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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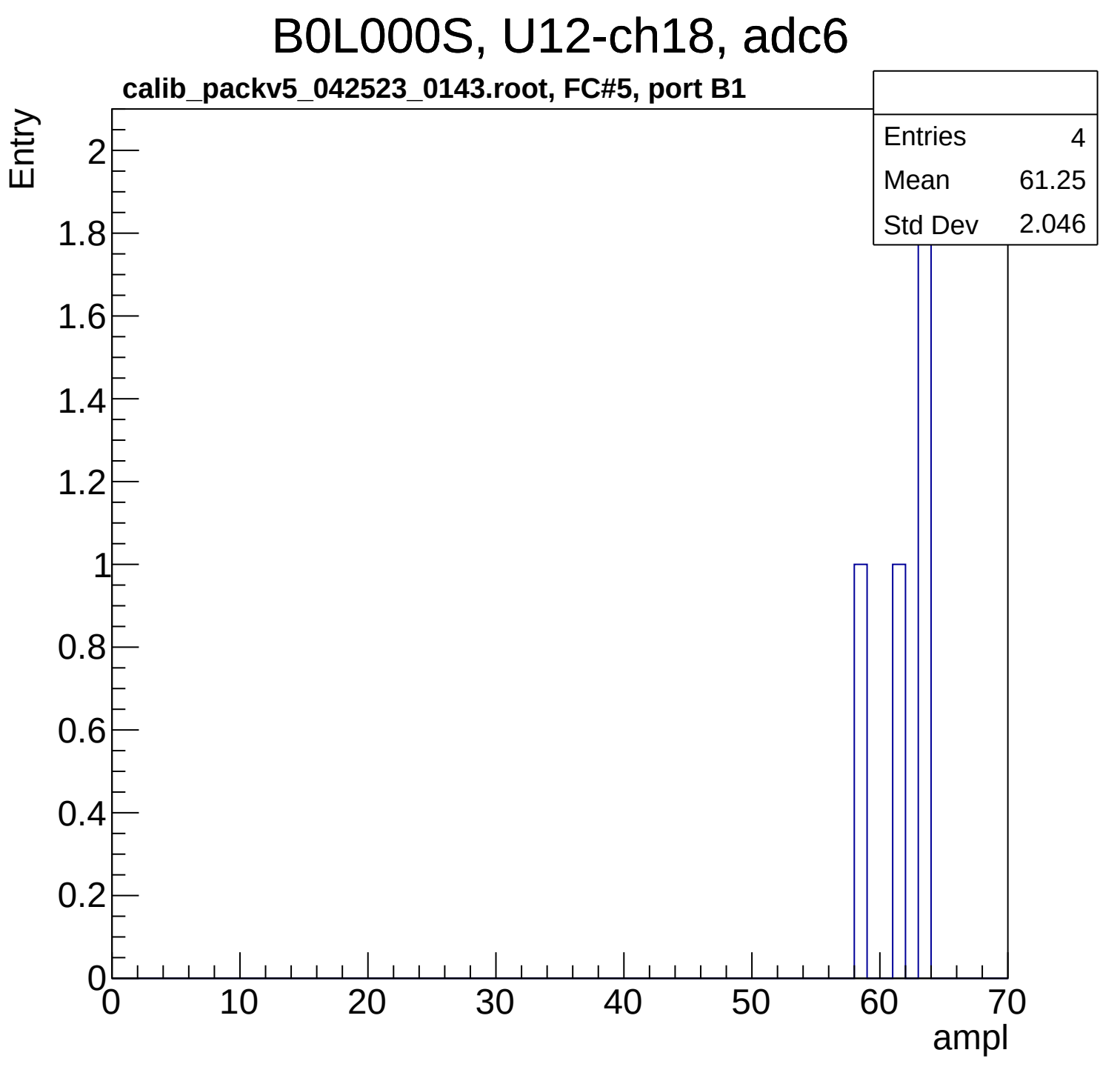
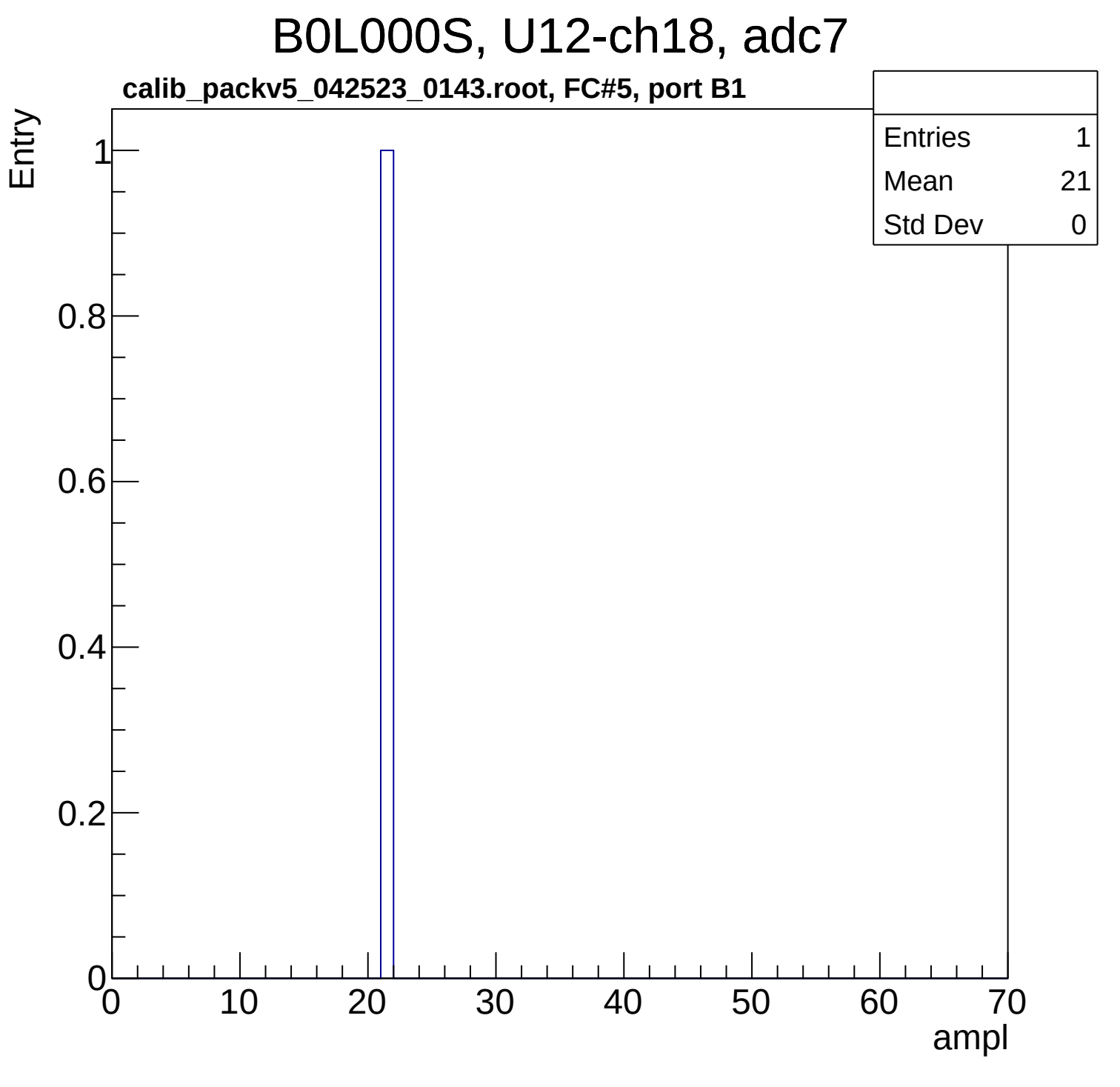
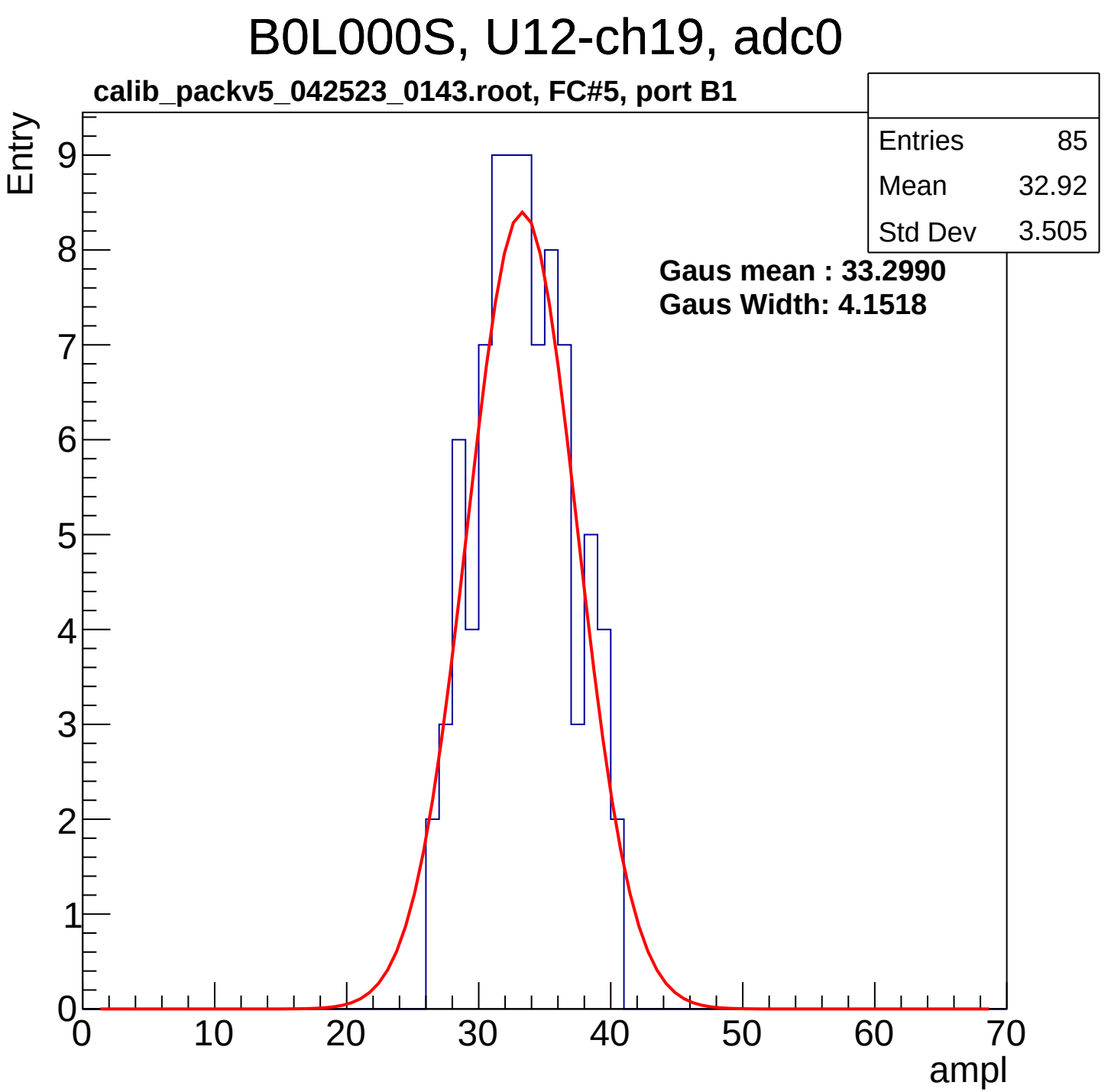

B0L000S, U12-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.25
Std Dev 2.046
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 32.92
Std Dev 3.505
Gaus mean : 33.2990
Gaus Width: 4.1518
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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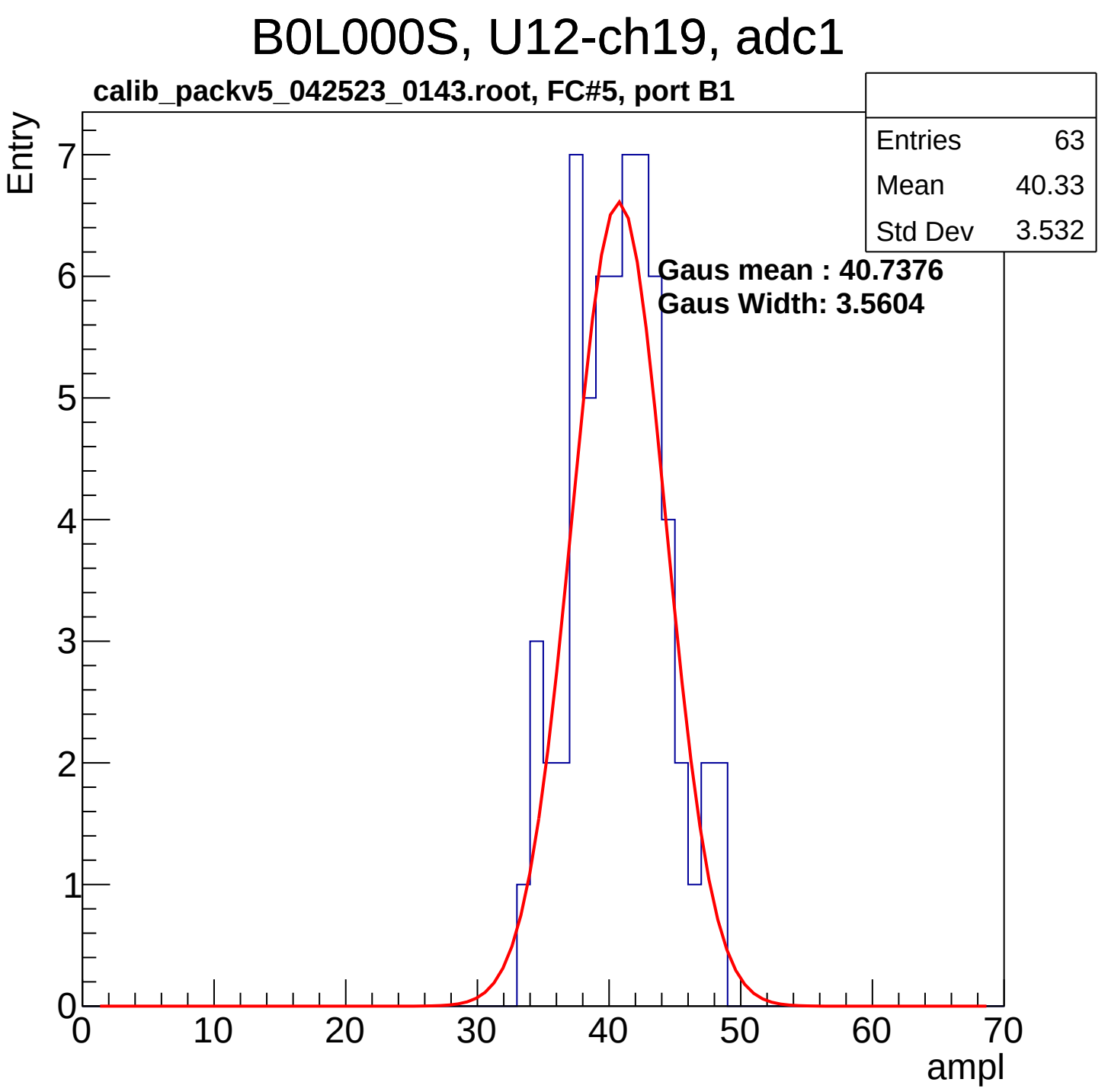
B0L000S, U12-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 40.33
Std Dev 3.532
Gaus mean : 40.7376
Gaus Width: 3.5604
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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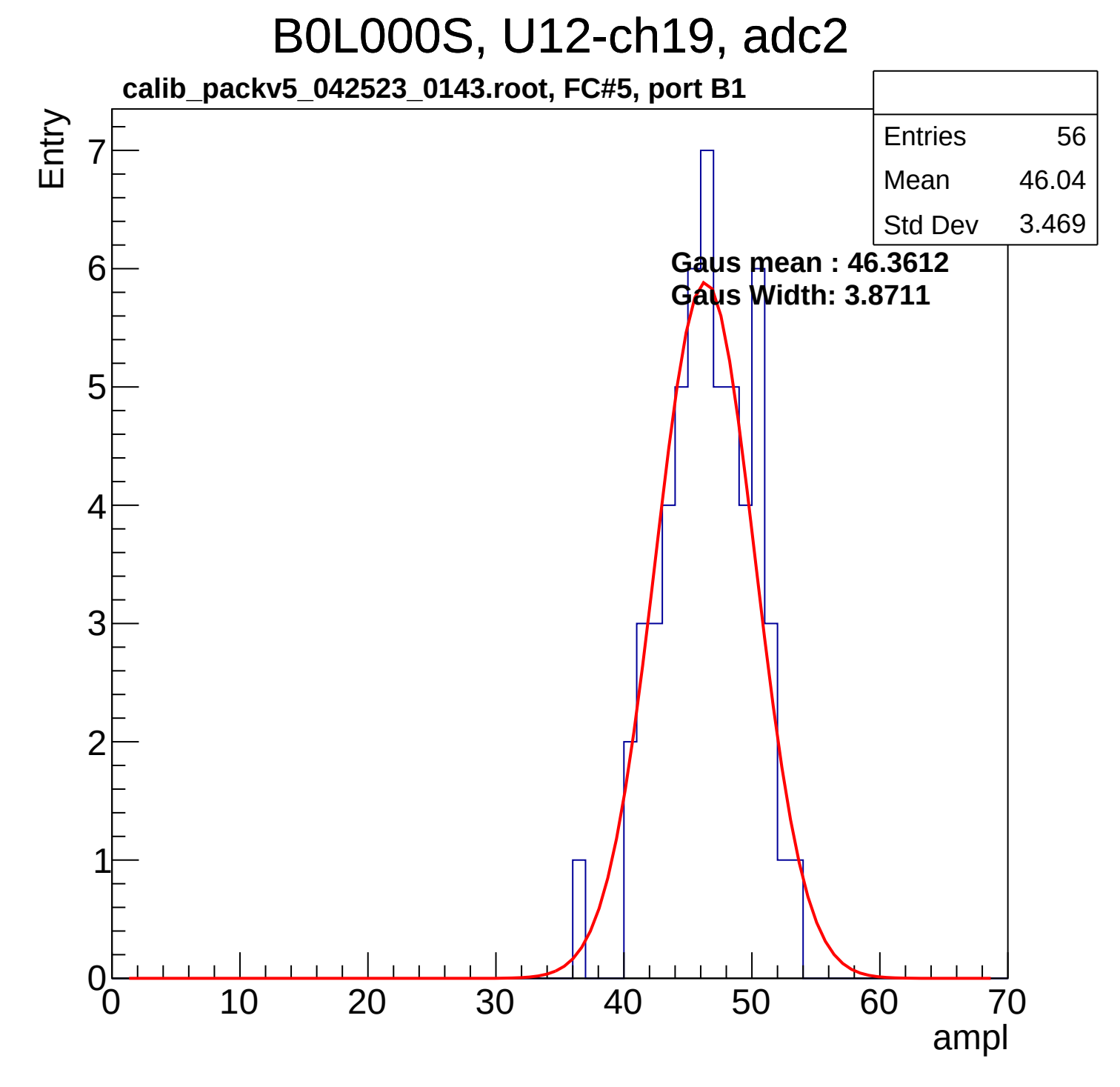
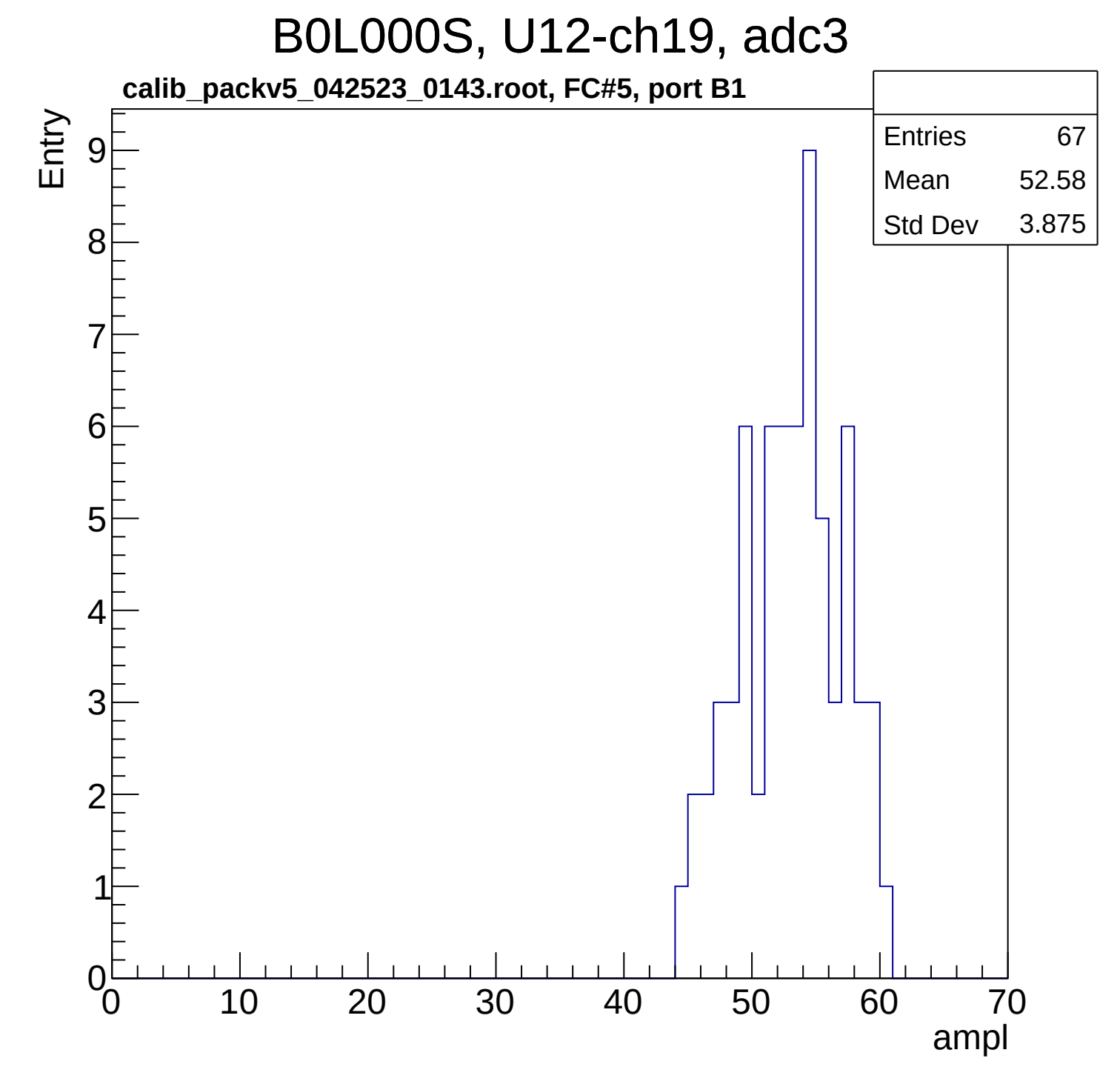

B0L000S, U12-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 46.04
Std Dev 3.469
Gaus mean : 46.3612
Gaus Width: 3.8711
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 52.58
Std Dev 3.875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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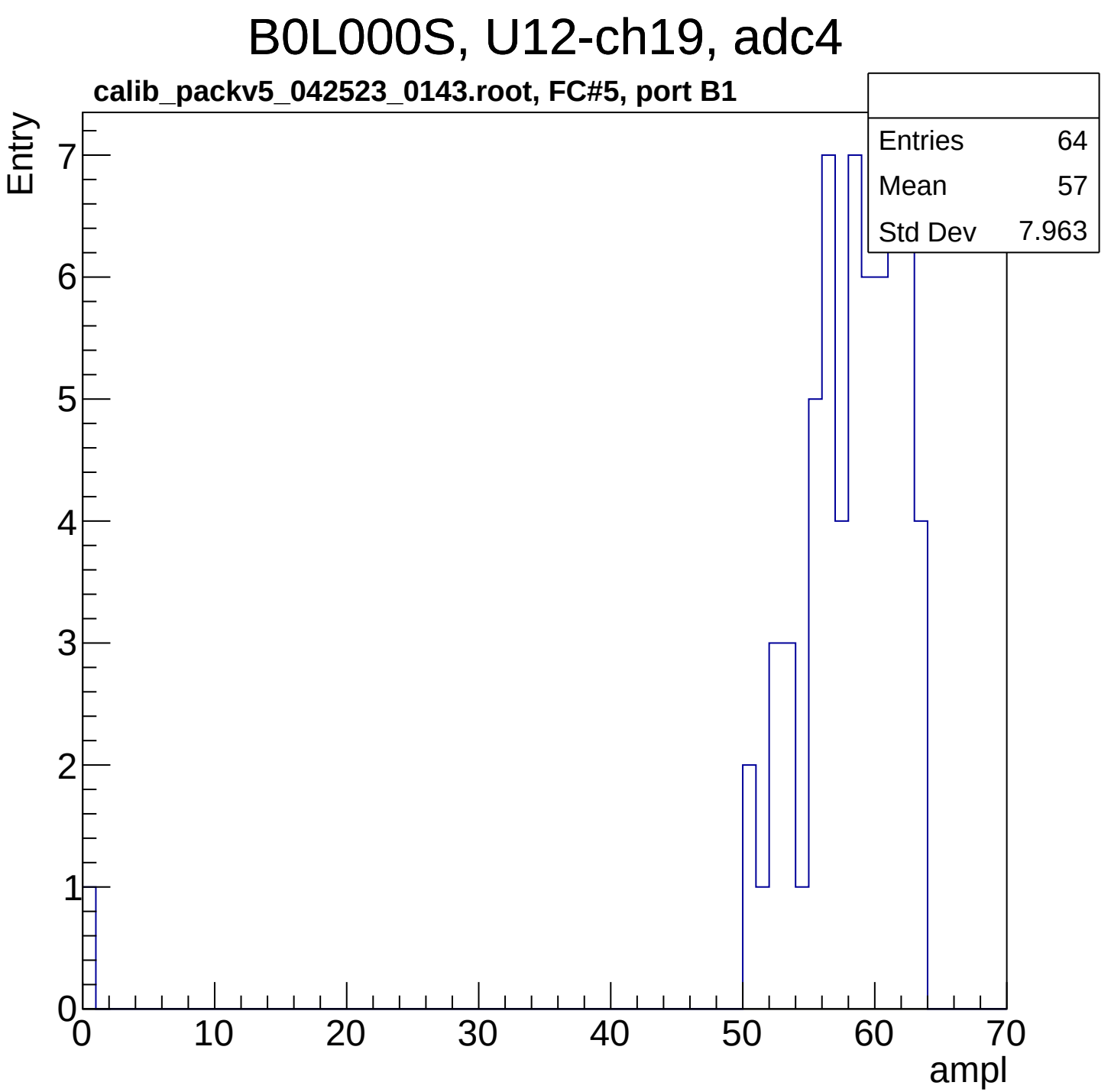
40
50
60
70


B0L000S, U12-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 57
Std Dev 7.963
Entry
ampl
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1
2
3
4
5
6
7
0
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40
50
60
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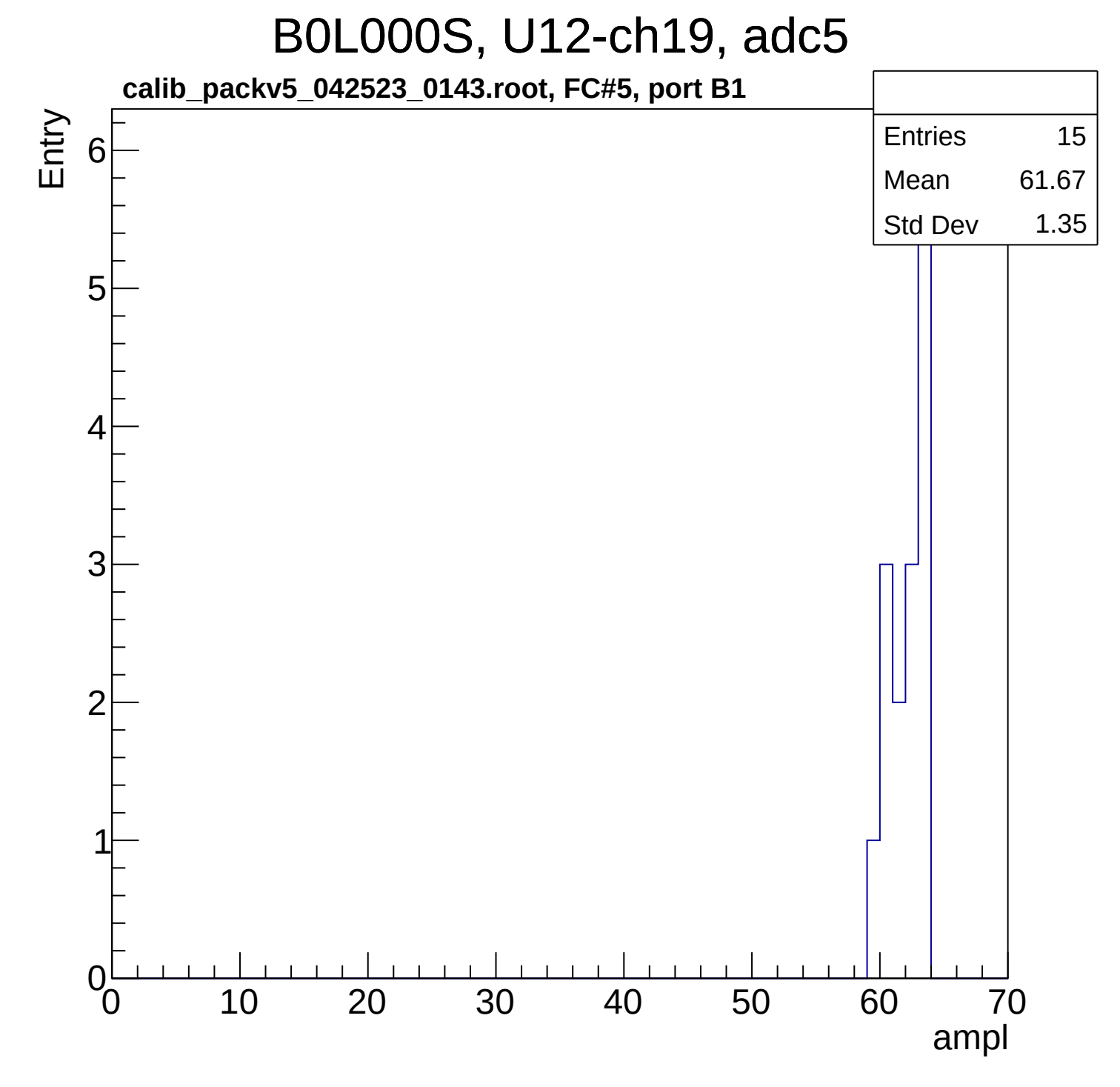
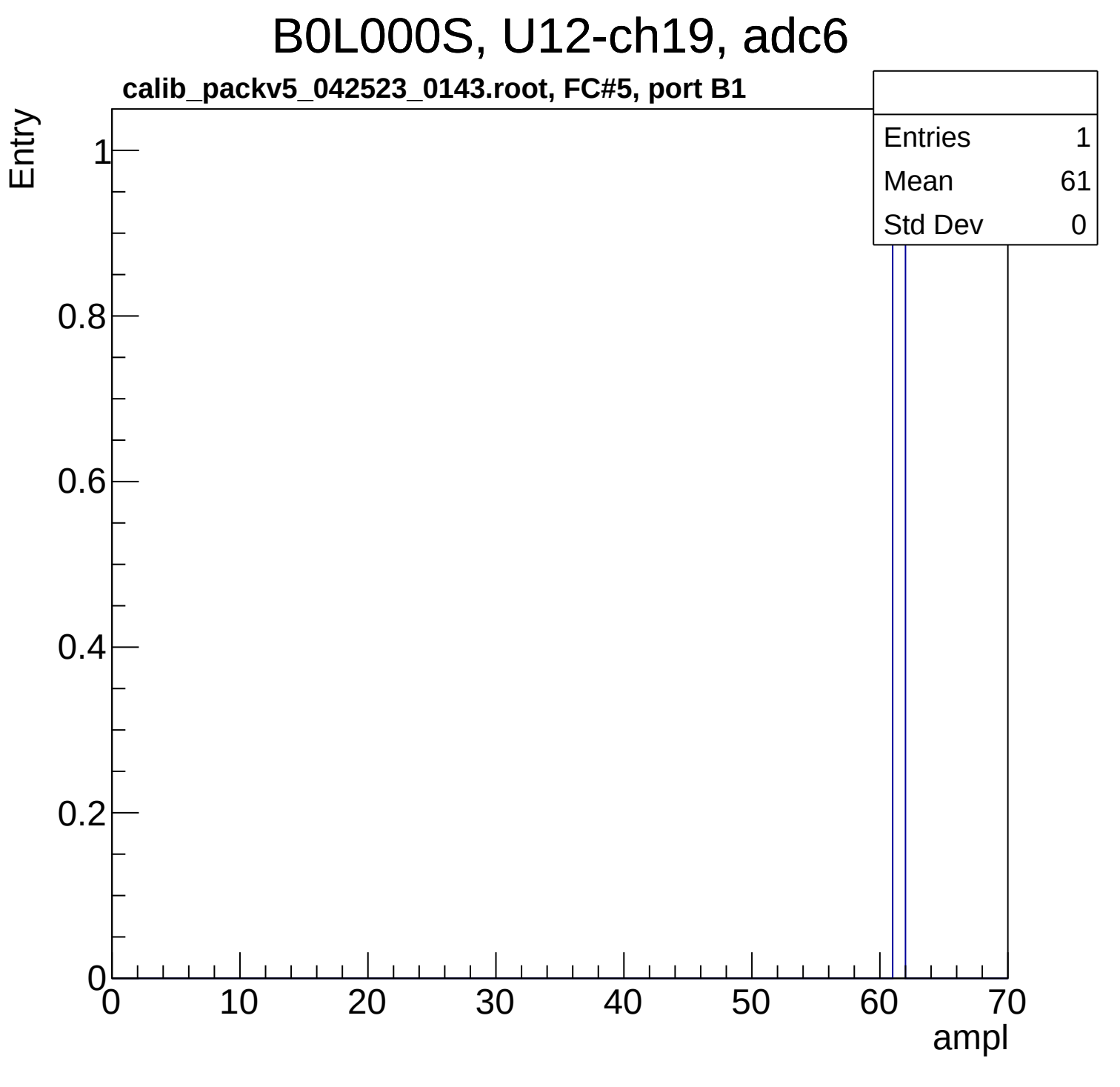
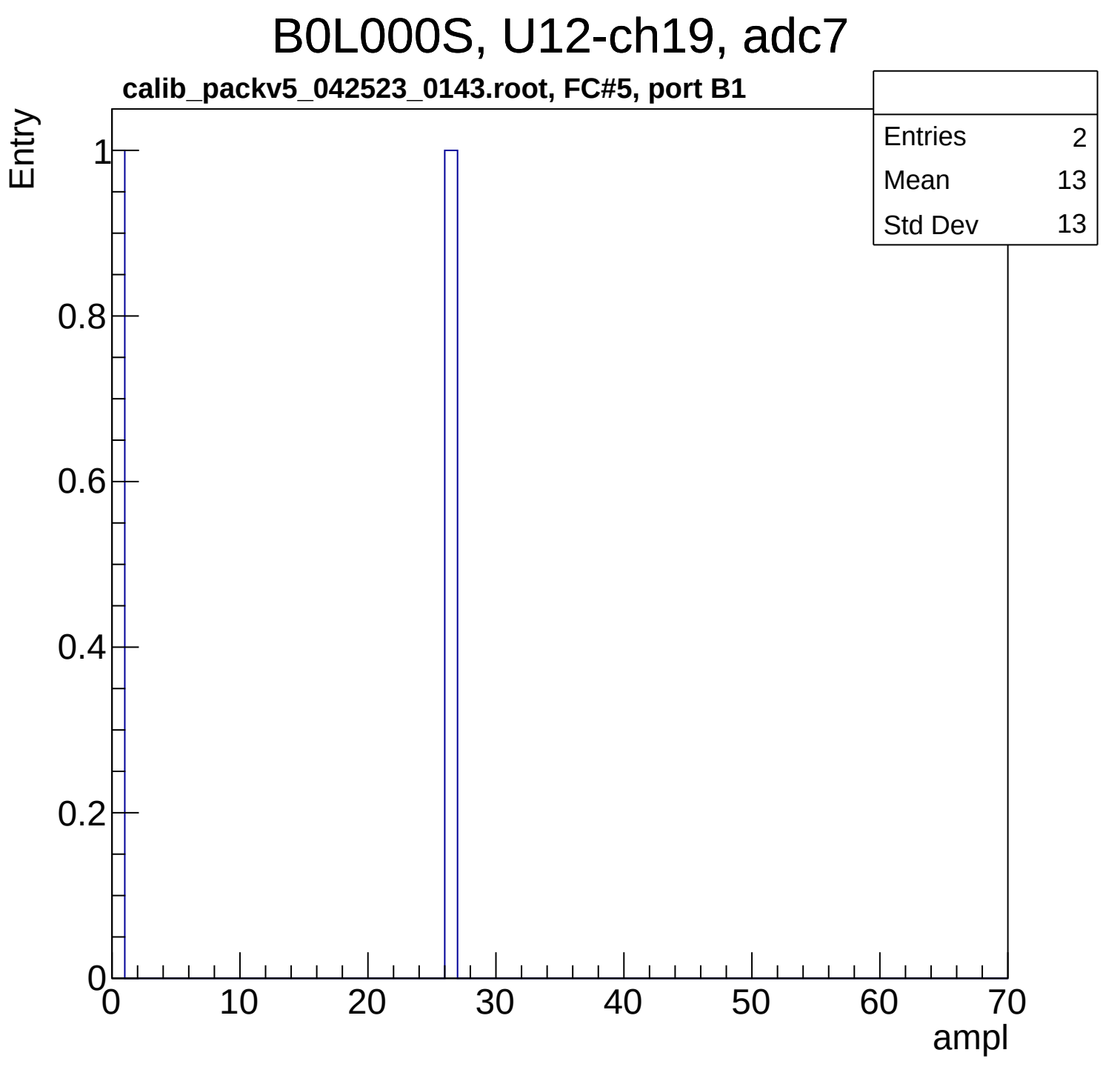

B0L000S, U12-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.67
Std Dev 1.35
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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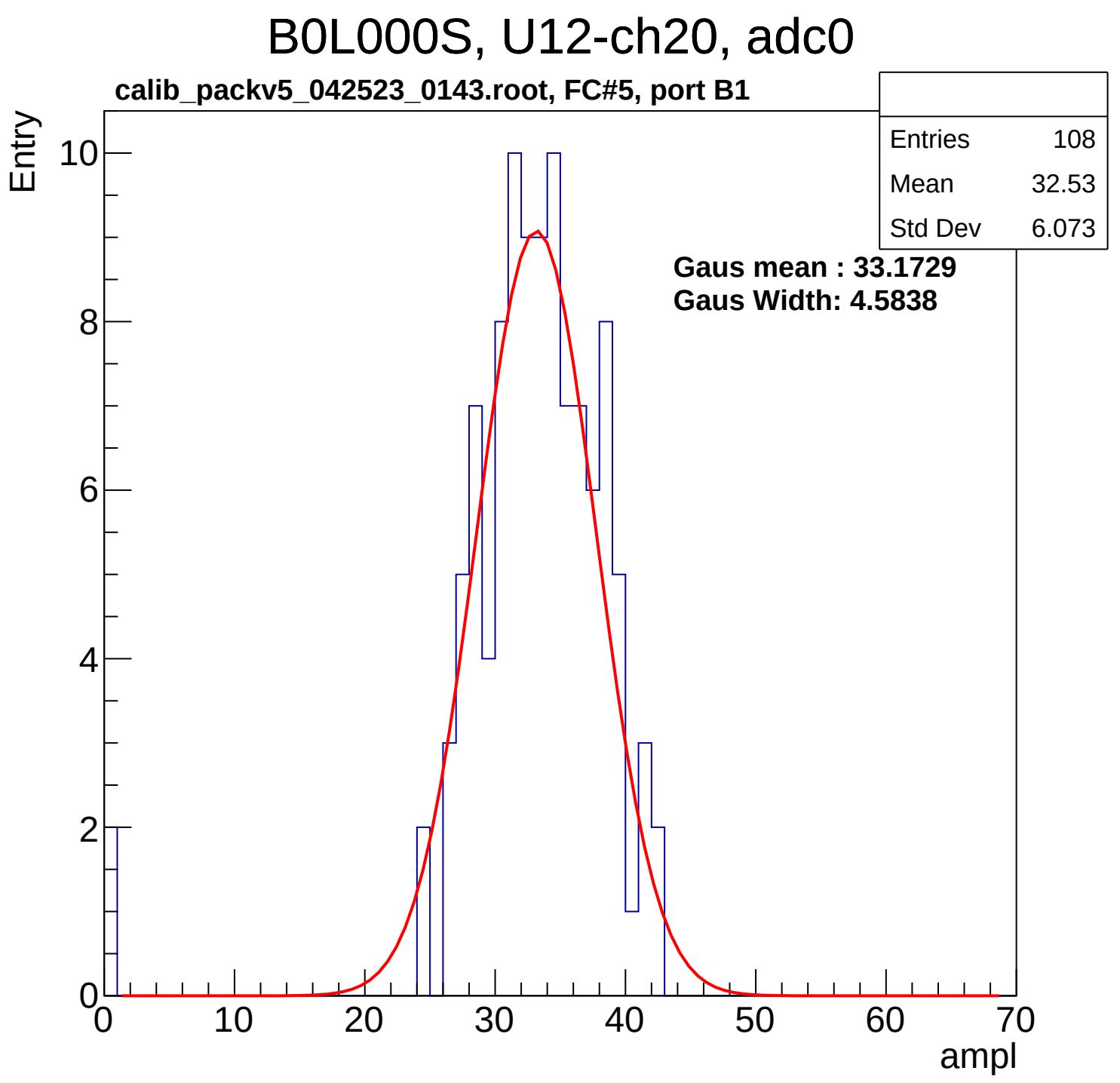
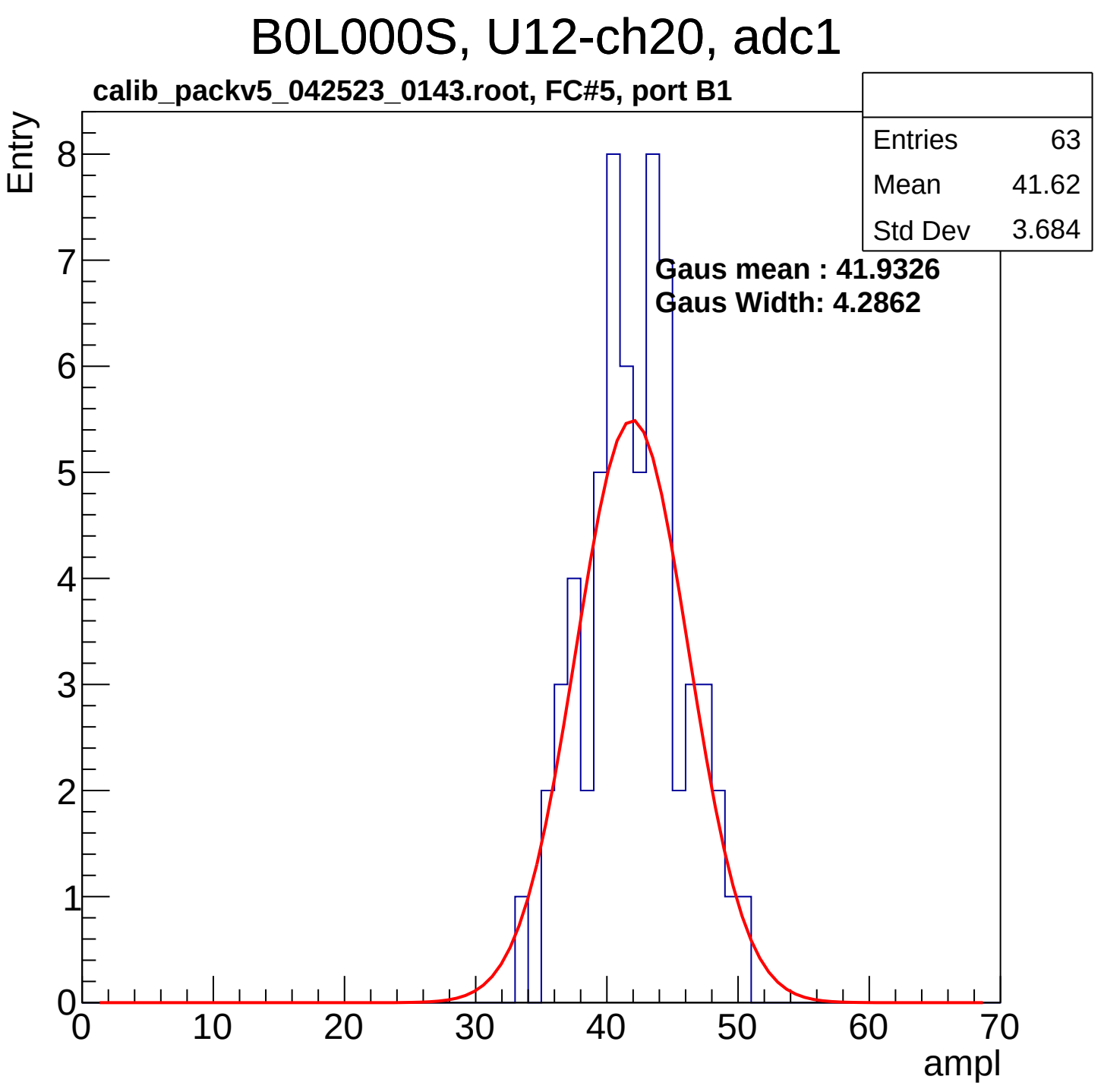
50
60
70
ampl


B0L000S, U12-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 108
Mean 32.53
Std Dev 6.073
Gaus mean : 33.1729
Gaus Width: 4.5838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.62
Std Dev 3.684
Gaus mean : 41.9326
Gaus Width: 4.2862
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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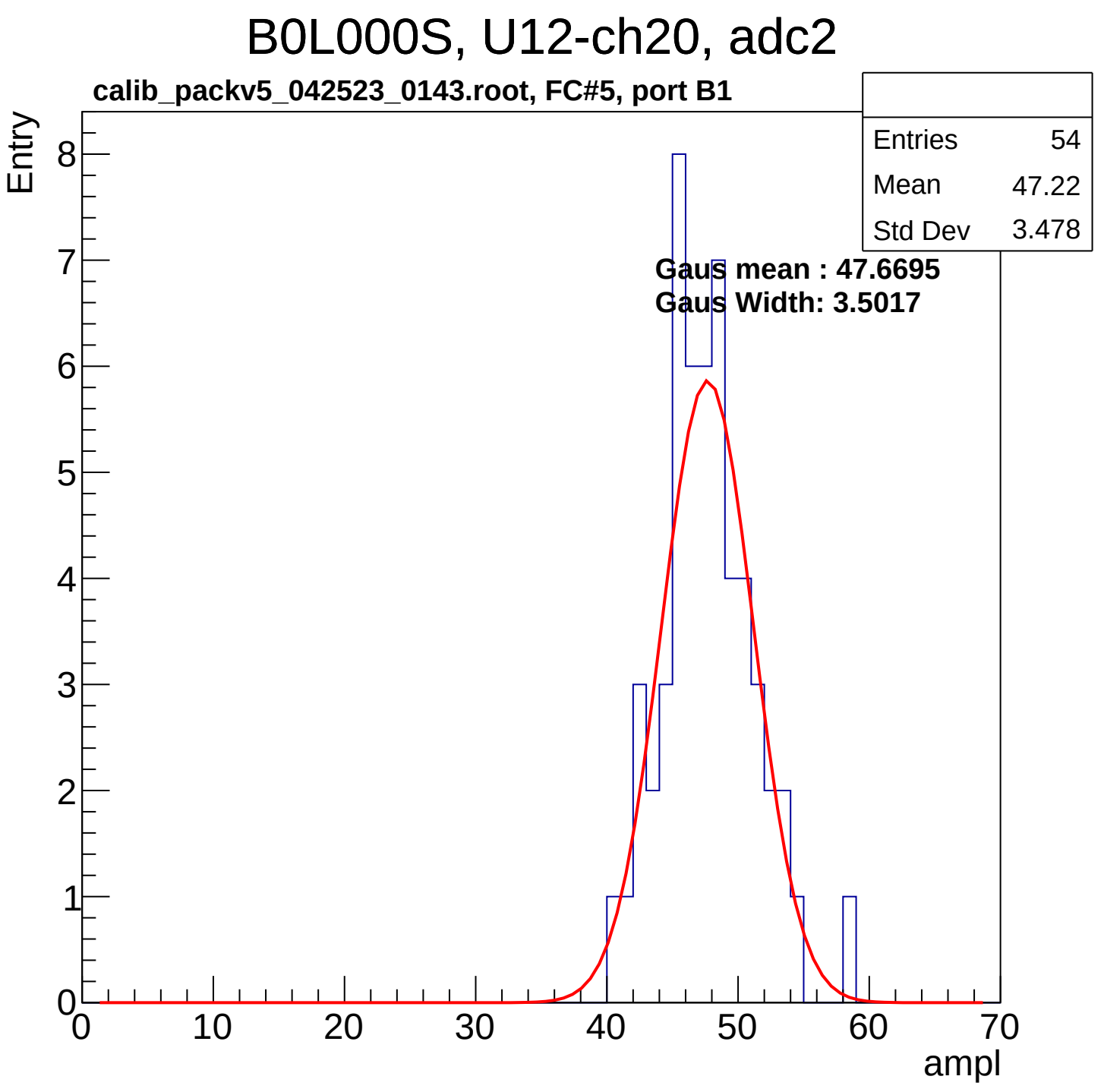
B0L000S, U12-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 47.22
Std Dev 3.478
Gaus mean : 47.6695
Gaus Width: 3.5017
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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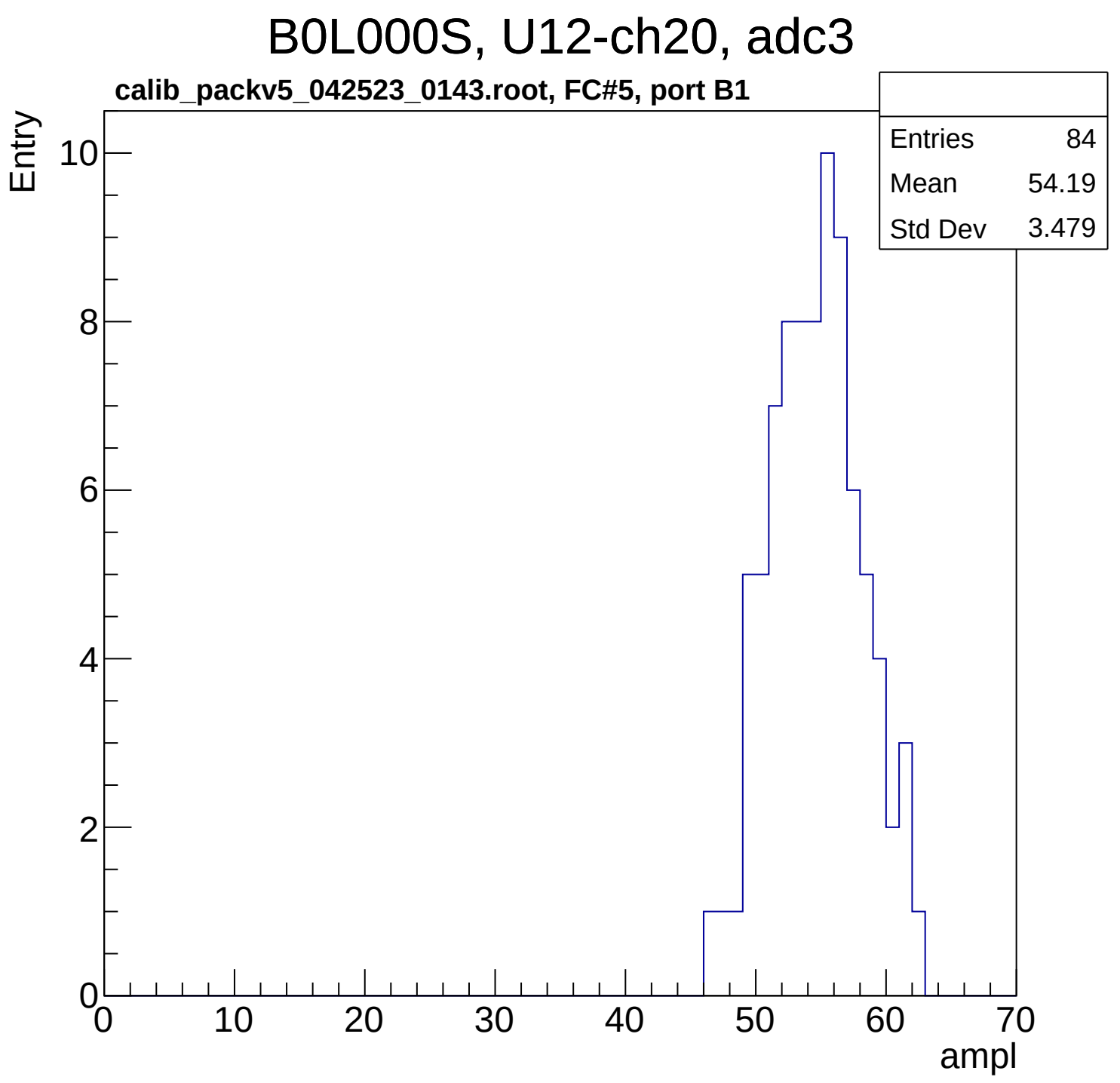
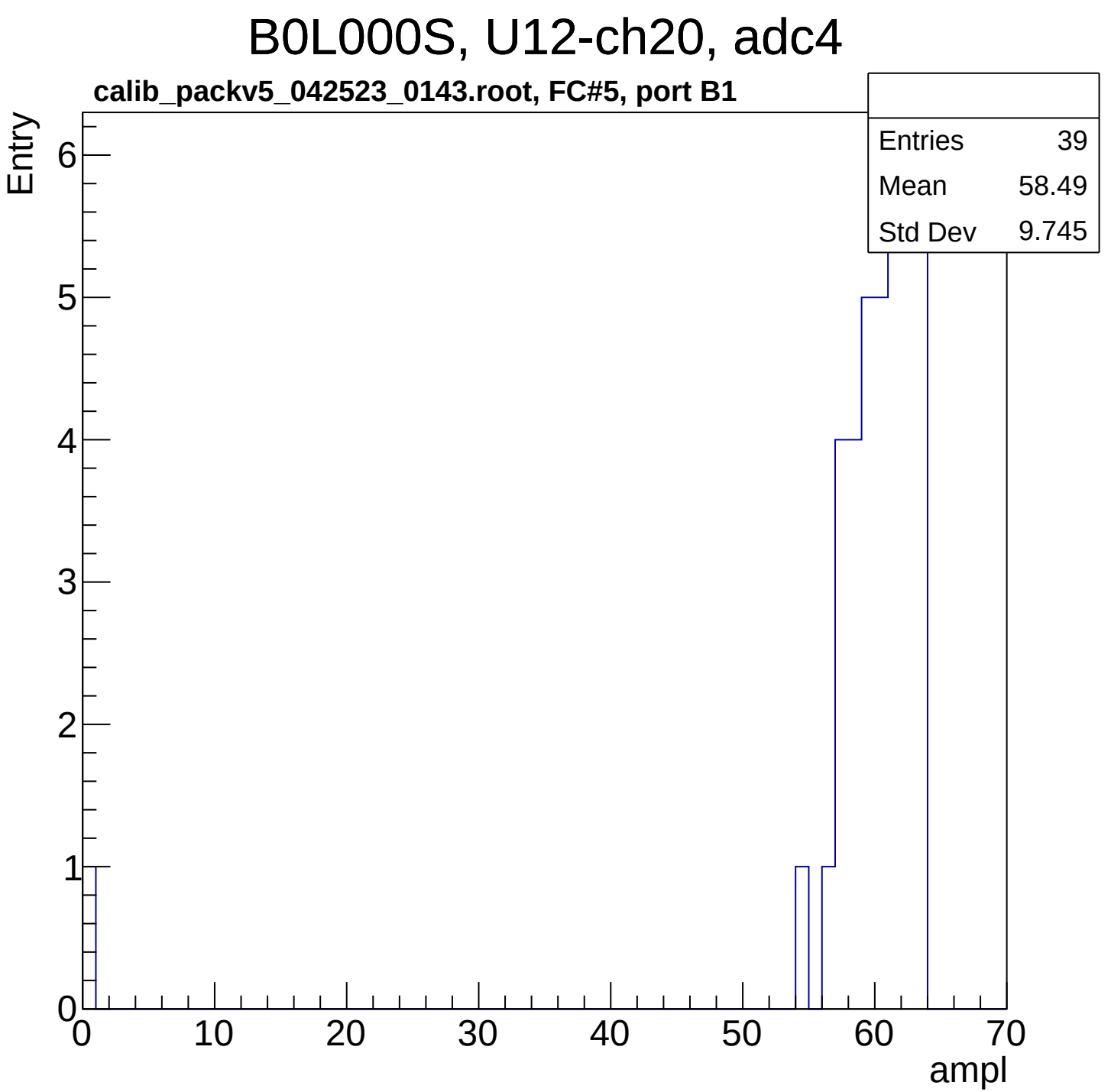
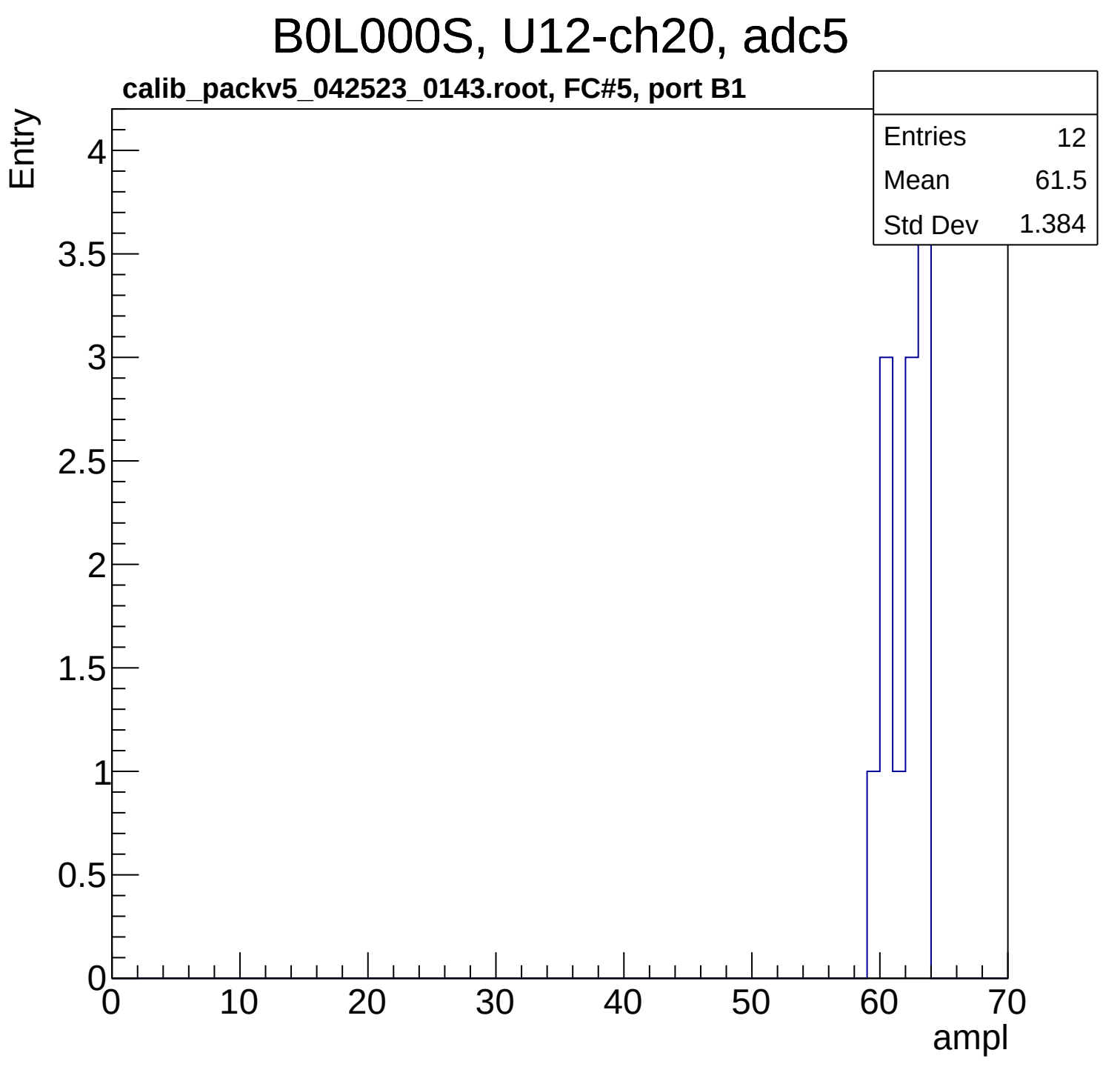
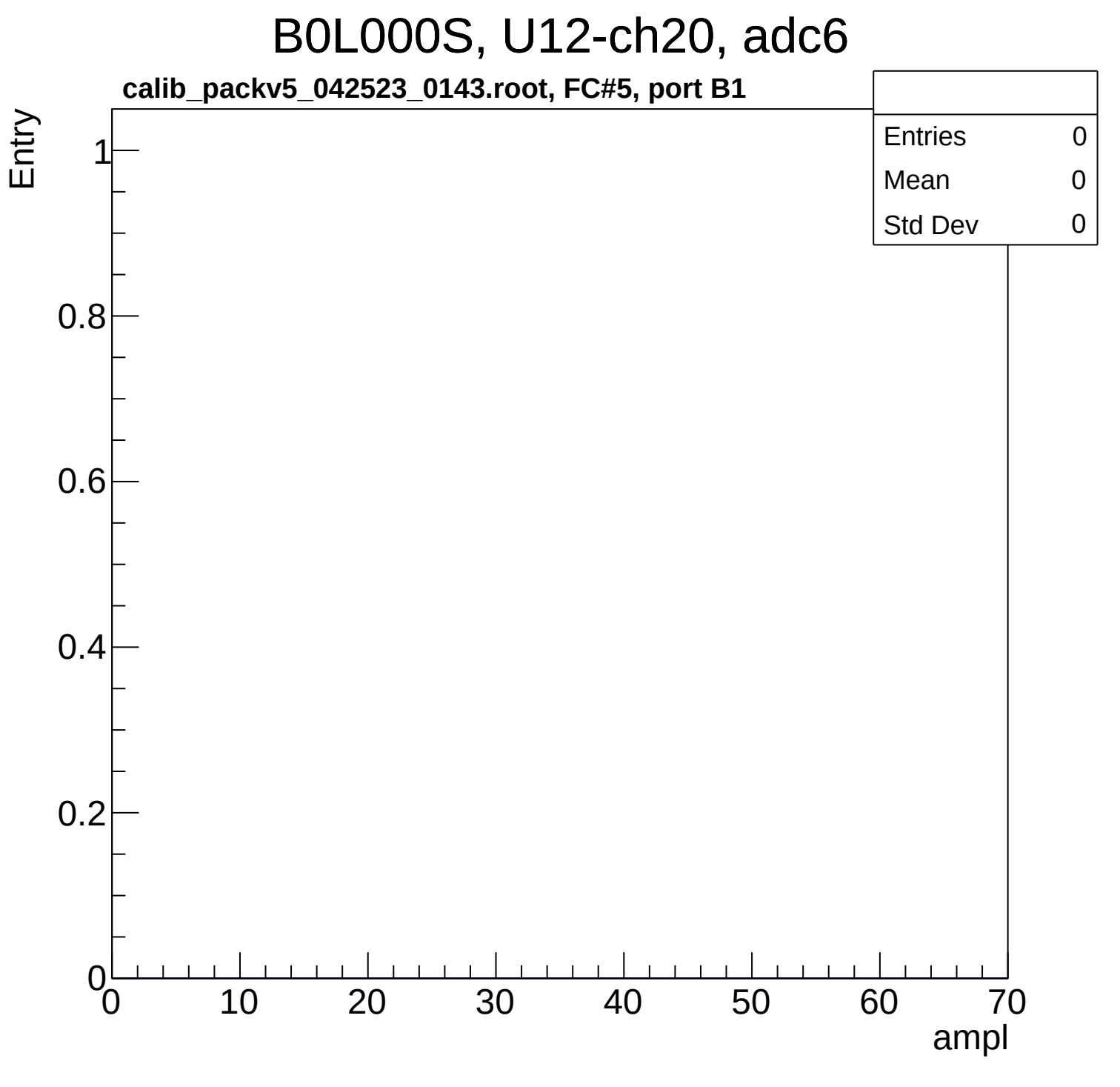
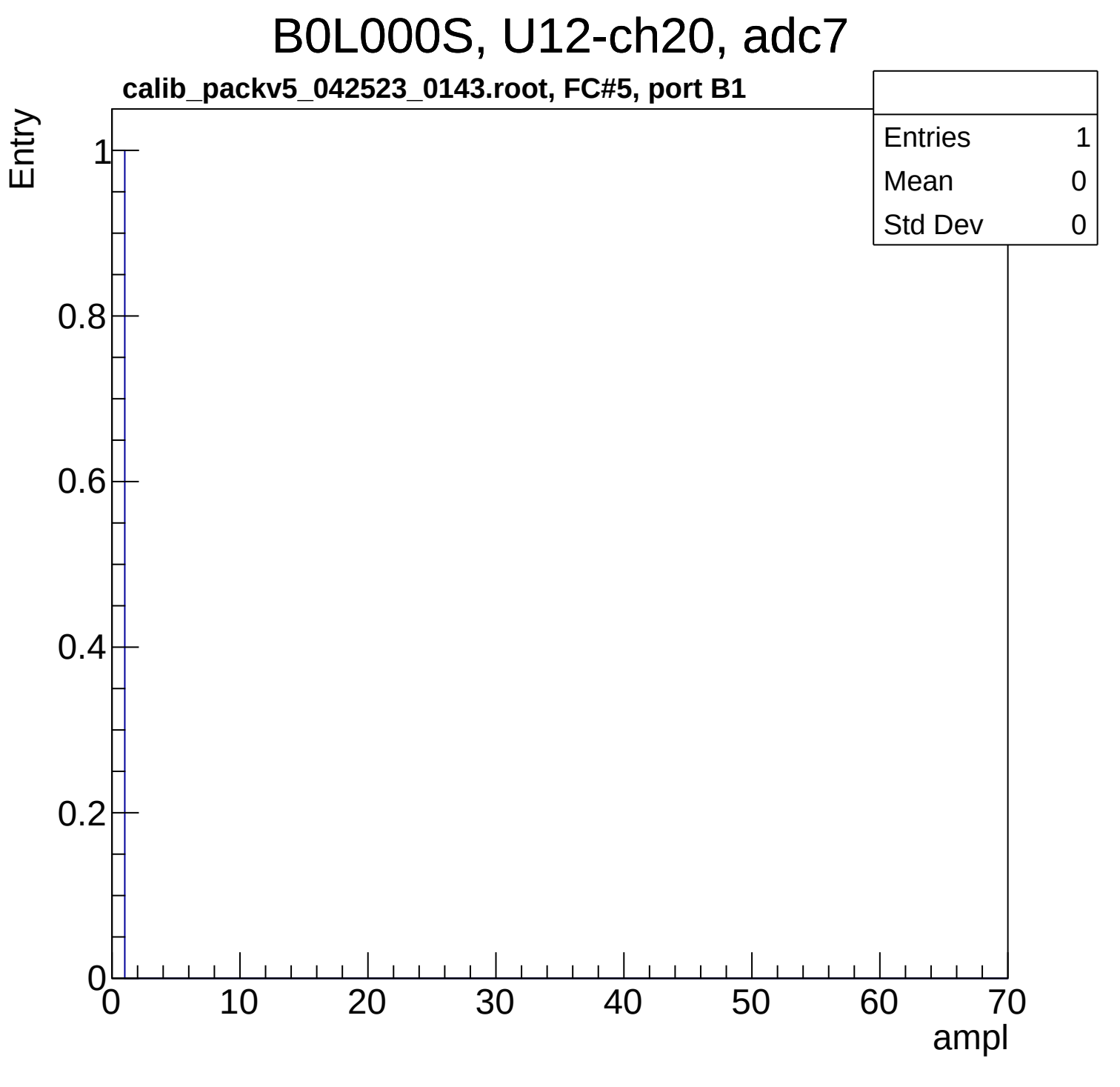
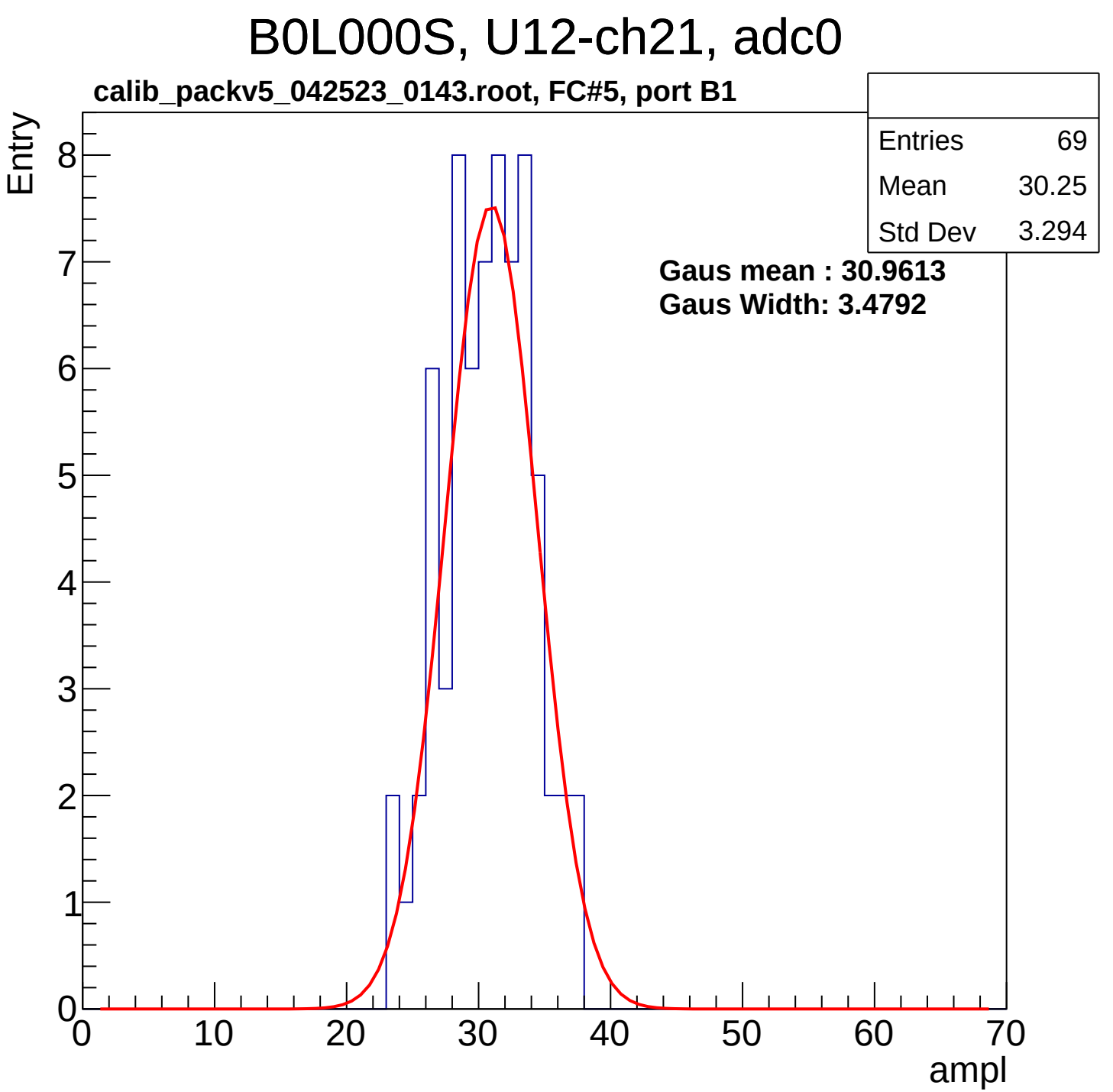

B0L000S, U12-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 54.19
Std Dev 3.479
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

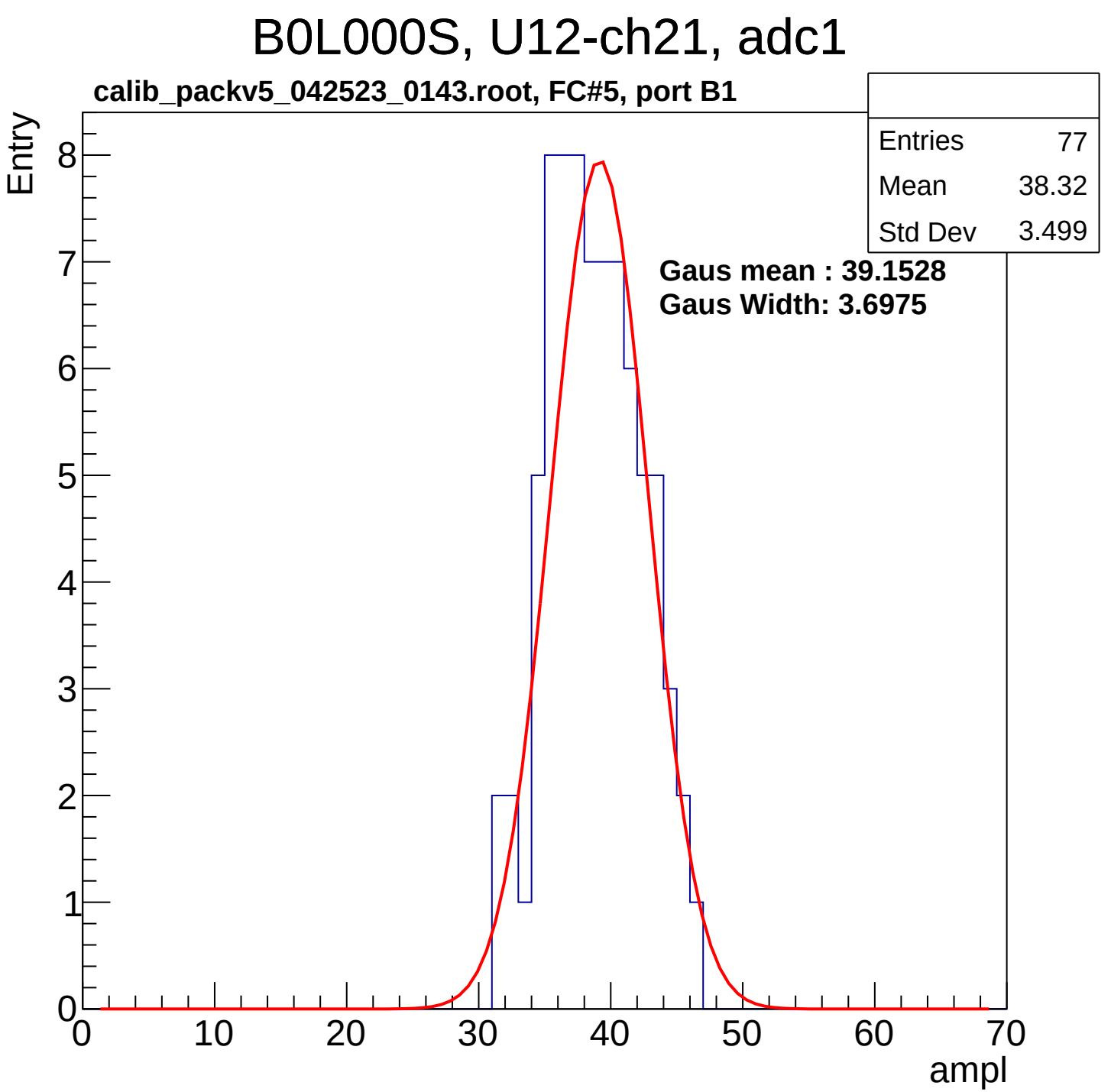

B0L000S, U12-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.49
Std Dev 9.745
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

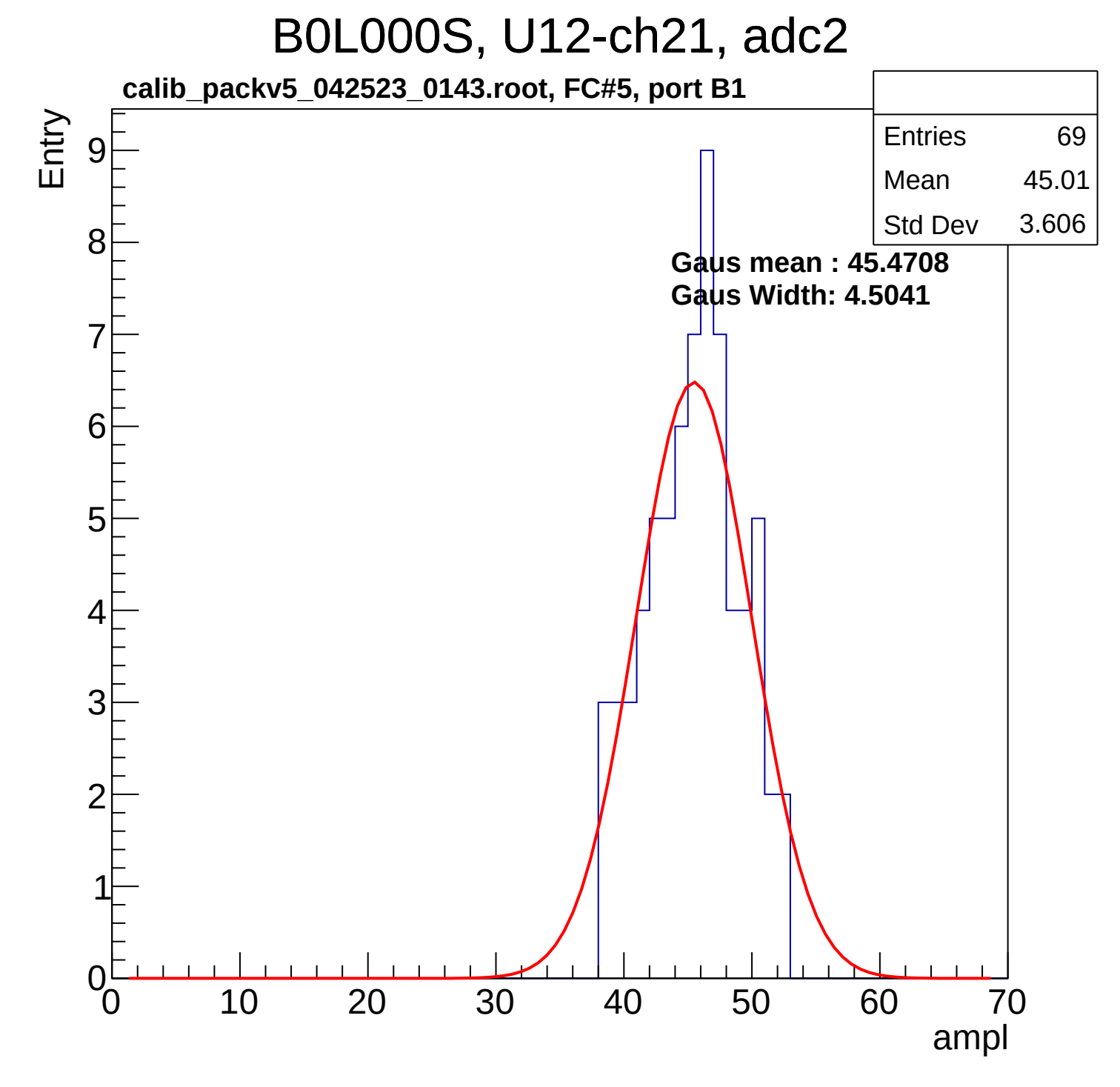

B0L000S, U12-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.5
Std Dev 1.384
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

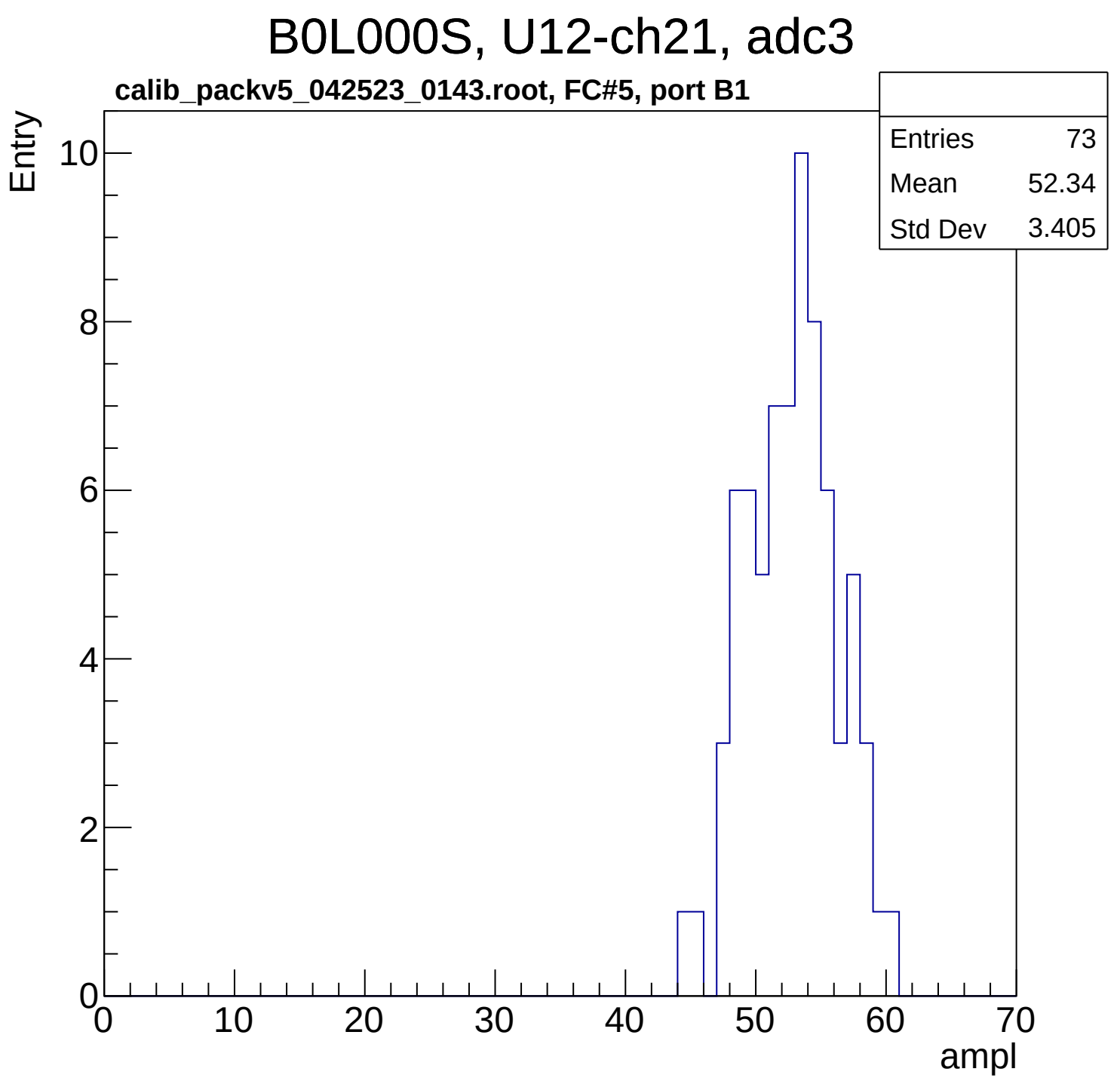

B0L000S, U12-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

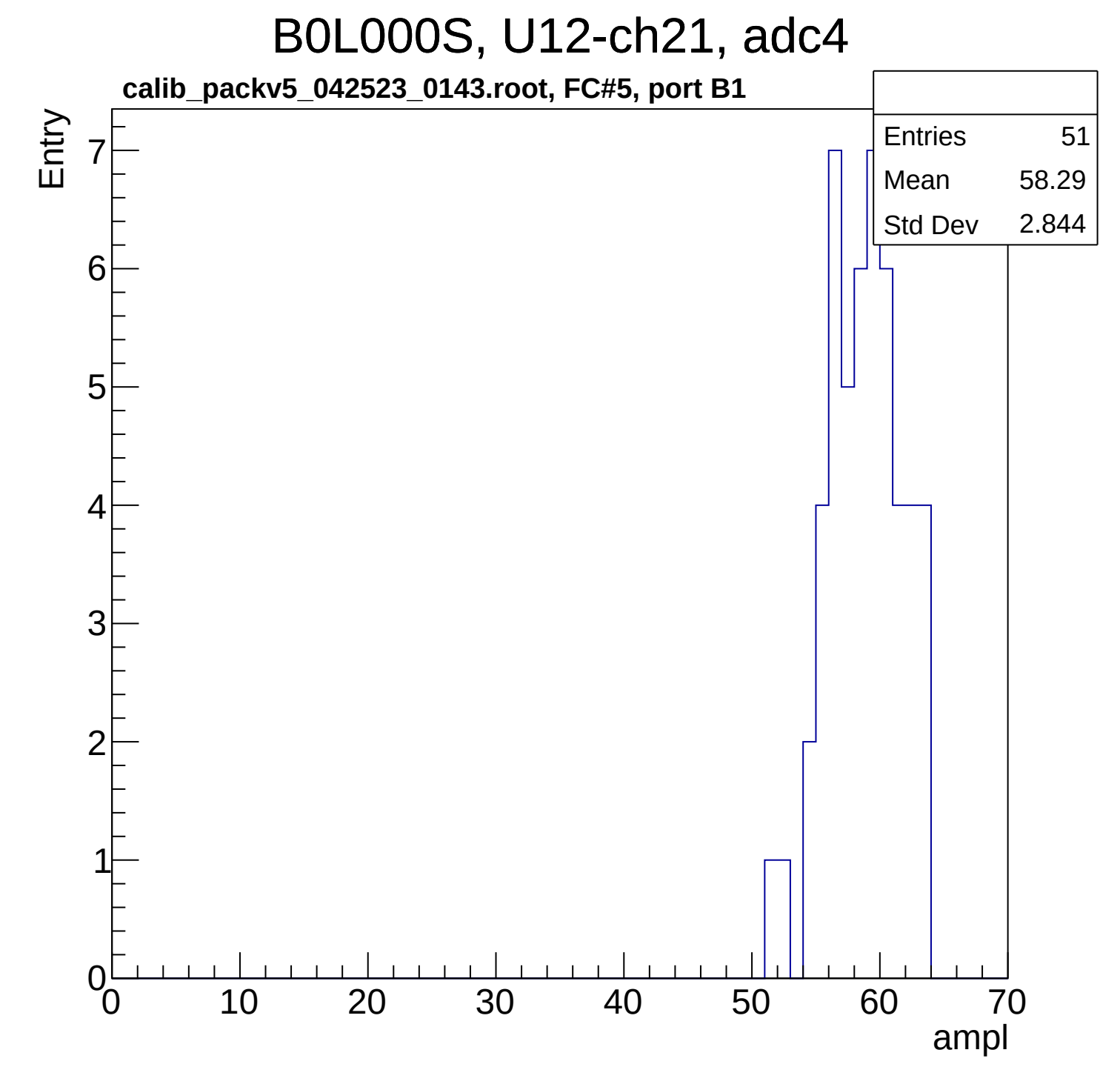

B0L000S, U12-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

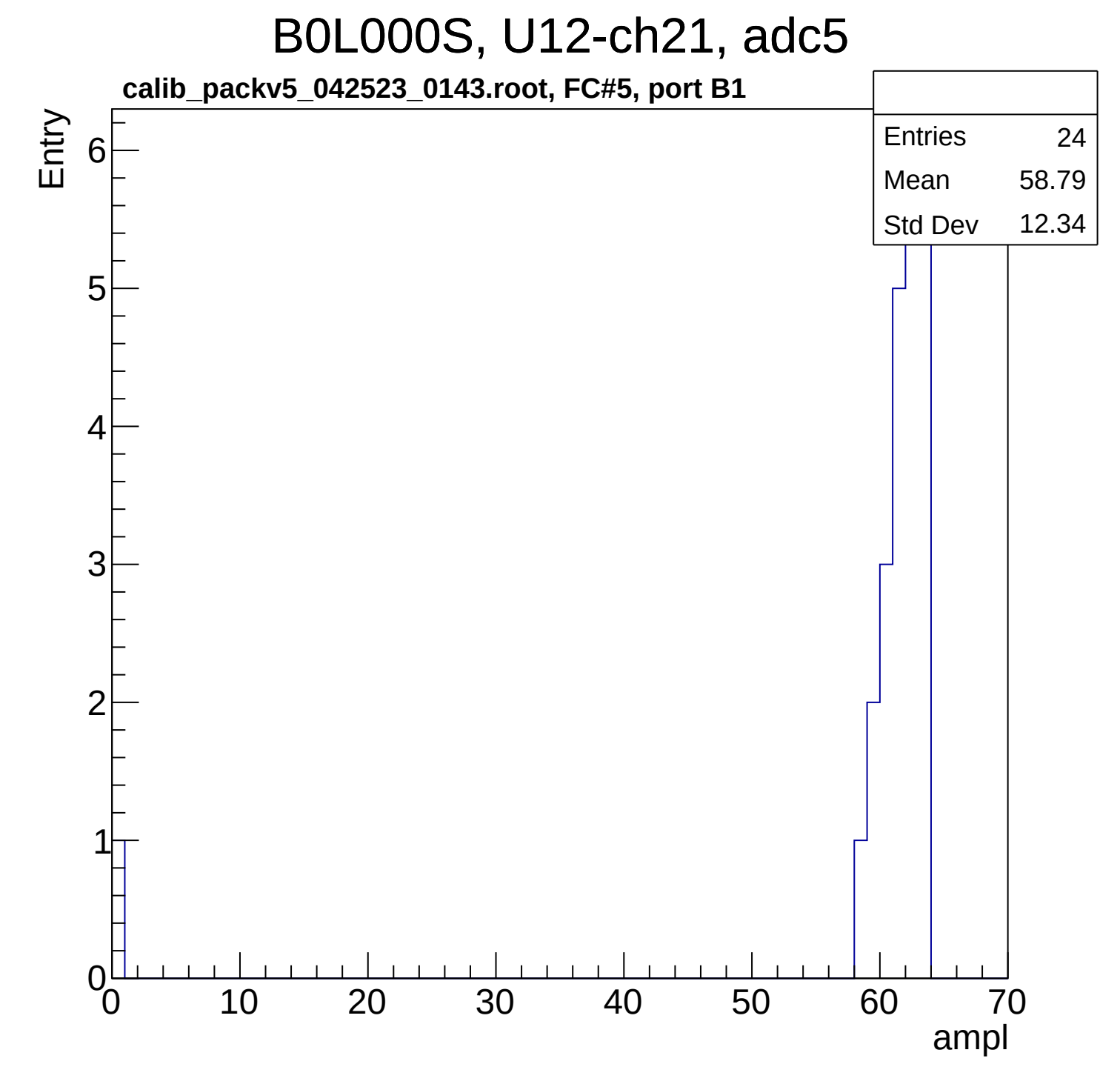

B0L000S, U12-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.25
Std Dev 3.294
Gaus mean : 30.9613
Gaus Width: 3.4792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

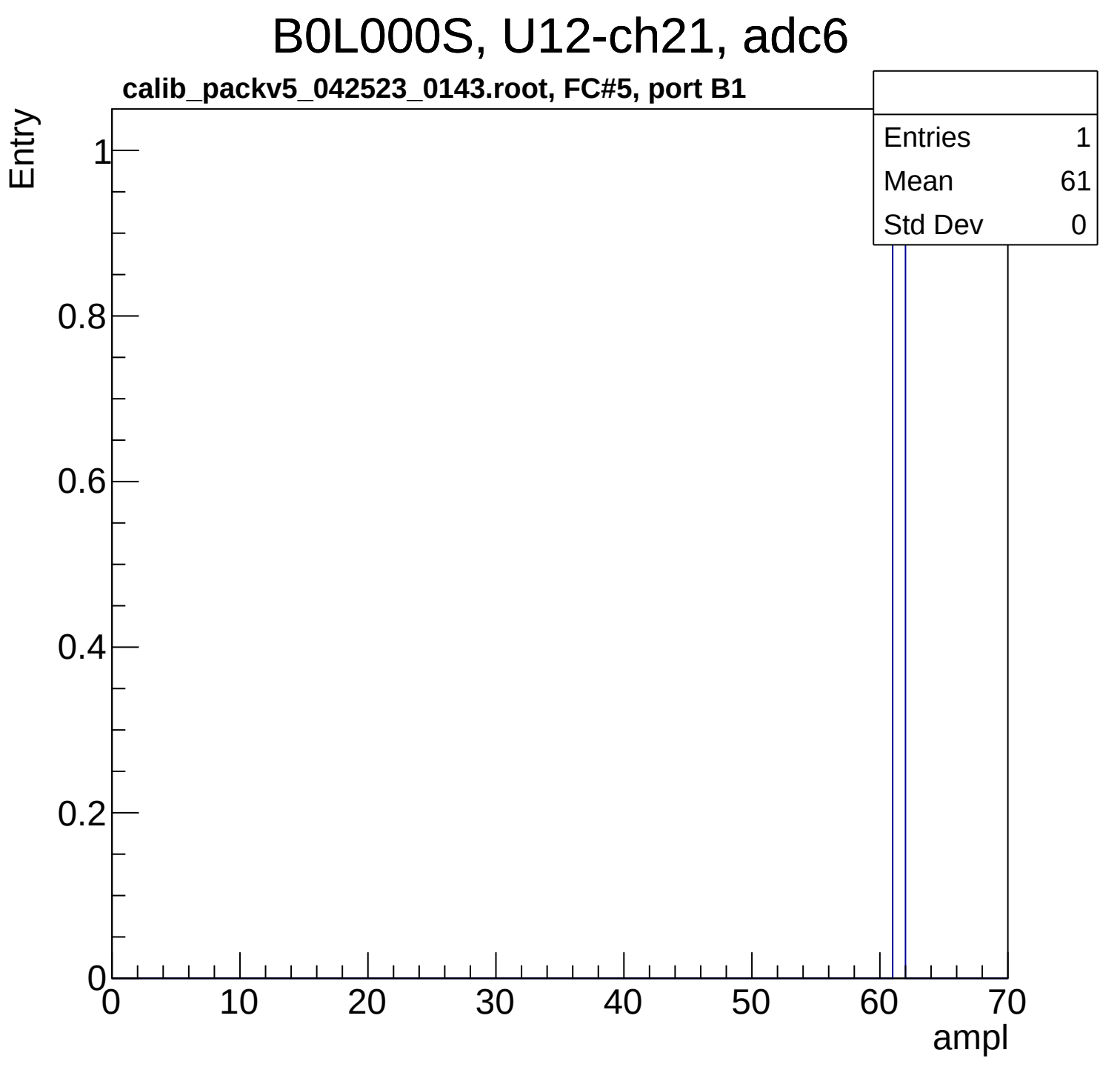

B0L000S, U12-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 38.32
Std Dev 3.499
Gaus mean : 39.1528
Gaus Width: 3.6975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

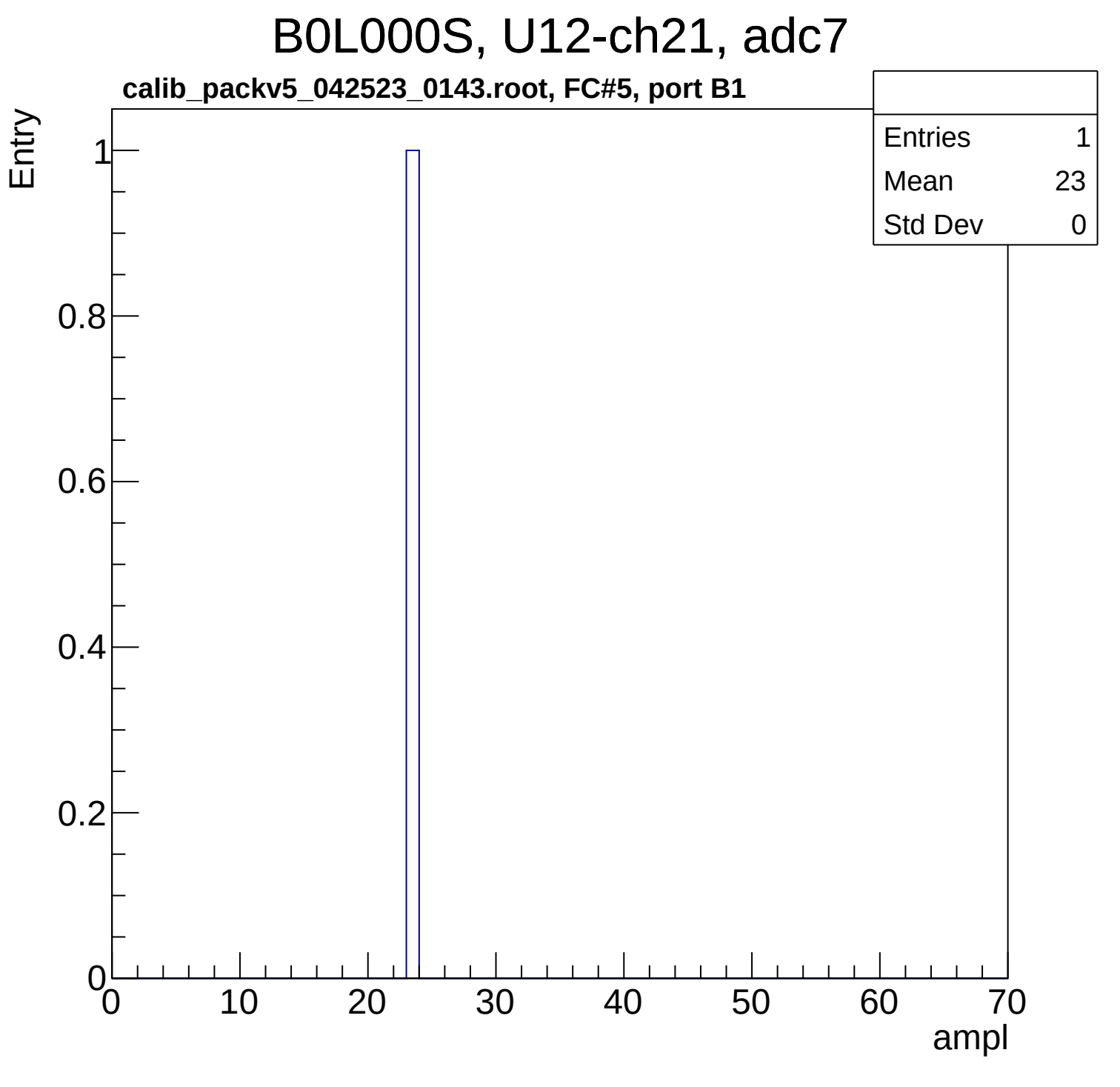

B0L000S, U12-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.01
Std Dev 3.606
Gaus mean : 45.4708
Gaus Width: 4.5041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

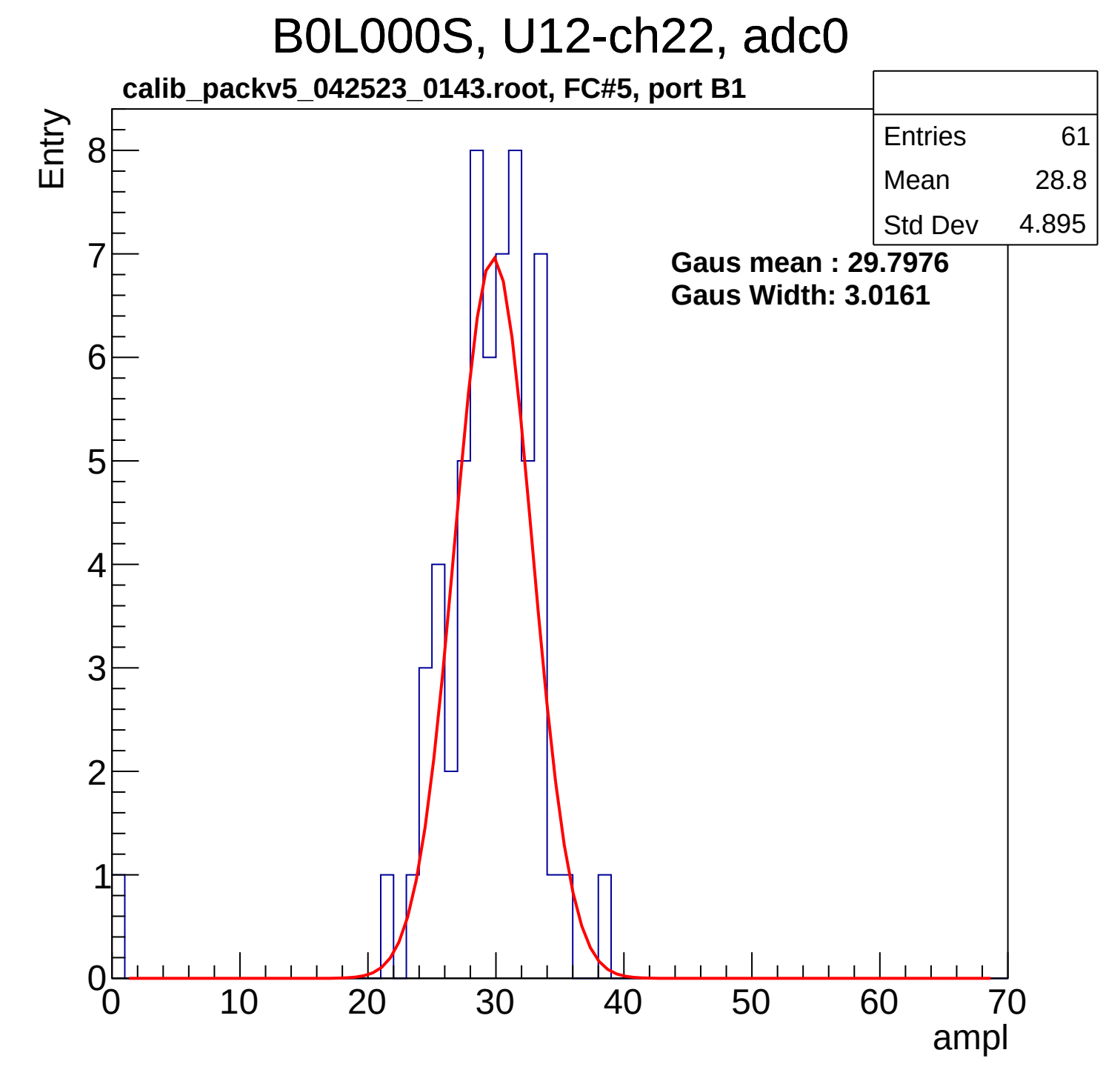

B0L000S, U12-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 52.34
Std Dev 3.405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

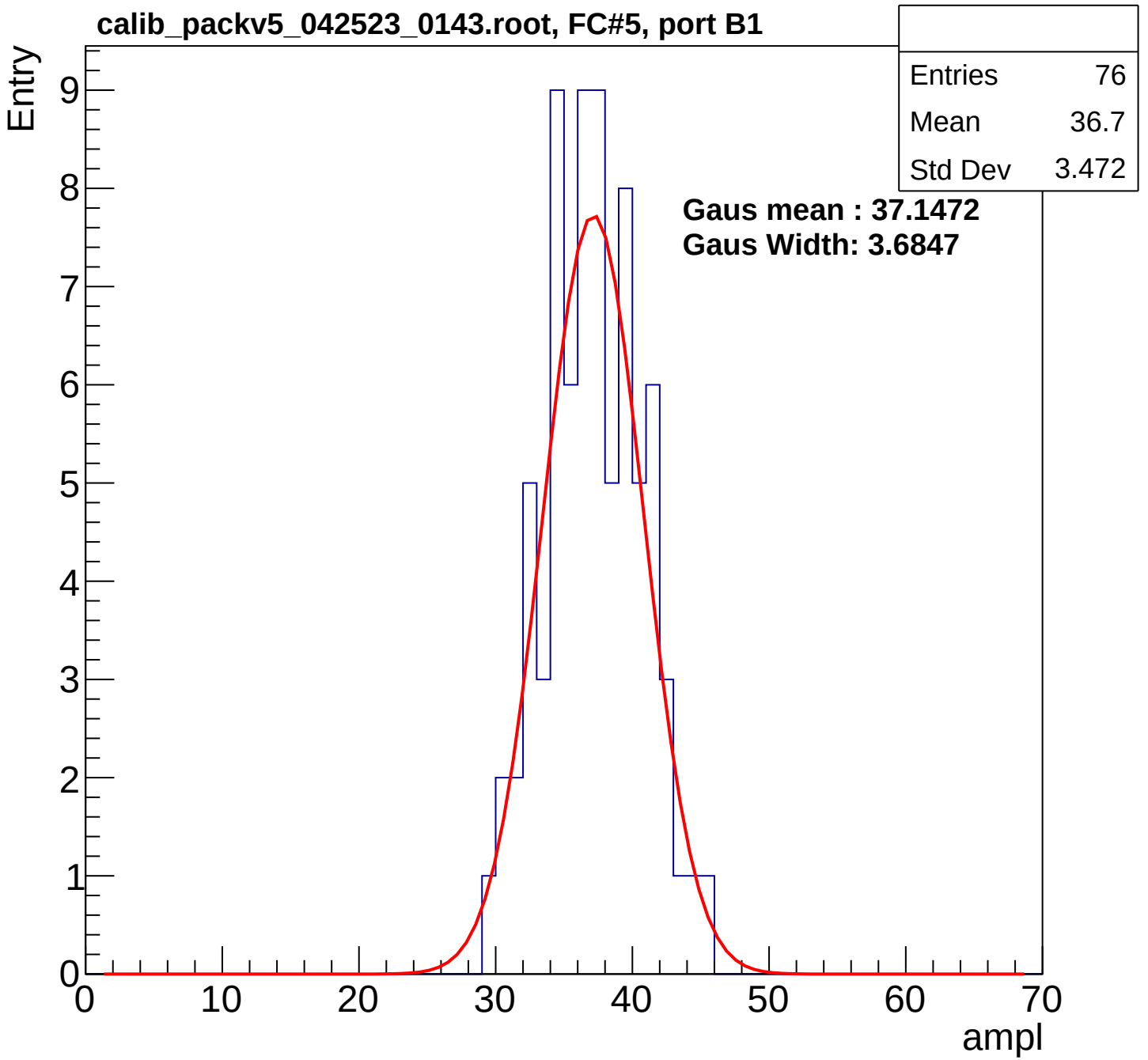

B0L000S, U12-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.29
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

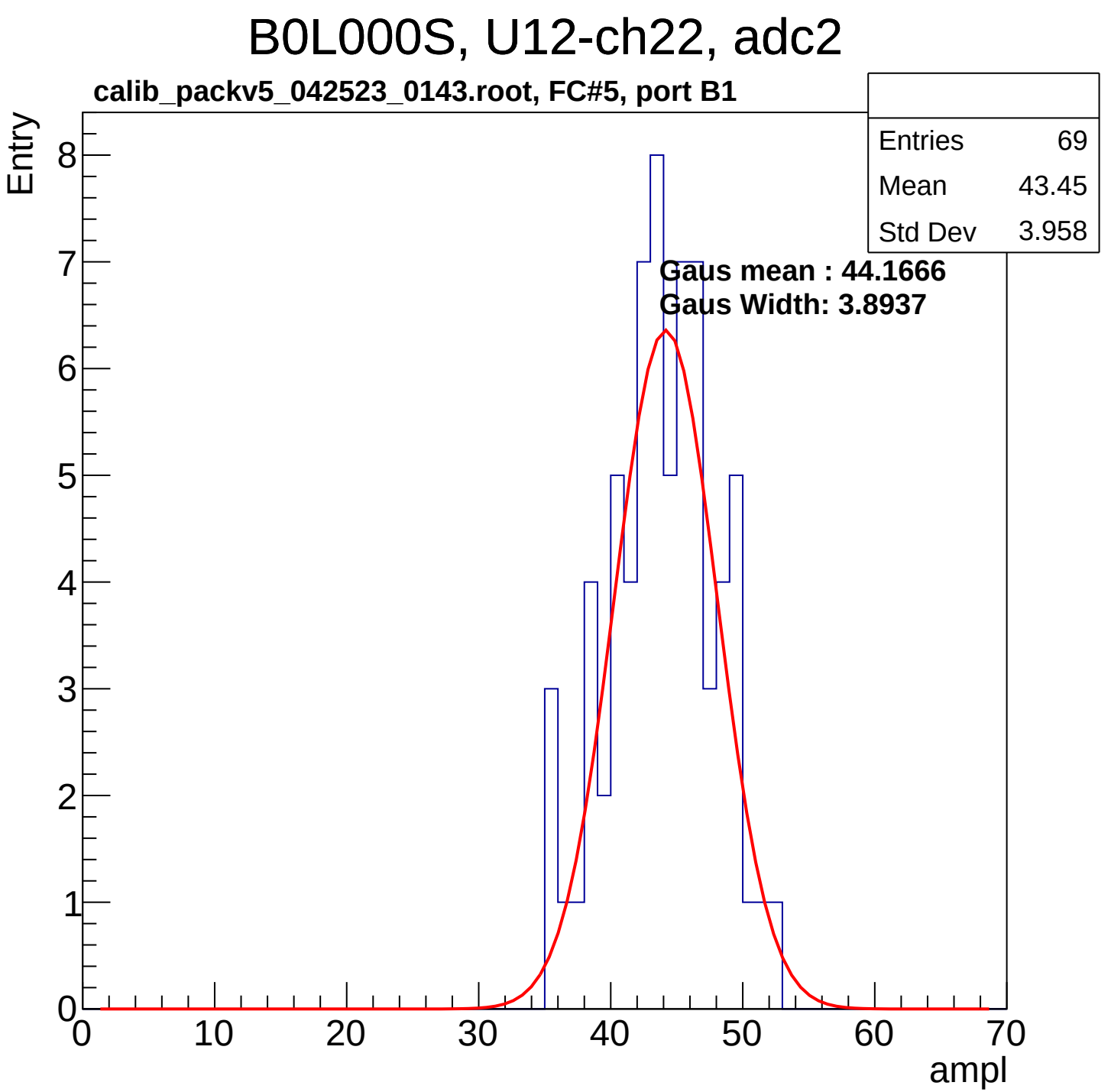

B0L000S, U12-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.79
Std Dev 12.34
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

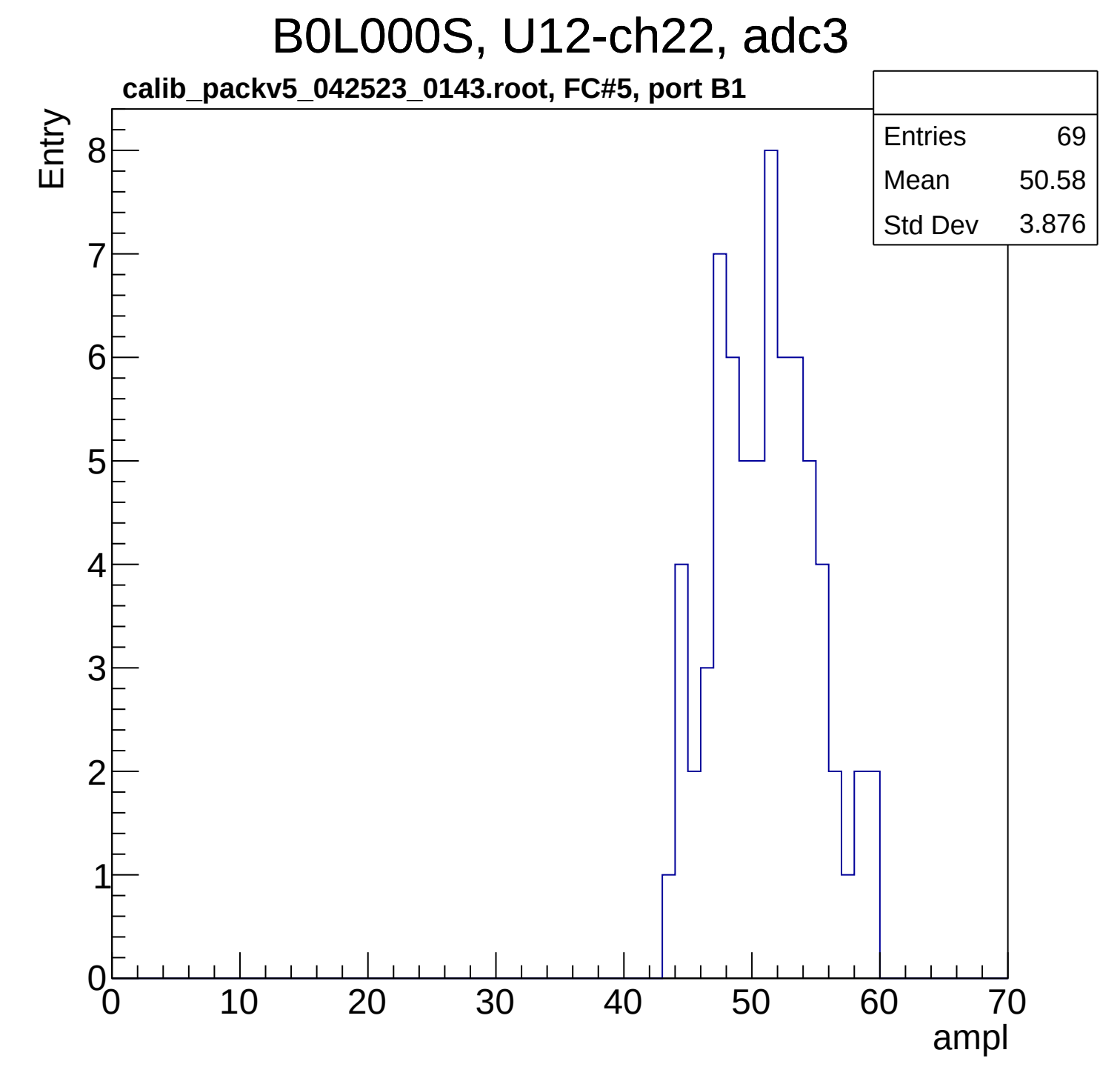

B0L000S, U12-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.8
Std Dev 4.895
Gaus mean : 29.7976
Gaus Width: 3.0161
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.7
Std Dev 3.472
Gaus mean : 37.1472
Gaus Width: 3.6847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.45
Std Dev 3.958
Gaus mean : 44.1666
Gaus Width: 3.8937
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.58
Std Dev 3.876
Entry
ampl
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1
2
3
4
5
6
7
8
0
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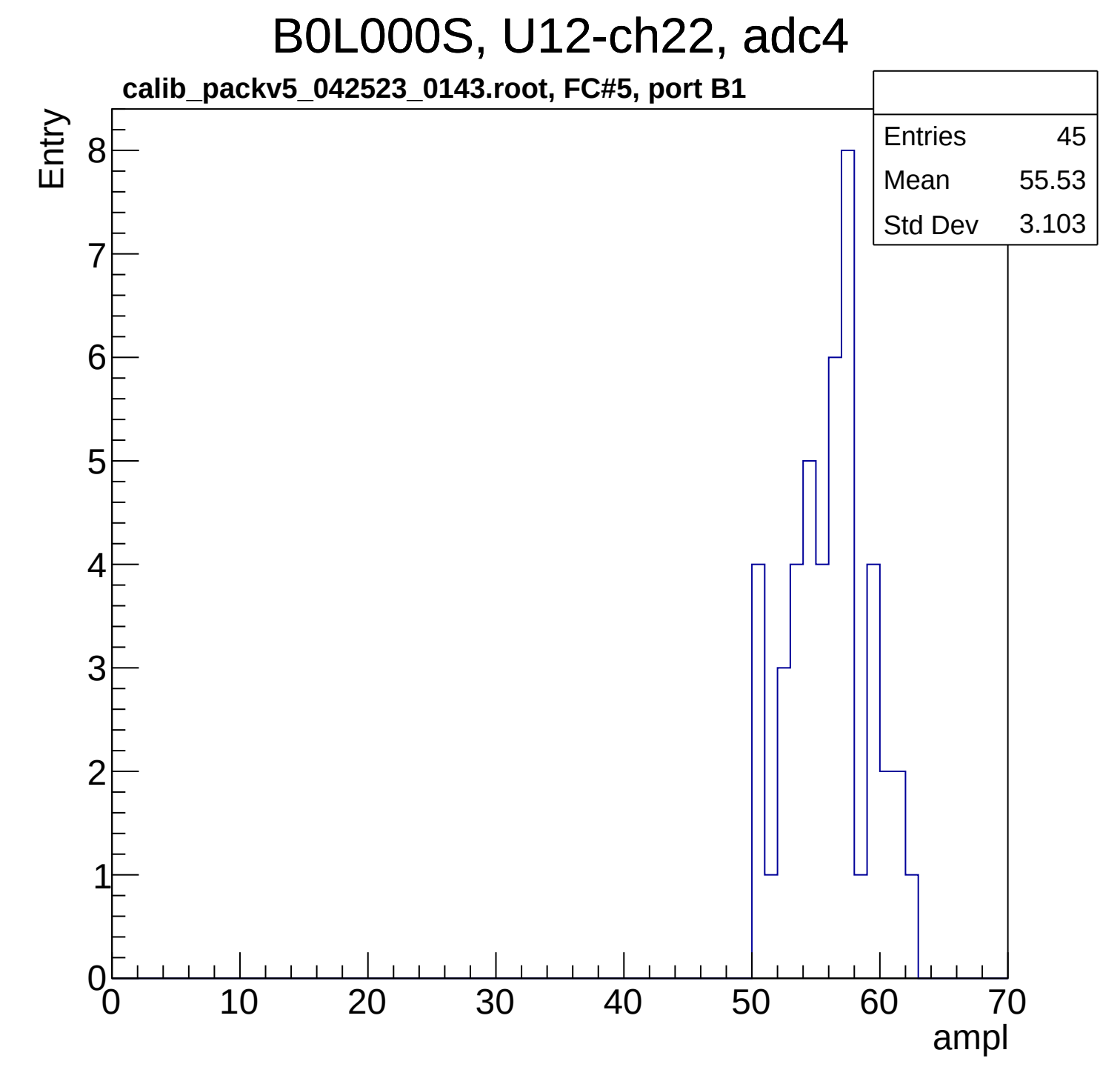
20
30
40
50
60
70


B0L000S, U12-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 55.53
Std Dev 3.103
Entry
ampl
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2
3
4
5
6
7
8
0
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20
30
40
50
60
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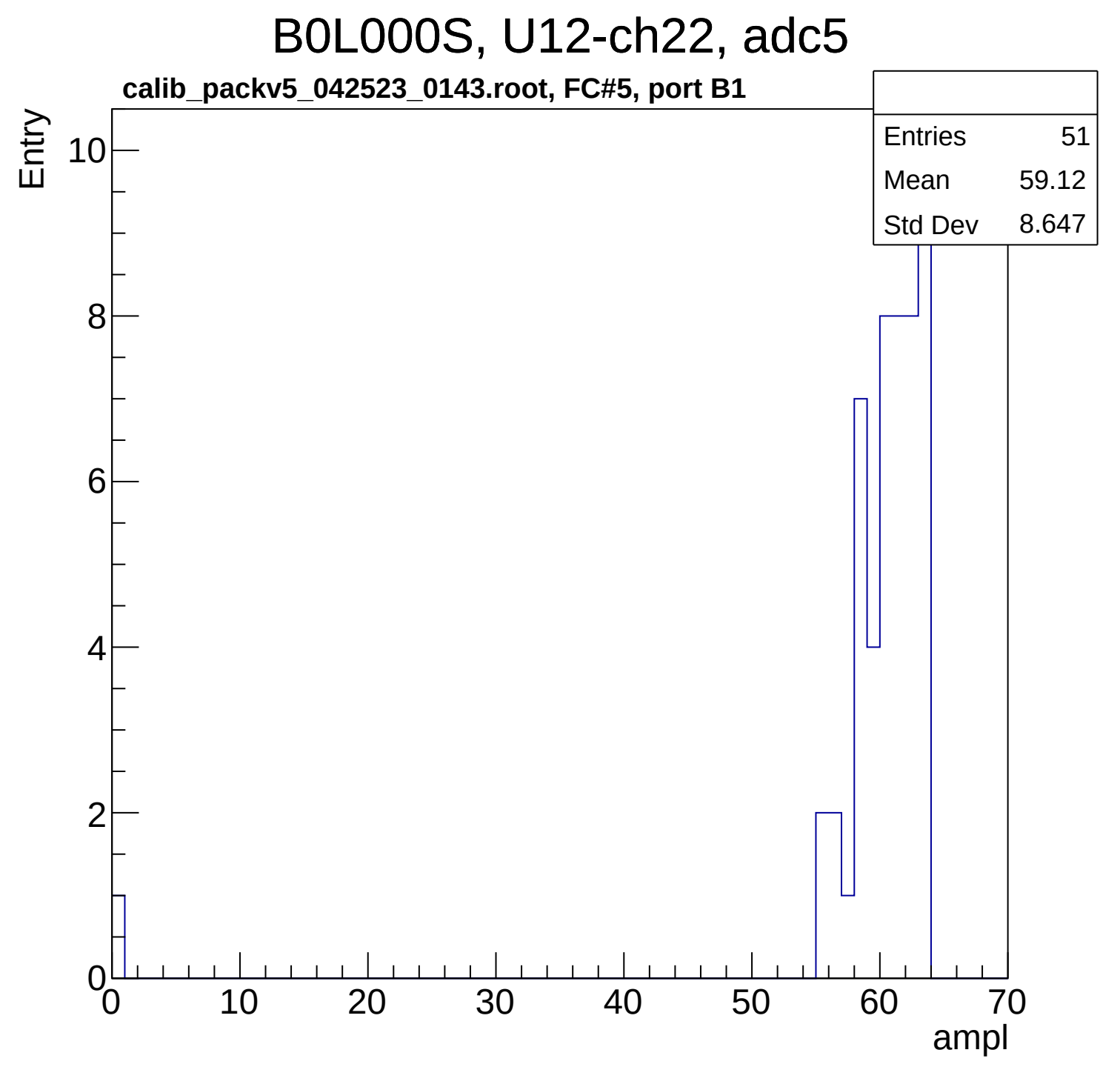
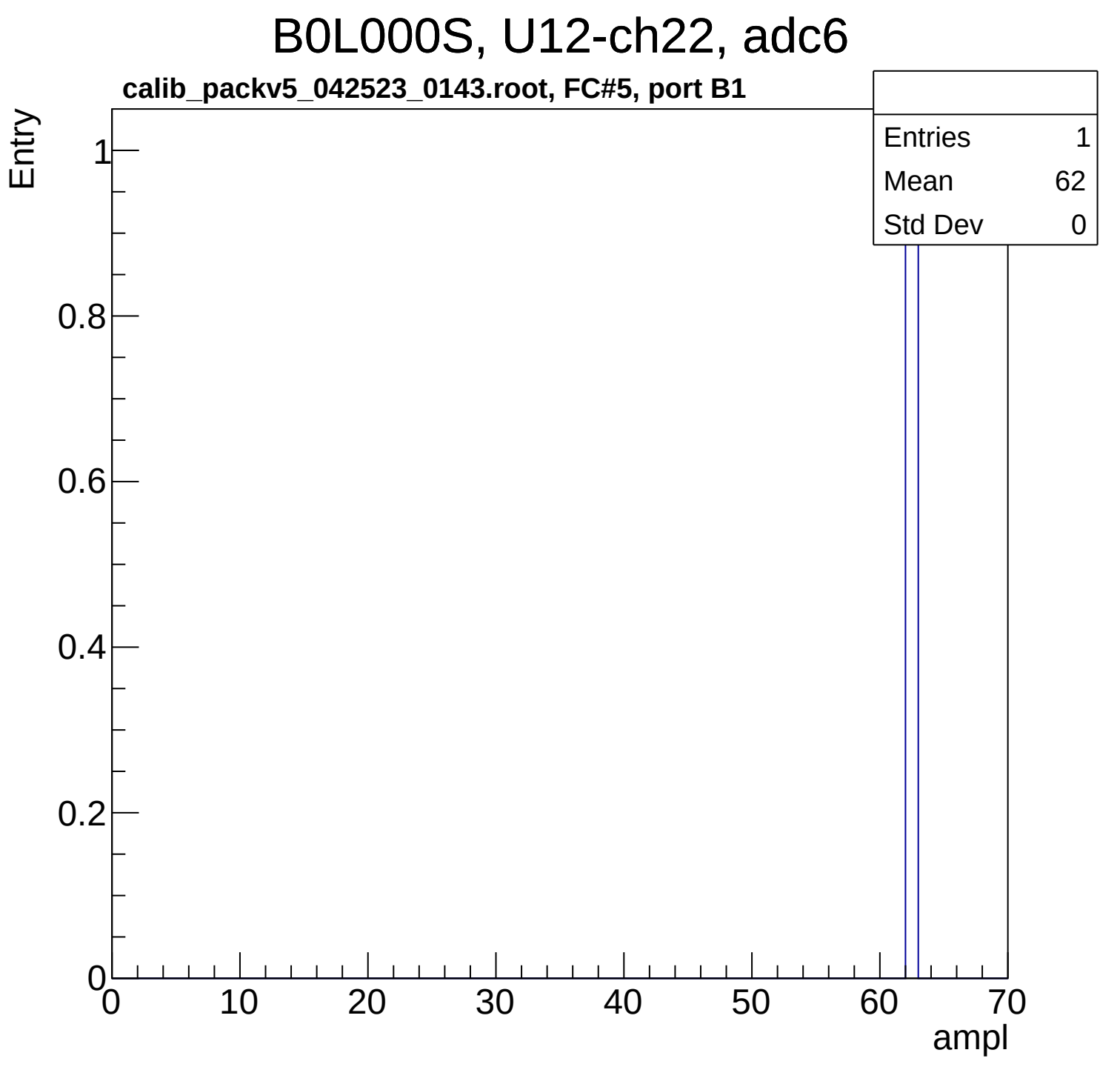

B0L000S, U12-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.12
Std Dev 8.647
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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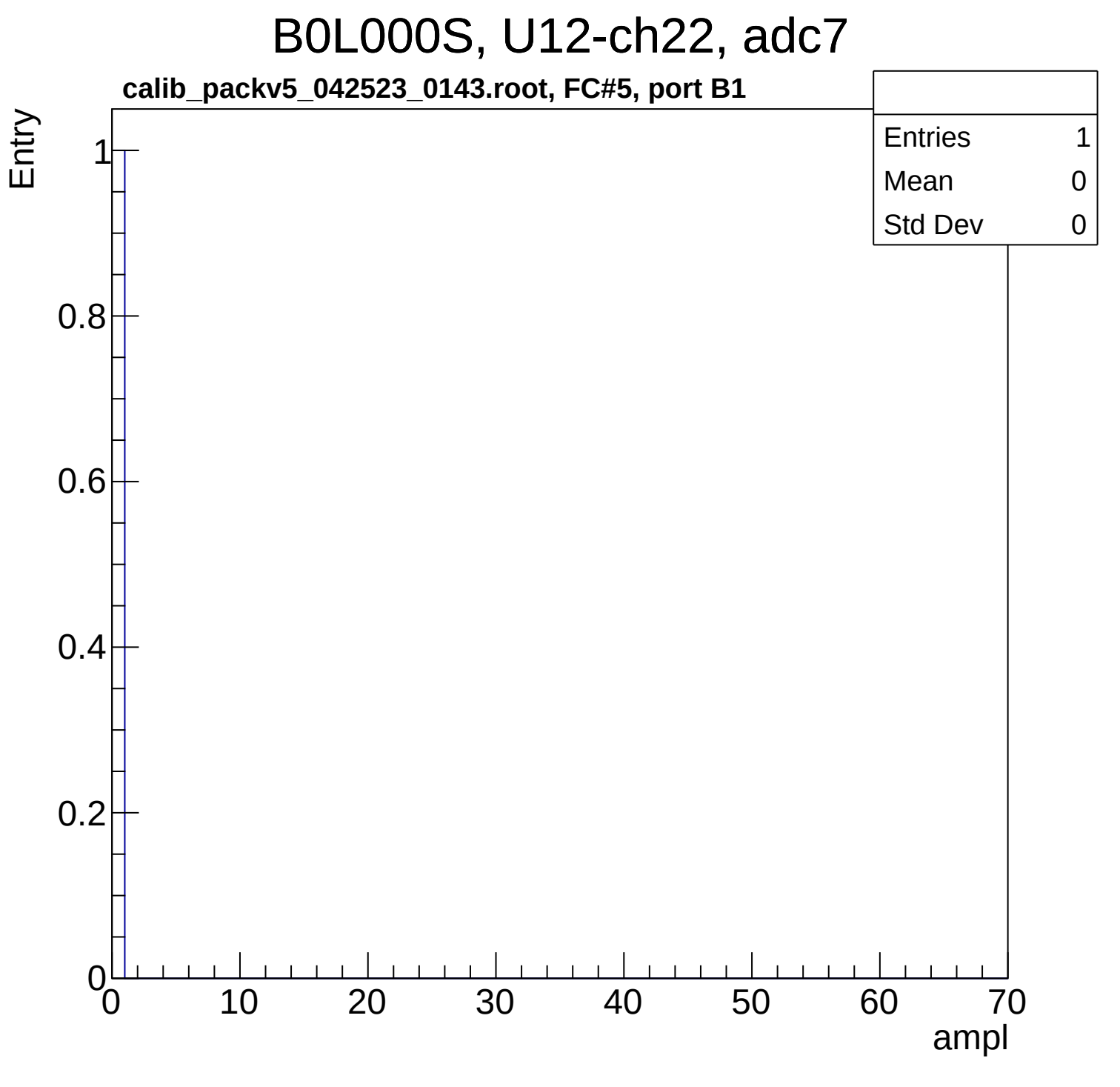
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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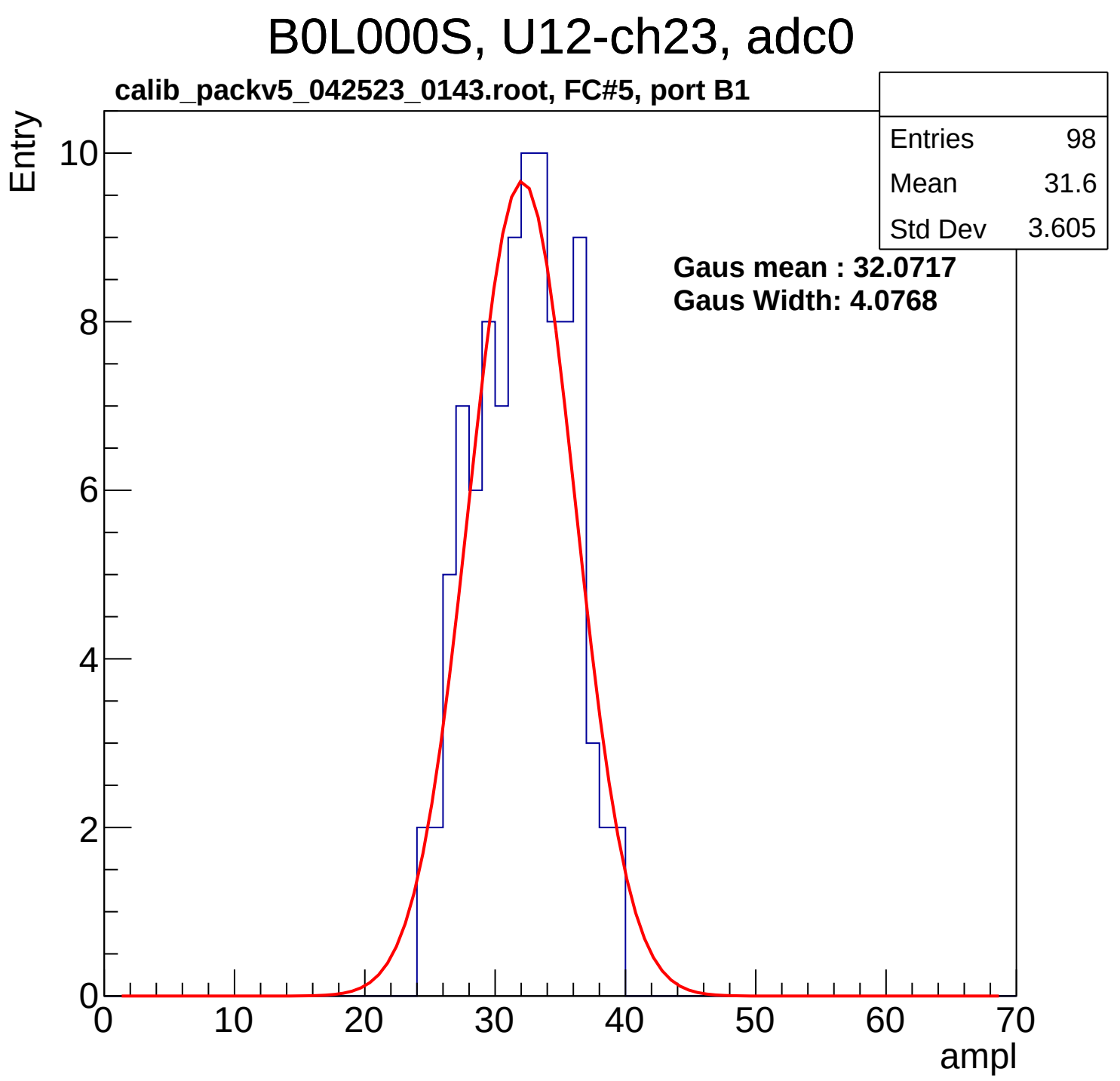
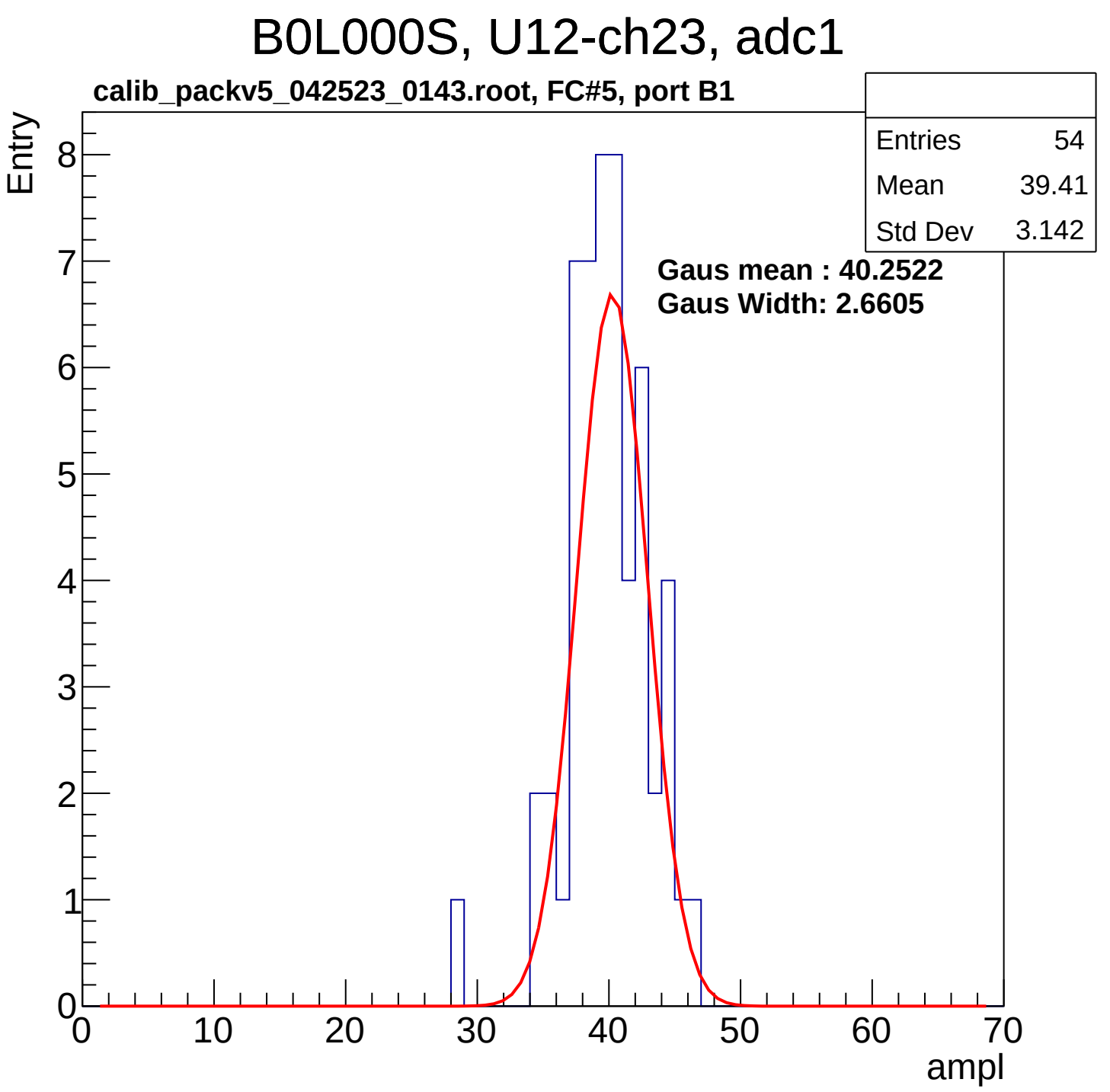
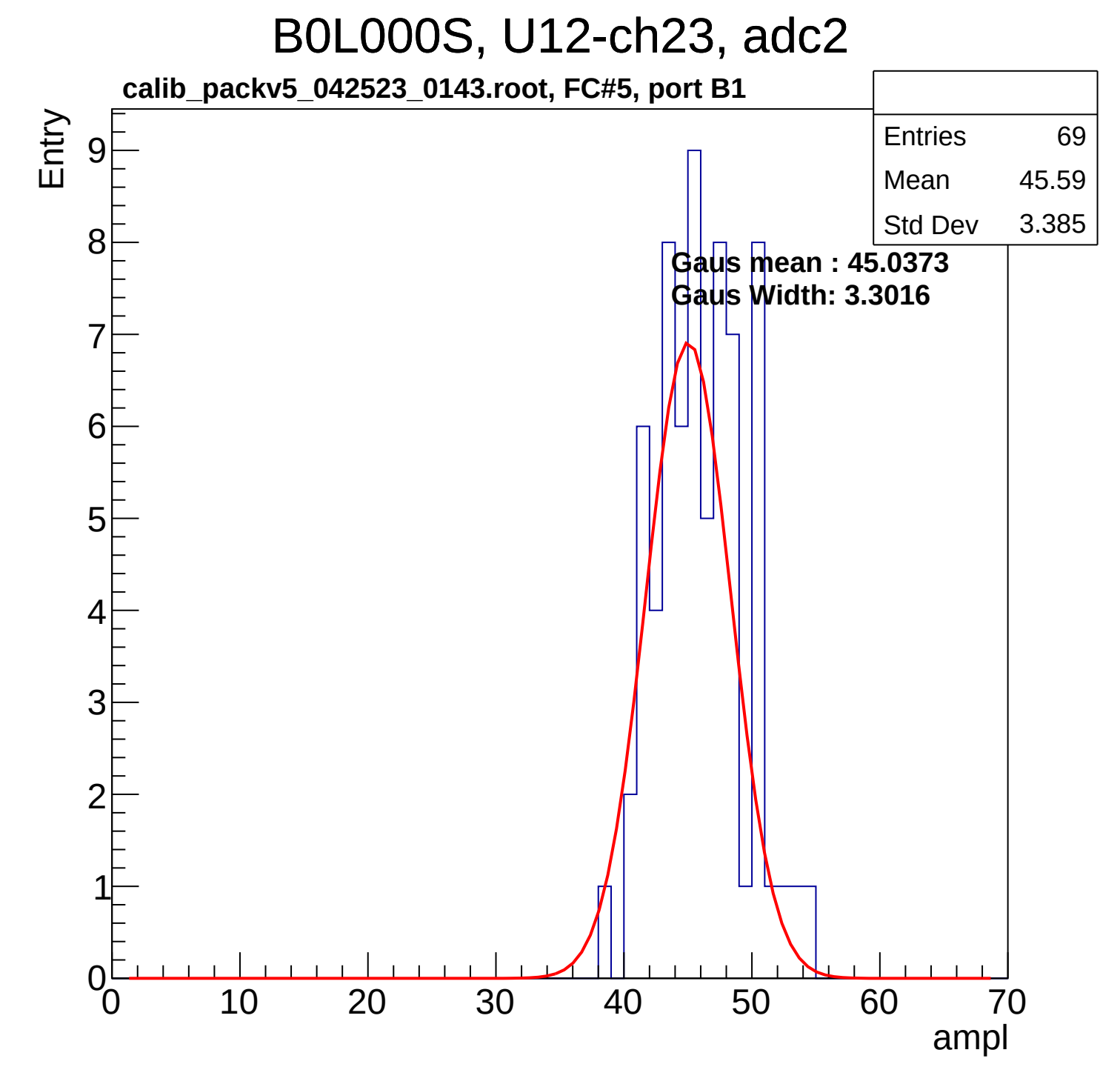
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 98
Mean 31.6
Std Dev 3.605
Gaus mean : 32.0717
Gaus Width: 4.0768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 39.41
Std Dev 3.142
Gaus mean : 40.2522
Gaus Width: 2.6605
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.59
Std Dev 3.385
Gaus mean : 45.0373
Gaus Width: 3.3016
Entry
ampl
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2
3
4
5
6
7
8
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0
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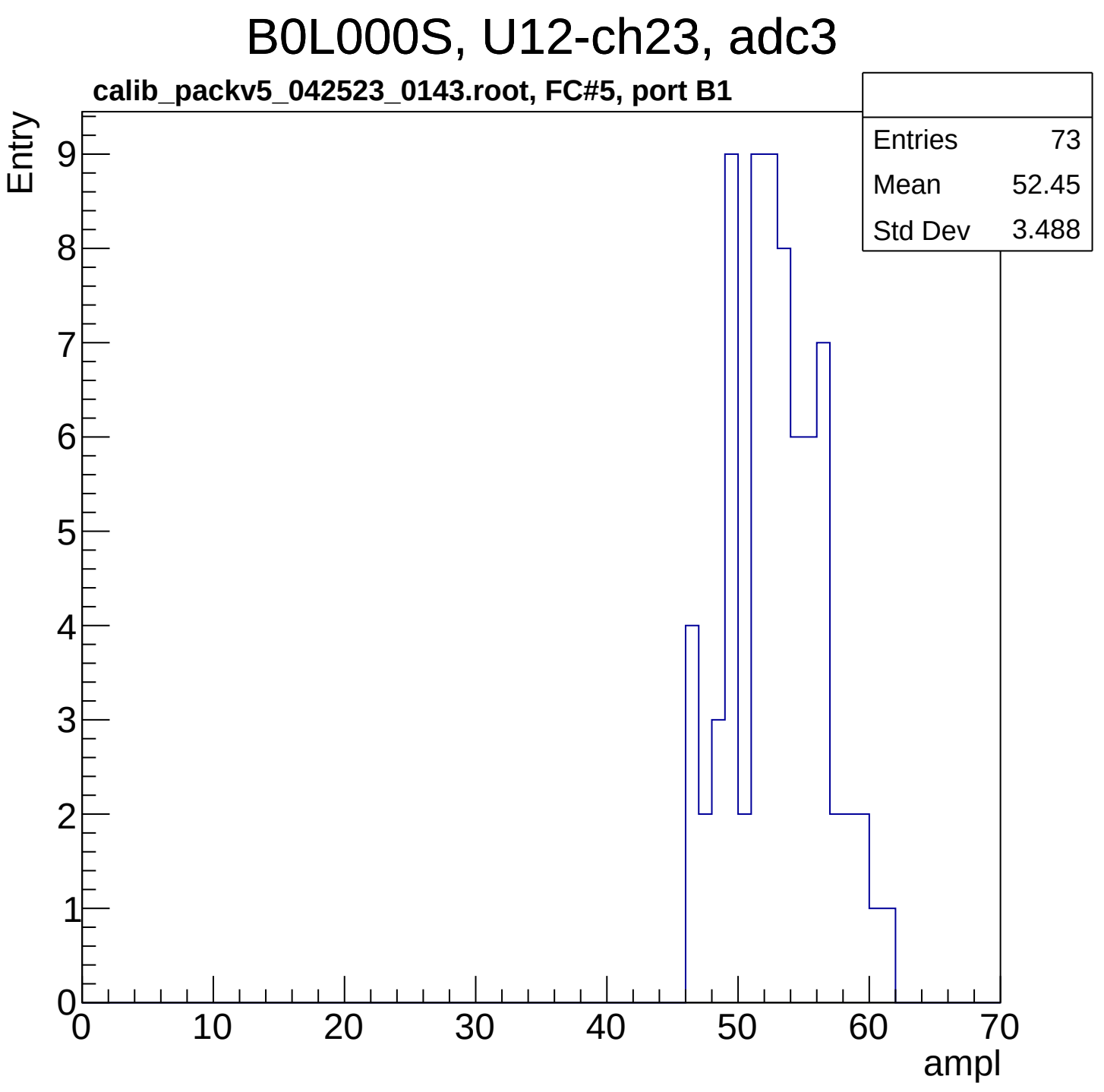
B0L000S, U12-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 52.45
Std Dev 3.488
Entry
ampl
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2
3
4
5
6
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8
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0
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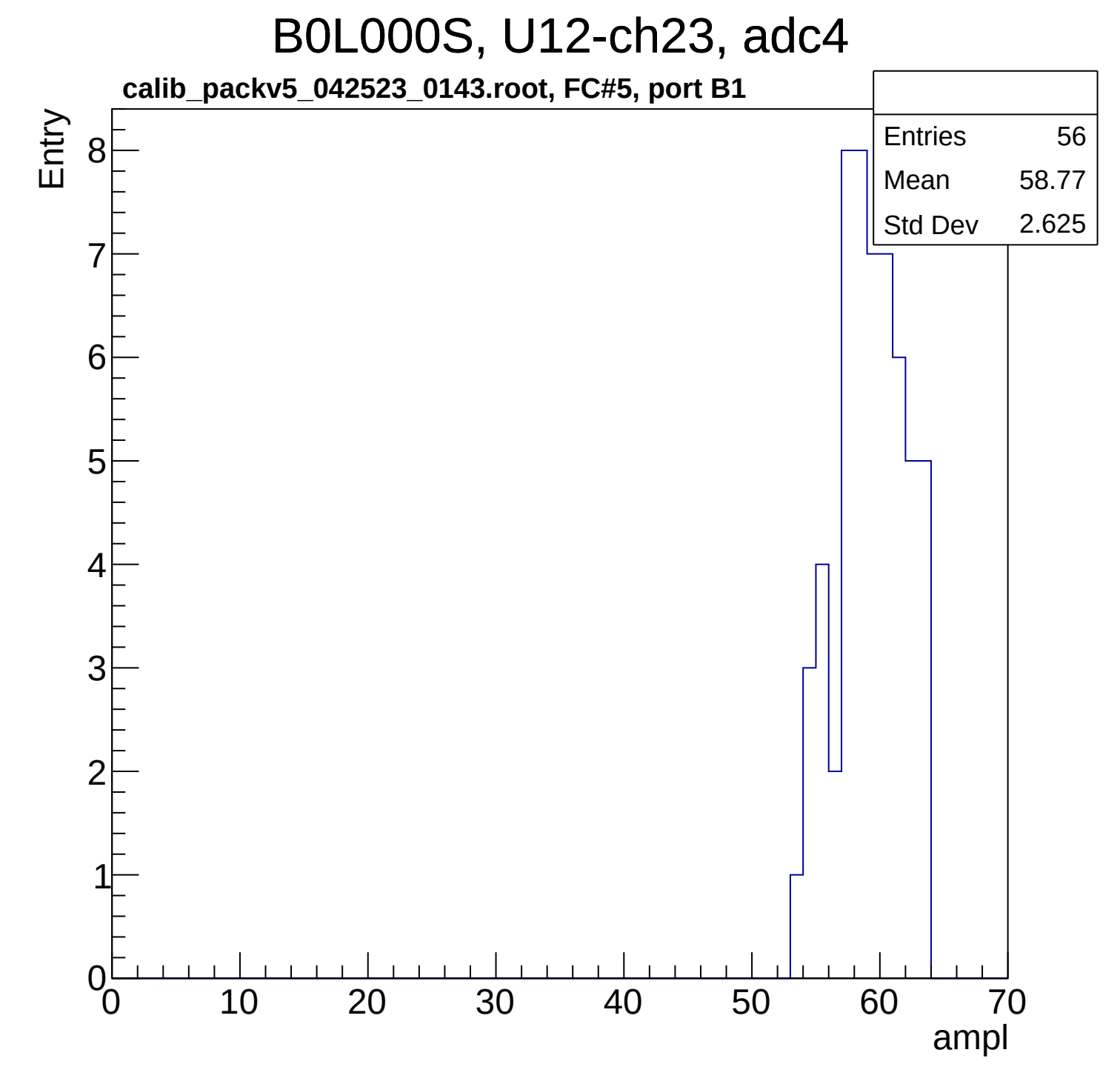
B0L000S, U12-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.77
Std Dev 2.625
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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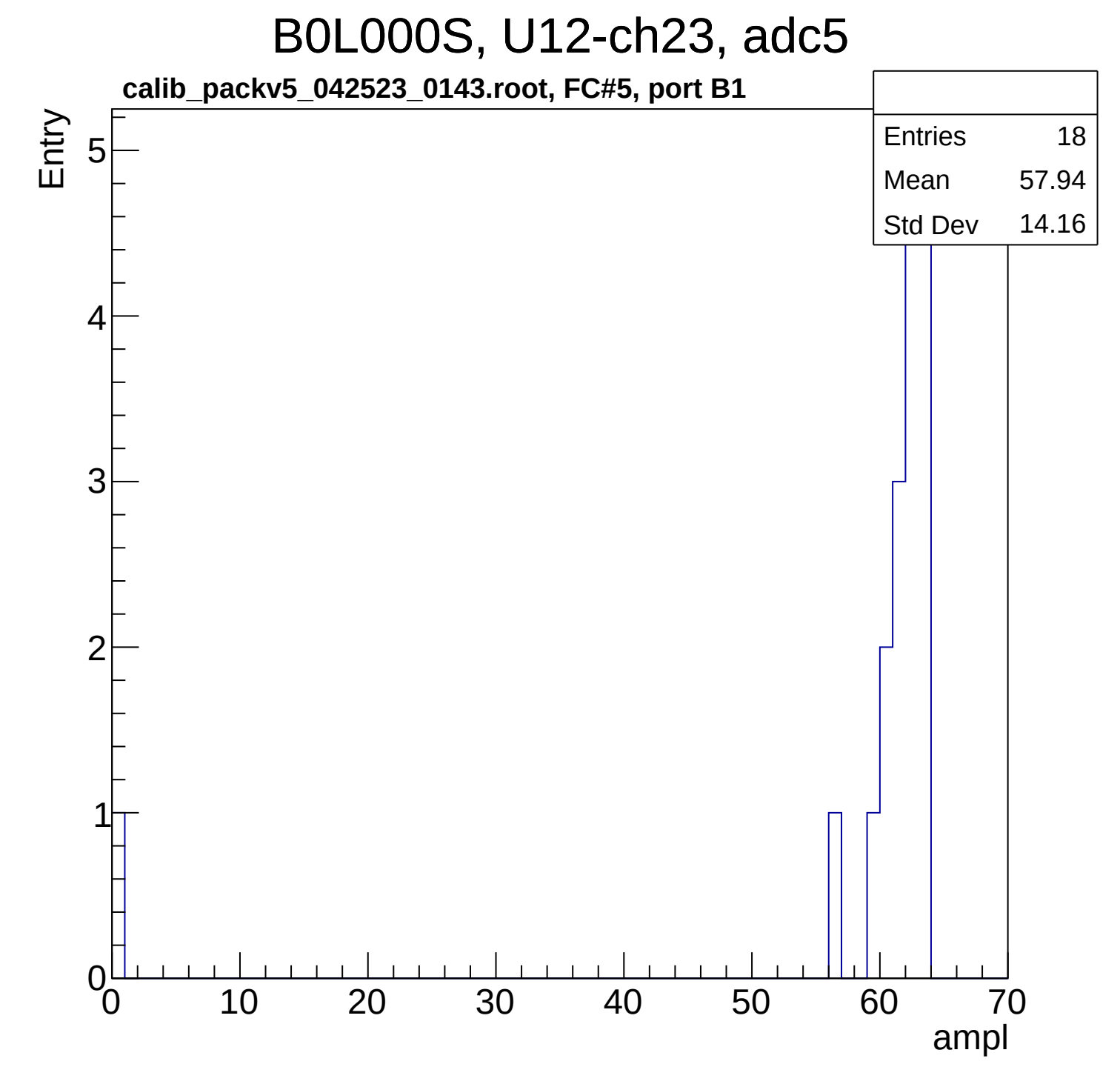
B0L000S, U12-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 57.94
Std Dev 14.16
Entry
0
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2
3
4
5
0
10
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50
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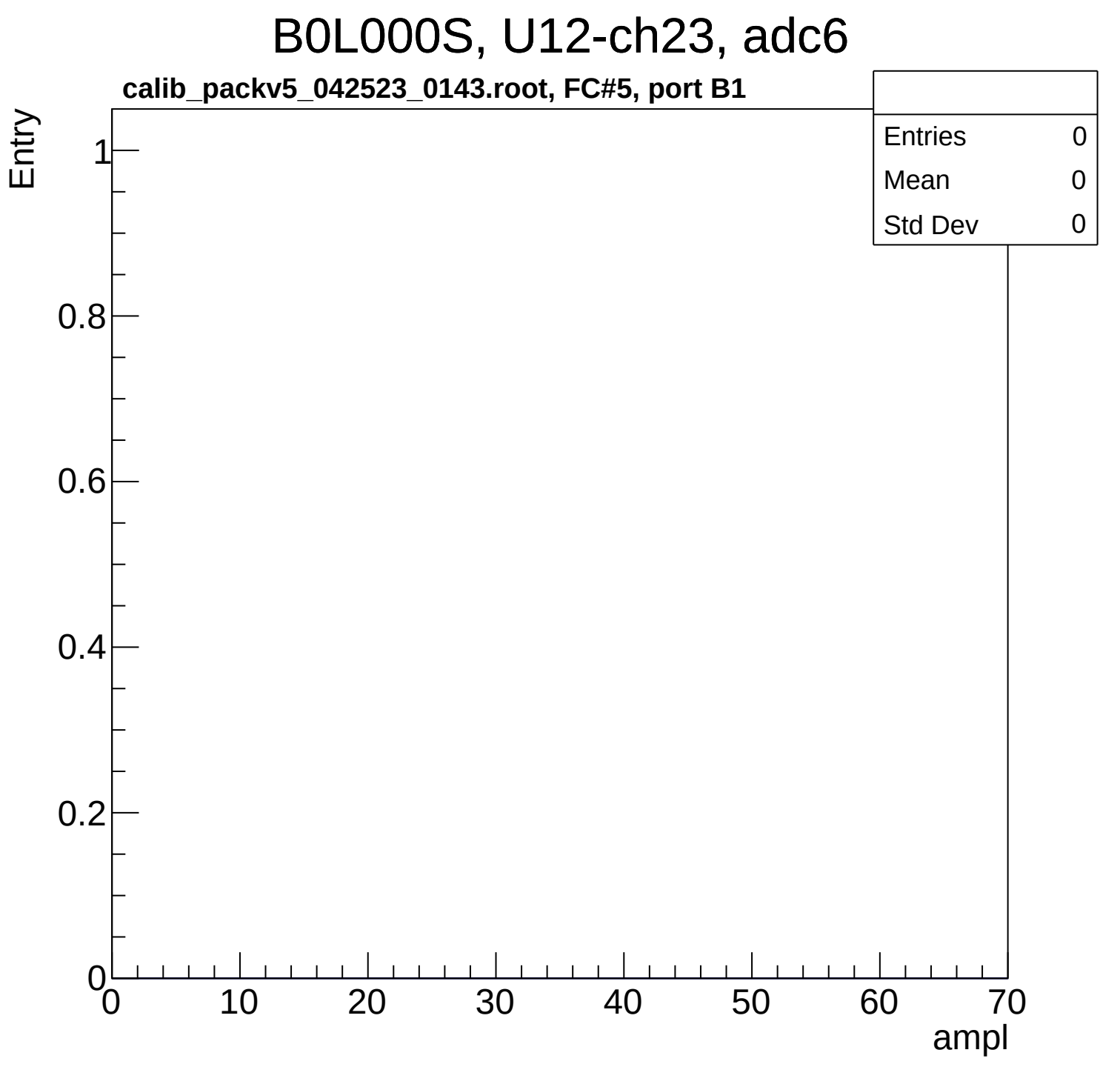
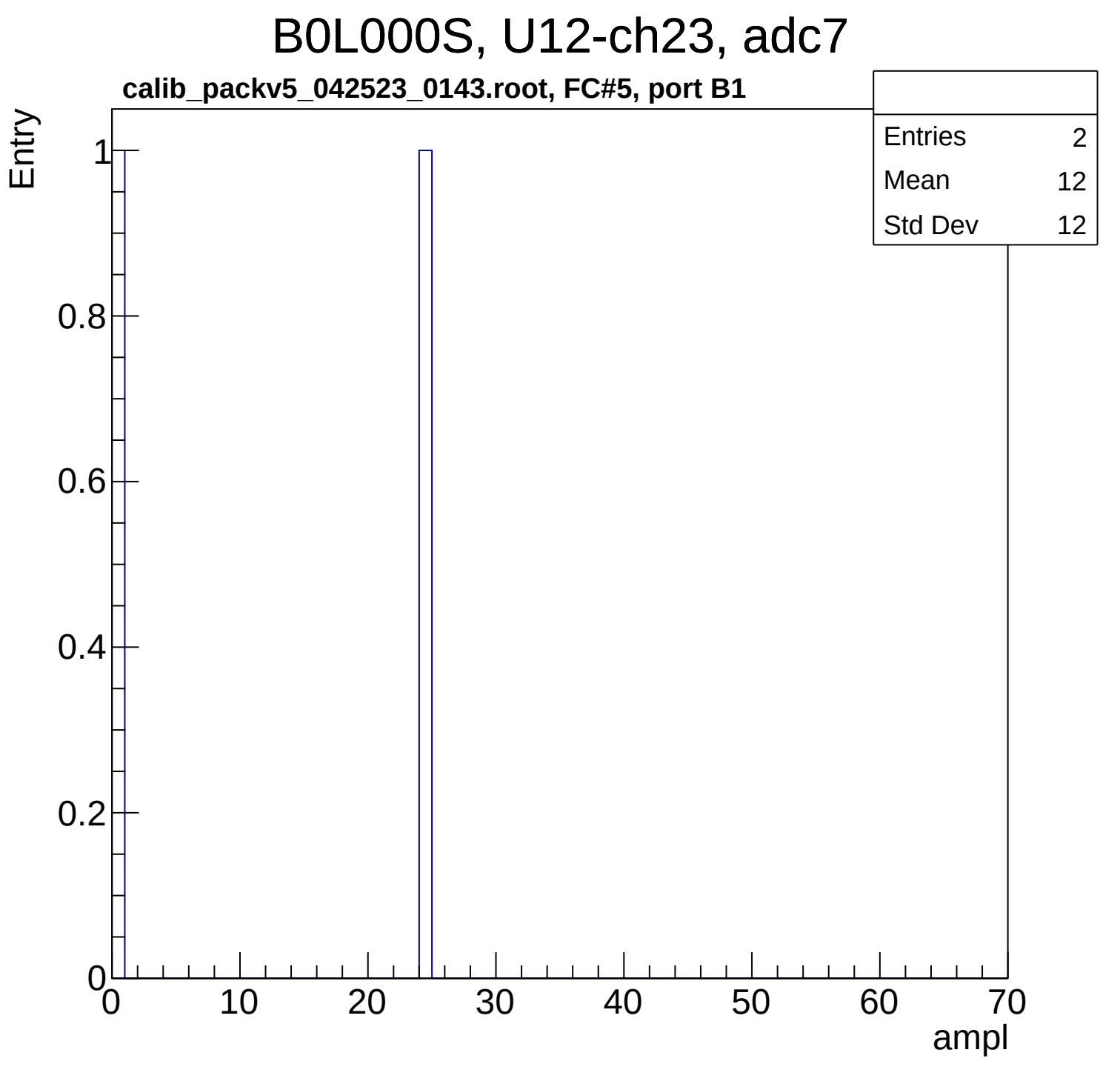
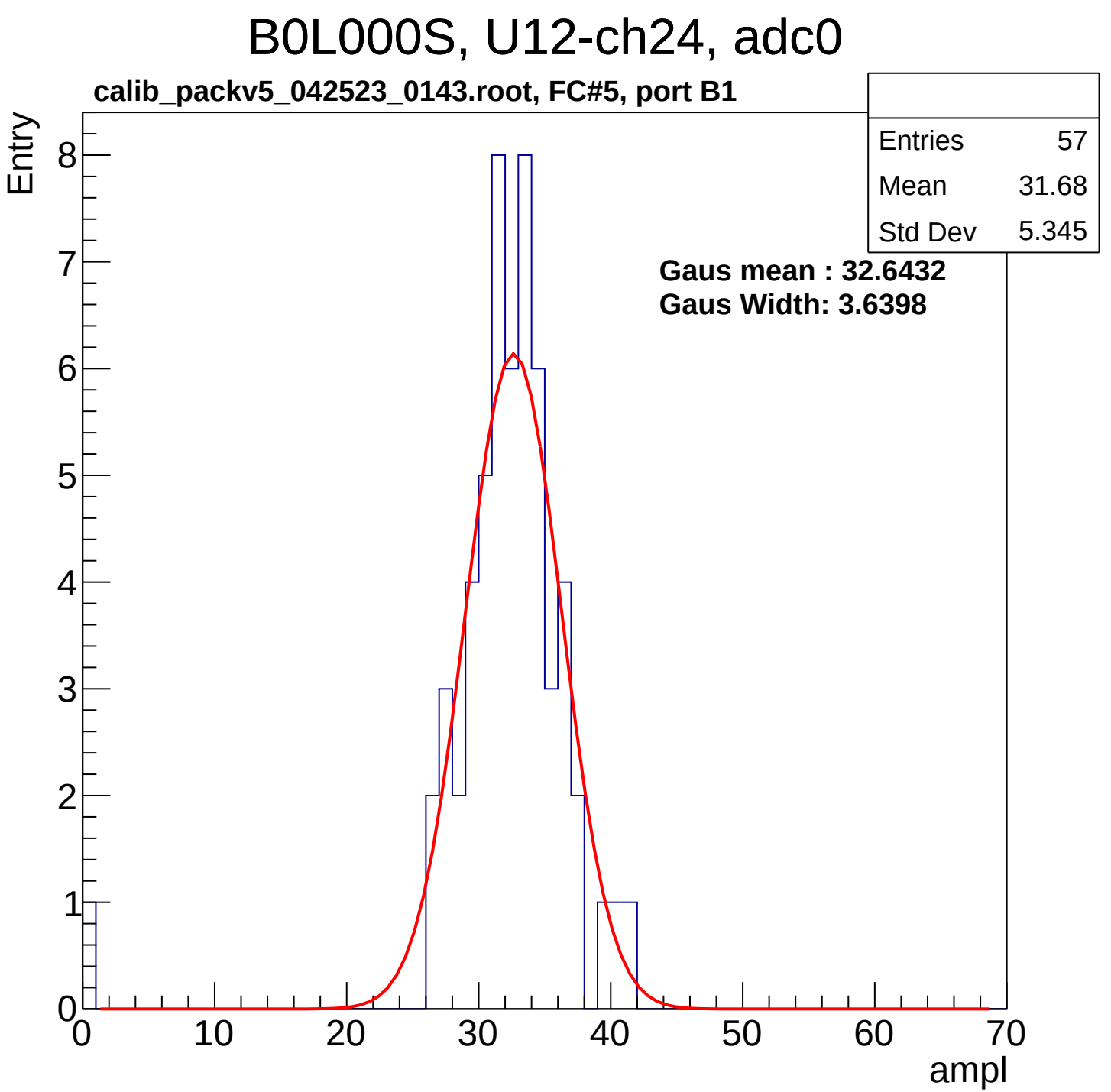
70
ampl


B0L000S, U12-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 31.68
Std Dev 5.345
Gaus mean : 32.6432
Gaus Width: 3.6398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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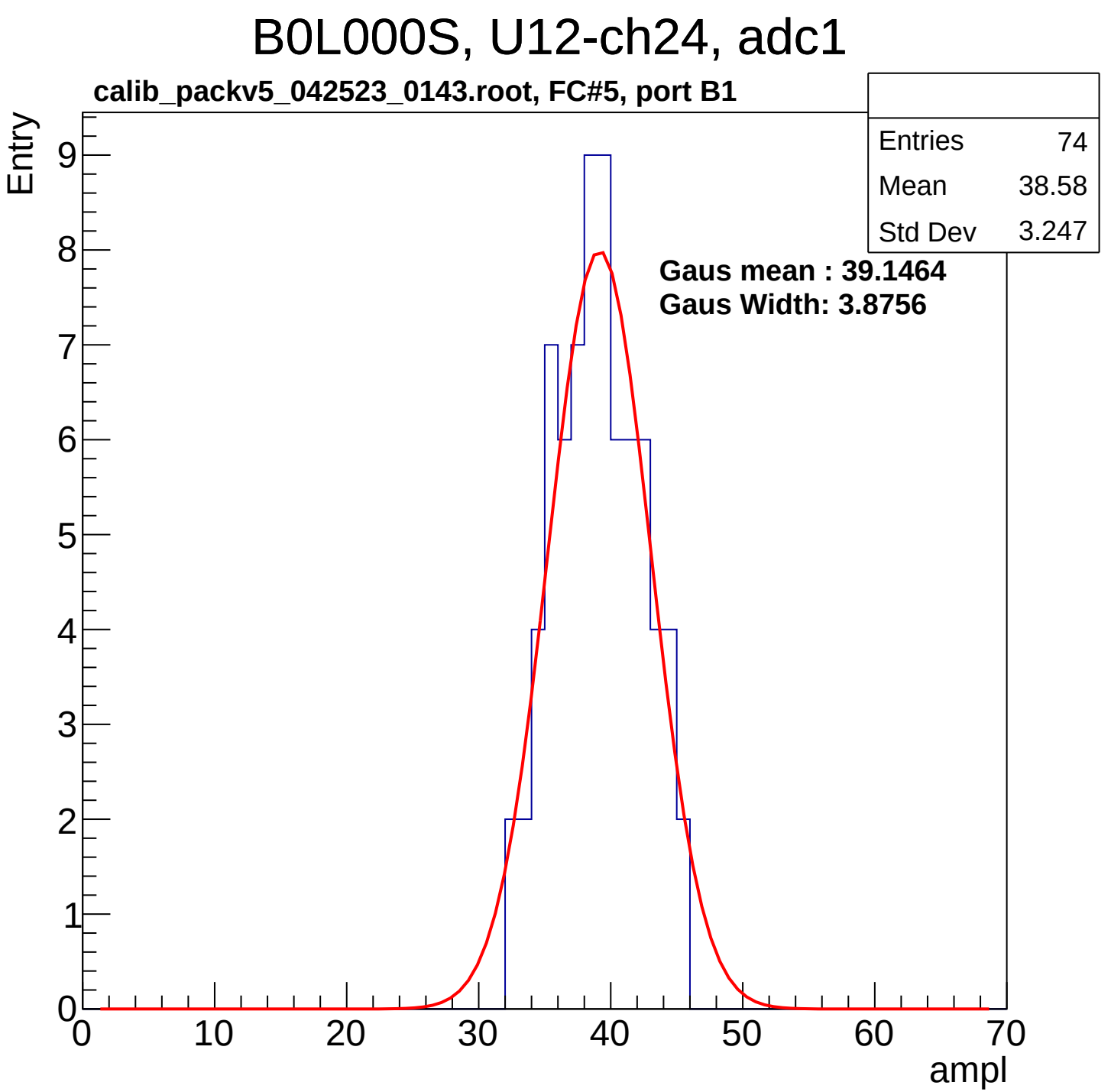
B0L000S, U12-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 38.58
Std Dev 3.247
Gaus mean : 39.1464
Gaus Width: 3.8756
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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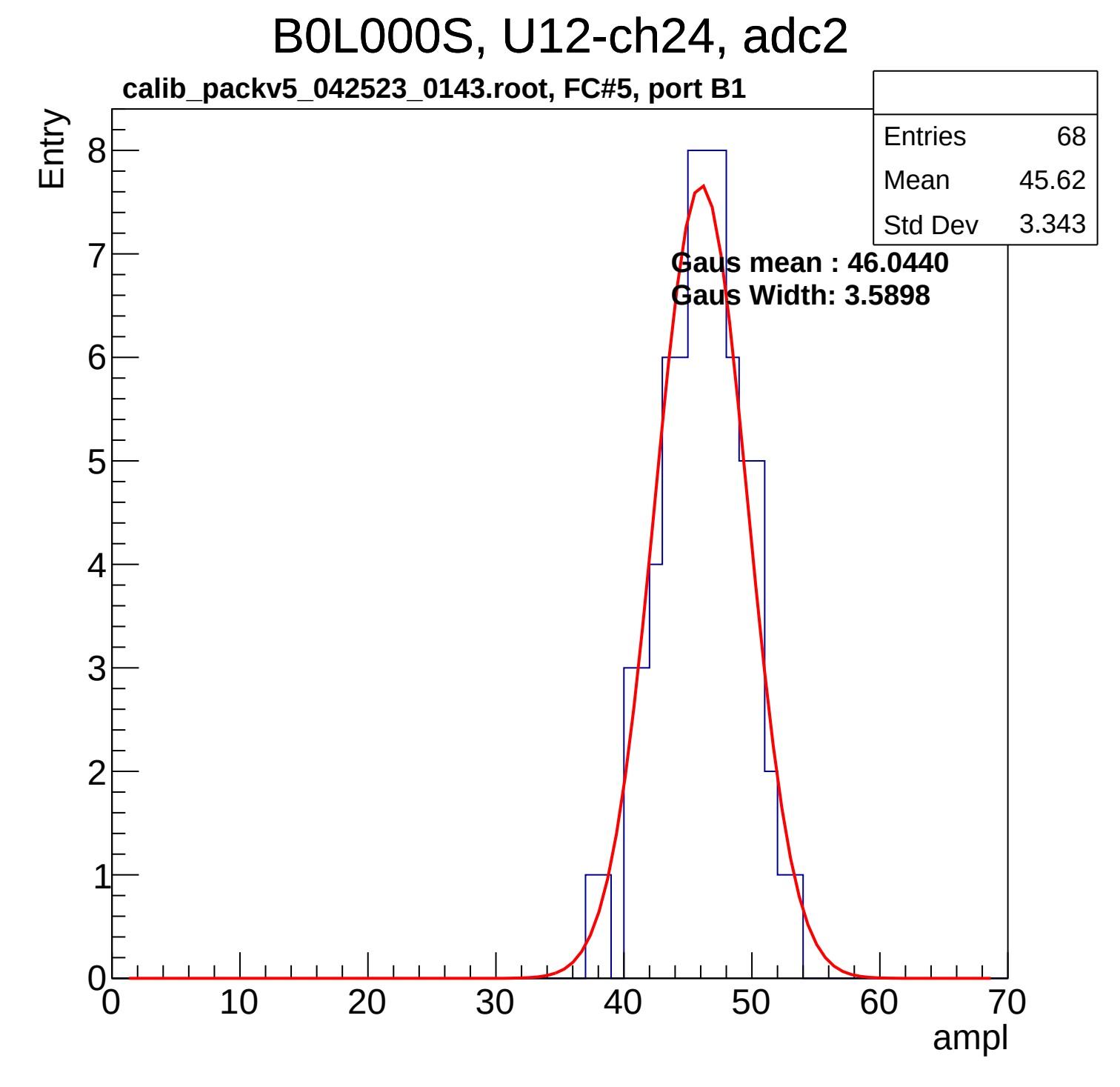
B0L000S, U12-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 45.62
Std Dev 3.343
Gaus mean : 46.0440
Gaus Width: 3.5898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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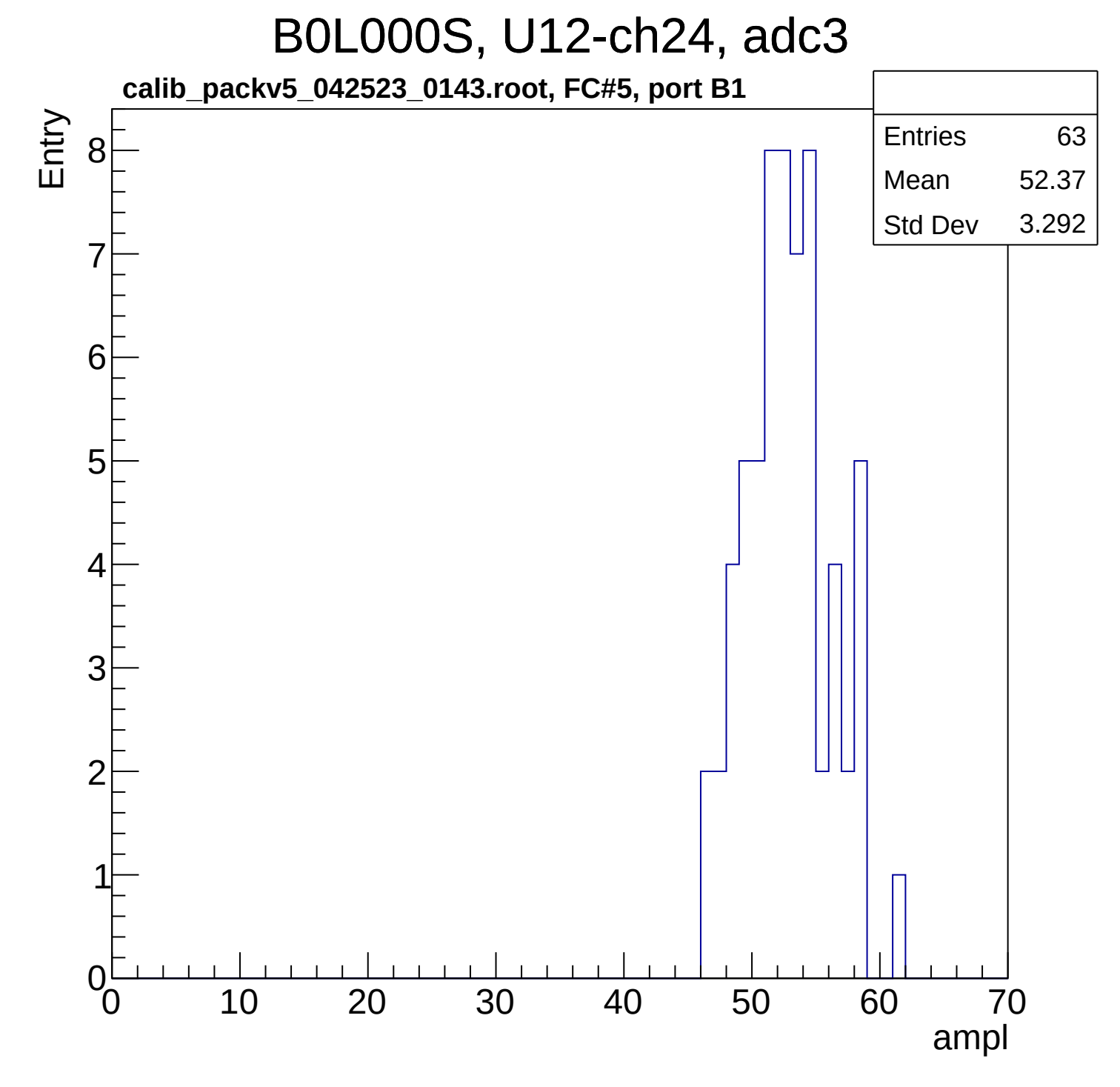
B0L000S, U12-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 52.37
Std Dev 3.292
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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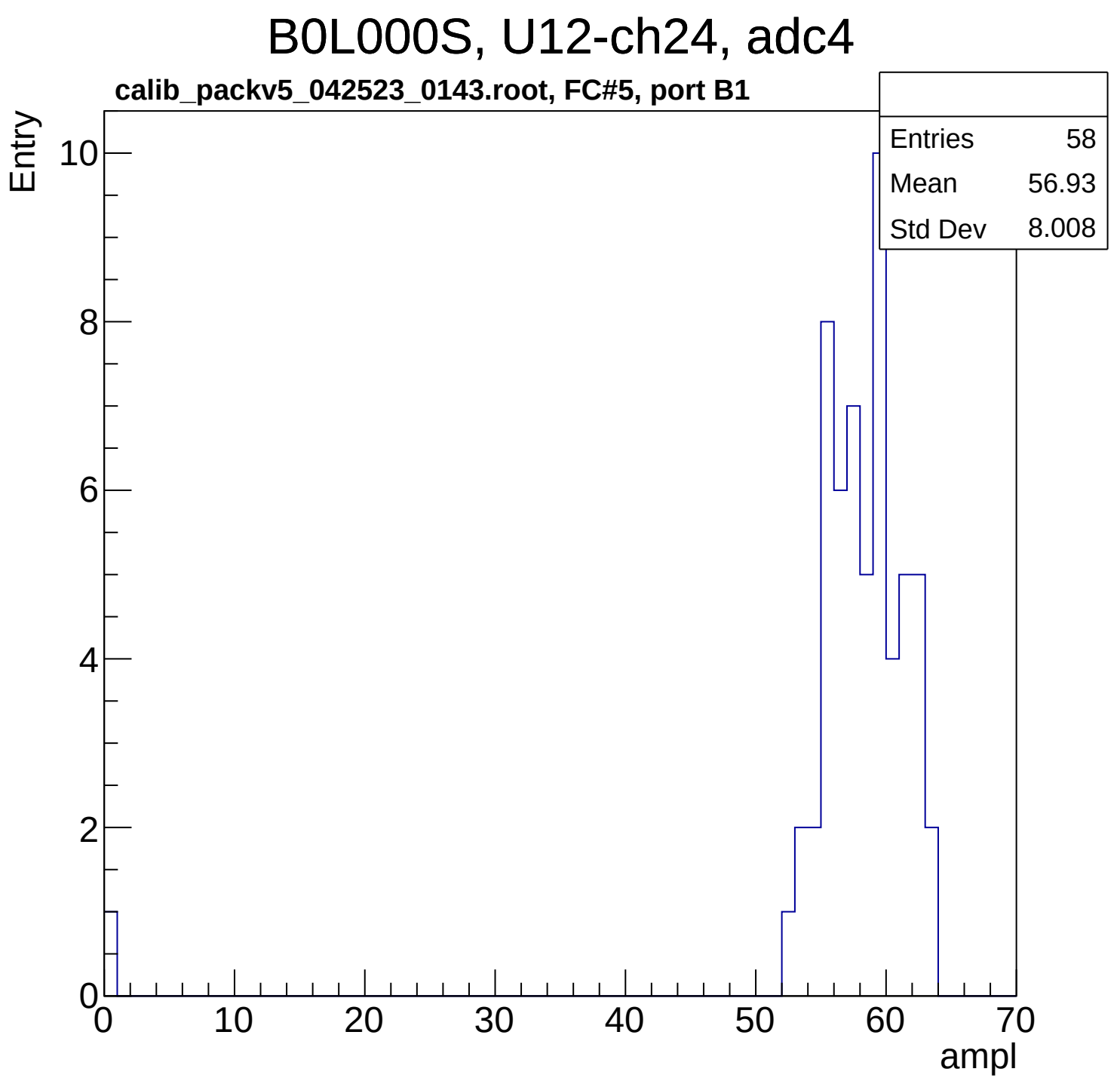
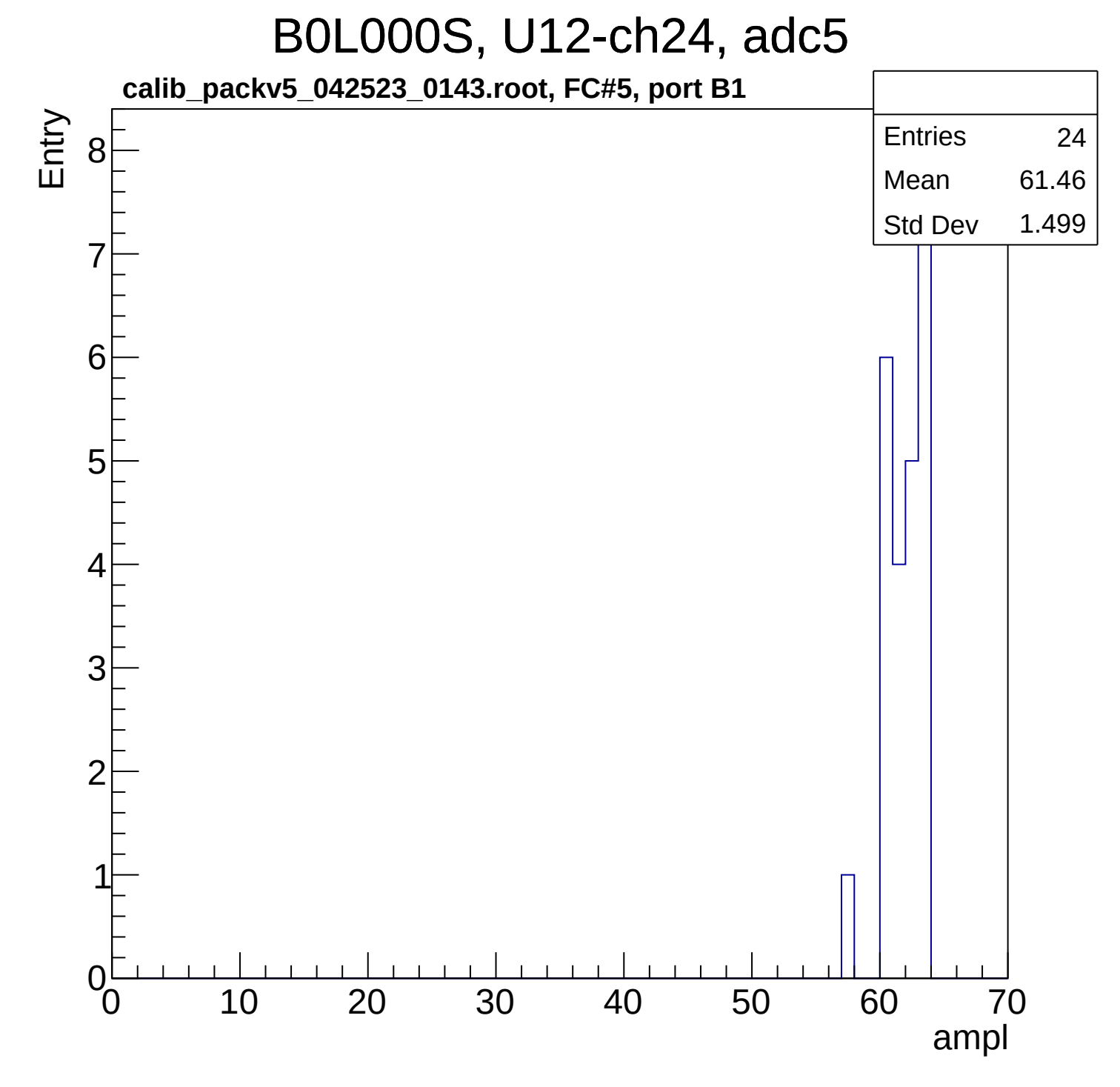

B0L000S, U12-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.93
Std Dev 8.008
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 61.46
Std Dev 1.499
Entry
ampl
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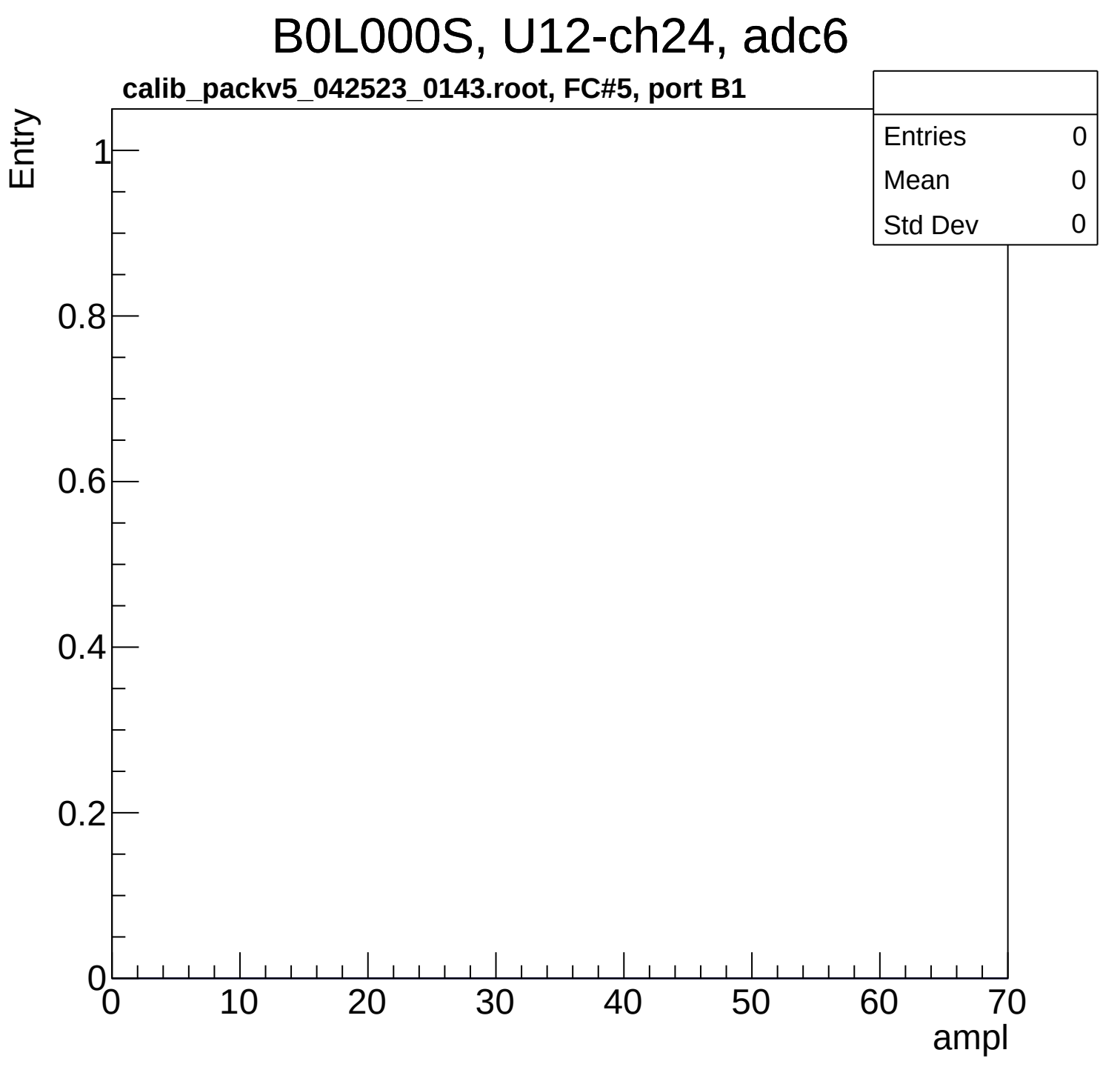
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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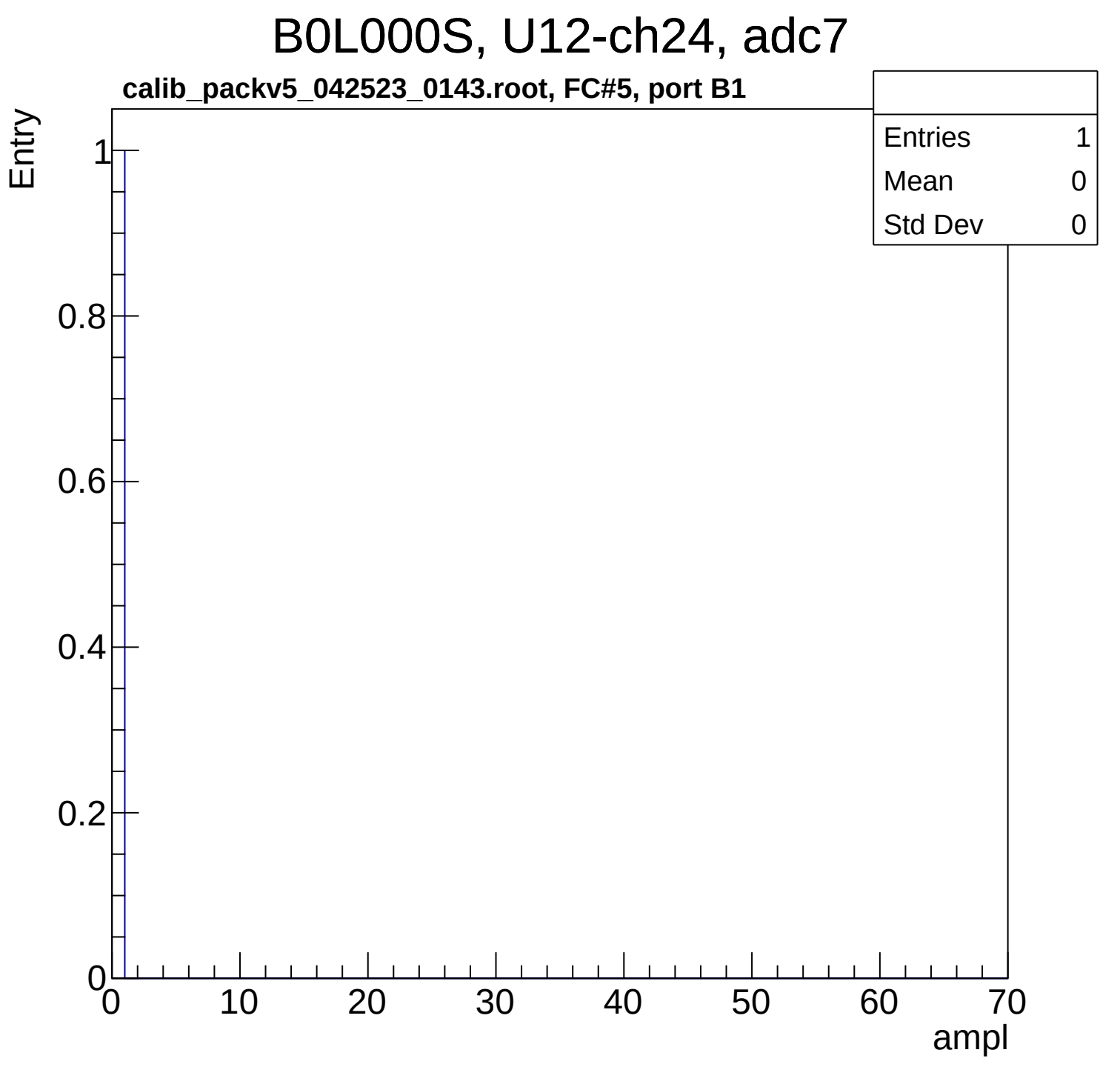
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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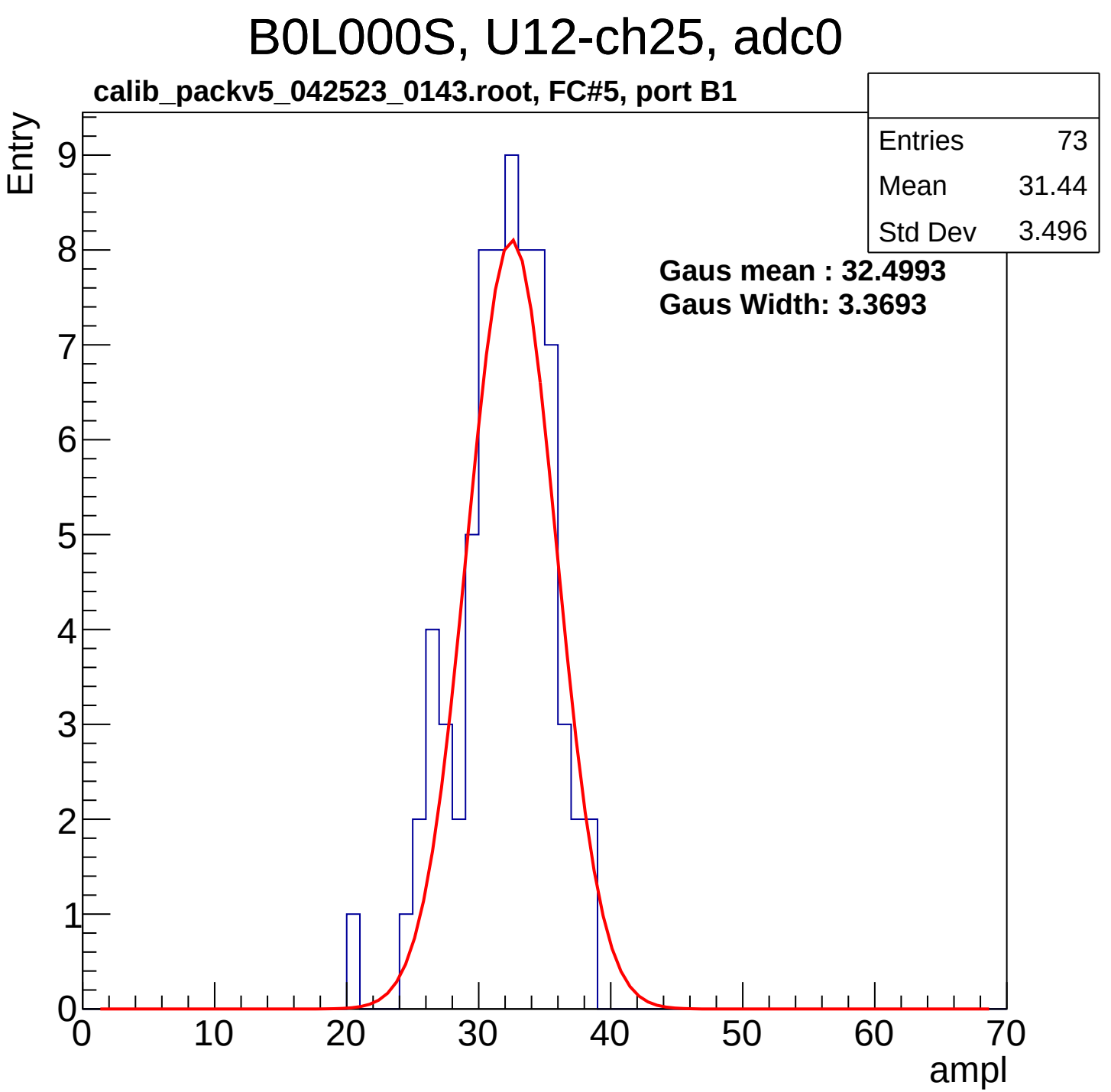
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 31.44
Std Dev 3.496
Gaus mean : 32.4993
Gaus Width: 3.3693
Entry
ampl
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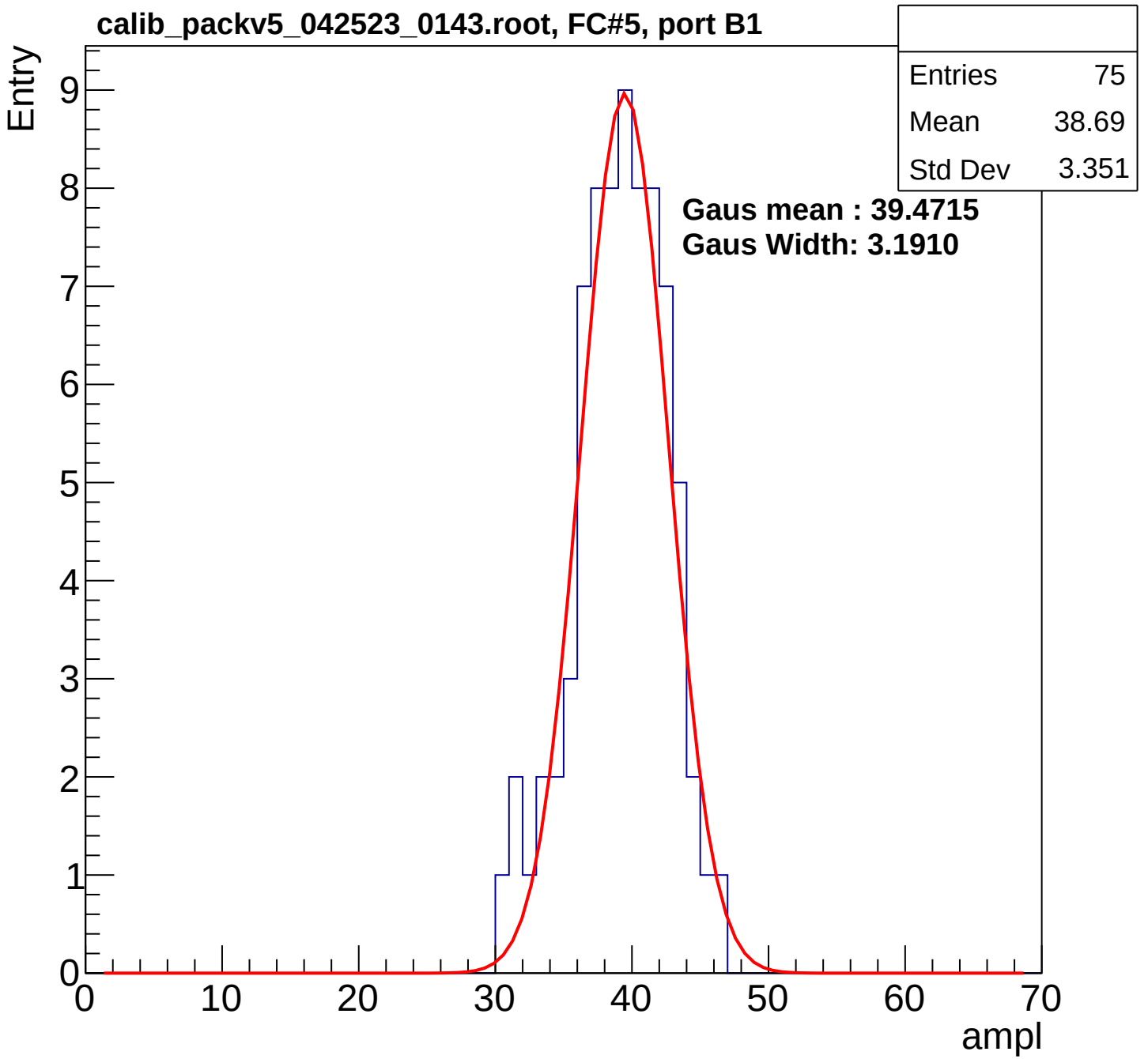
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 38.69
Std Dev 3.351
Gaus mean : 39.4715
Gaus Width: 3.1910
Entry
ampl
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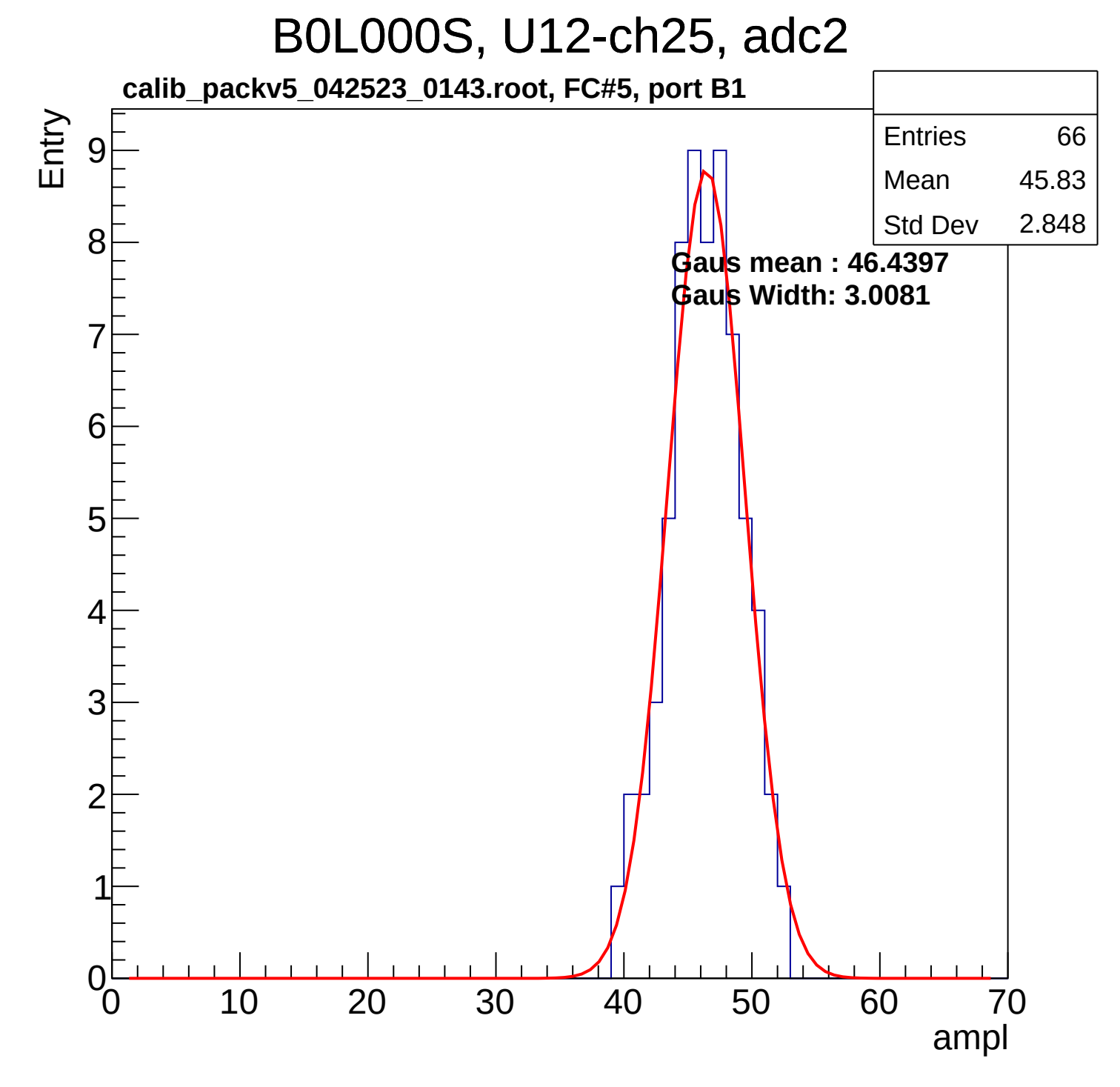
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 45.83
Std Dev 2.848
Gaus mean : 46.4397
Gaus Width: 3.0081
Entry
ampl
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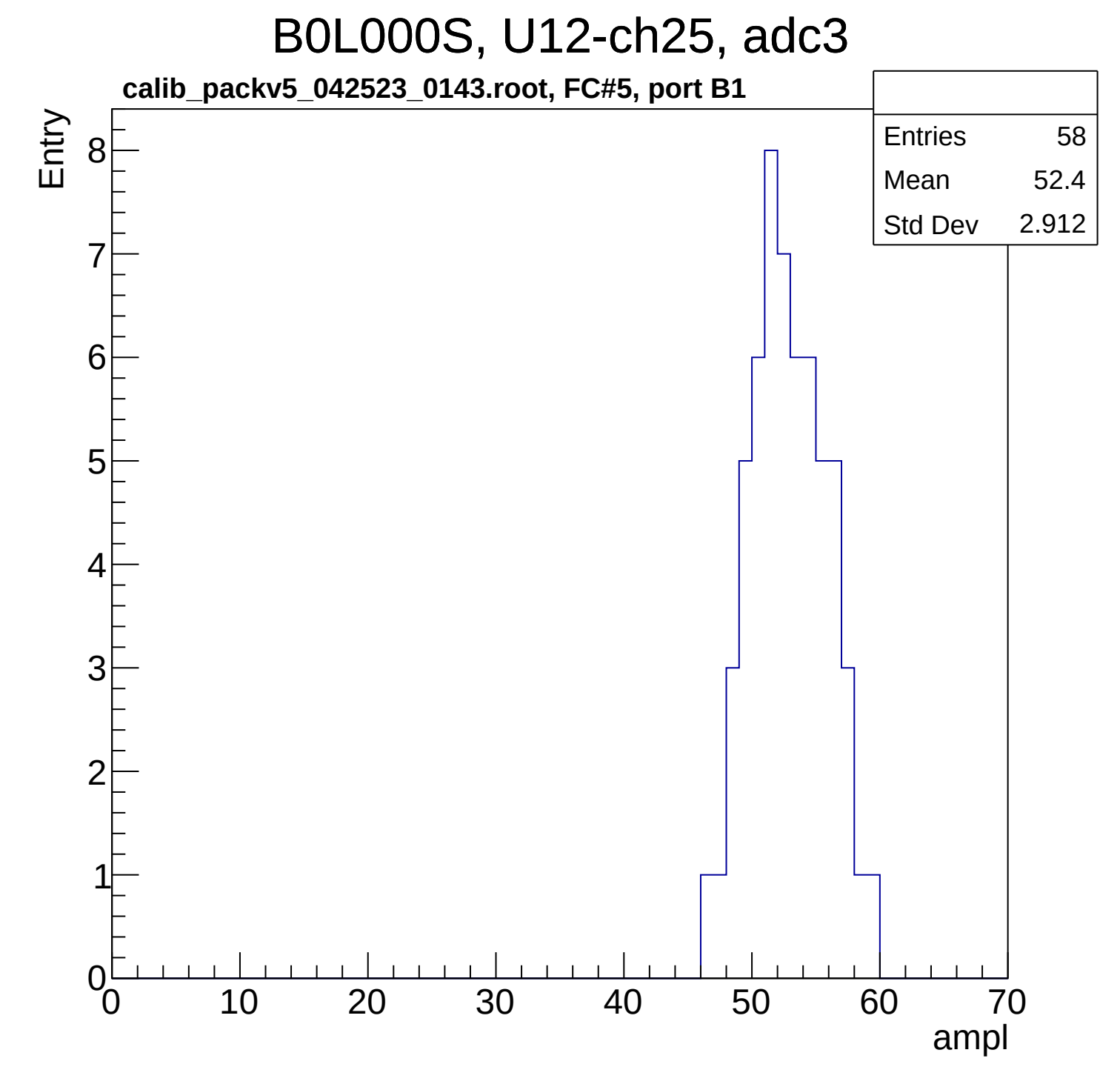
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 52.4
Std Dev 2.912
Entry
0
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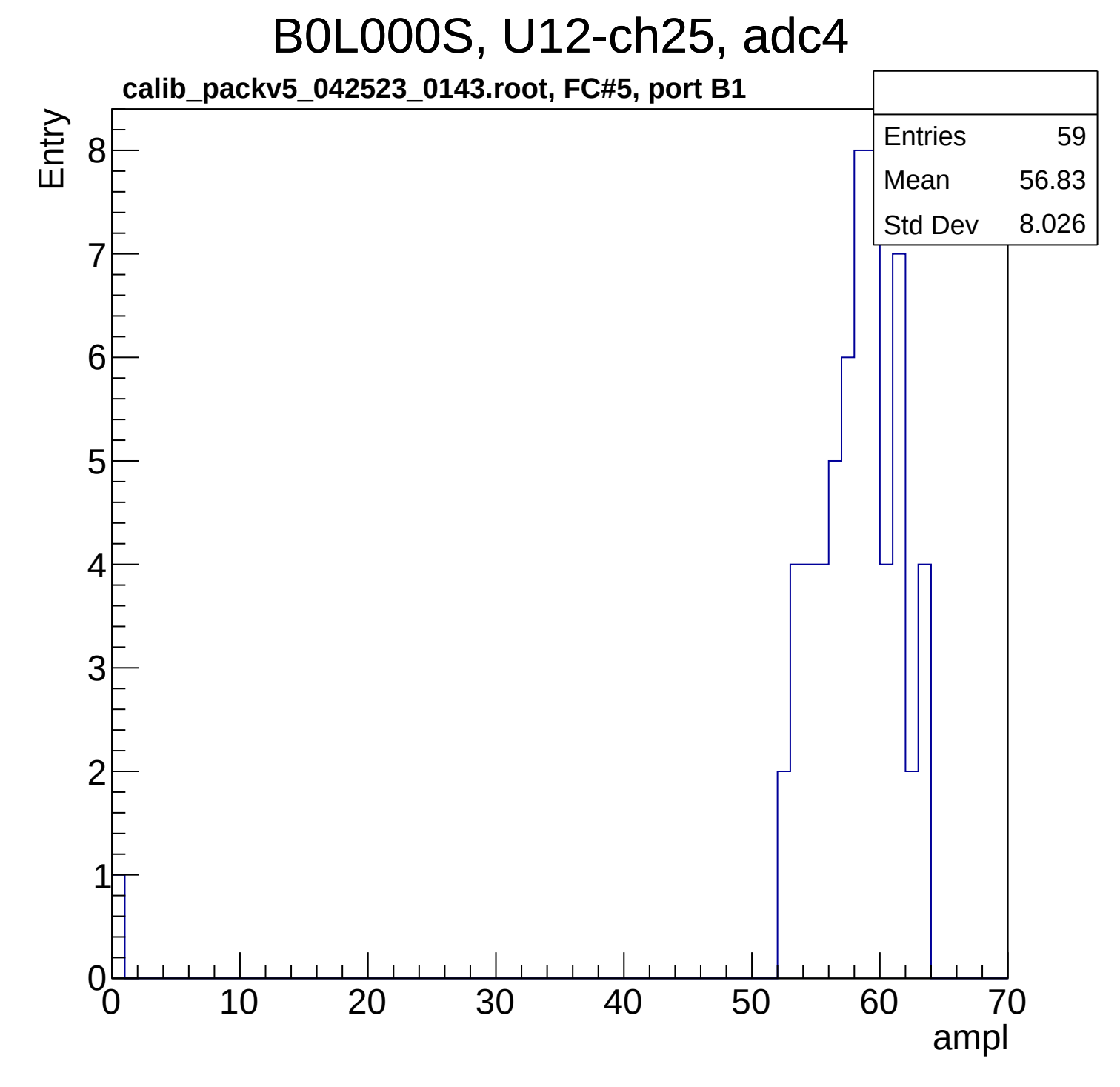
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.83
Std Dev 8.026
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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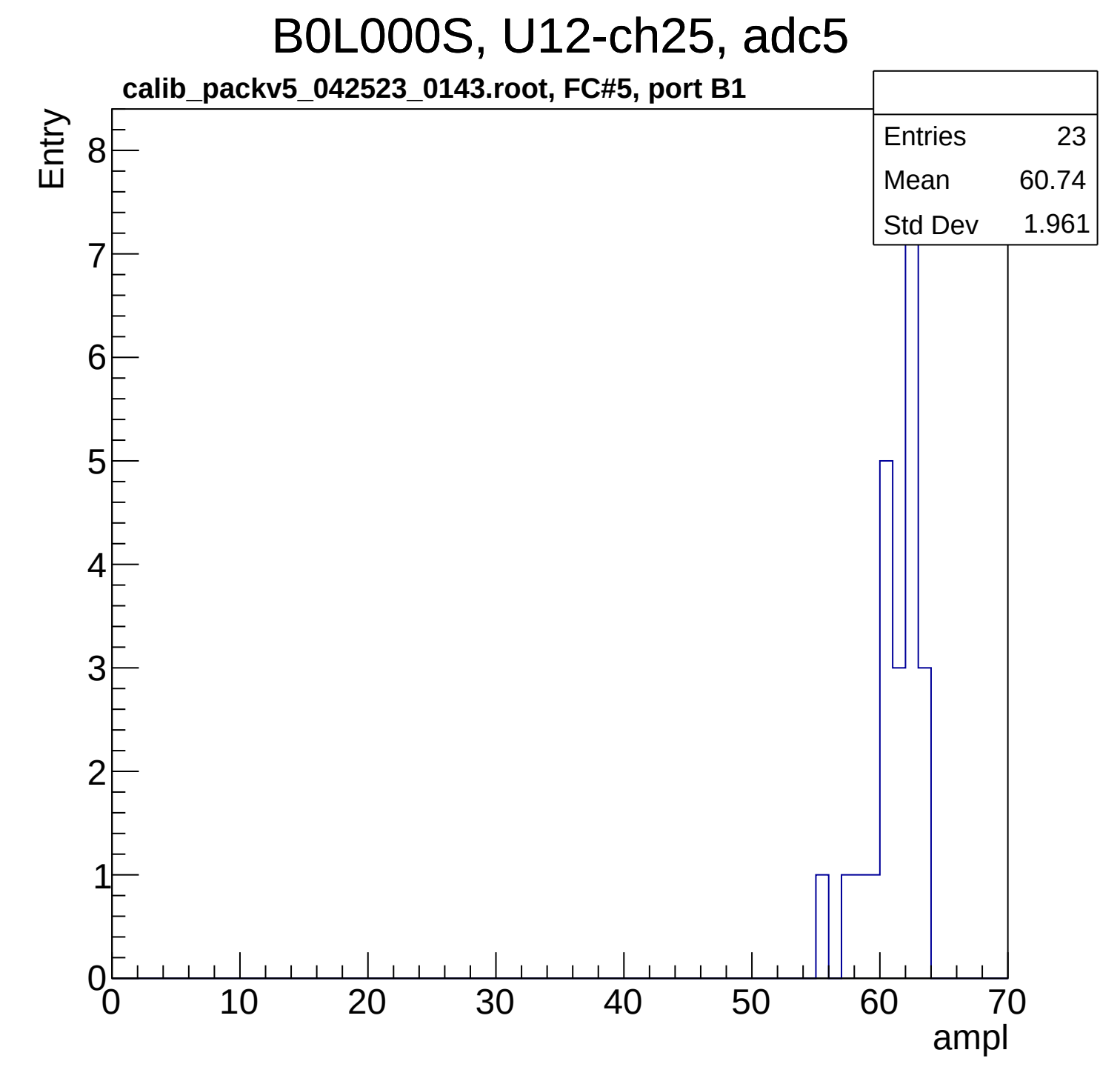
B0L000S, U12-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 60.74
Std Dev 1.961
Entry
ampl
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1
2
3
4
5
6
7
8
0
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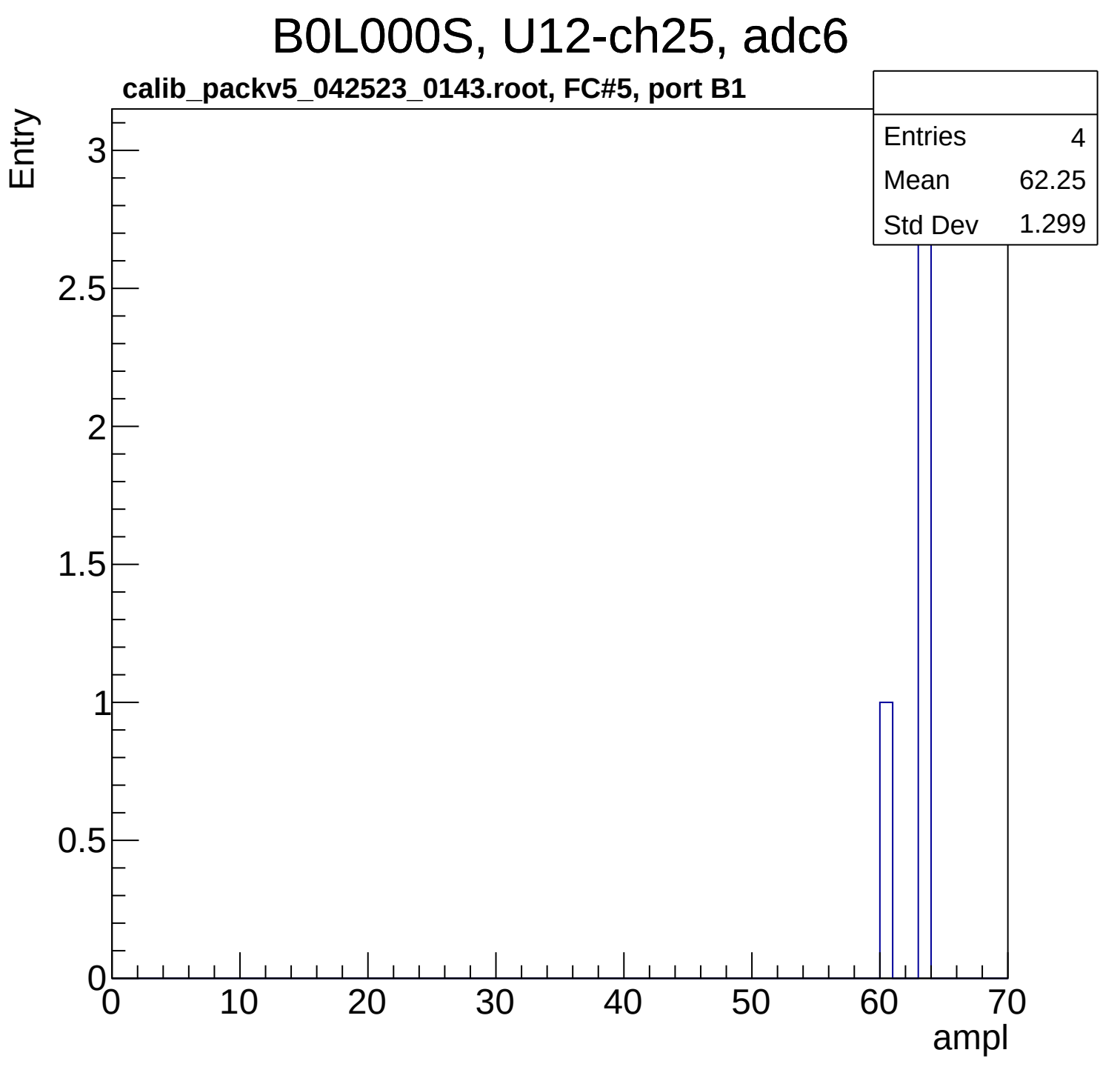
B0L000S, U12-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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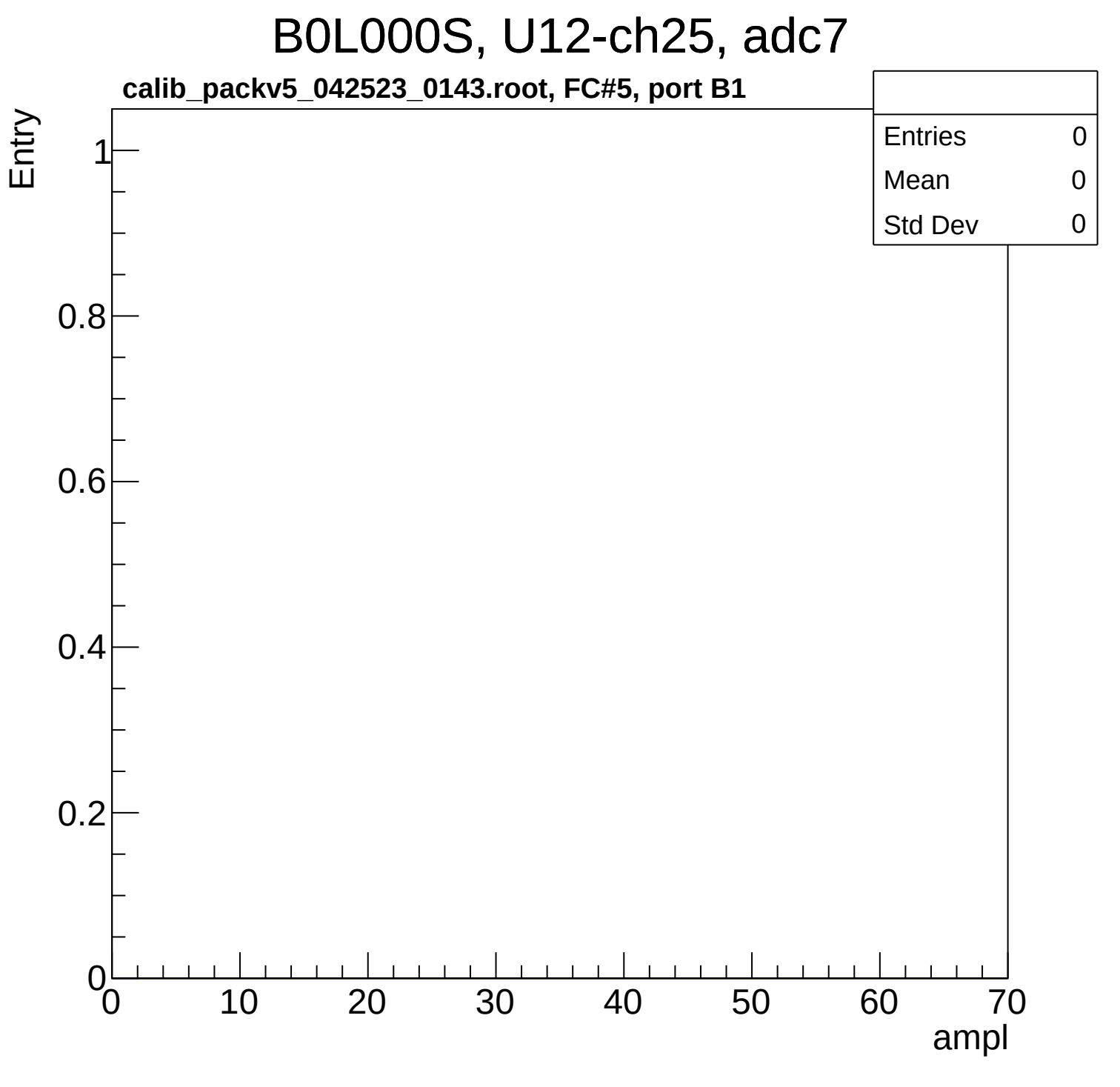
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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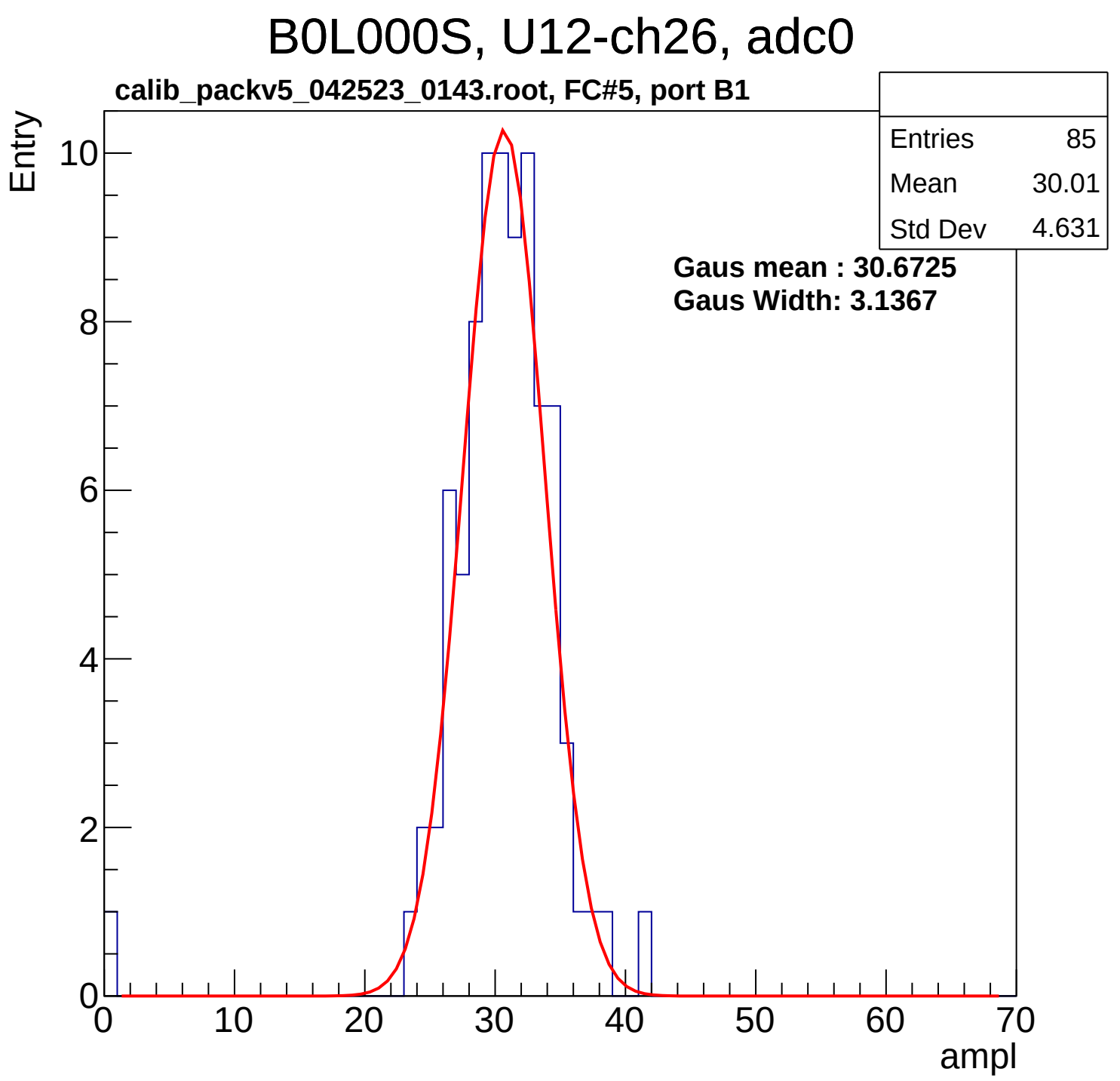
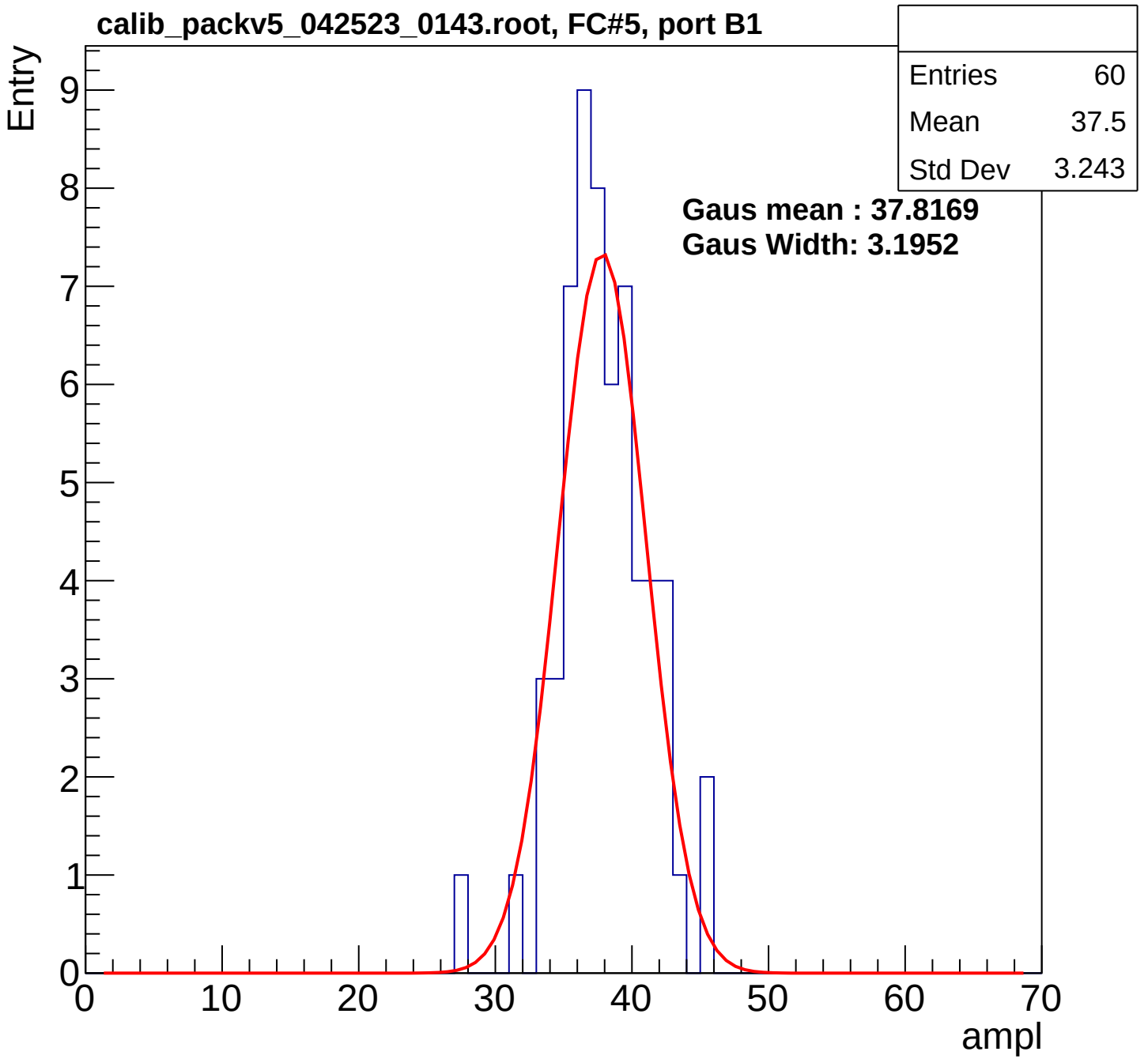
50
60
70
ampl


B0L000S, U12-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.01
Std Dev 4.631
Gaus mean : 30.6725
Gaus Width: 3.1367
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.5
Std Dev 3.243
Gaus mean : 37.8169
Gaus Width: 3.1952
Entry
ampl
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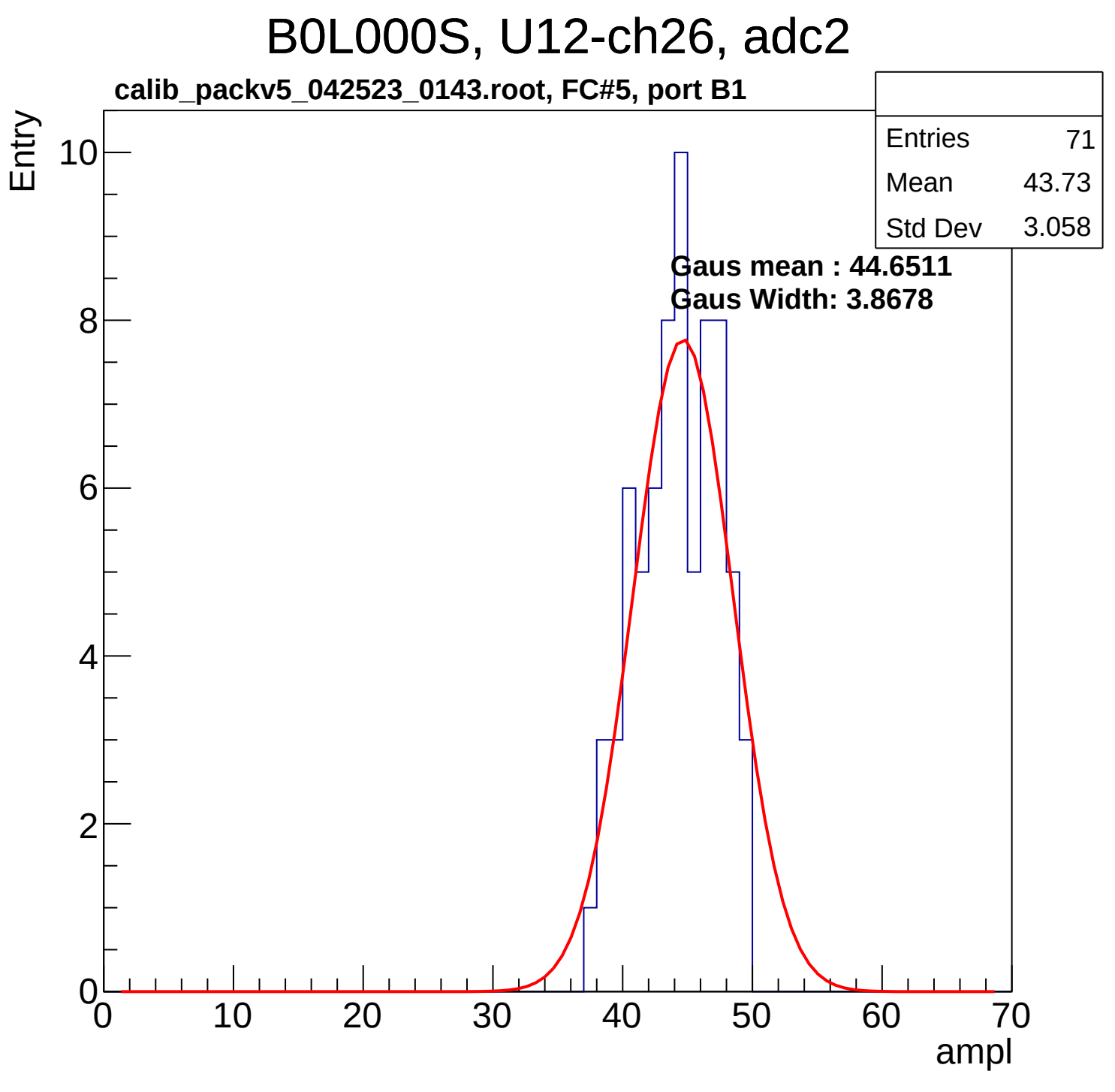
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.73
Std Dev 3.058
Gaus mean : 44.6511
Gaus Width: 3.8678
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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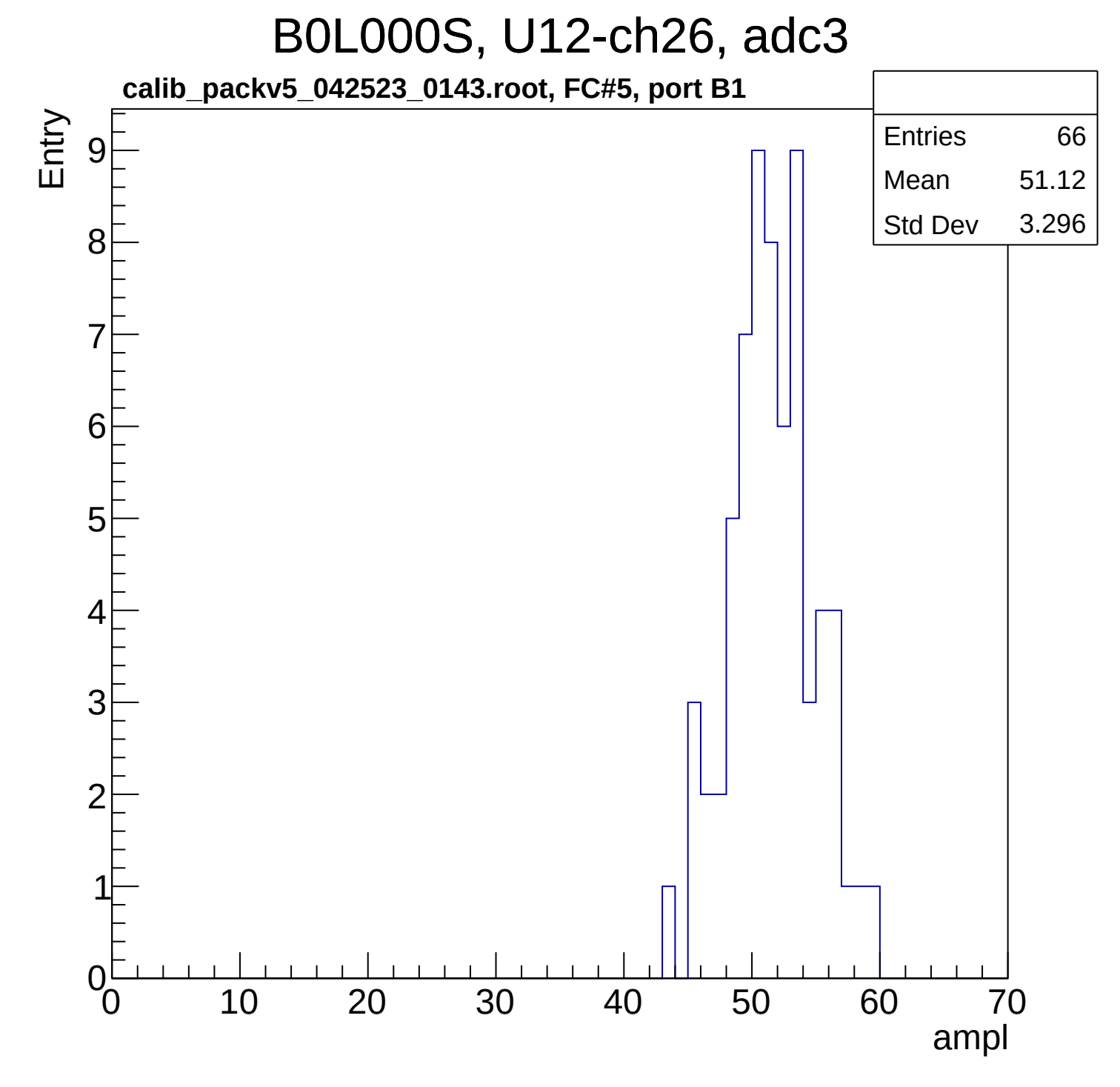
B0L000S, U12-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.12
Std Dev 3.296
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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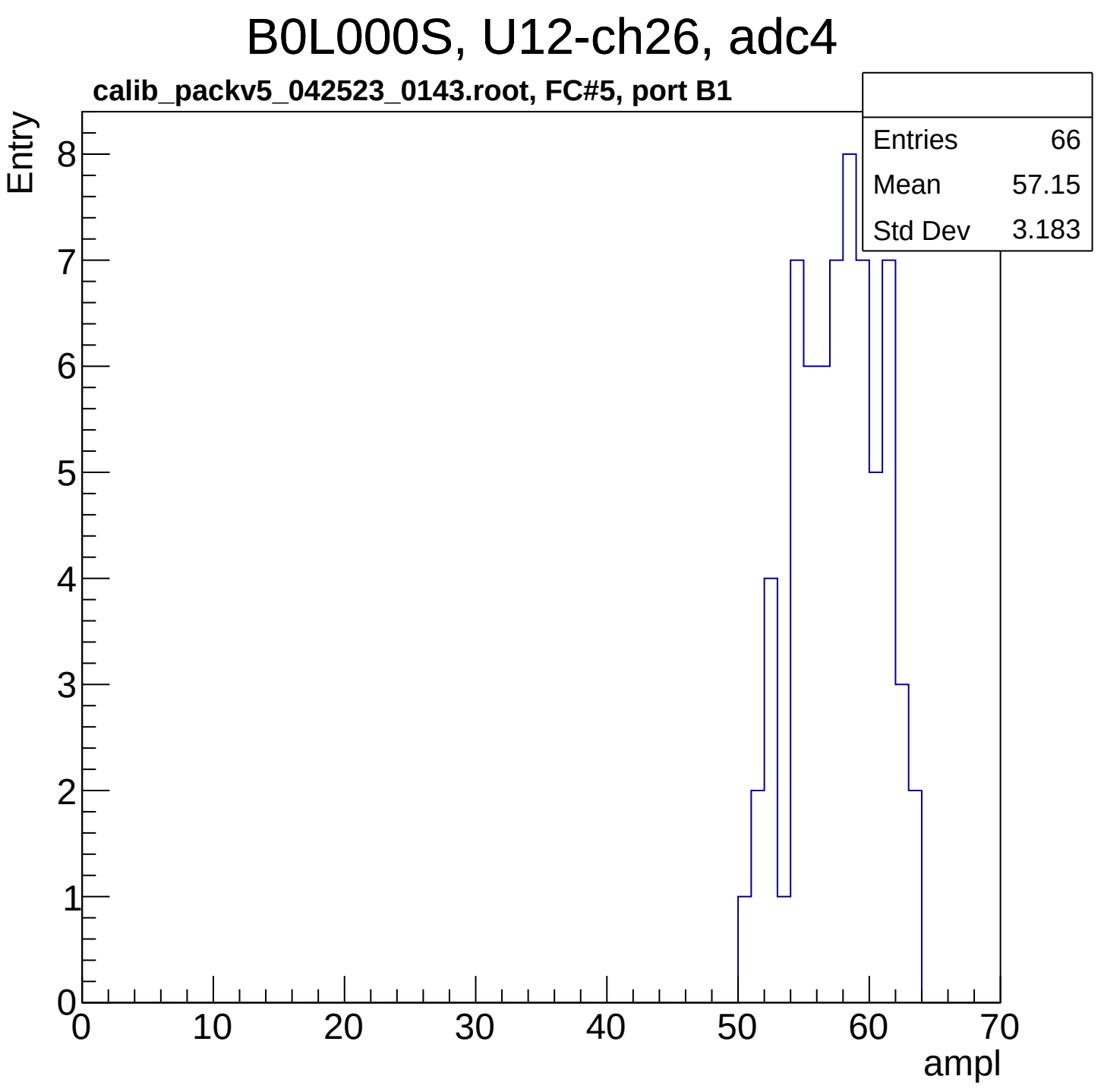
B0L000S, U12-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.15
Std Dev 3.183
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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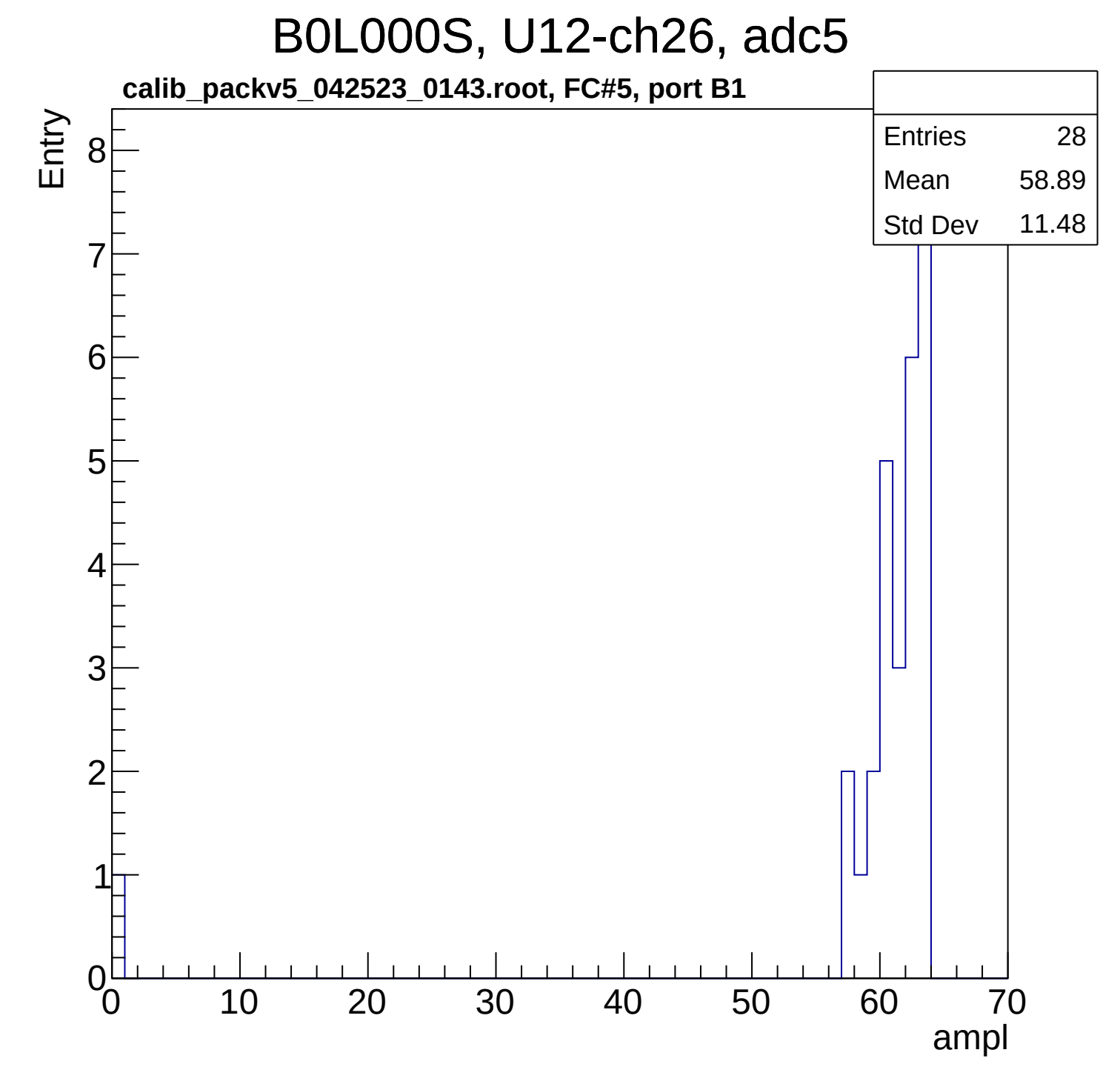
B0L000S, U12-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.89
Std Dev 11.48
Entry
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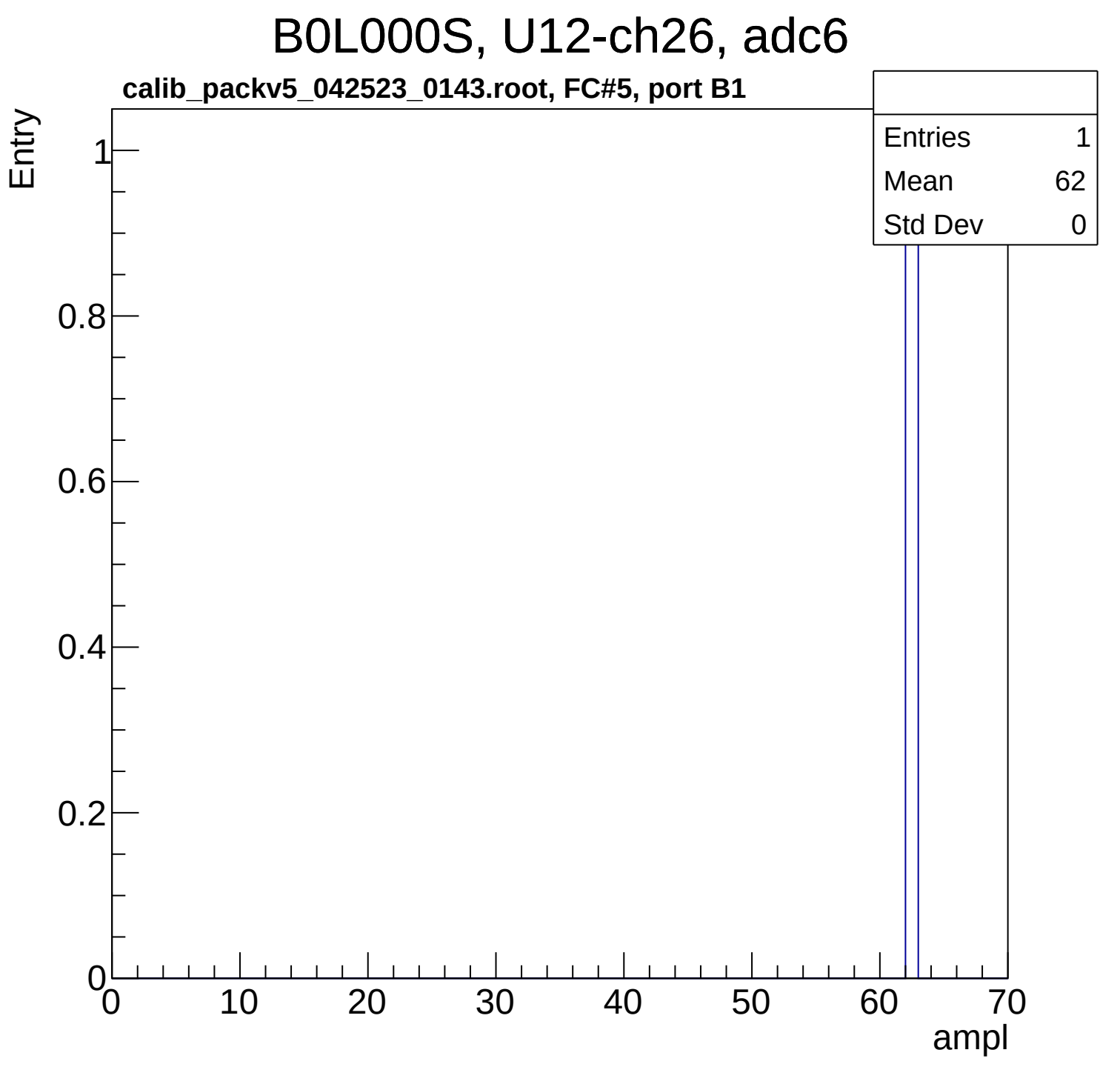
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
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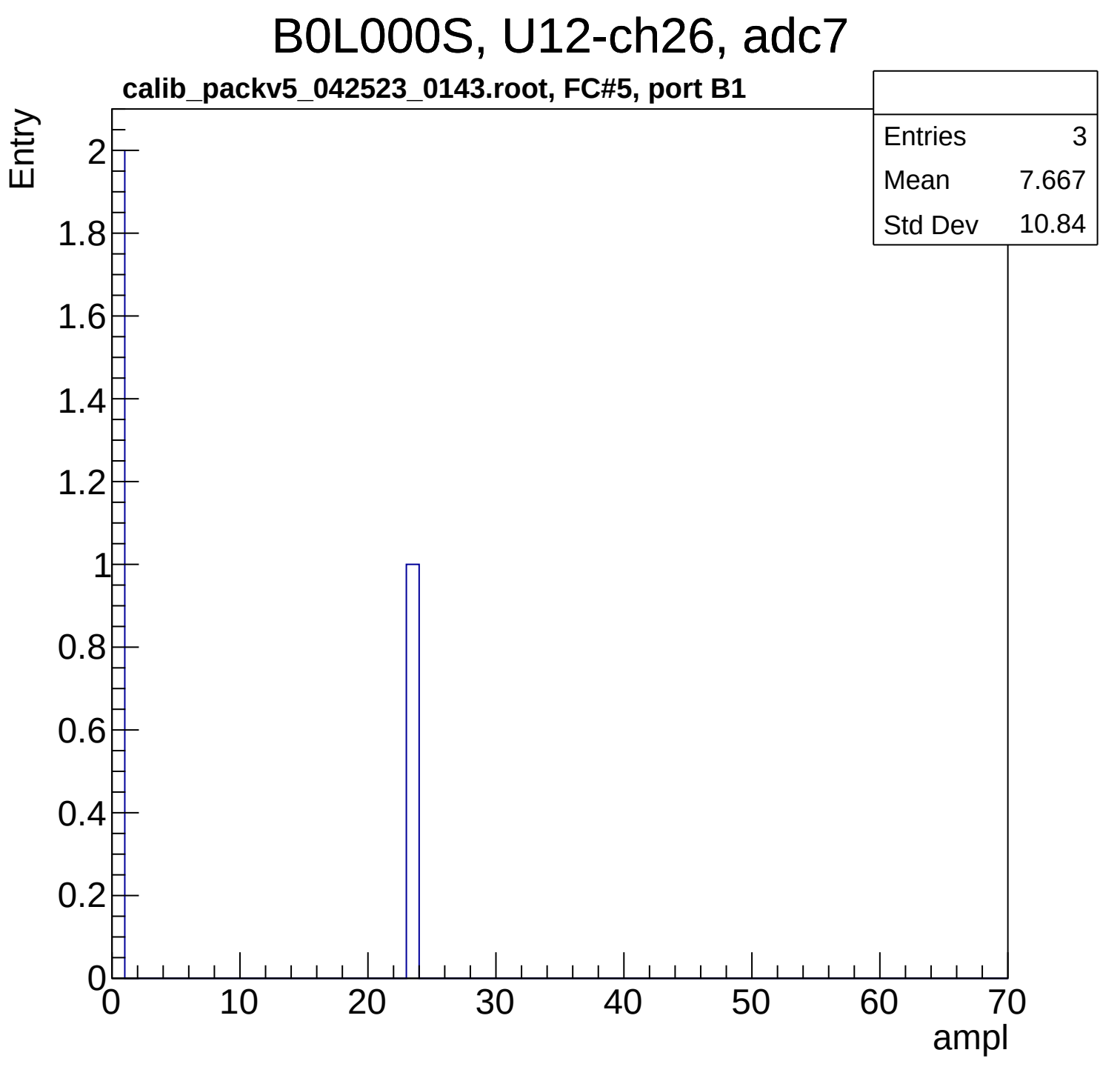
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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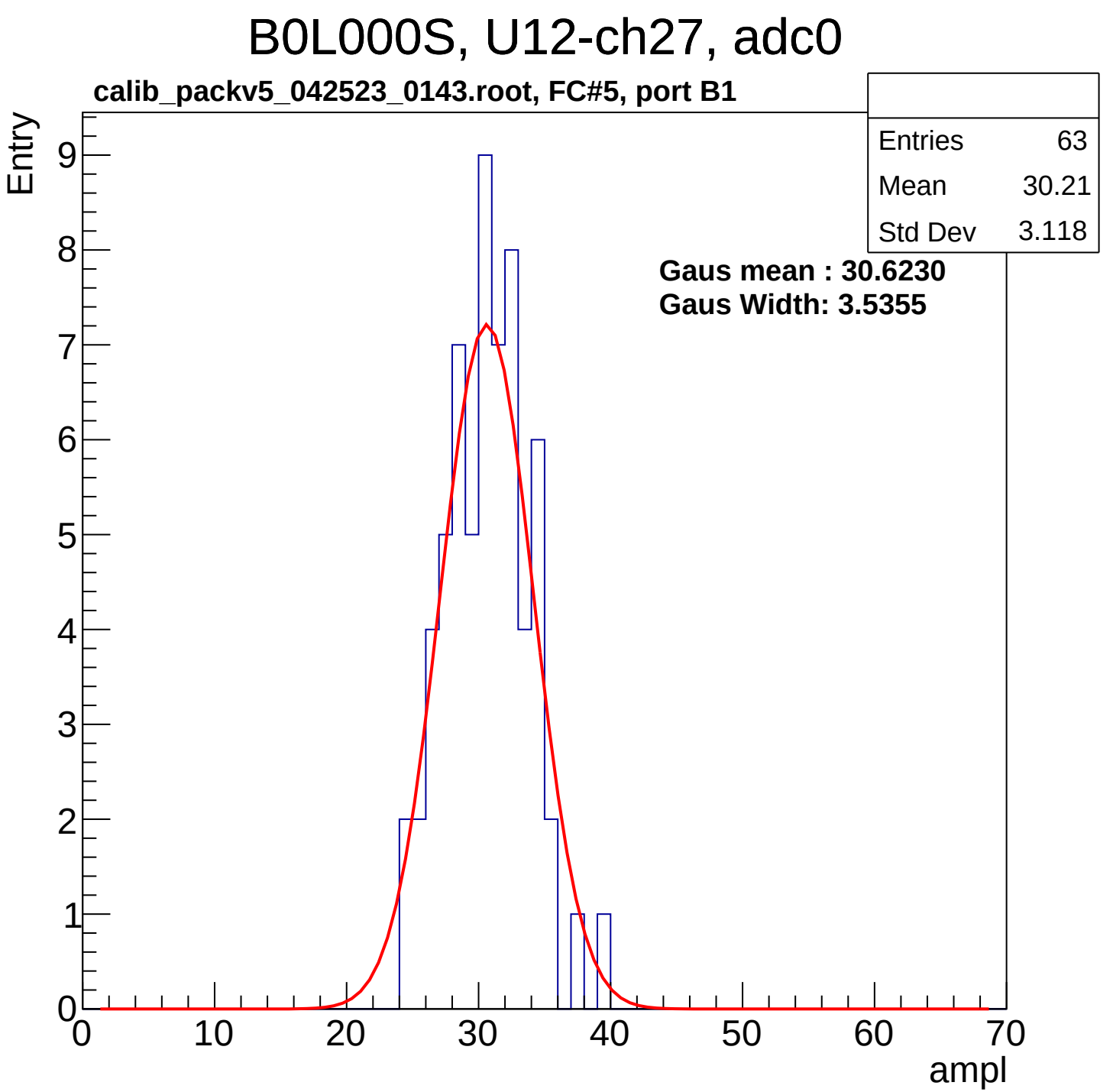
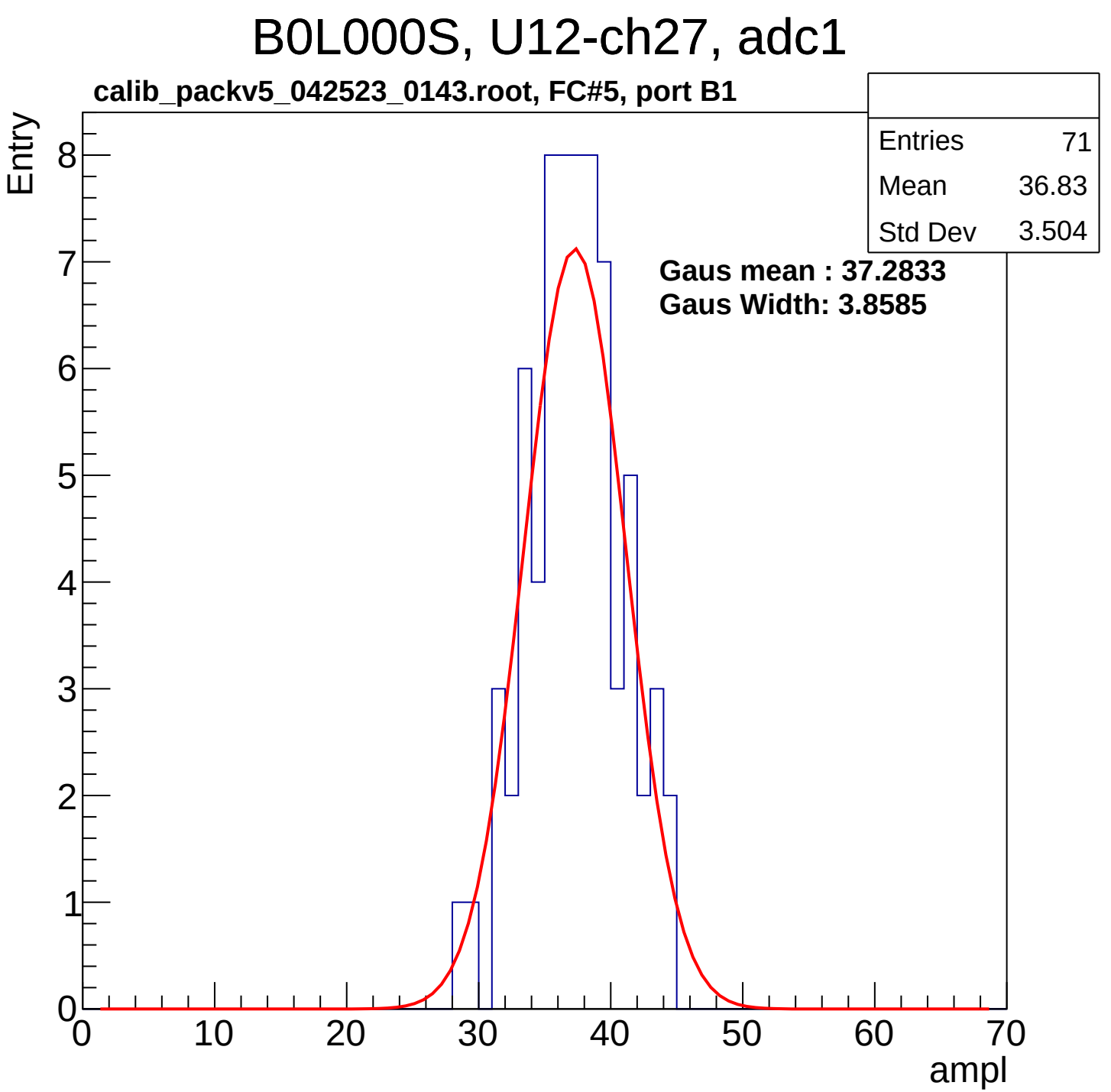
40
50
60
70


B0L000S, U12-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.21
Std Dev 3.118
Gaus mean : 30.6230
Gaus Width: 3.5355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.83
Std Dev 3.504
Gaus mean : 37.2833
Gaus Width: 3.8585
Entry
ampl
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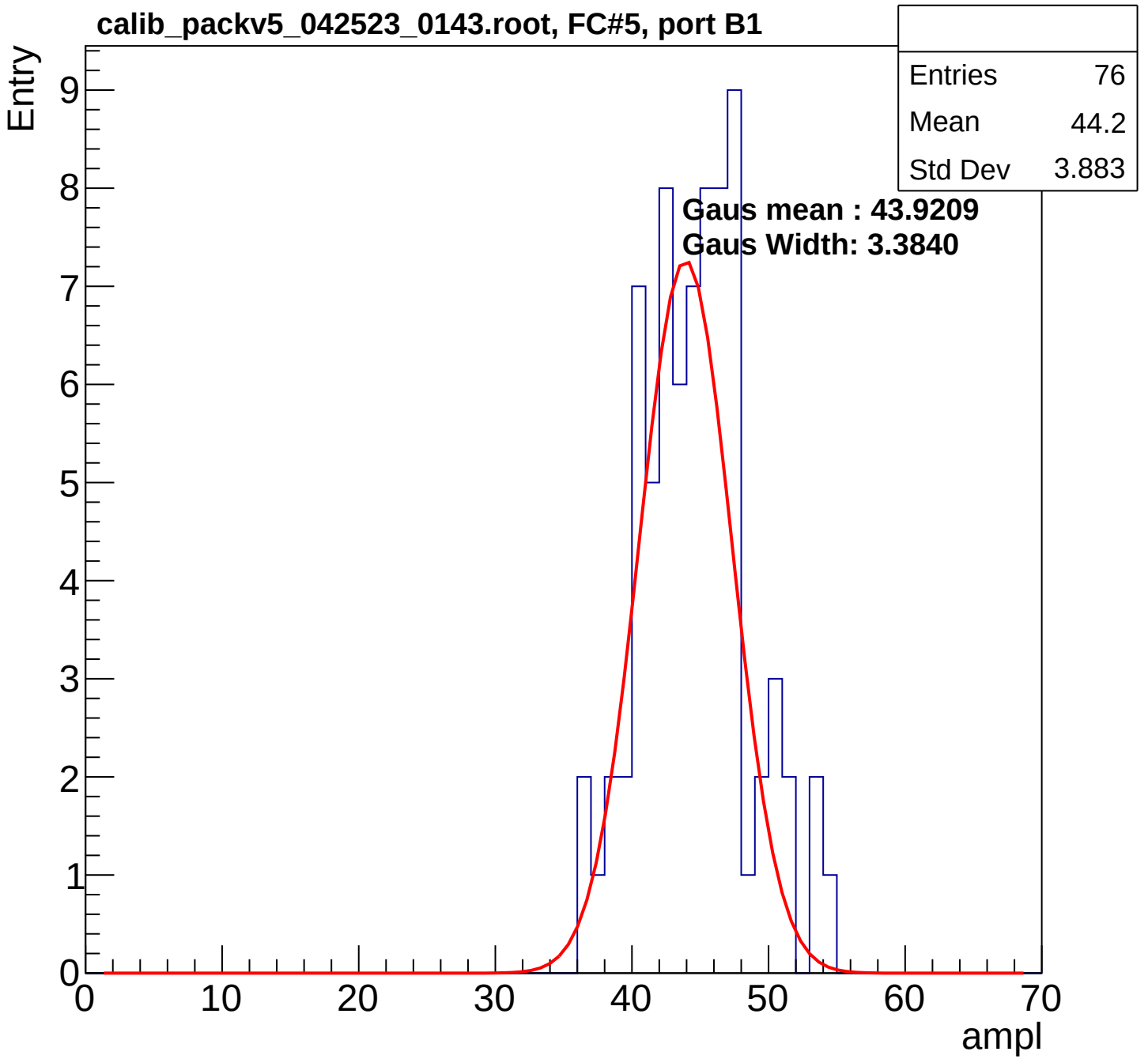
B0L000S, U12-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 44.2
Std Dev 3.883
Gaus mean : 43.9209
Gaus Width: 3.3840
Entry
ampl
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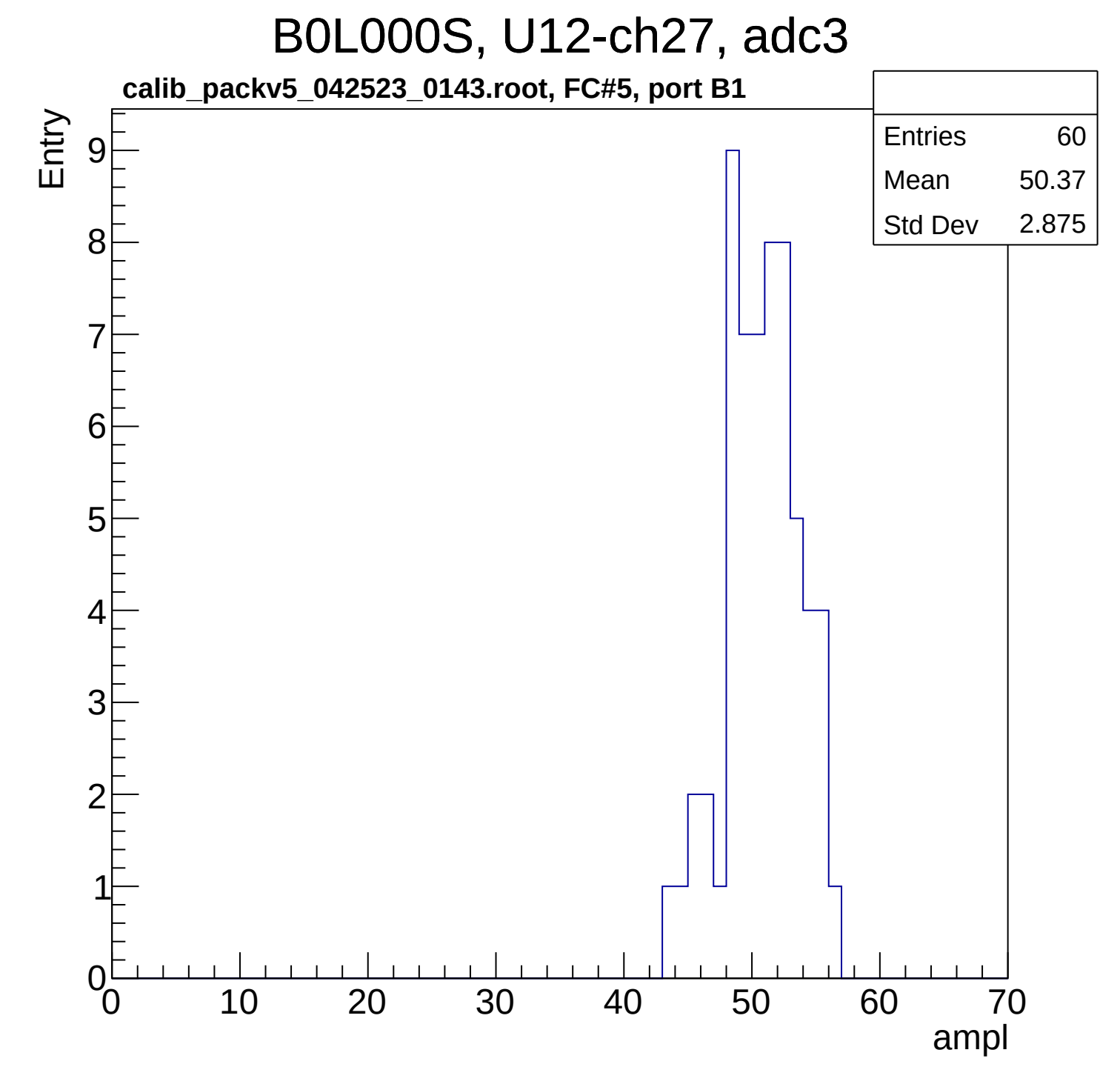
B0L000S, U12-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 50.37
Std Dev 2.875
Entry
ampl
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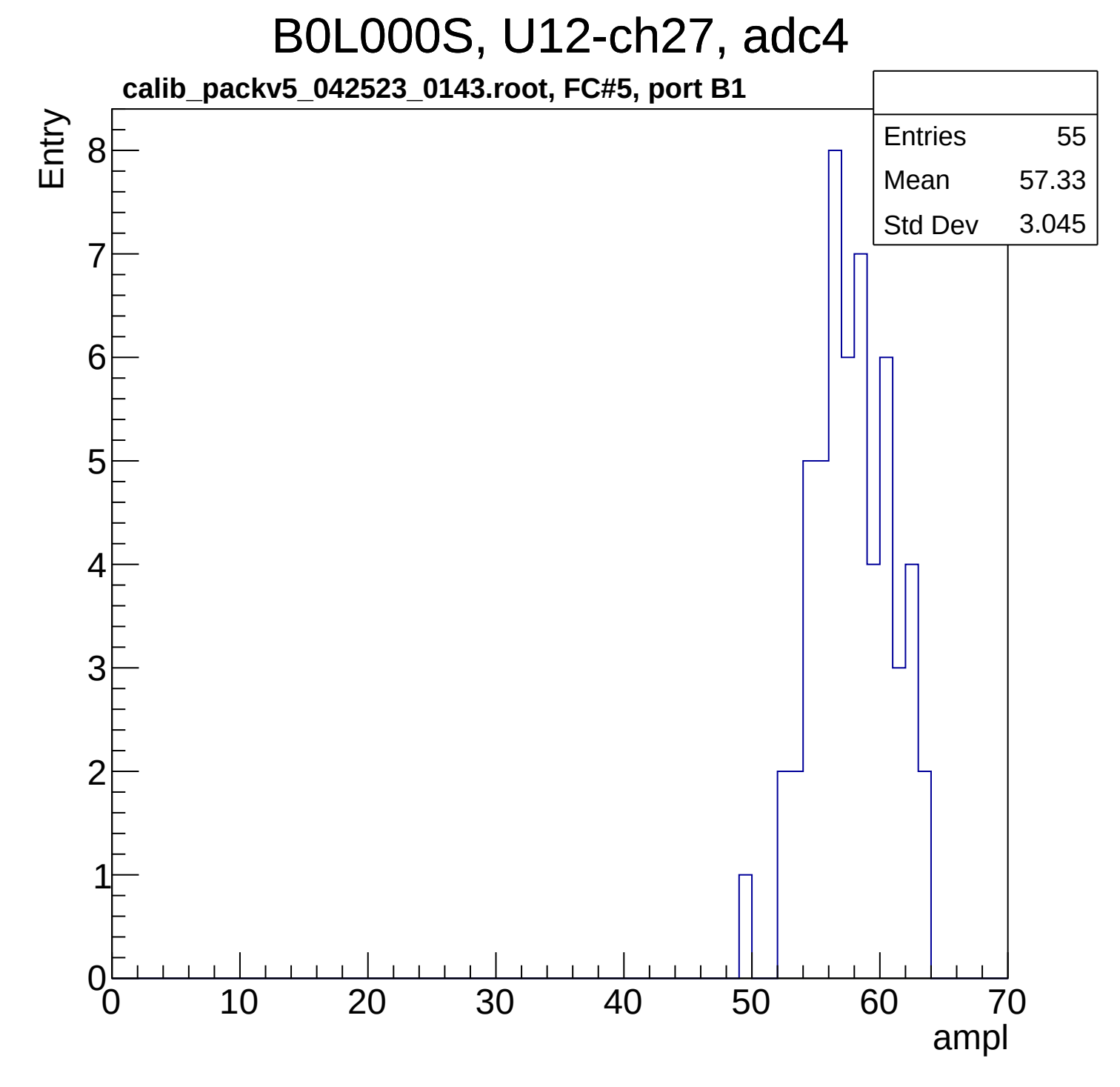
B0L000S, U12-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.33
Std Dev 3.045
Entry
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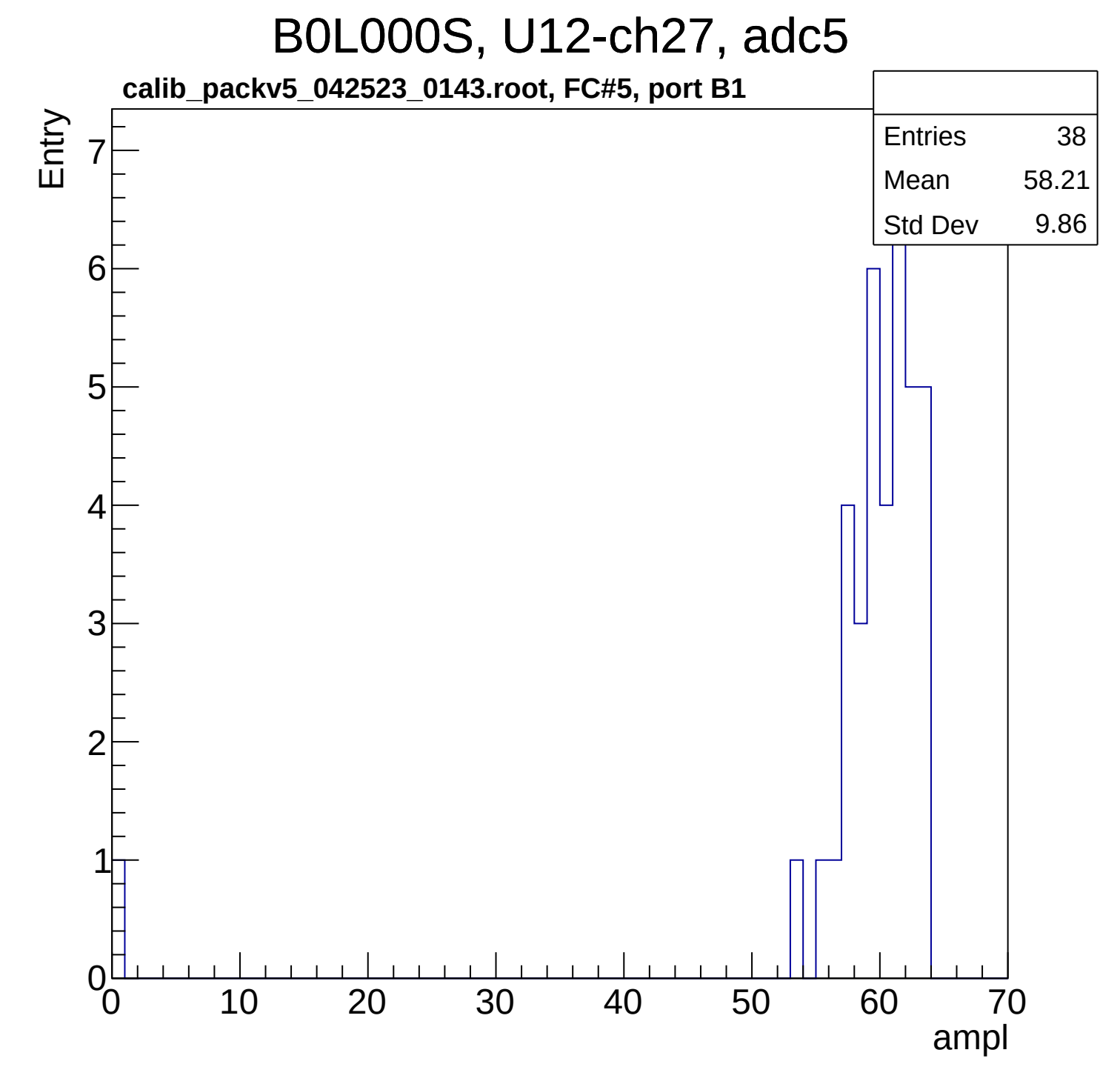
ampl


B0L000S, U12-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.21
Std Dev 9.86
Entry
ampl
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0
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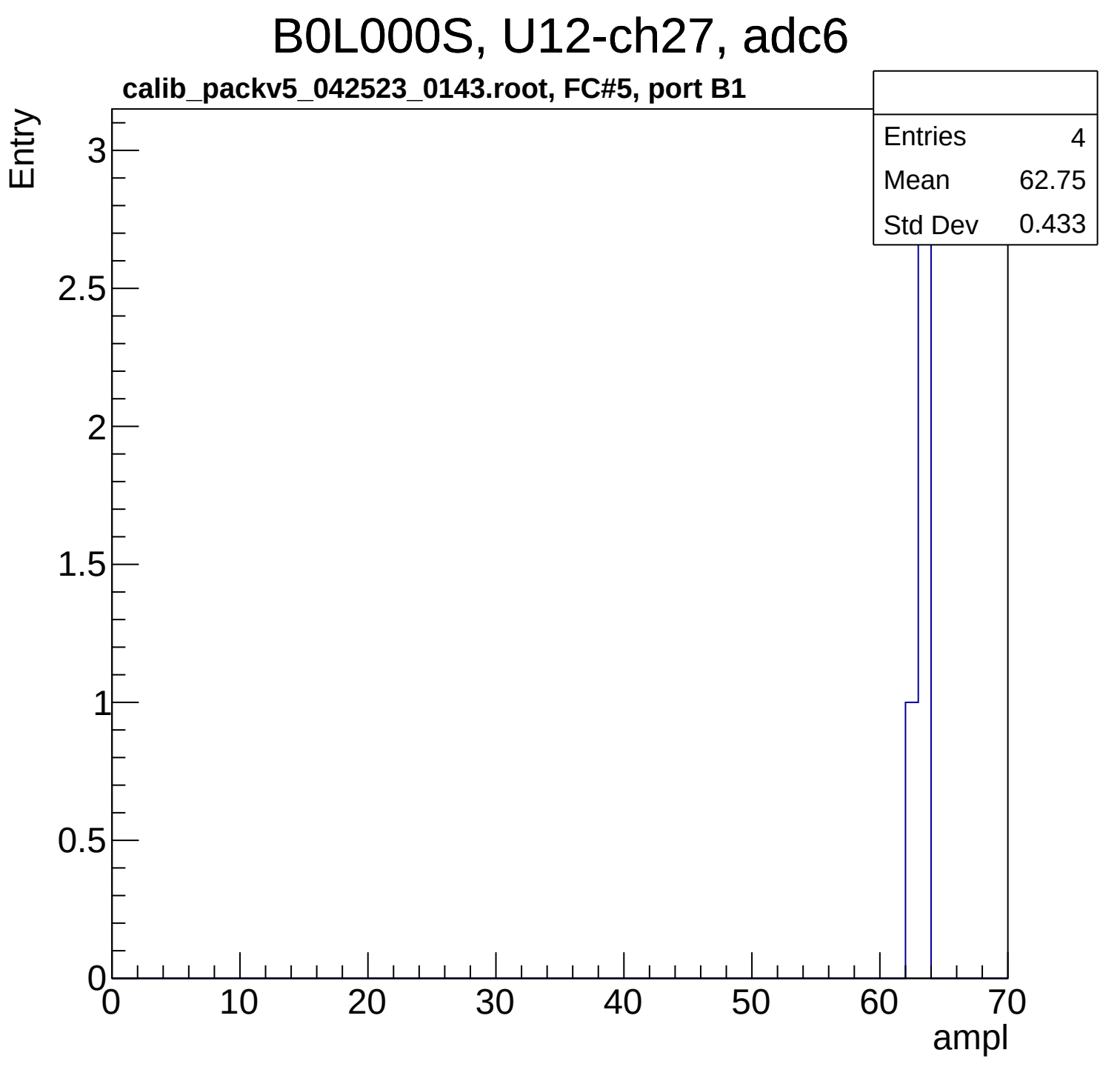
B0L000S, U12-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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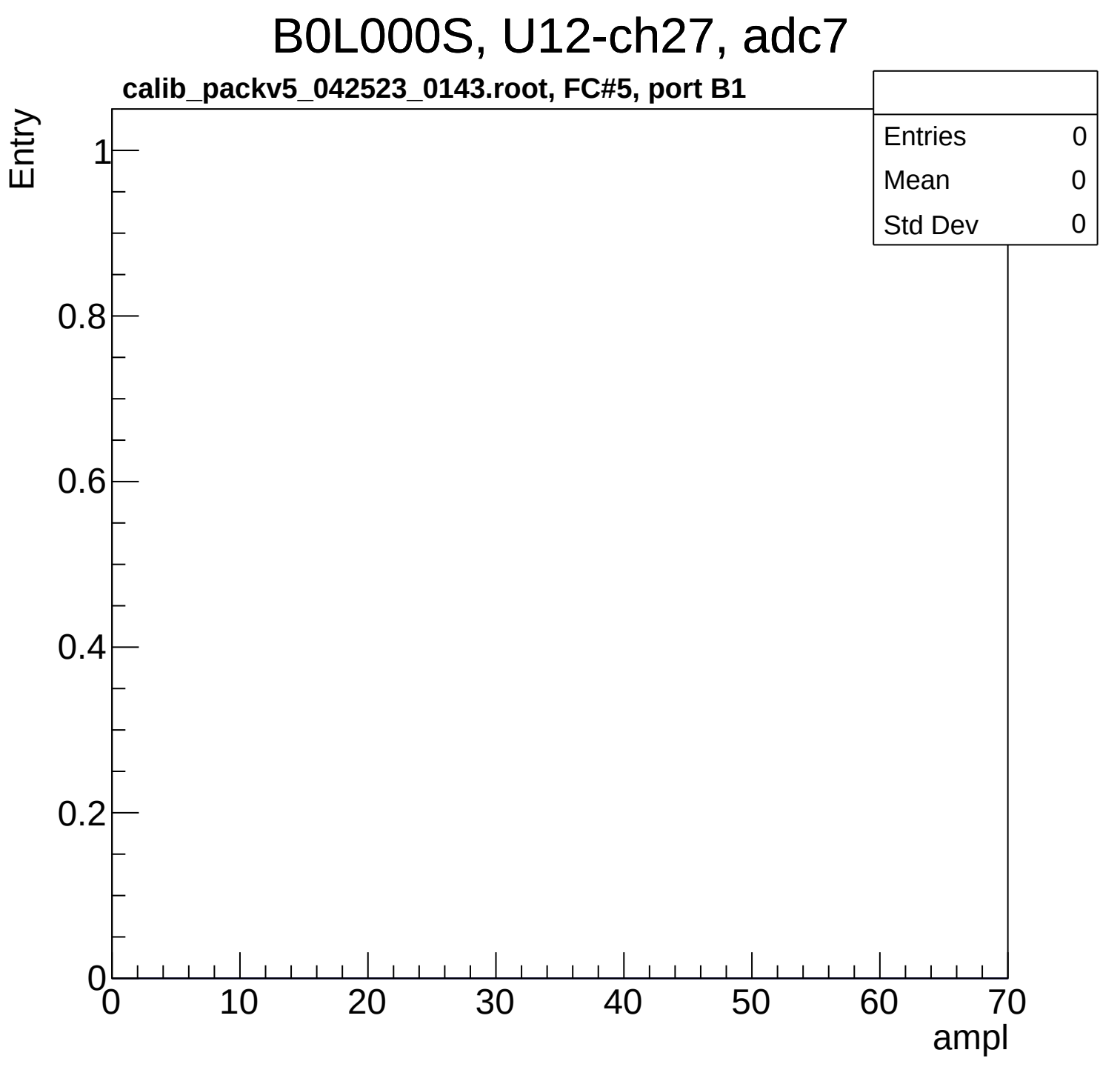
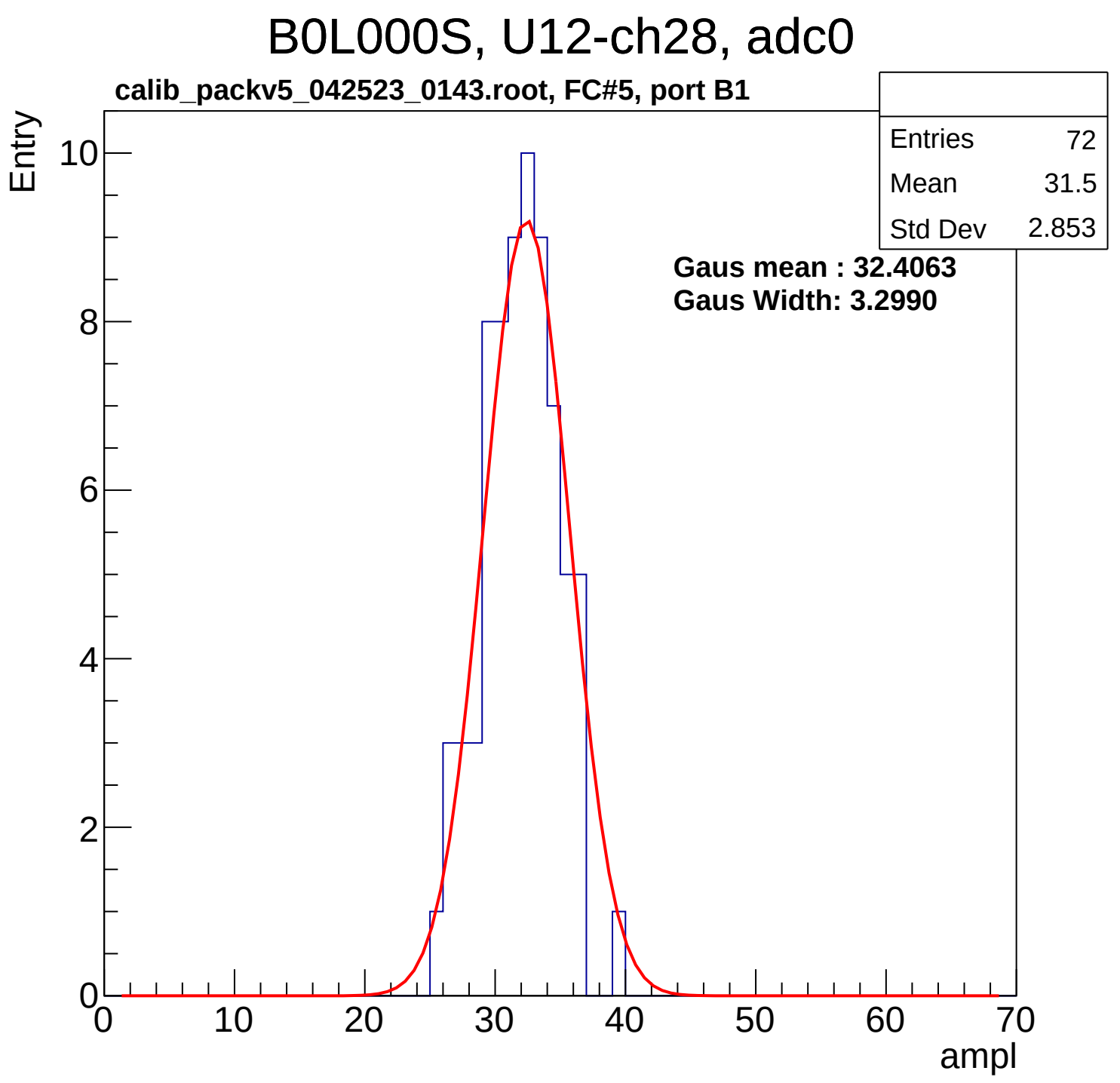

B0L000S, U12-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 31.5
Std Dev 2.853
Gaus mean : 32.4063
Gaus Width: 3.2990
Entry
ampl
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0
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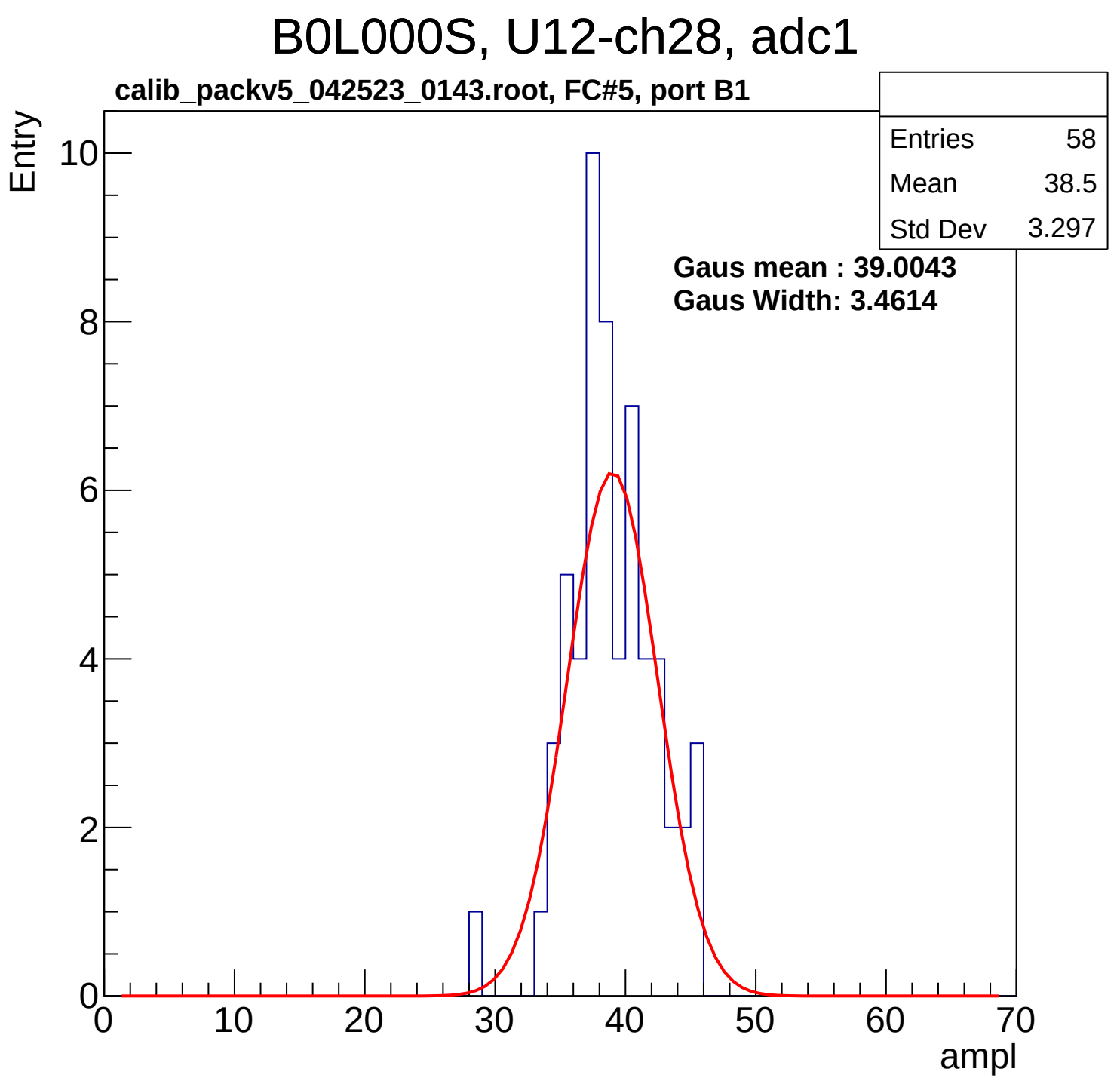
B0L000S, U12-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 38.5
Std Dev 3.297
Gaus mean : 39.0043
Gaus Width: 3.4614
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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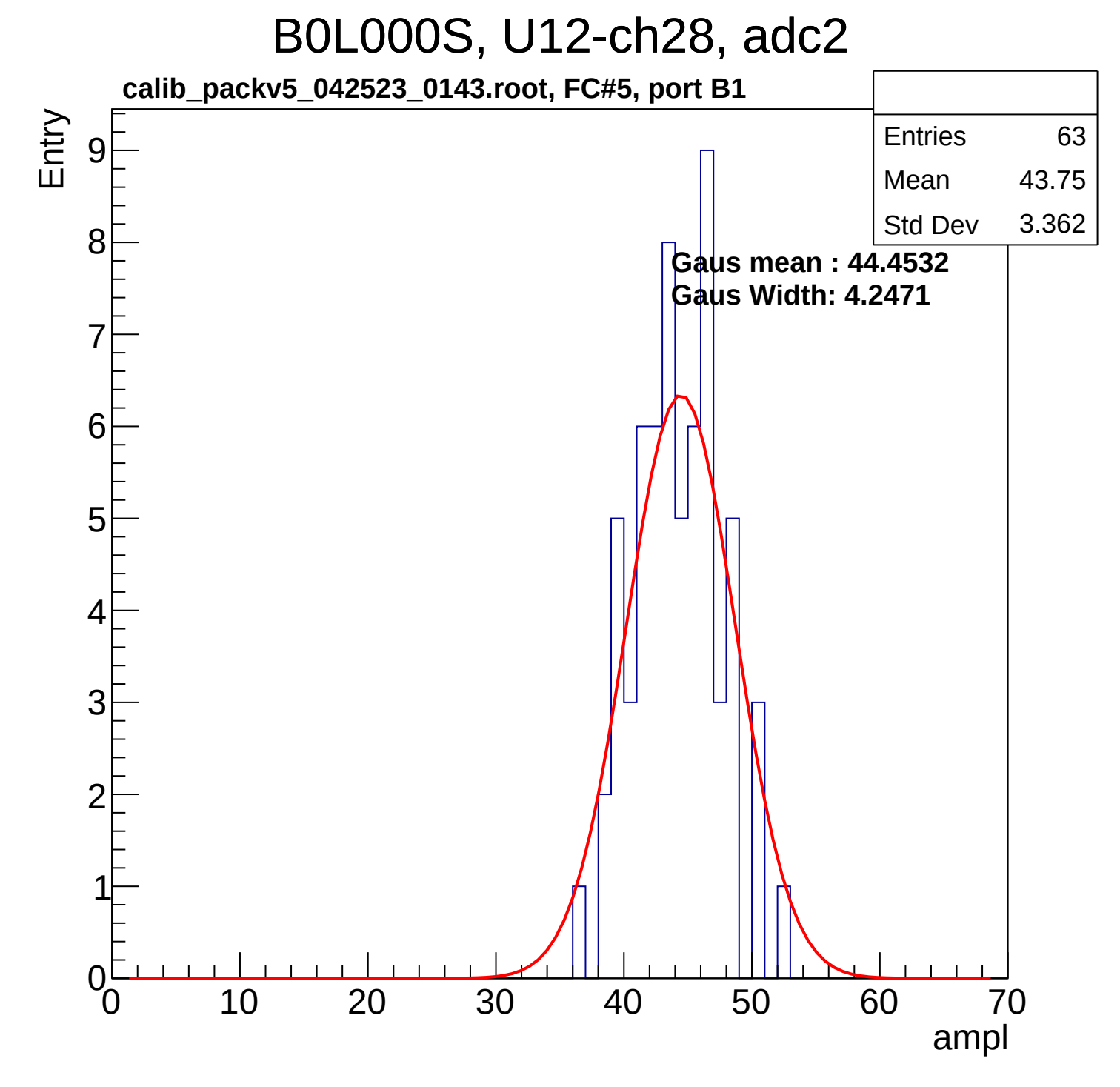
B0L000S, U12-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.75
Std Dev 3.362
Gaus mean : 44.4532
Gaus Width: 4.2471
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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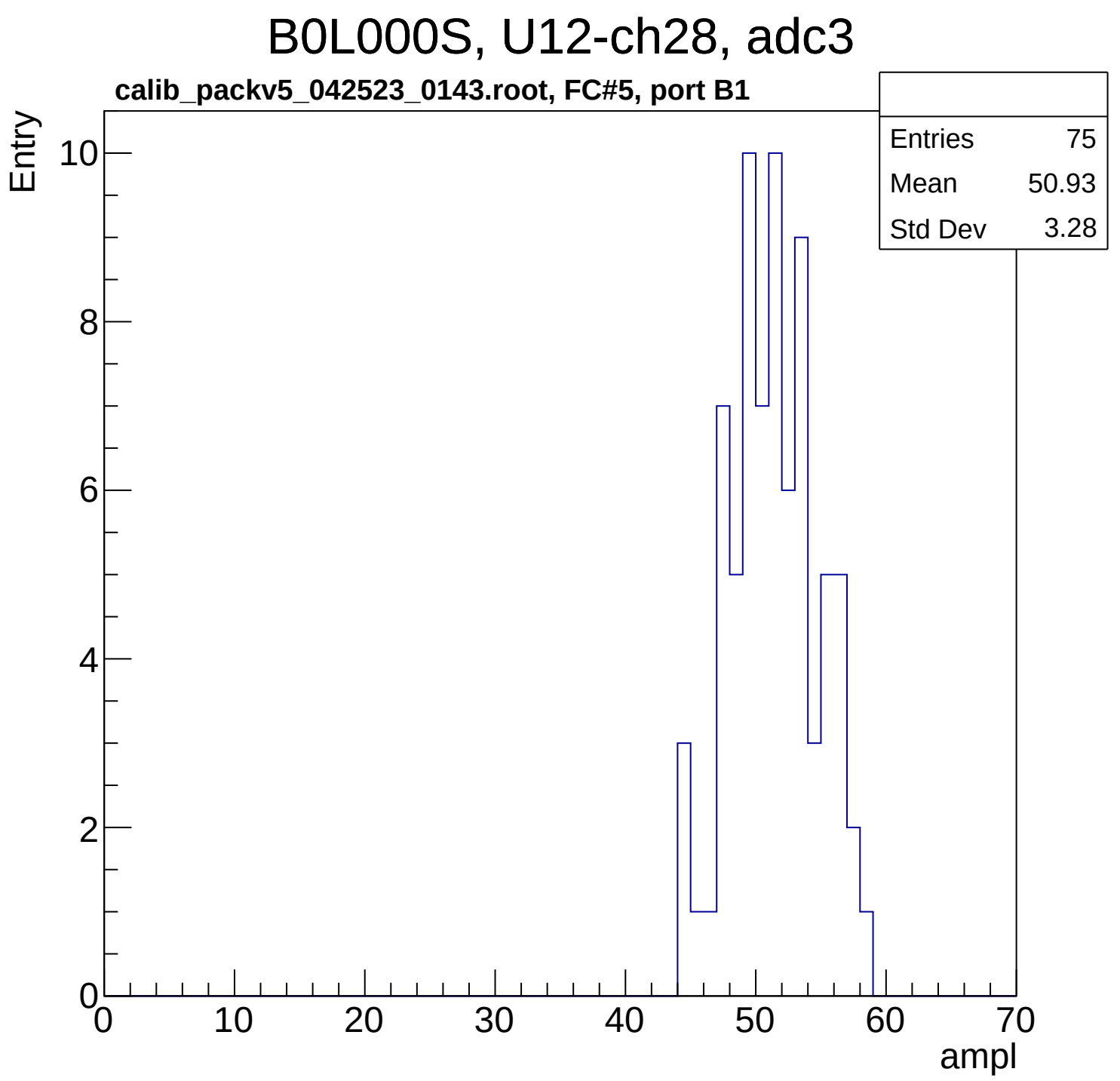
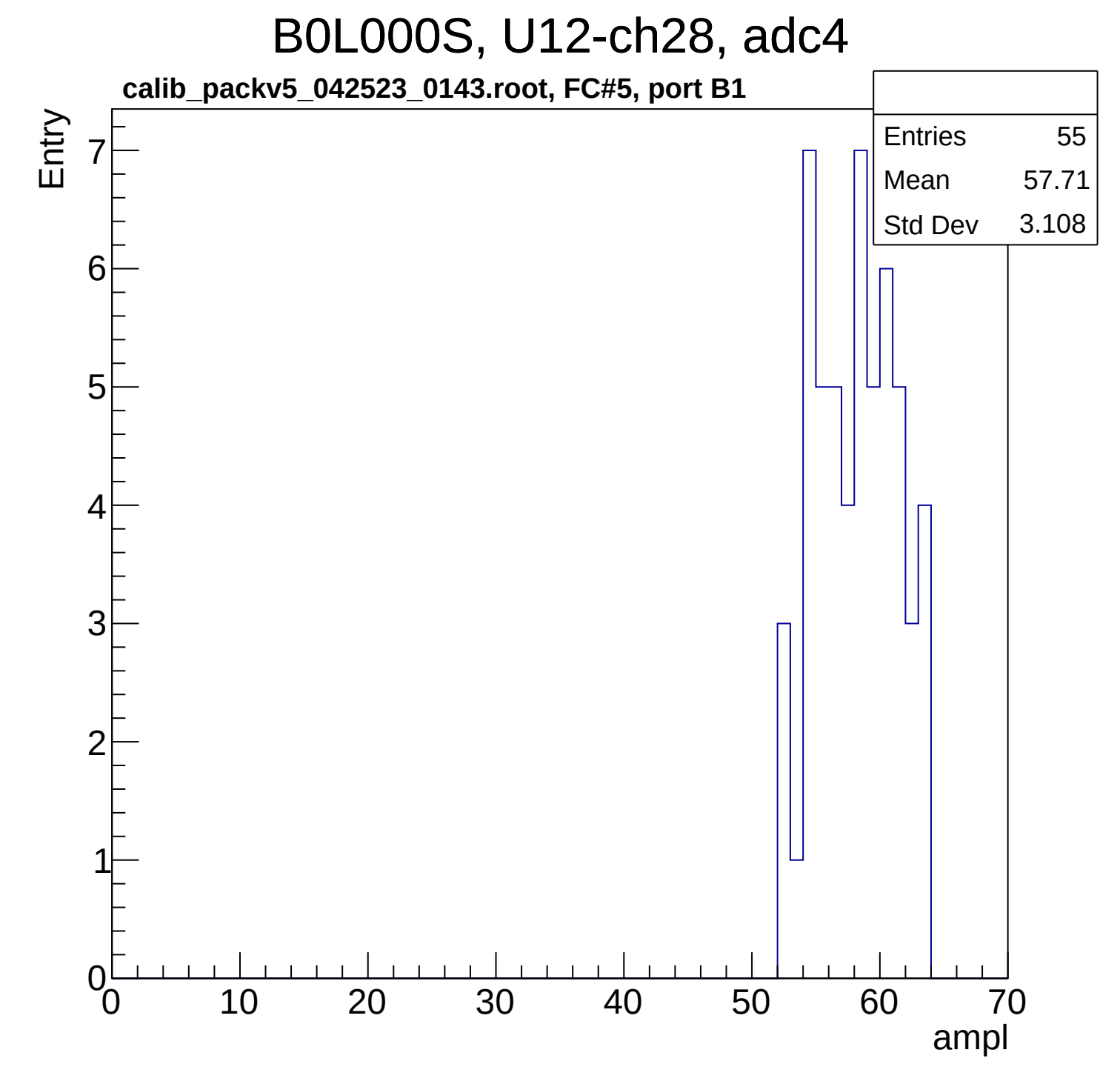

B0L000S, U12-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.93
Std Dev 3.28
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.71
Std Dev 3.108
Entry
ampl
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1
2
3
4
5
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0
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20
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40
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60
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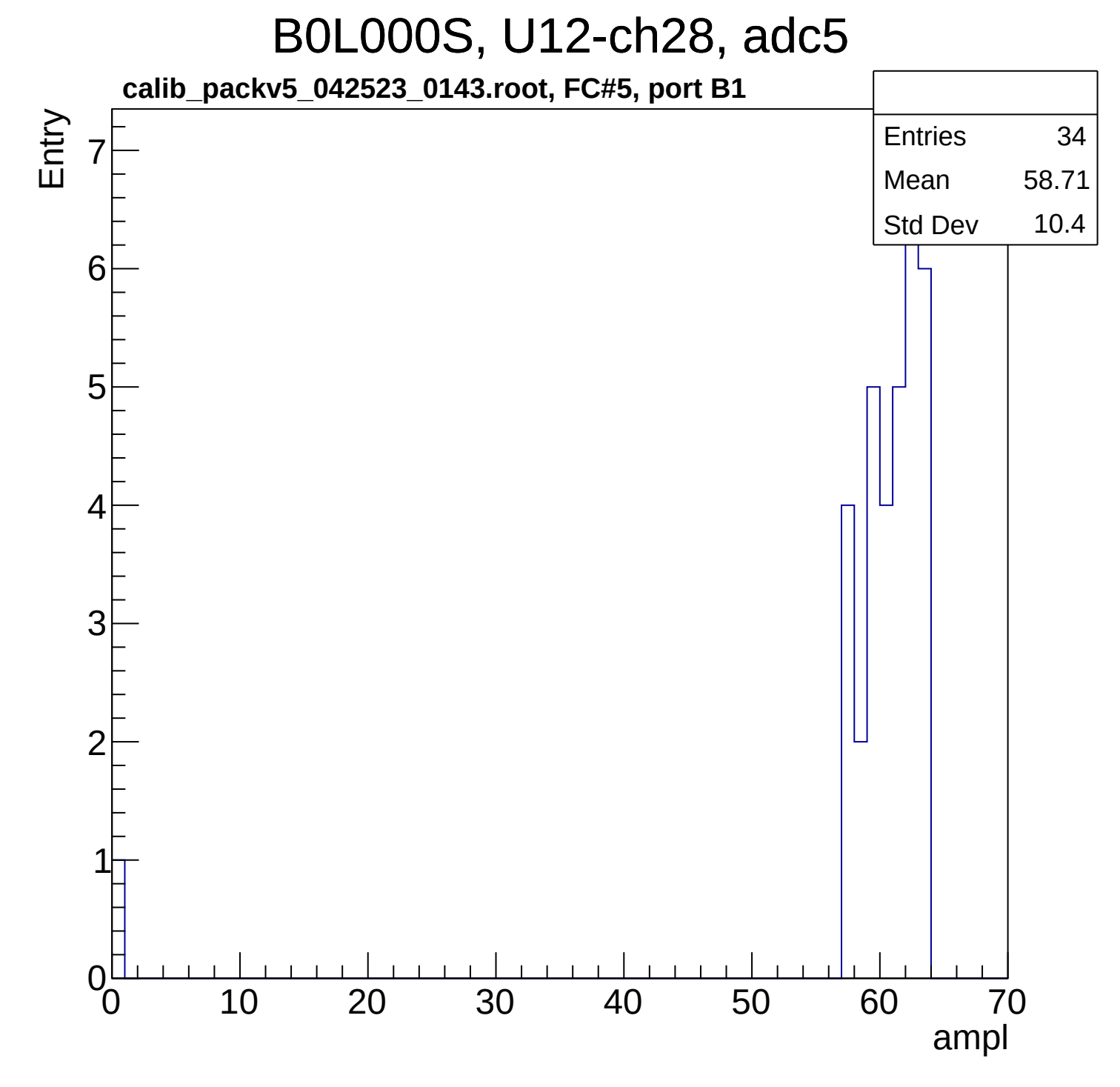
B0L000S, U12-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 58.71
Std Dev 10.4
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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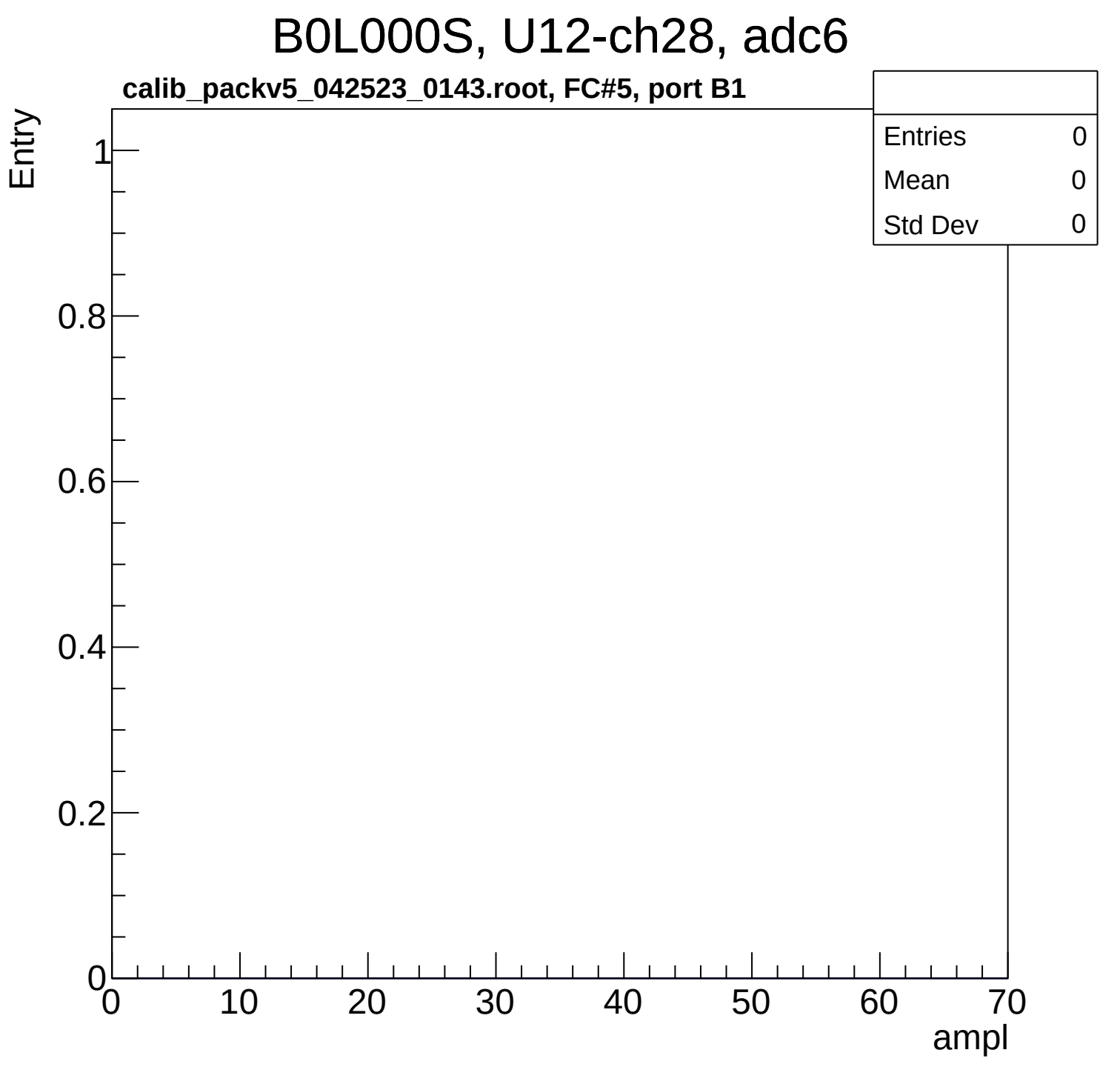
B0L000S, U12-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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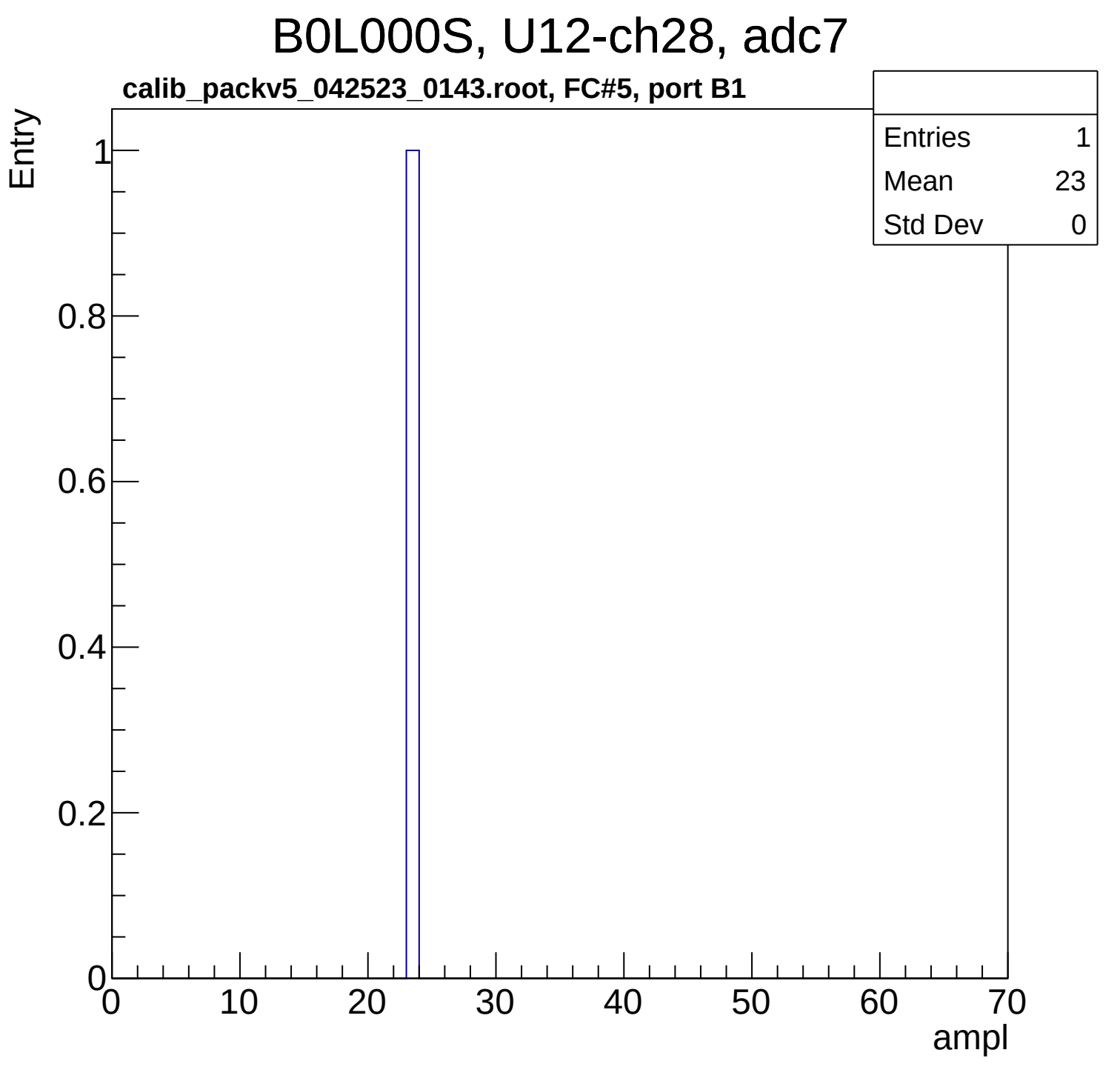
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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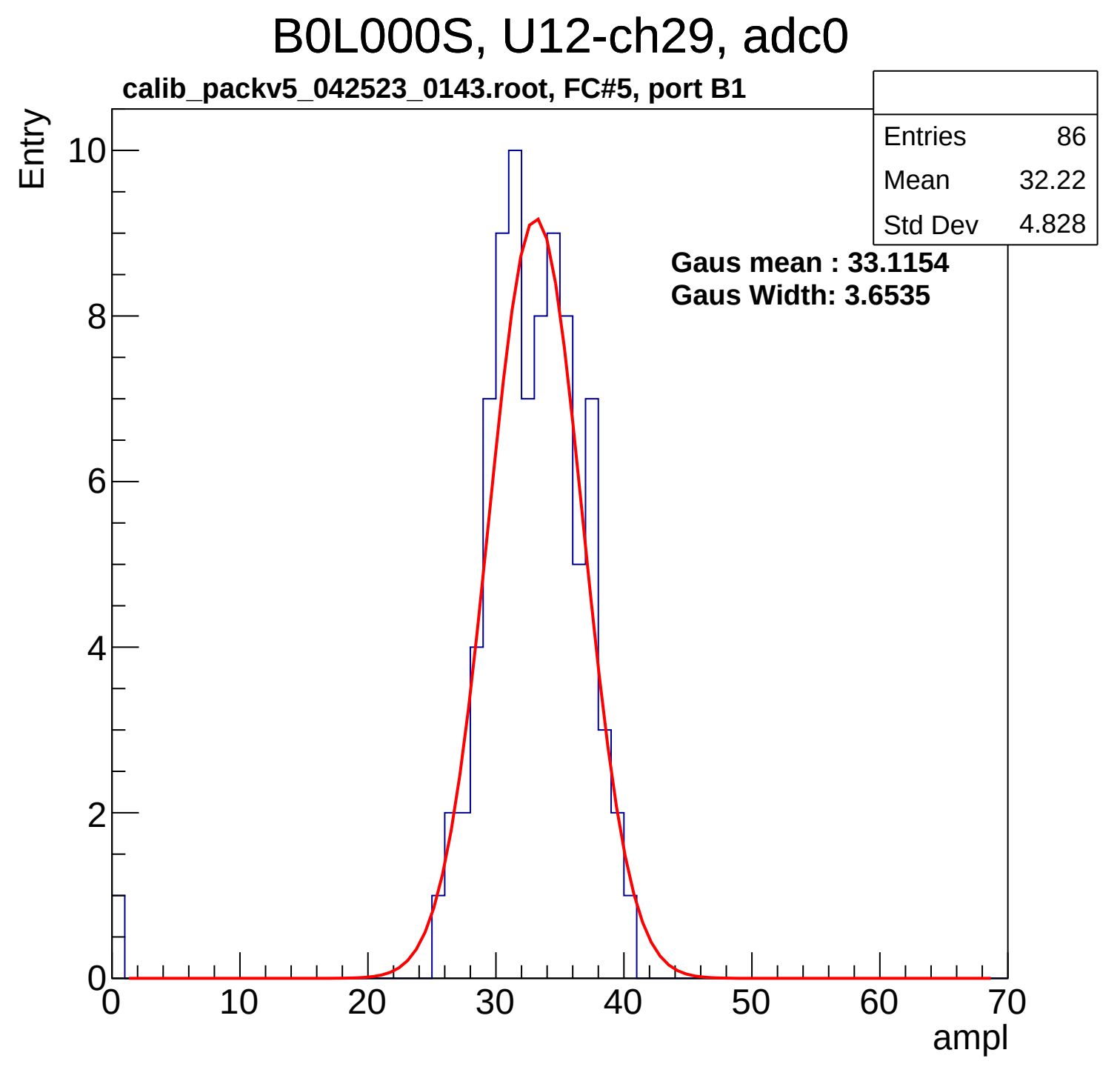
50
60
70
ampl


B0L000S, U12-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 32.22
Std Dev 4.828
Gaus mean : 33.1154
Gaus Width: 3.6535
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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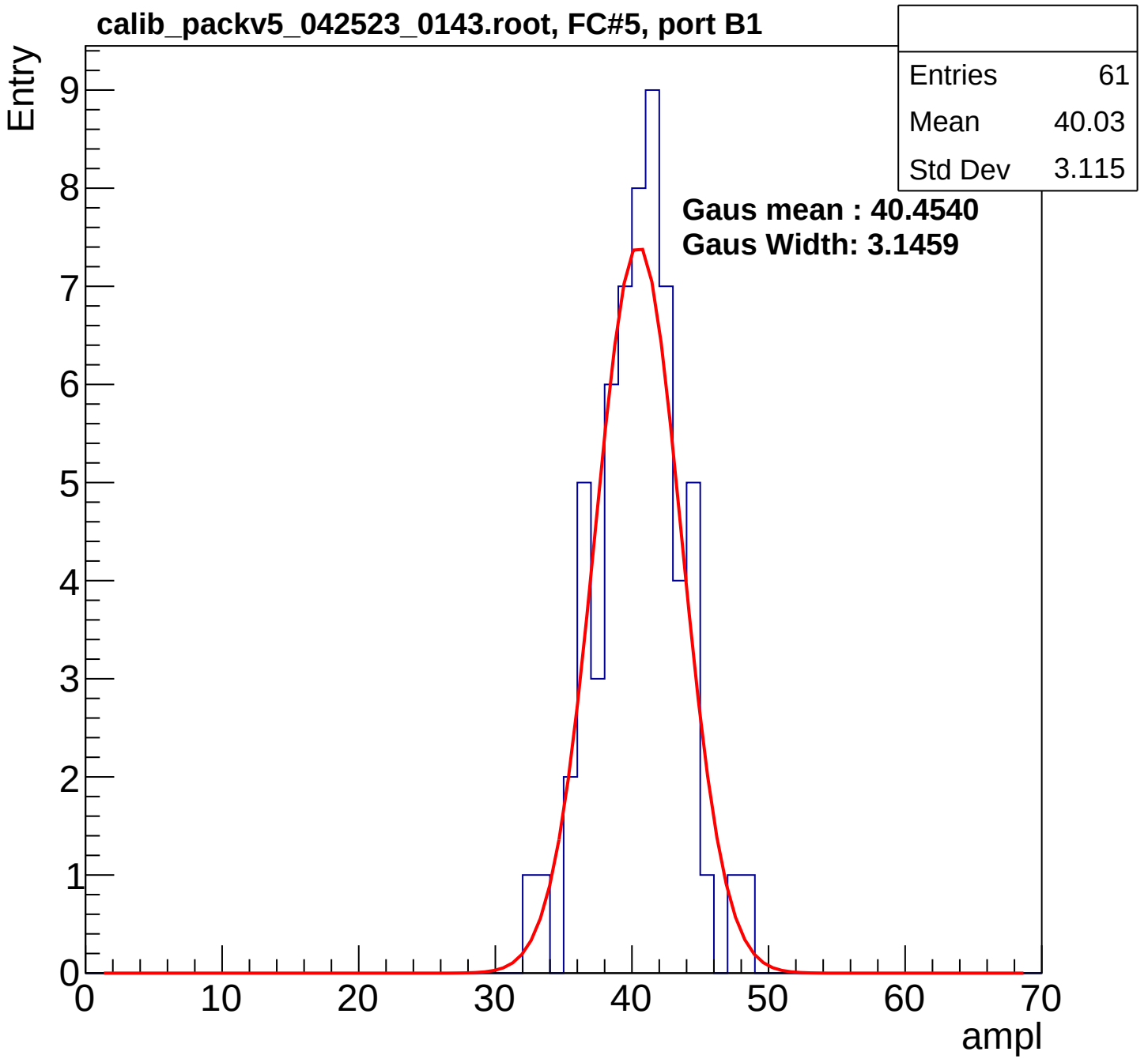
B0L000S, U12-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 40.03
Std Dev 3.115
Gaus mean : 40.4540
Gaus Width: 3.1459
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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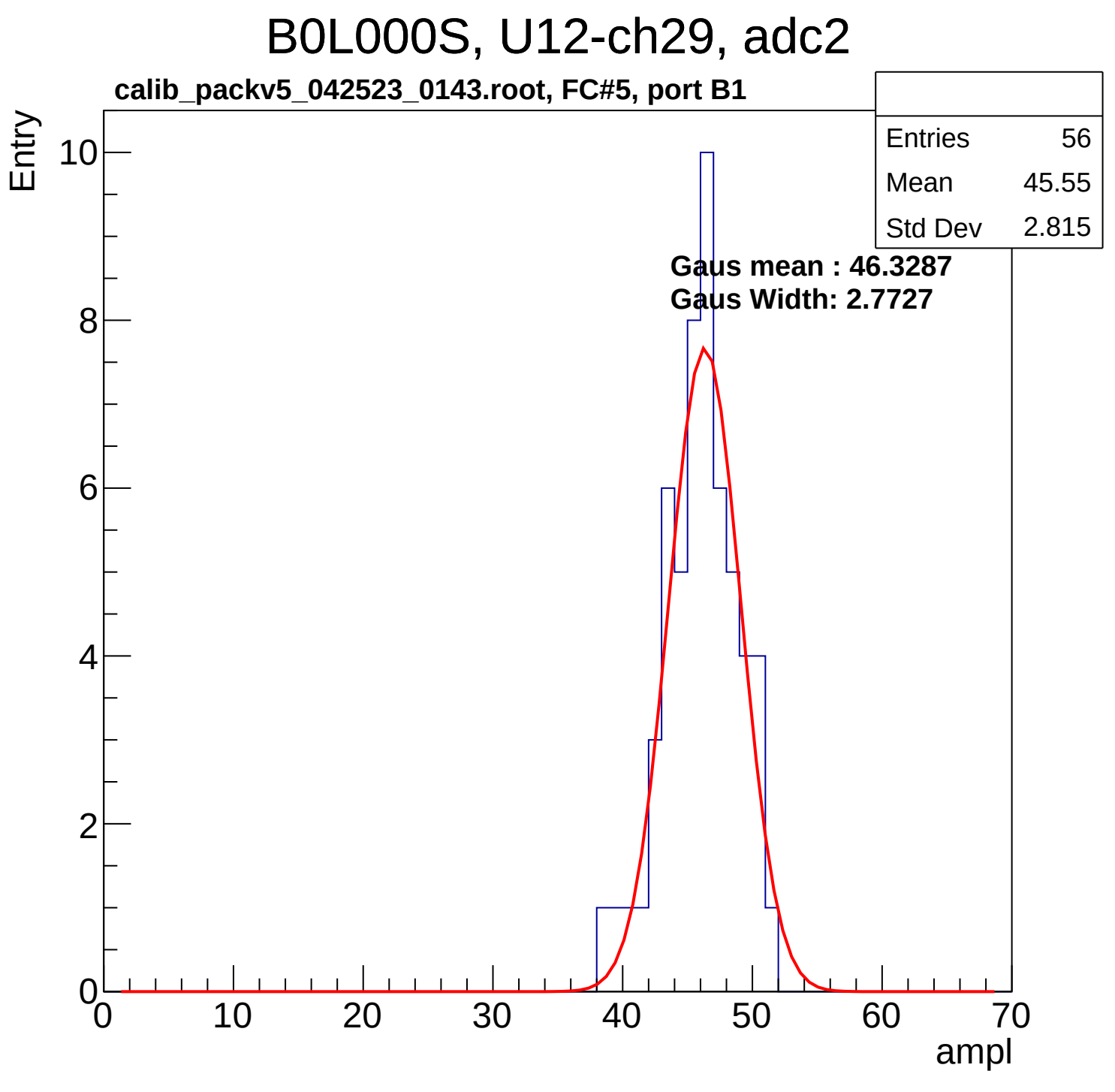
B0L000S, U12-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 45.55
Std Dev 2.815
Gaus mean : 46.3287
Gaus Width: 2.7727
Entry
ampl
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2
4
6
8
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0
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40
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60
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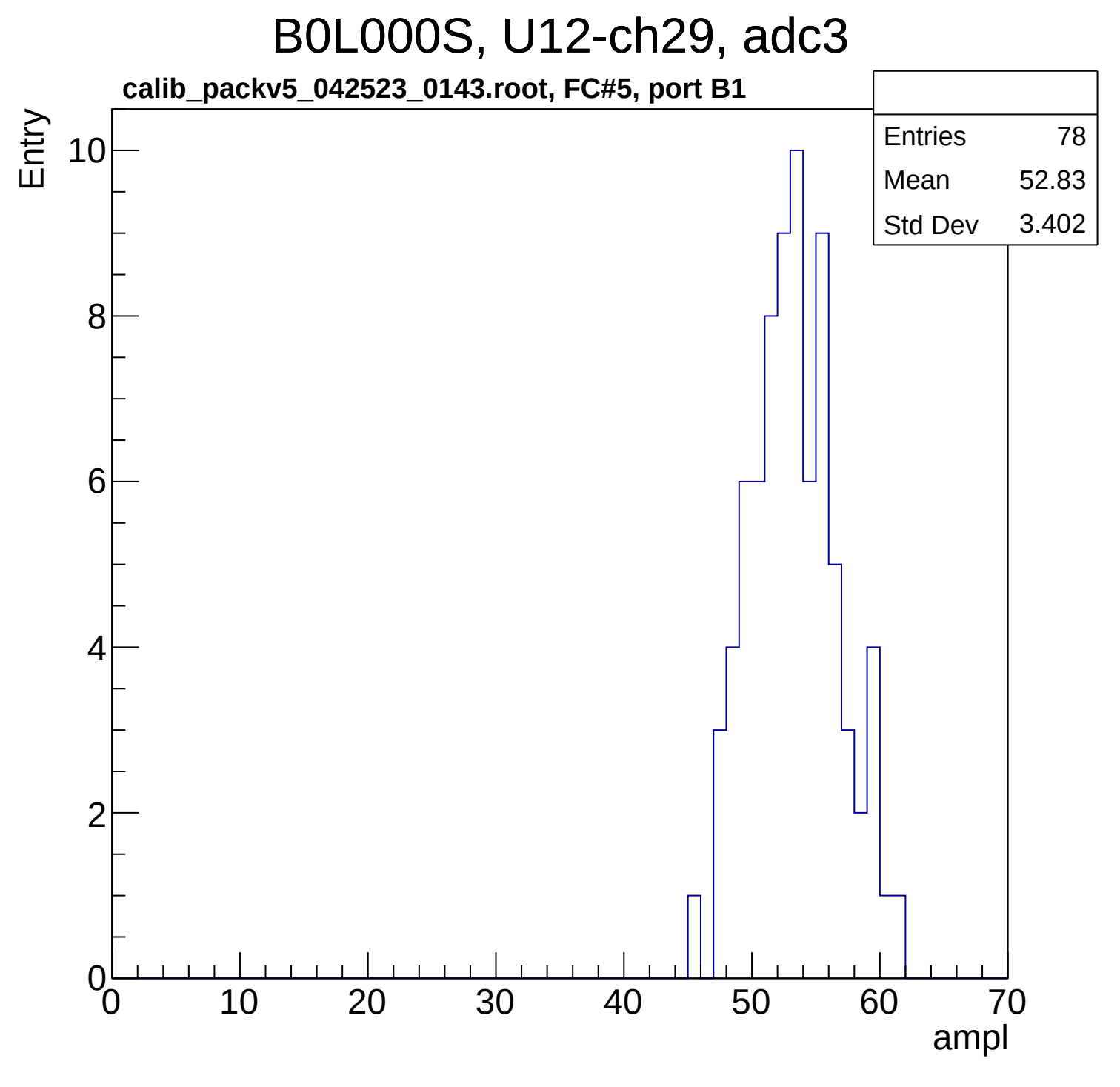
B0L000S, U12-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 52.83
Std Dev 3.402
Entry
ampl
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0
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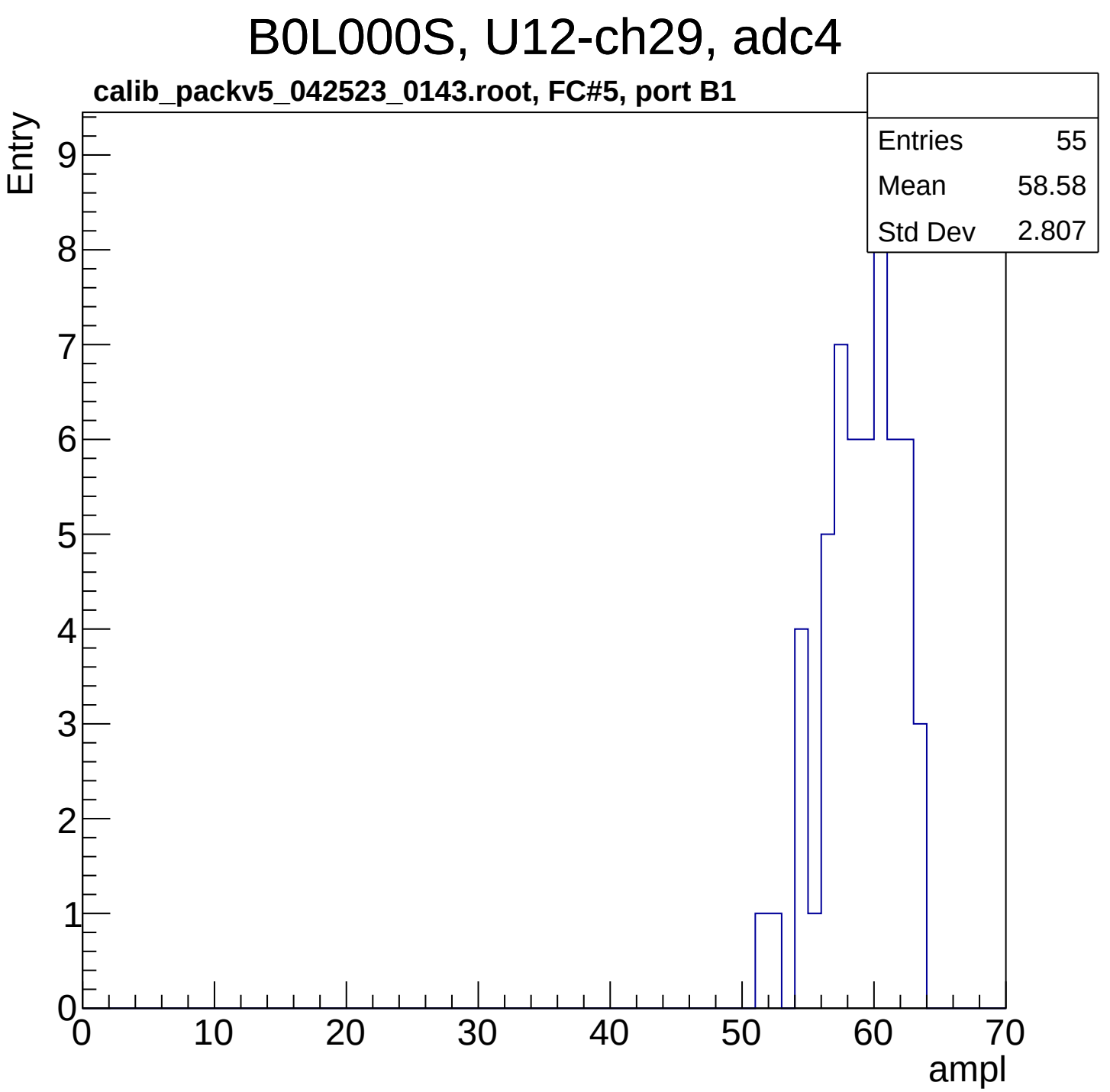
B0L000S, U12-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.58
Std Dev 2.807
Entry
ampl
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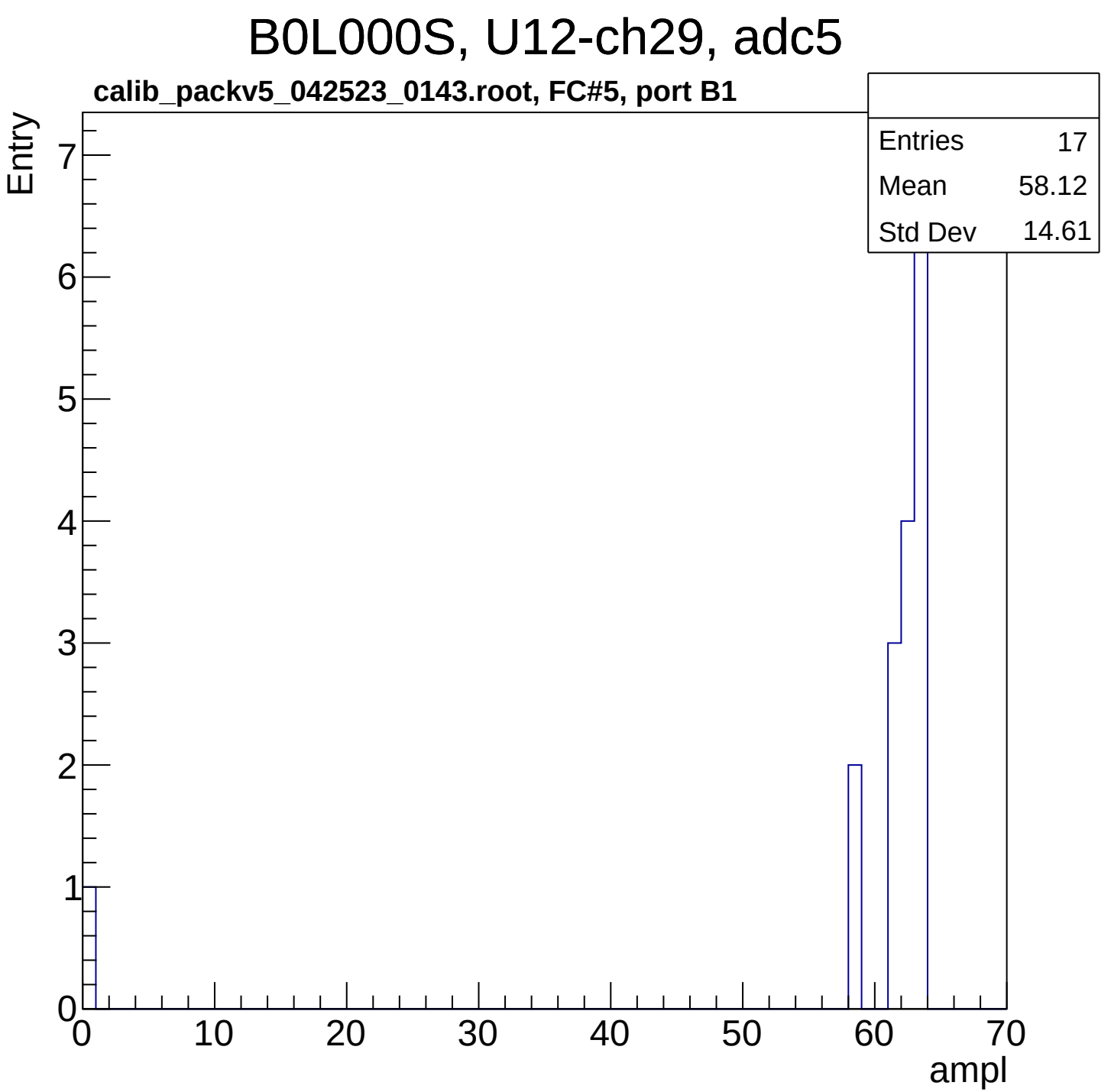
B0L000S, U12-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 58.12
Std Dev 14.61
Entry
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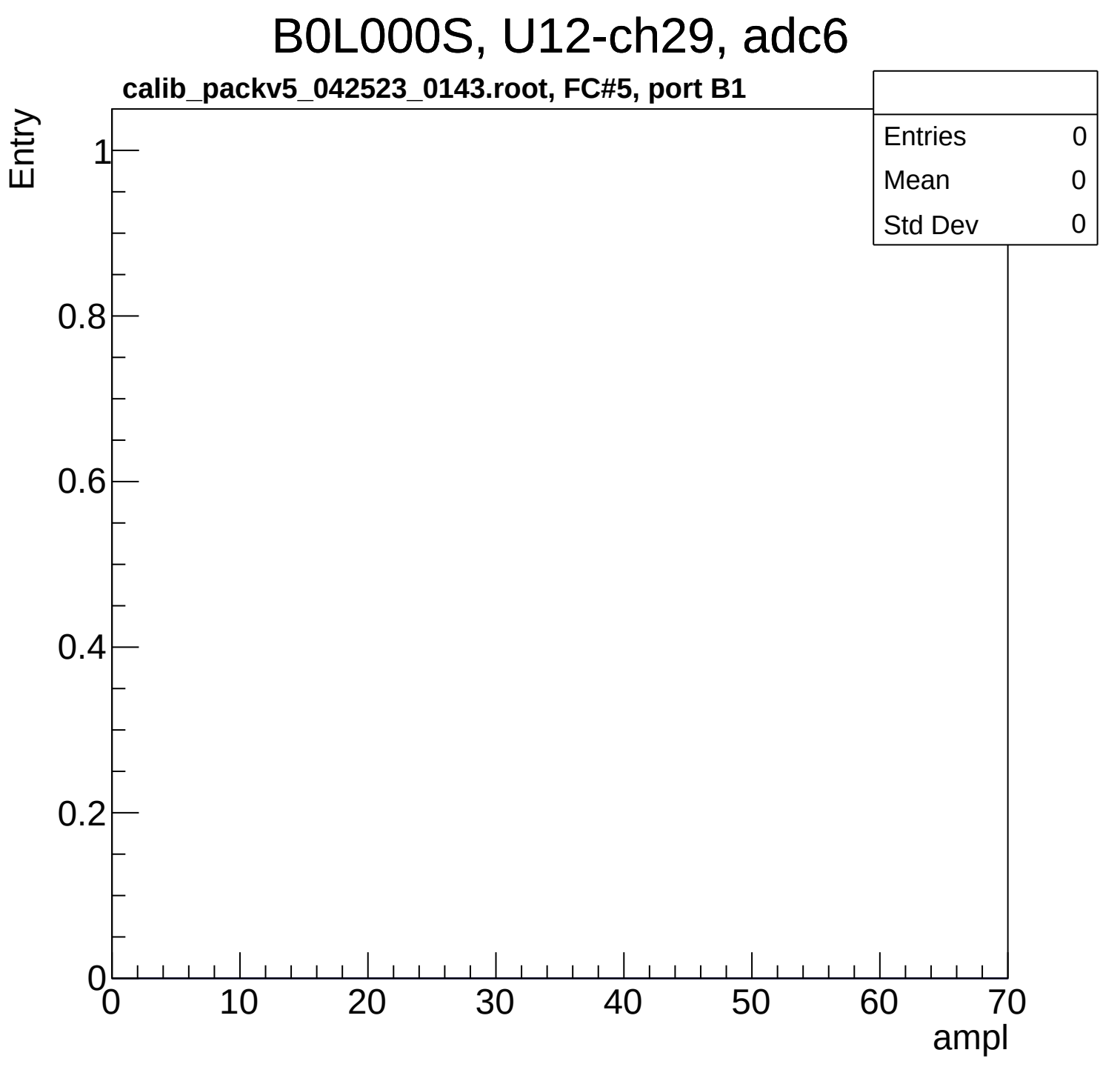
B0L000S, U12-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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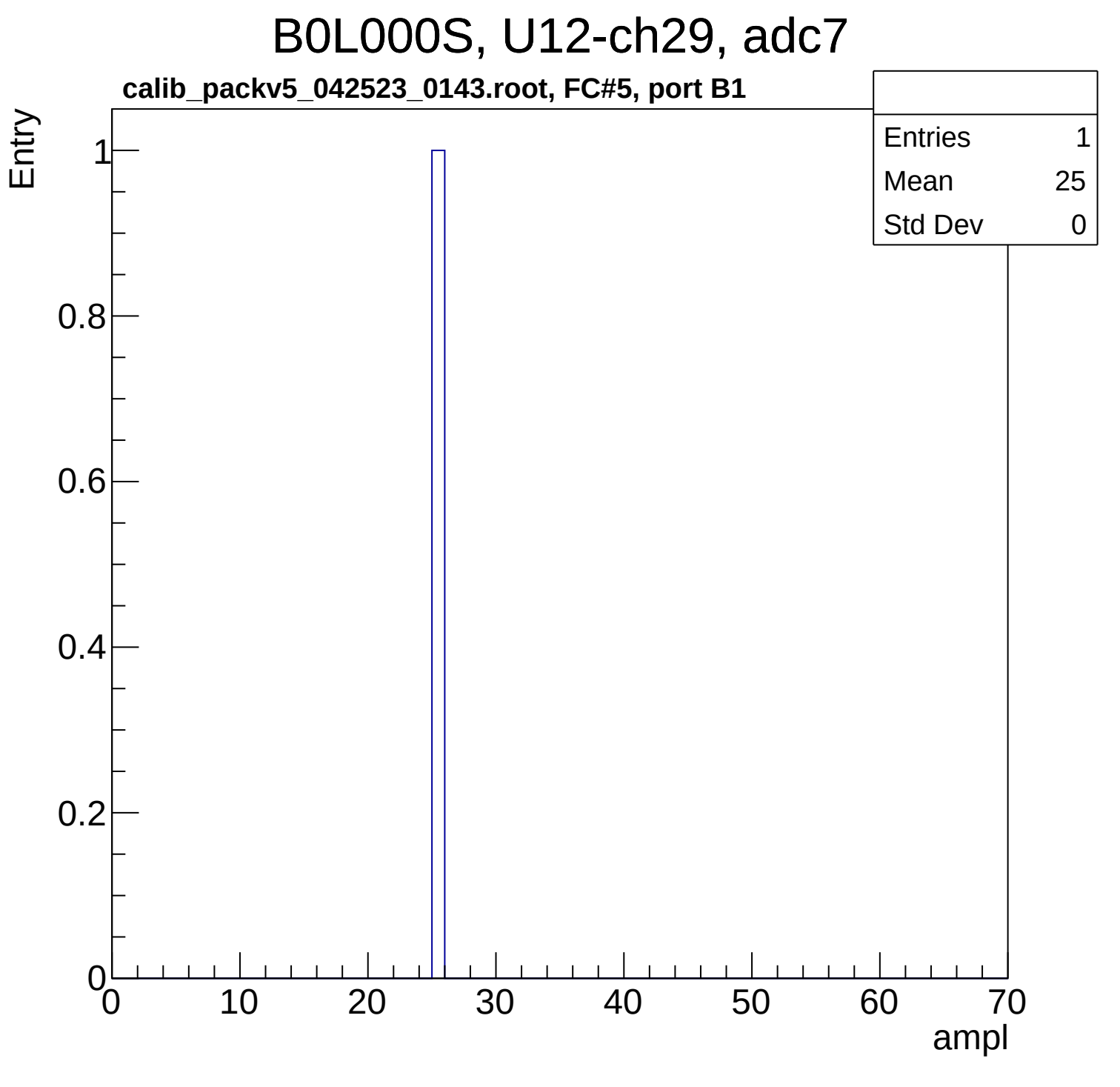
40
50
60
70
ampl


B0L000S, U12-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 25
Std Dev 0
Entry
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0.4
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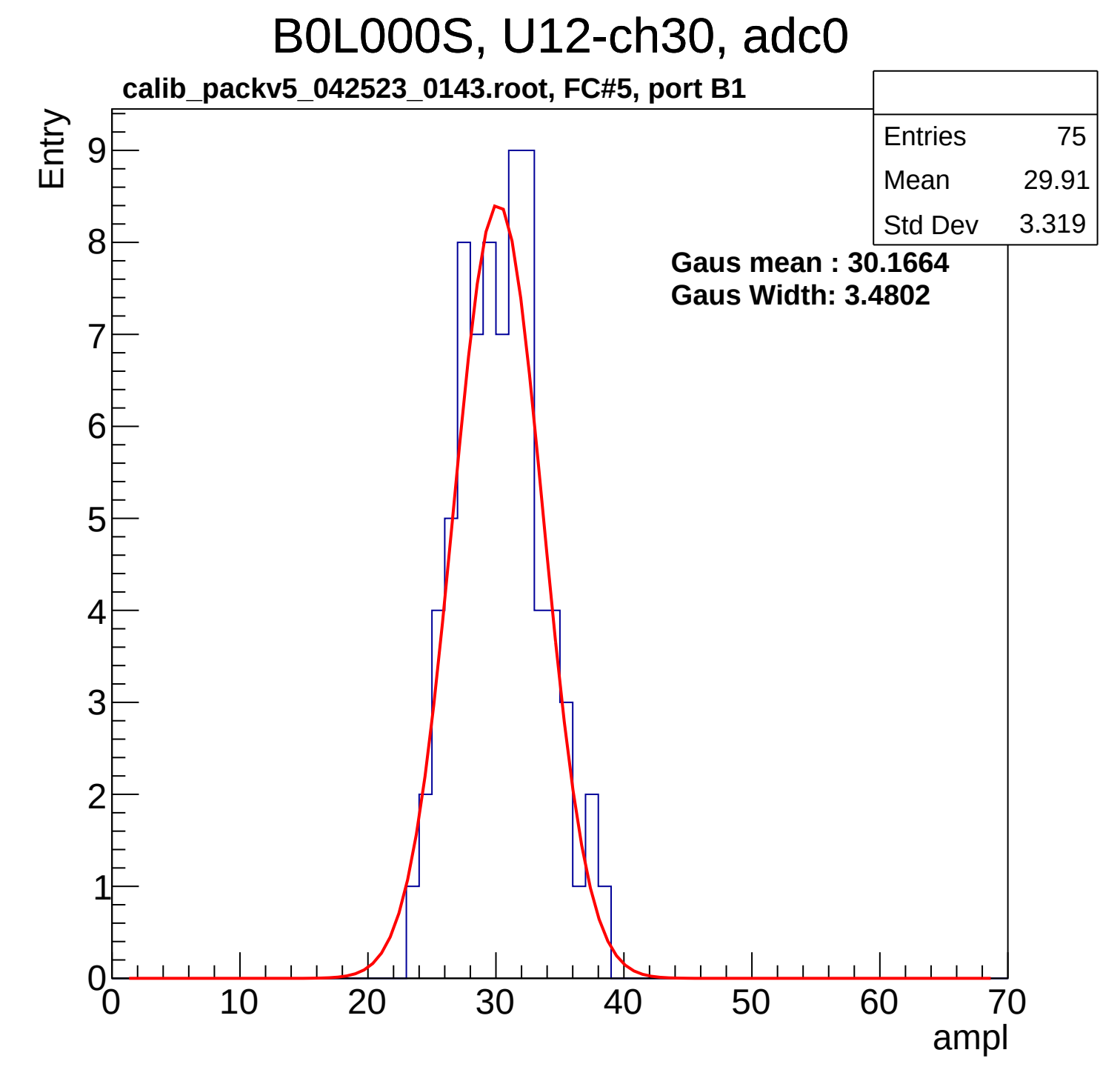
70
ampl


B0L000S, U12-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.91
Std Dev 3.319
Gaus mean : 30.1664
Gaus Width: 3.4802
Entry
ampl
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4
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6
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8
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0
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60
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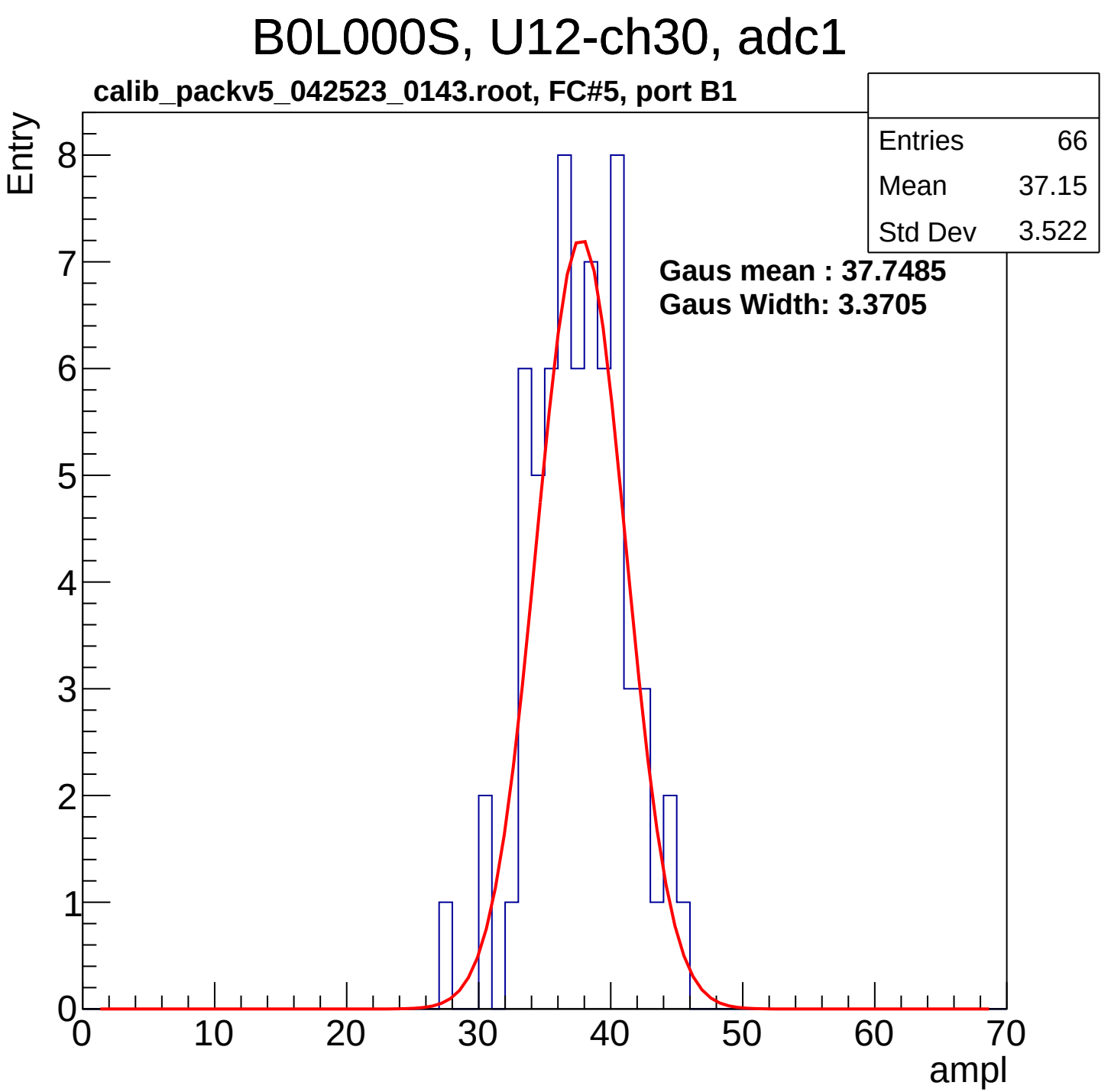
B0L000S, U12-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.15
Std Dev 3.522
Gaus mean : 37.7485
Gaus Width: 3.3705
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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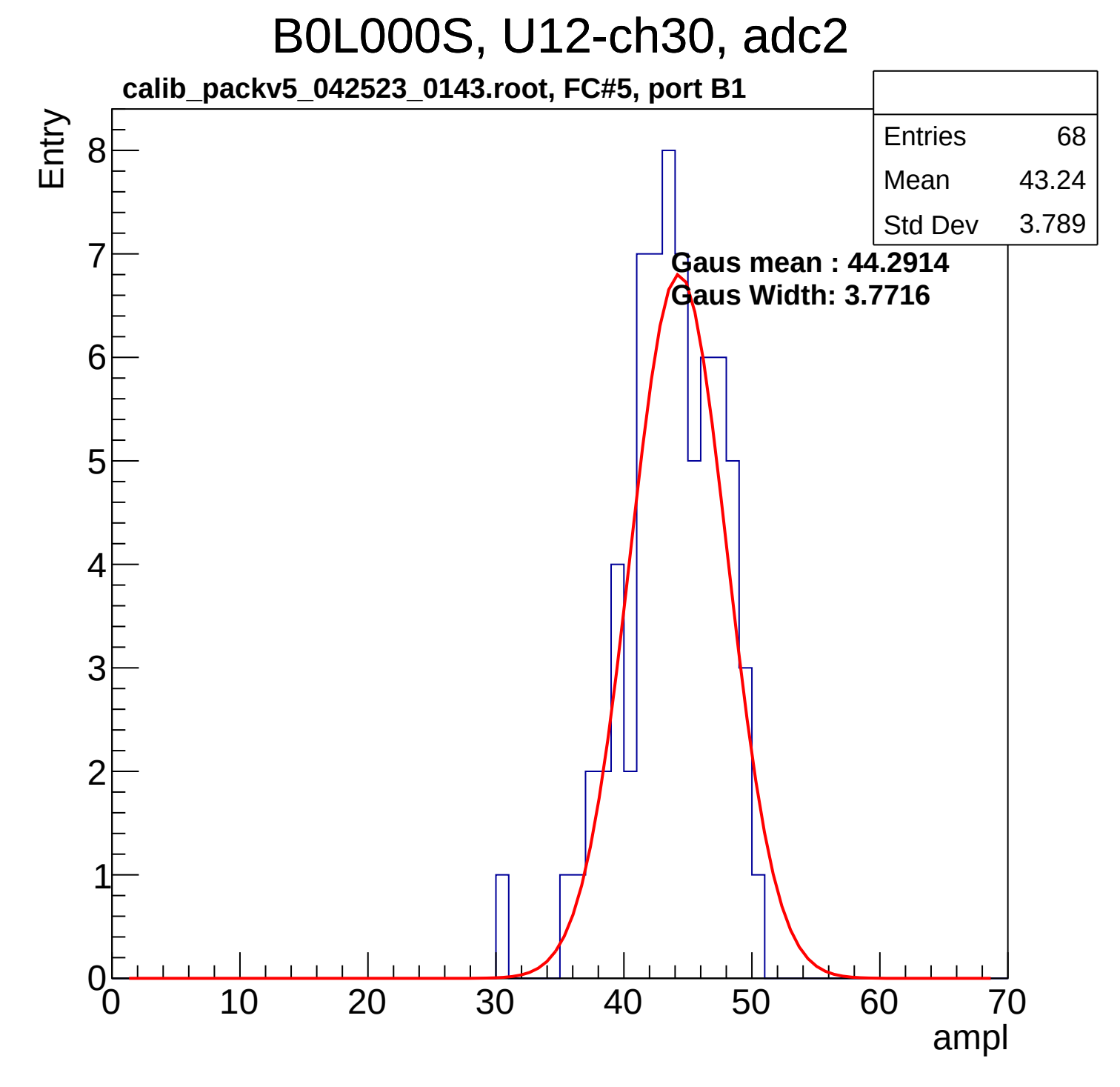
B0L000S, U12-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.24
Std Dev 3.789
Gaus mean : 44.2914
Gaus Width: 3.7716
Entry
ampl
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3
4
5
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0
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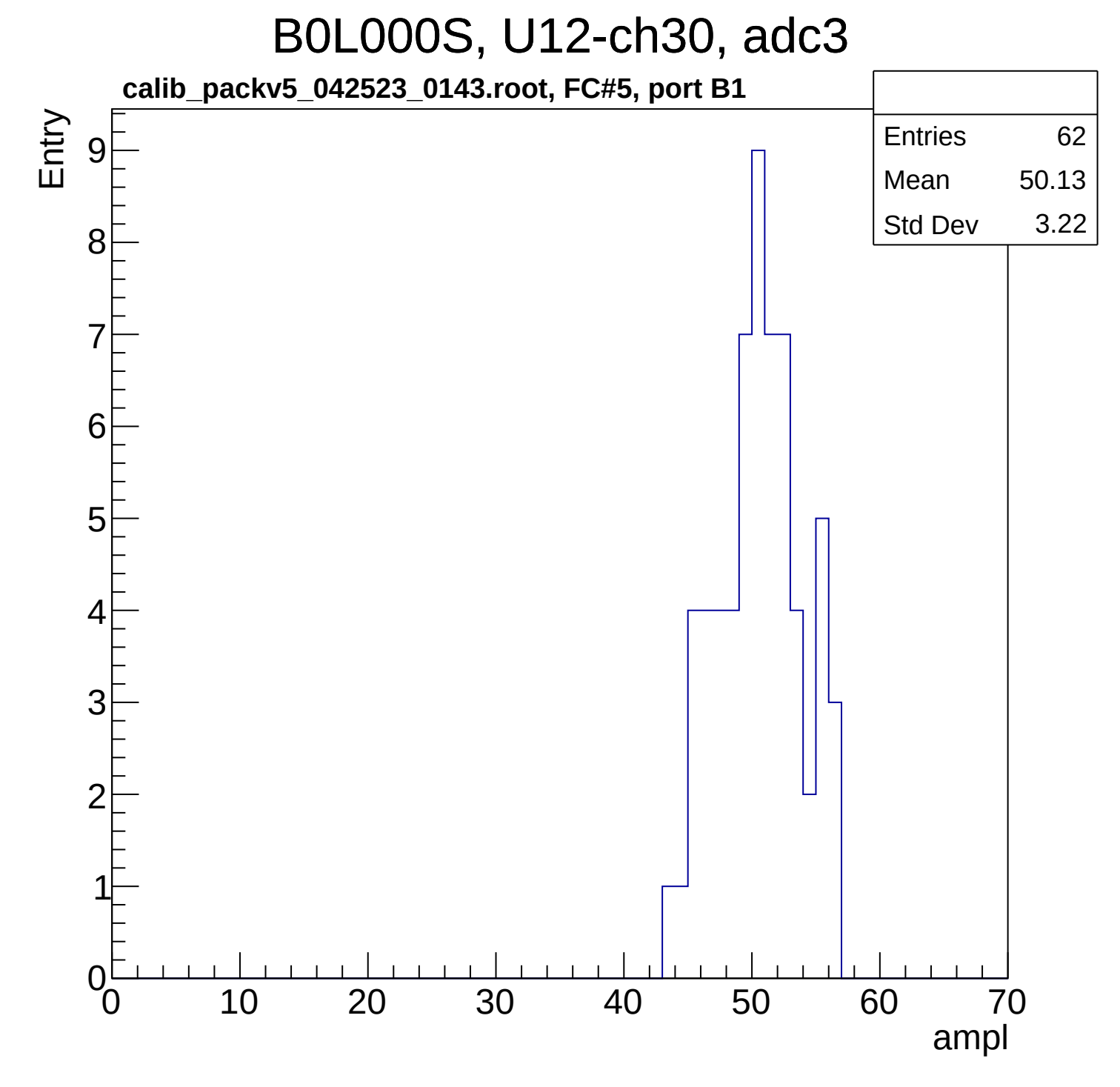
B0L000S, U12-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.13
Std Dev 3.22
Entry
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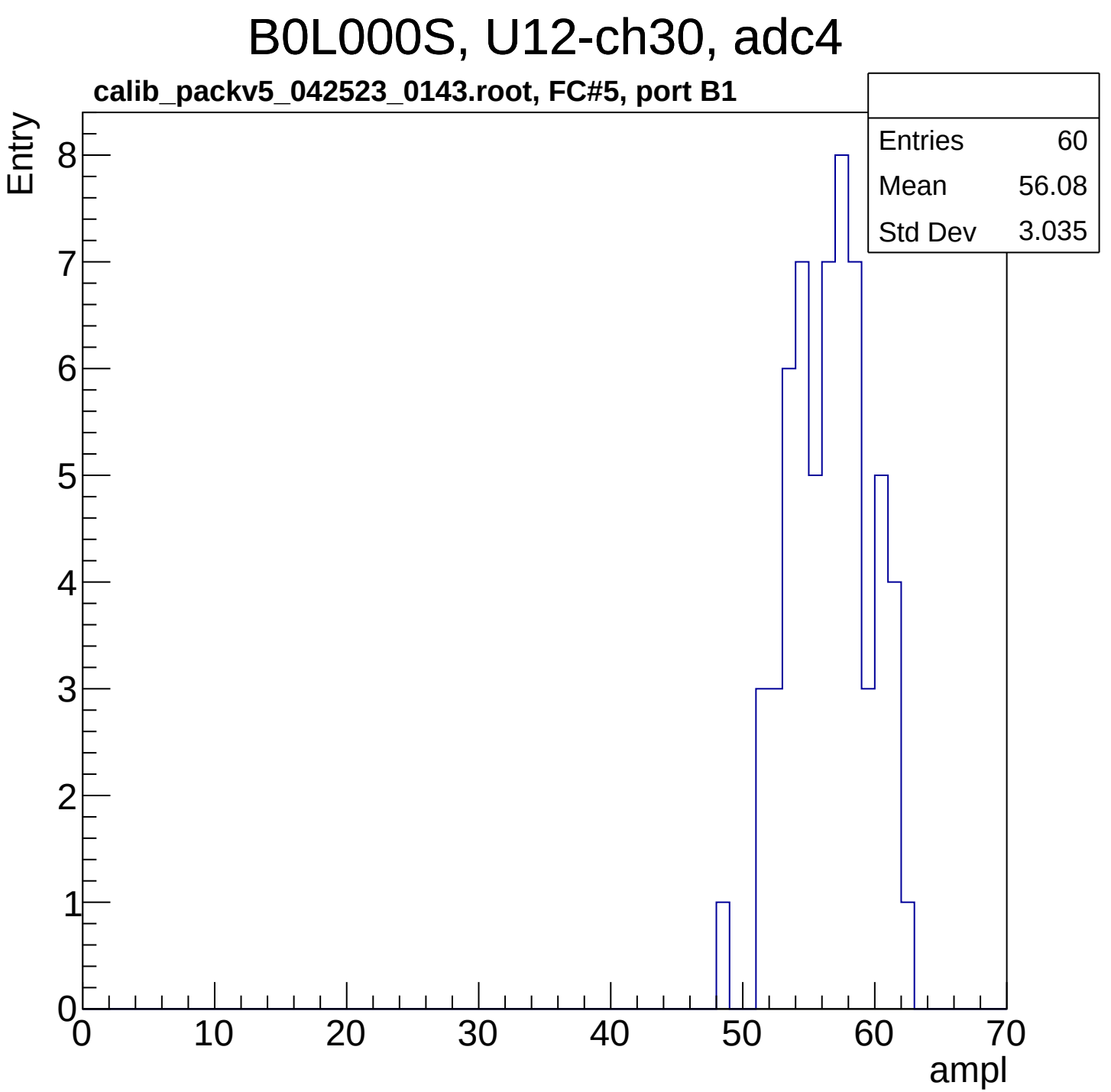
70
ampl


B0L000S, U12-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.08
Std Dev 3.035
Entry
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3
4
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6
7
8
0
10
20
30
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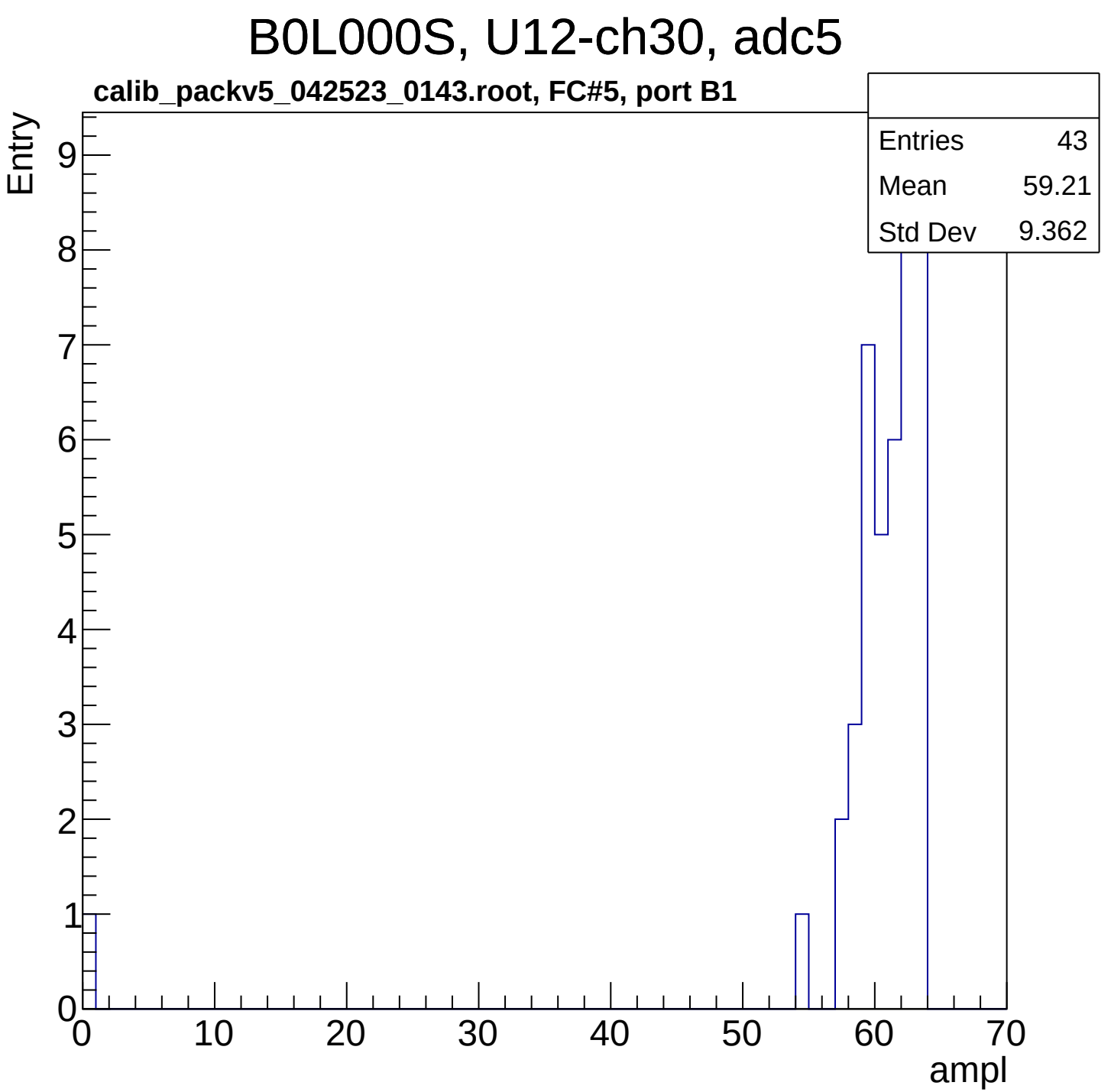
50
60
70
ampl


B0L000S, U12-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.21
Std Dev 9.362
Entry
ampl
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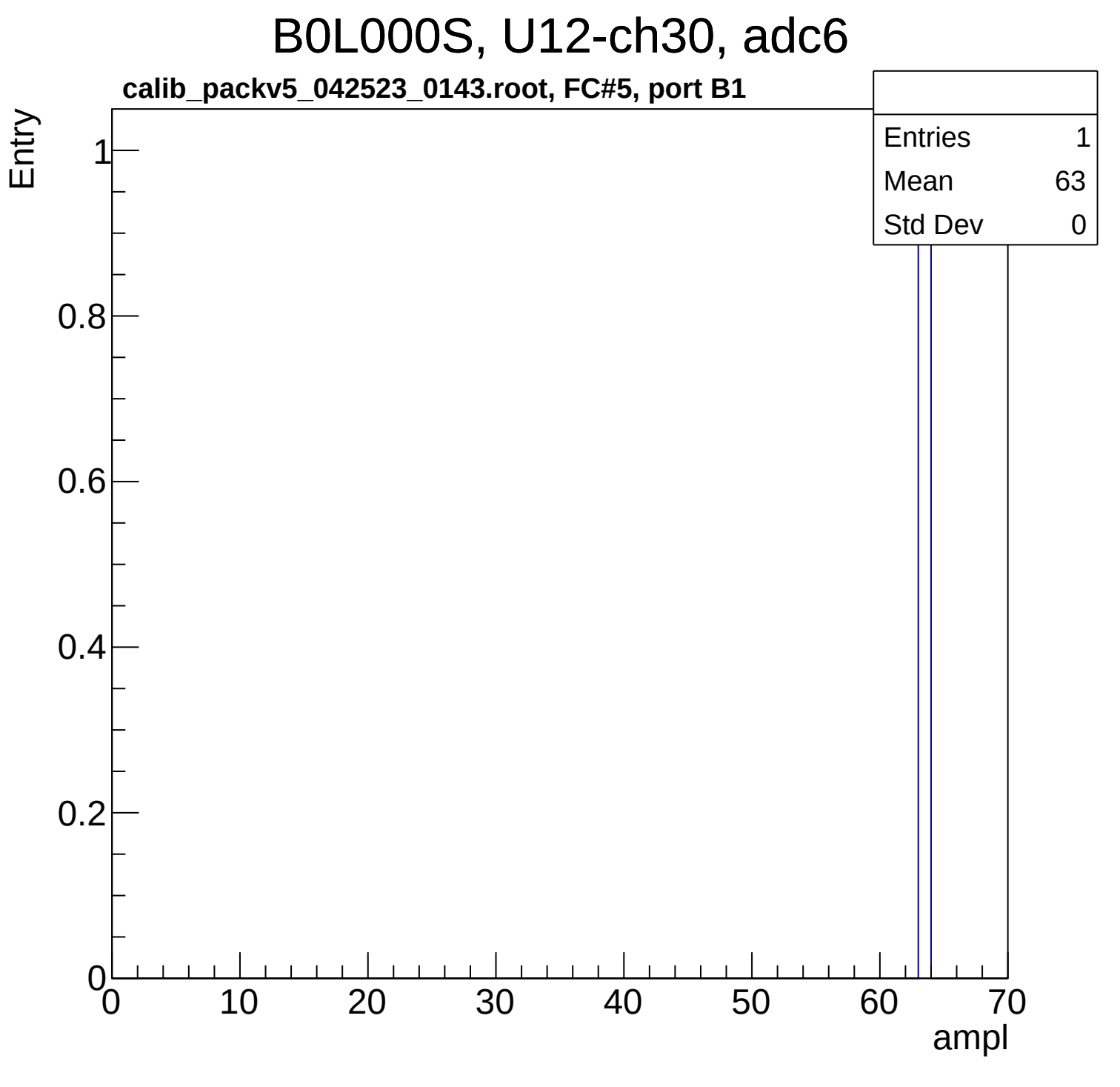
B0L000S, U12-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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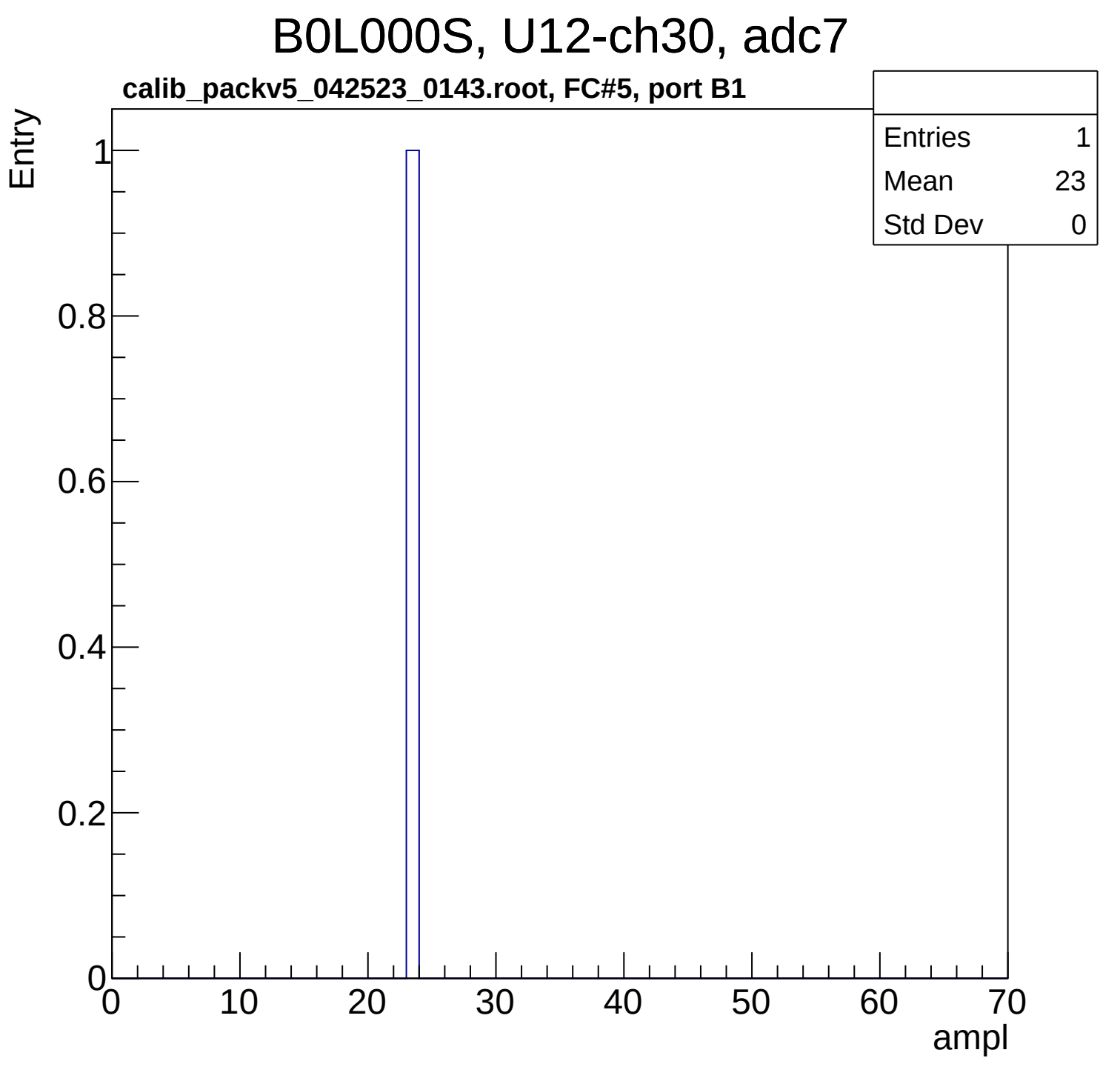
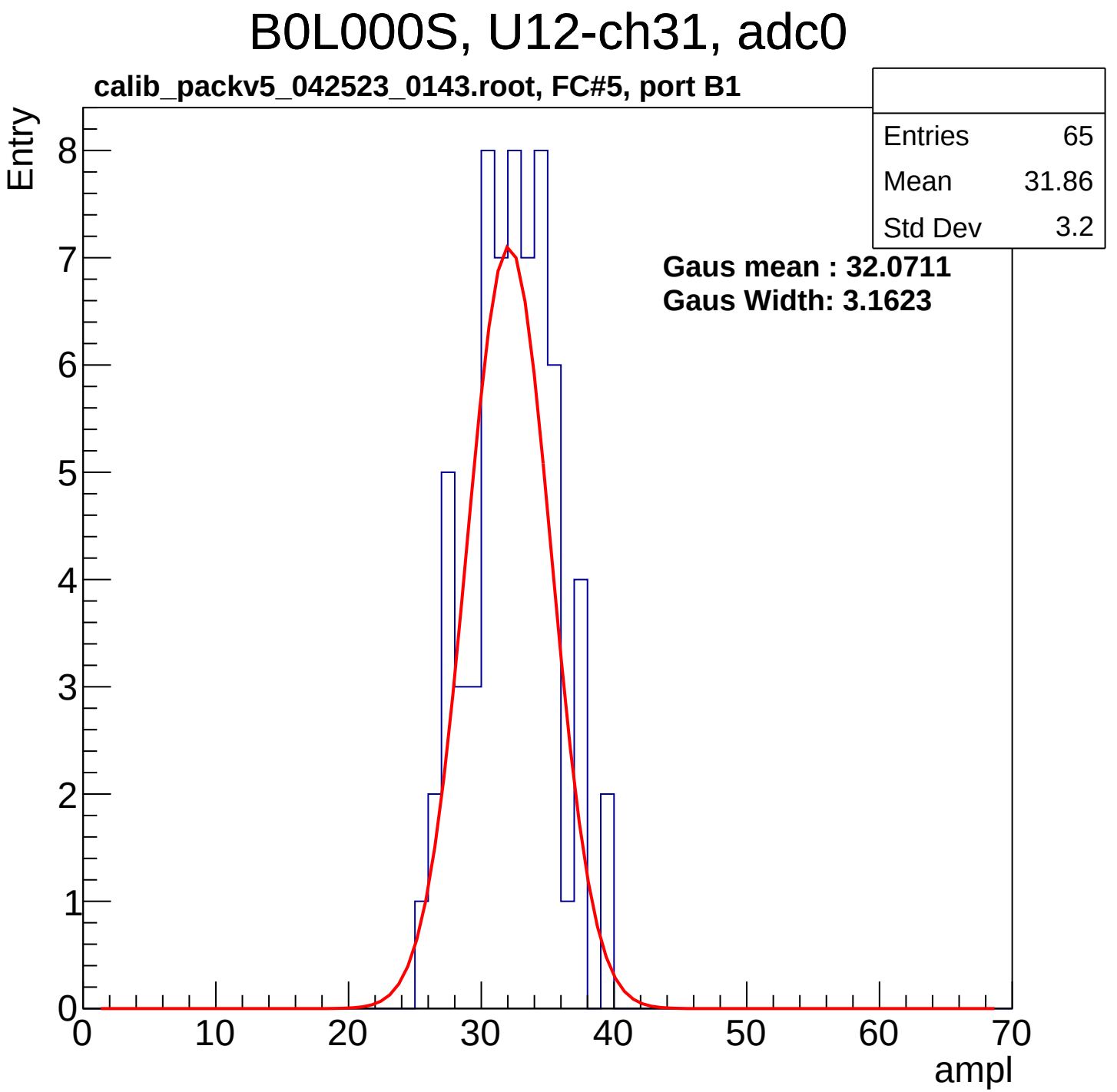
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 31.86
Std Dev 3.2
Gaus mean : 32.0711
Gaus Width: 3.1623
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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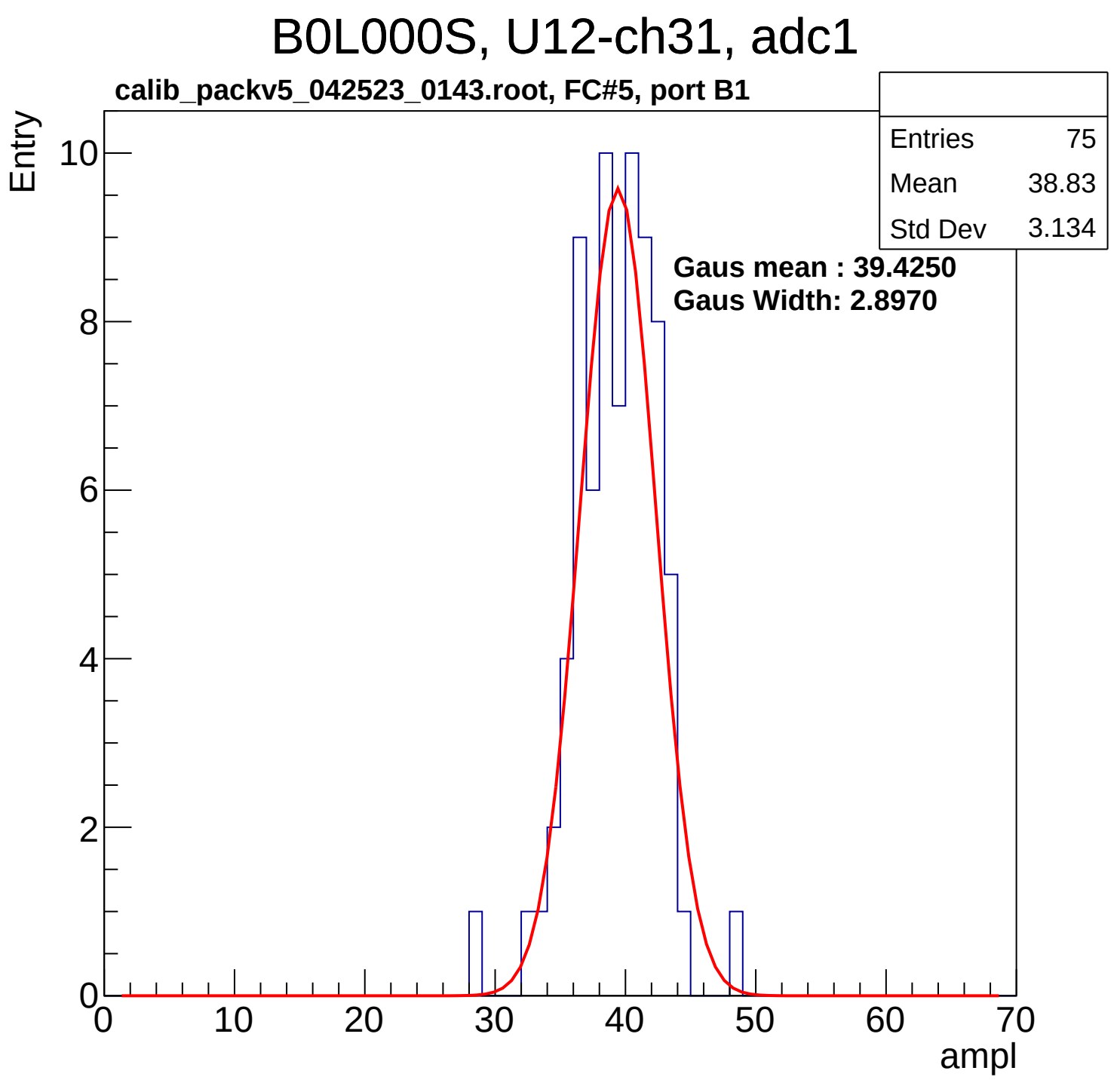
B0L000S, U12-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 38.83
Std Dev 3.134
Gaus mean : 39.4250
Gaus Width: 2.8970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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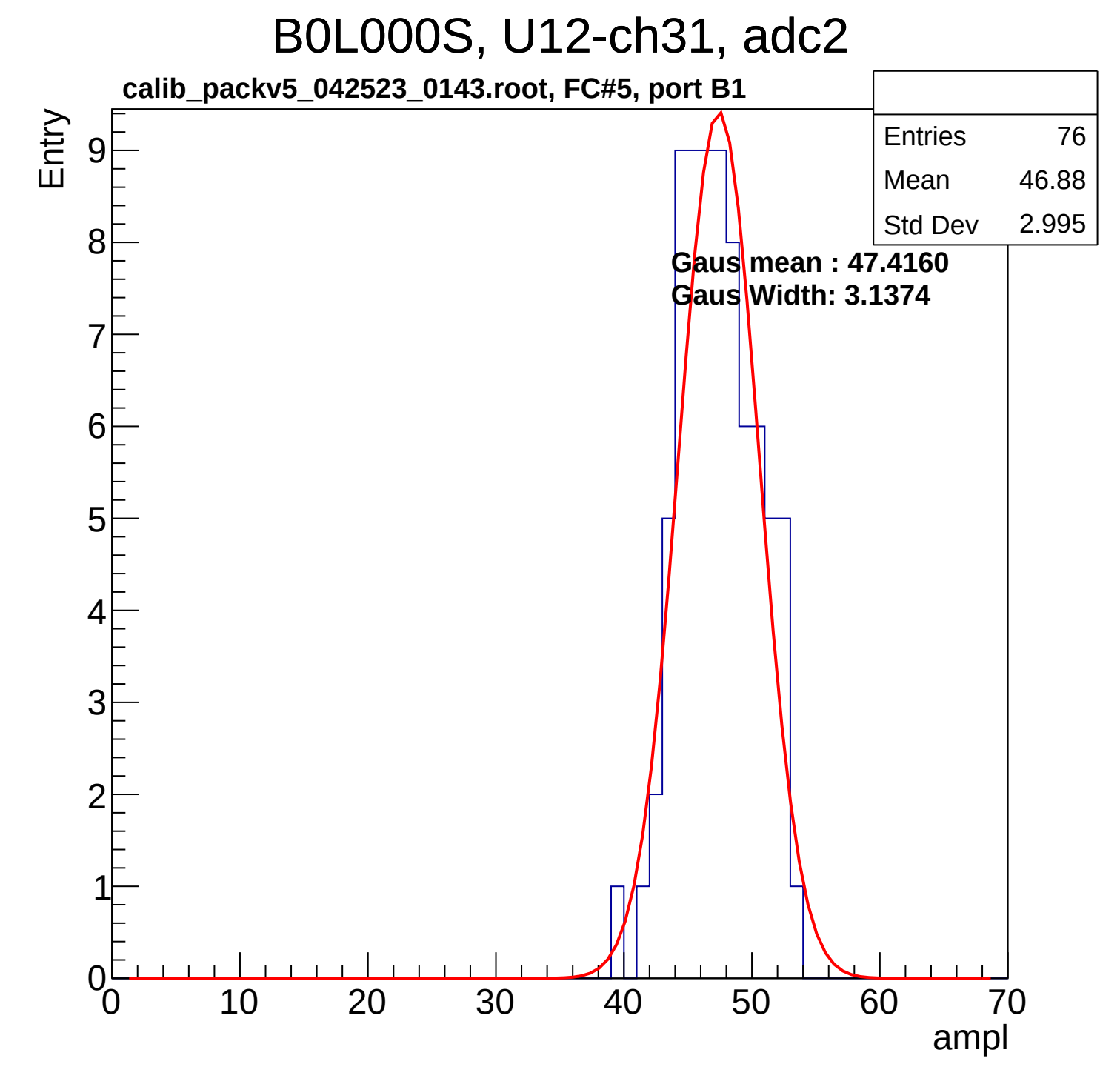
B0L000S, U12-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 46.88
Std Dev 2.995
Gaus mean : 47.4160
Gaus Width: 3.1374
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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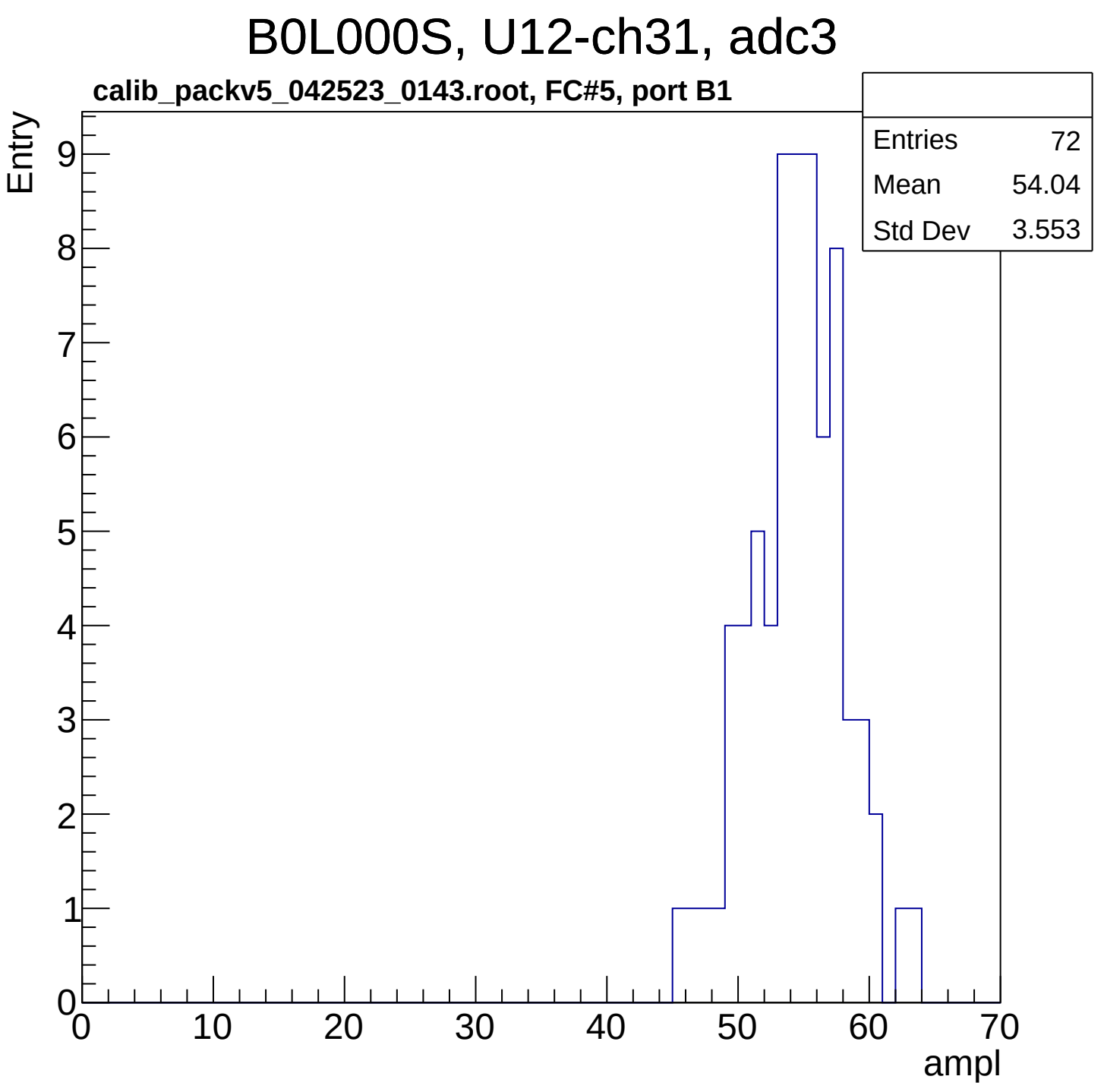
B0L000S, U12-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 54.04
Std Dev 3.553
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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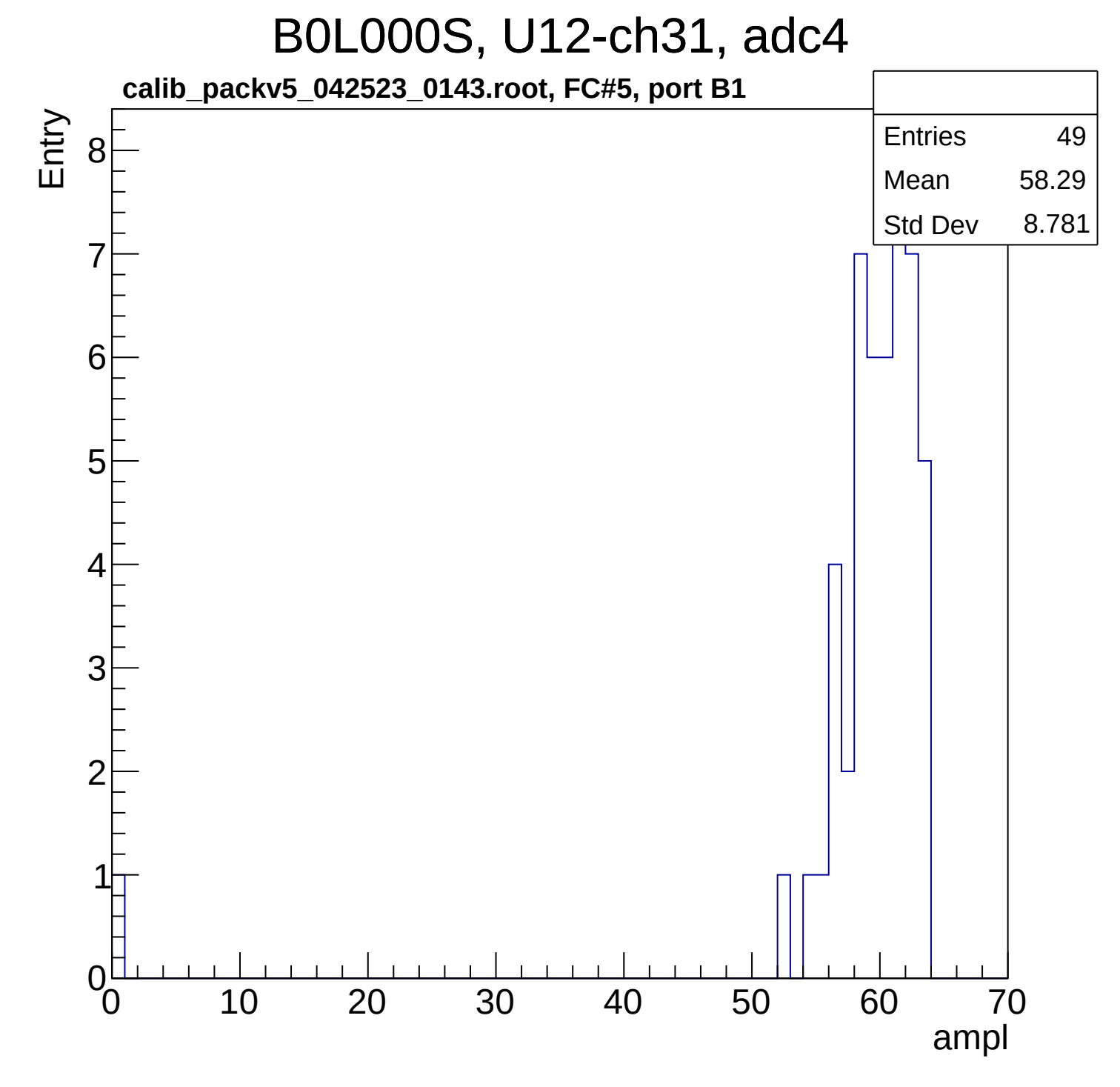
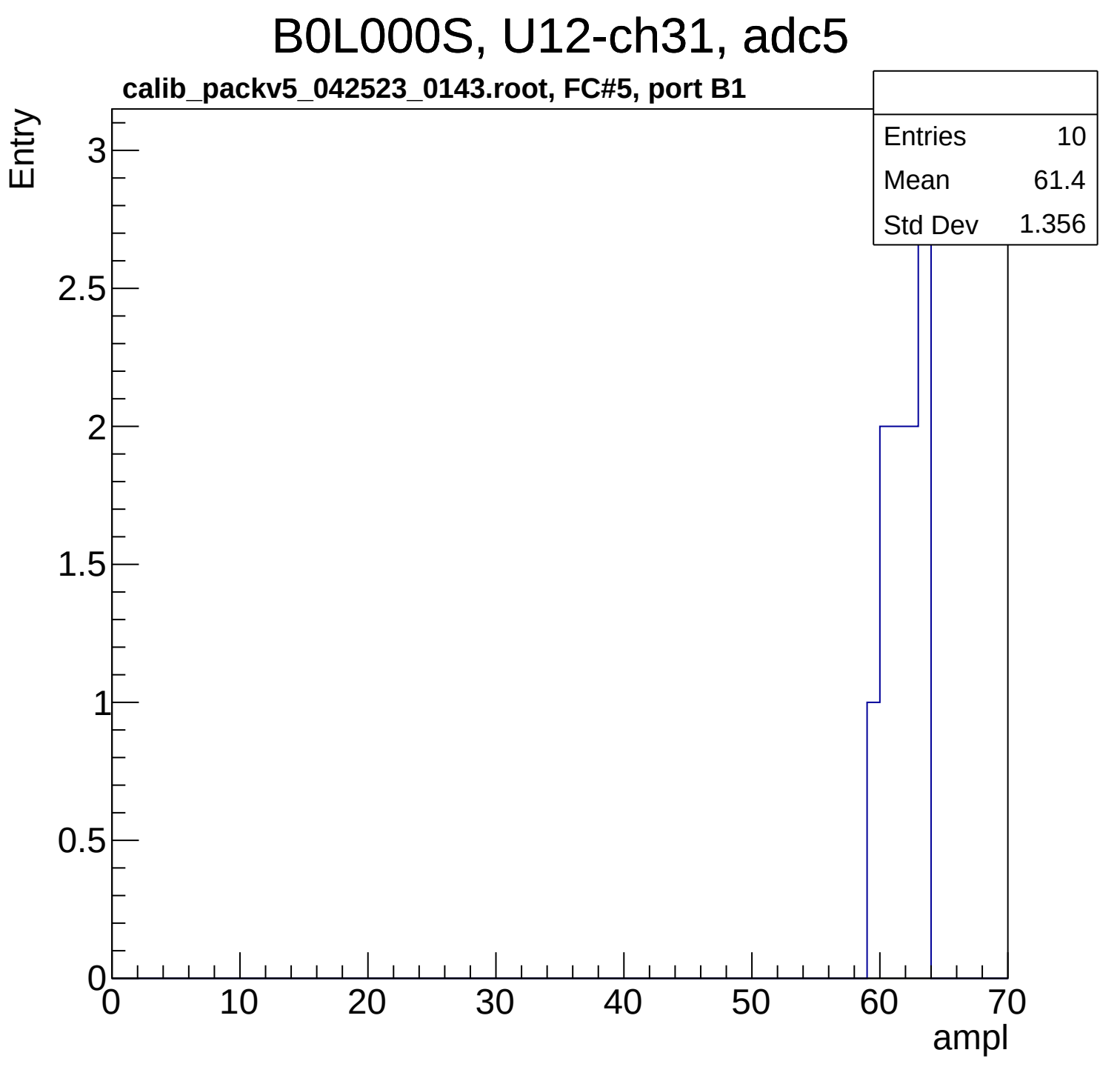

B0L000S, U12-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.29
Std Dev 8.781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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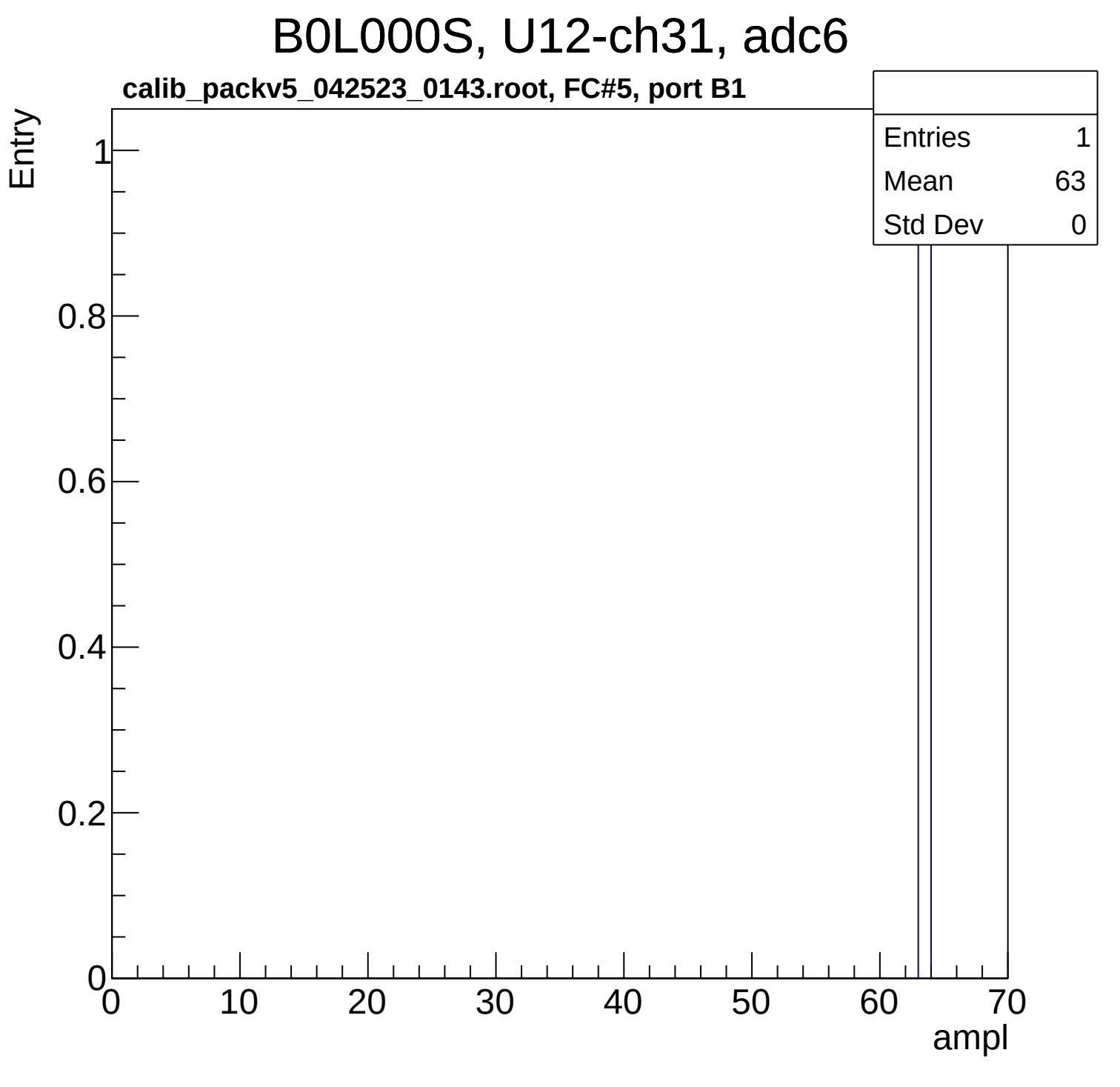
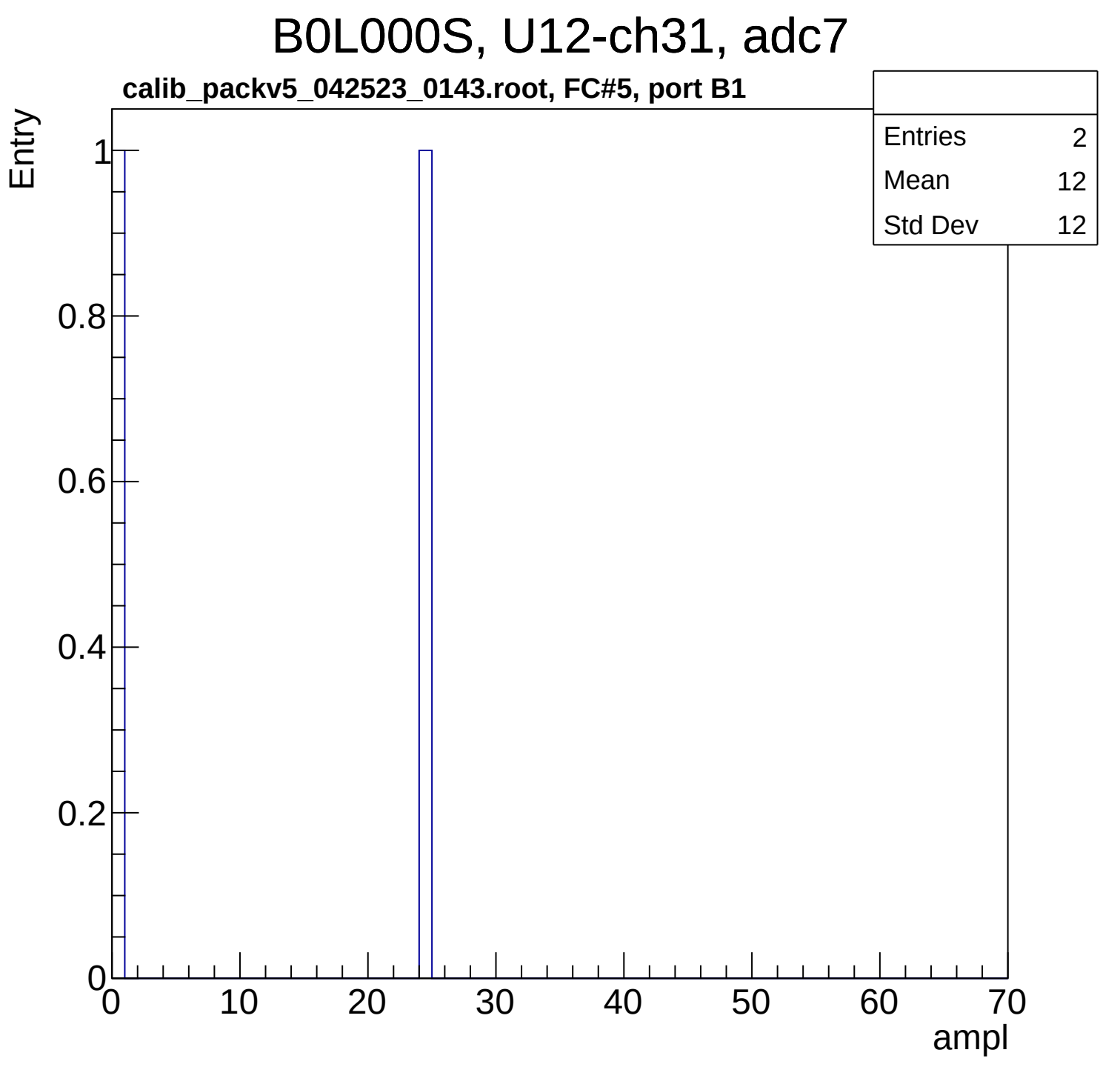

B0L000S, U12-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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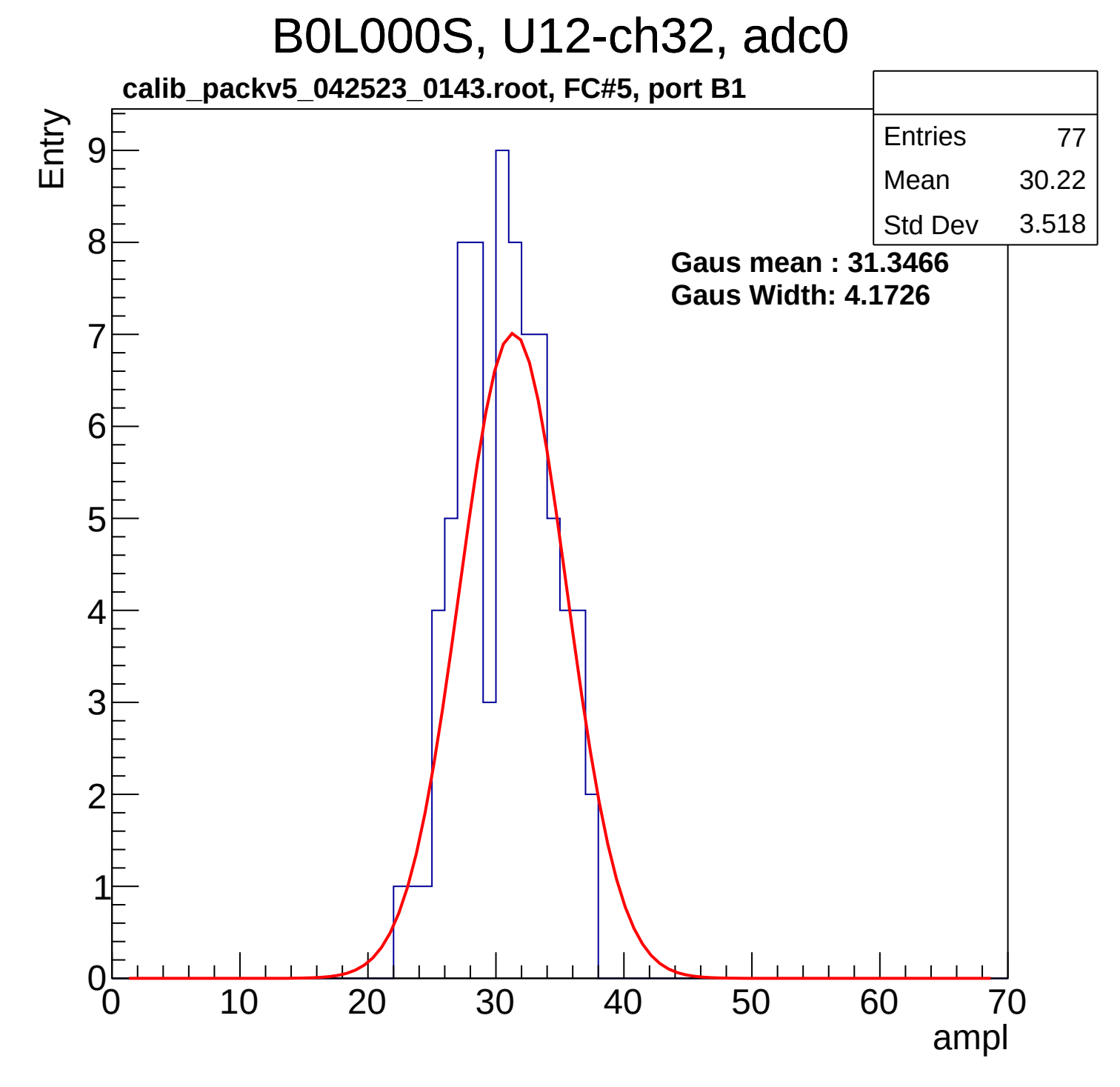
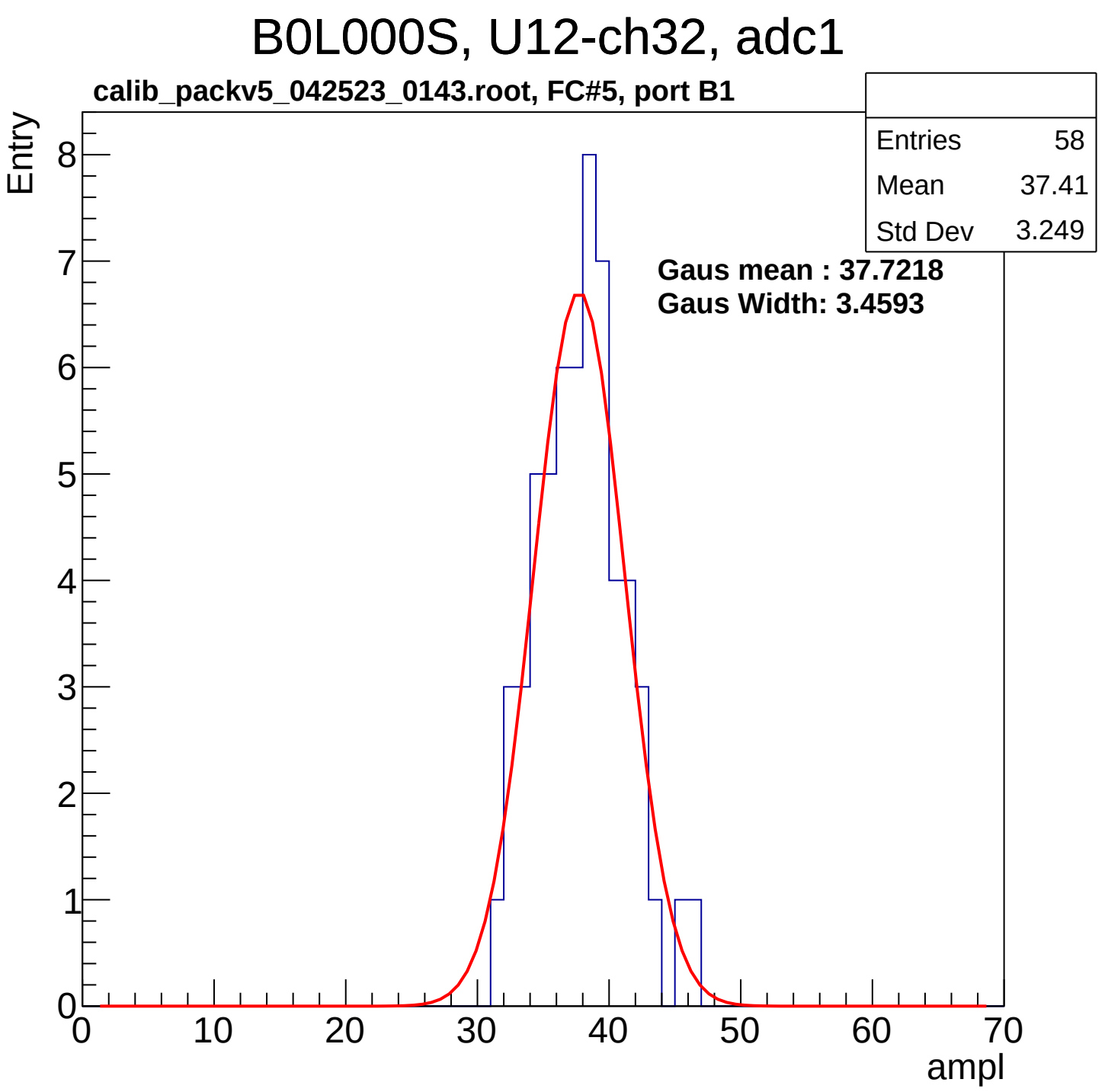
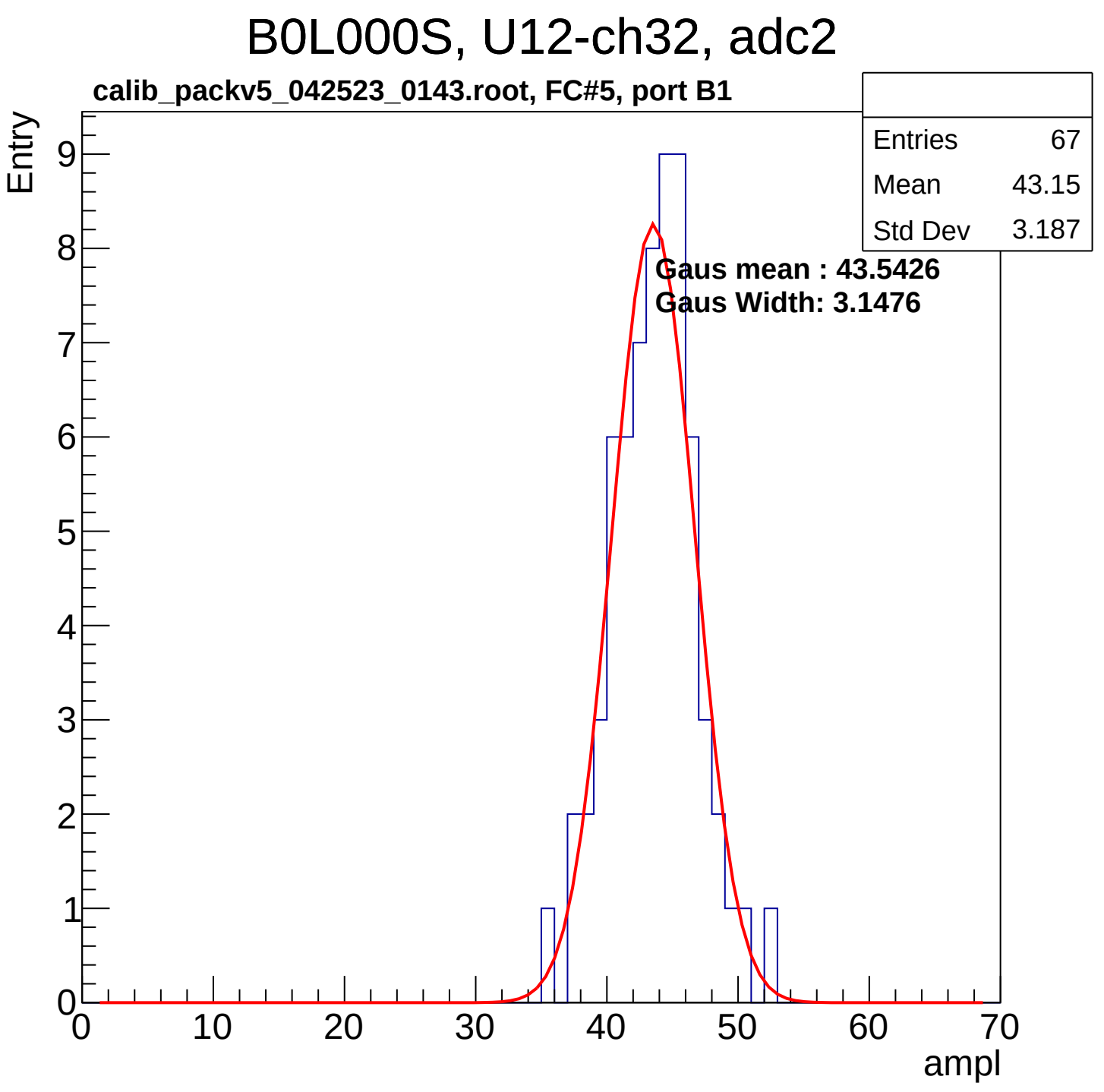
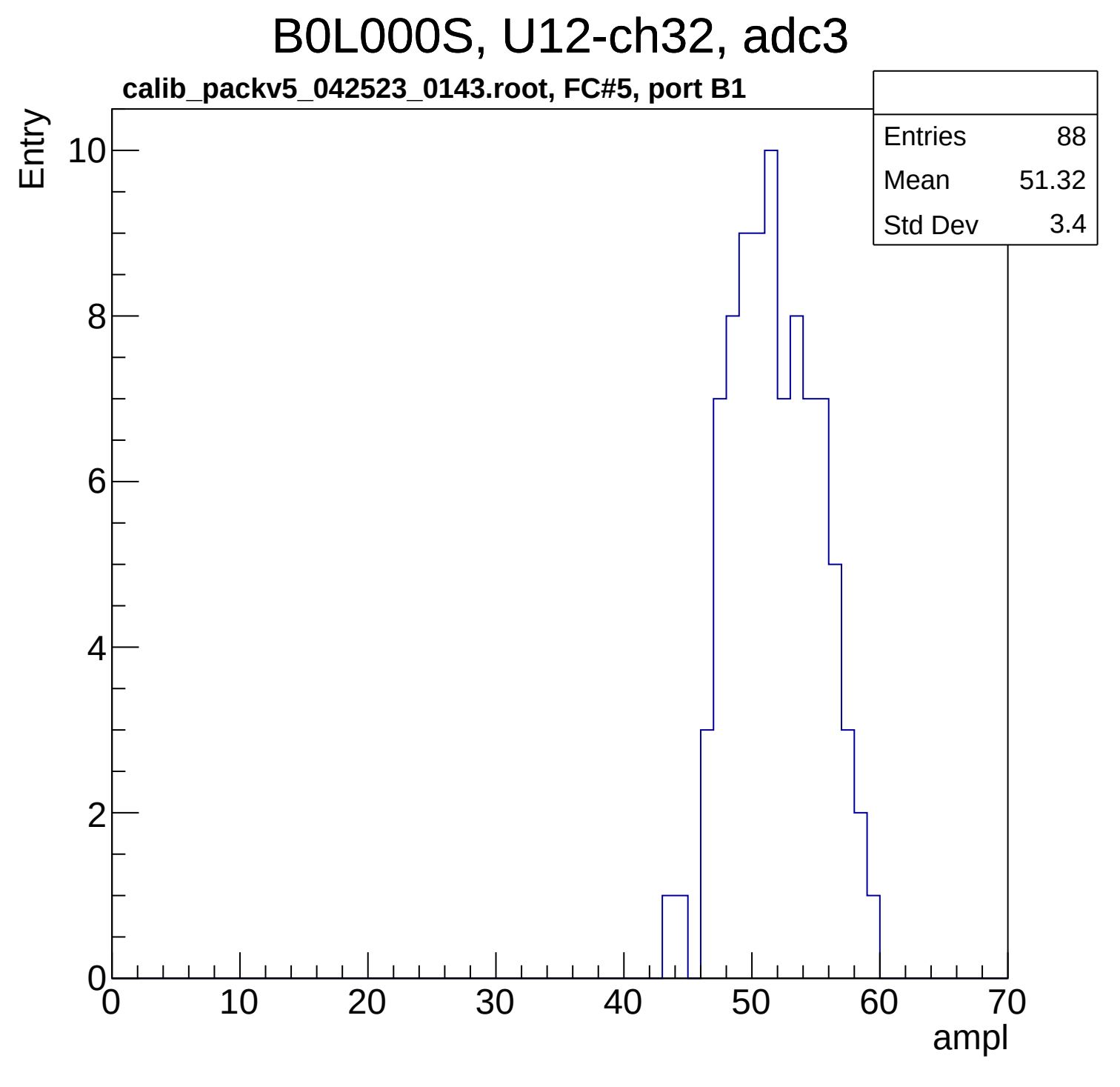
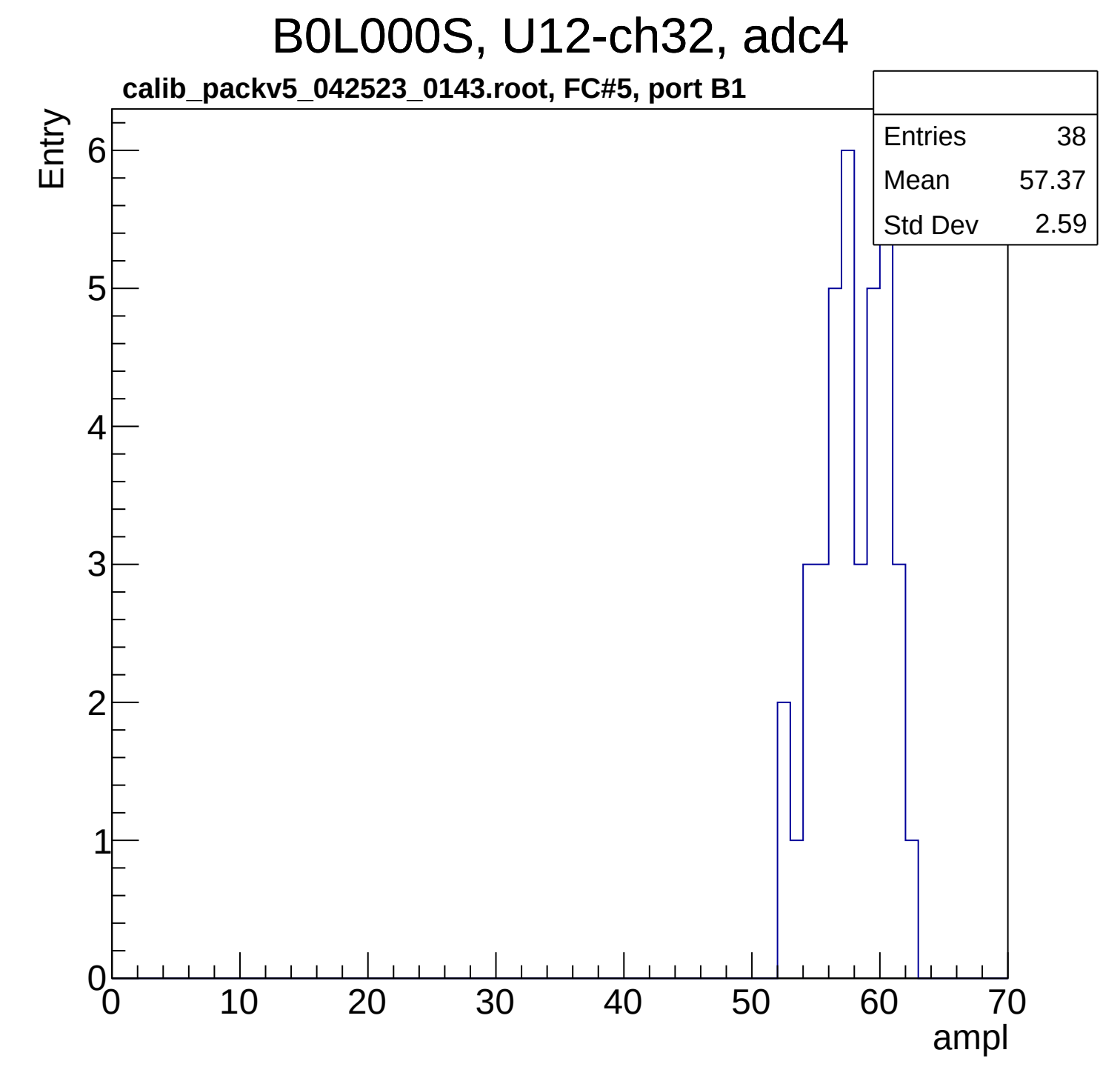
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

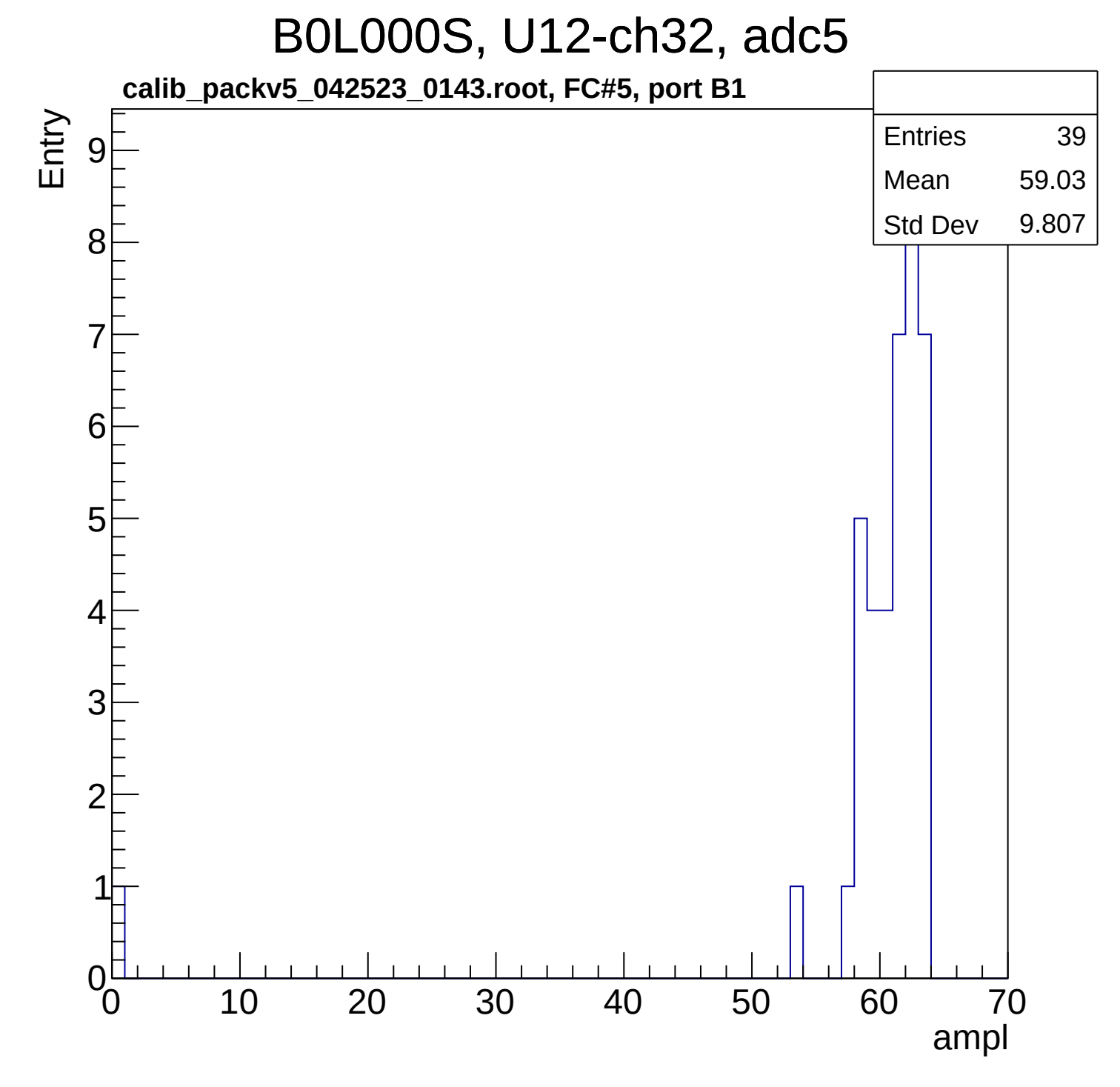

B0L000S, U12-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 30.22
Std Dev 3.518
Gaus mean : 31.3466
Gaus Width: 4.1726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

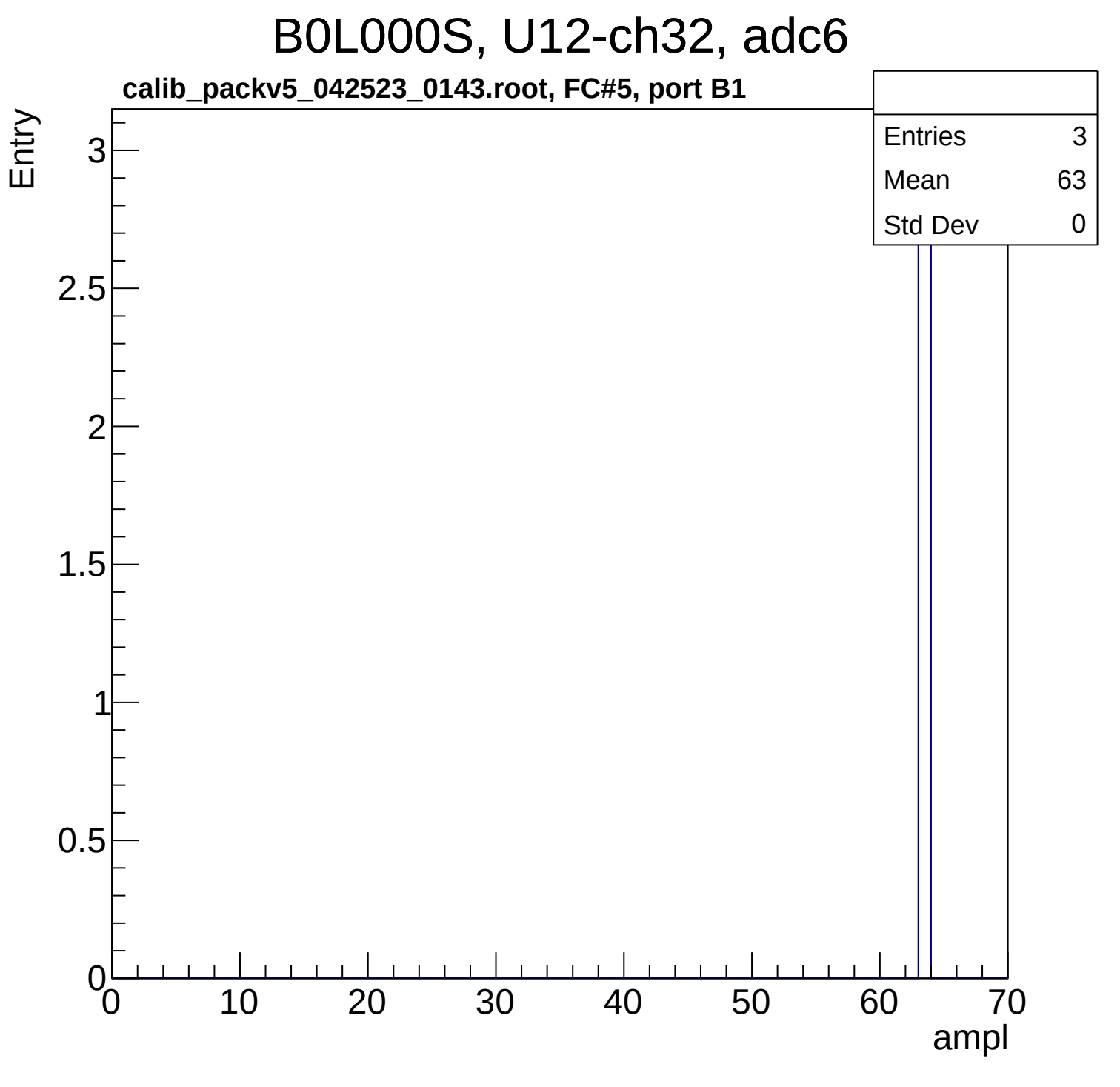

B0L000S, U12-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 37.41
Std Dev 3.249
Gaus mean : 37.7218
Gaus Width: 3.4593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

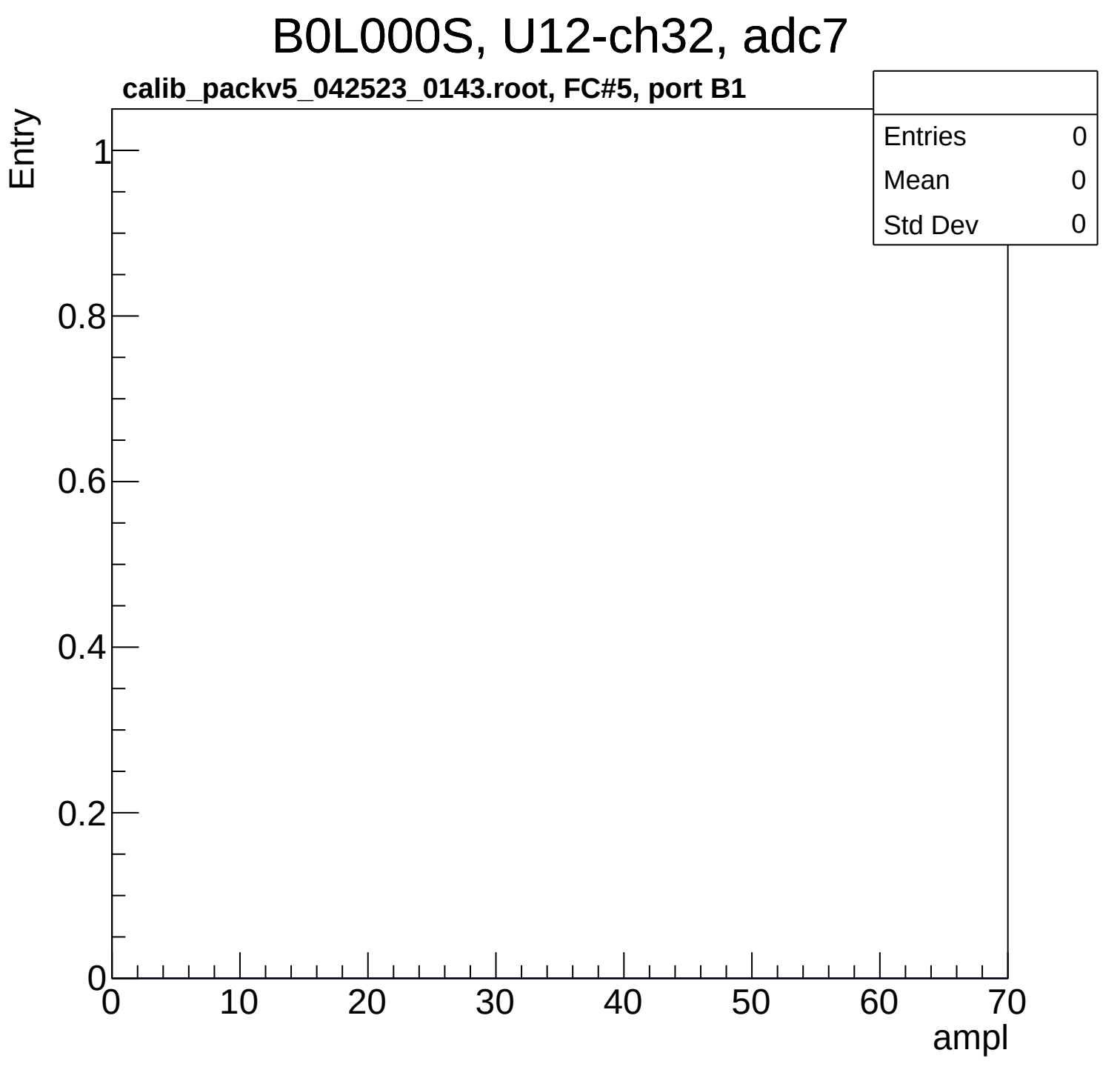

B0L000S, U12-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.15
Std Dev 3.187
Gaus mean : 43.5426
Gaus Width: 3.1476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

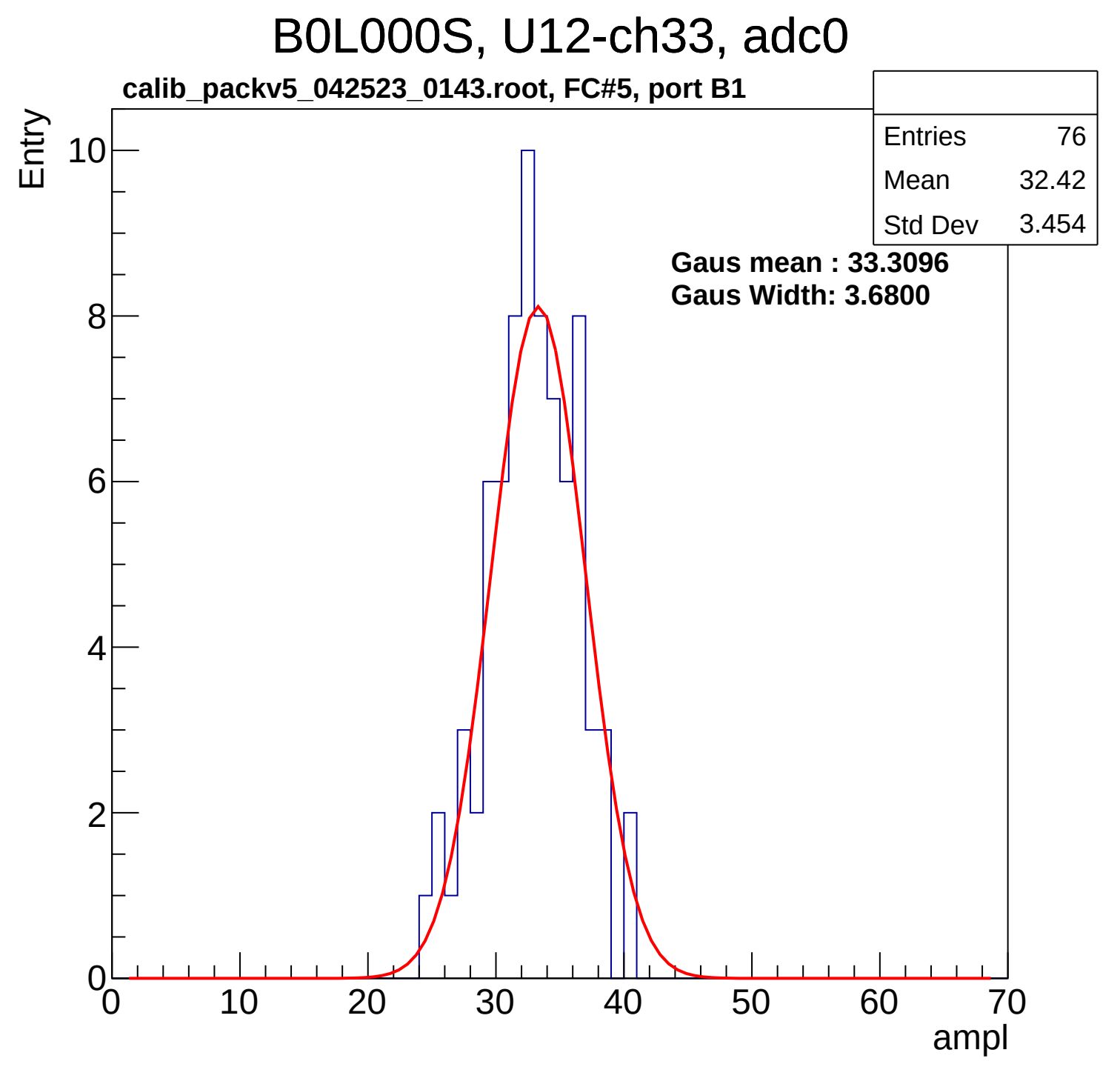

B0L000S, U12-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 51.32
Std Dev 3.4
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

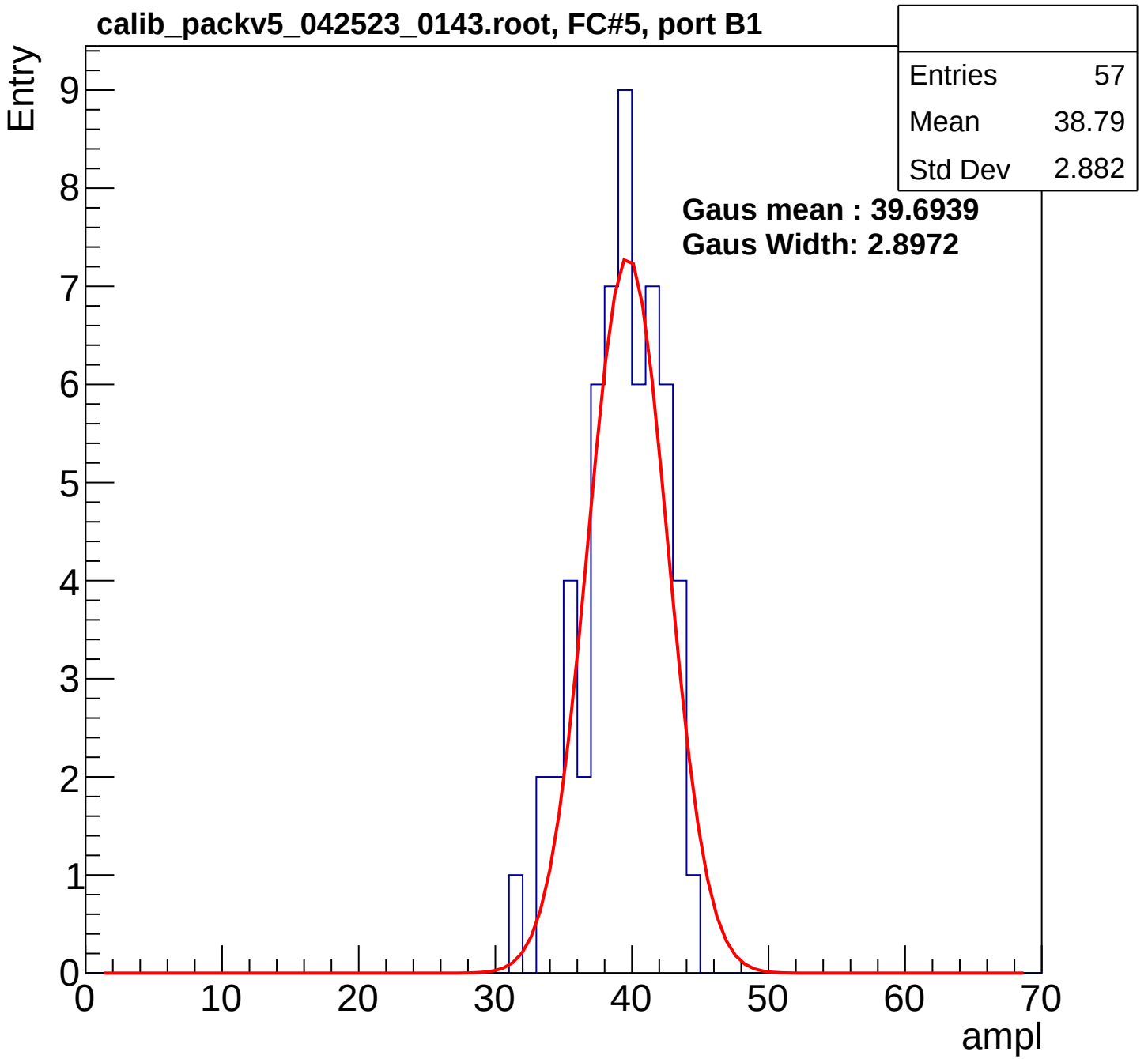

B0L000S, U12-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 57.37
Std Dev 2.59
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59.03
Std Dev 9.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 32.42
Std Dev 3.454
Gaus mean : 33.3096
Gaus Width: 3.6800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 38.79
Std Dev 2.882
Gaus mean : 39.6939
Gaus Width: 2.8972
Entry
ampl
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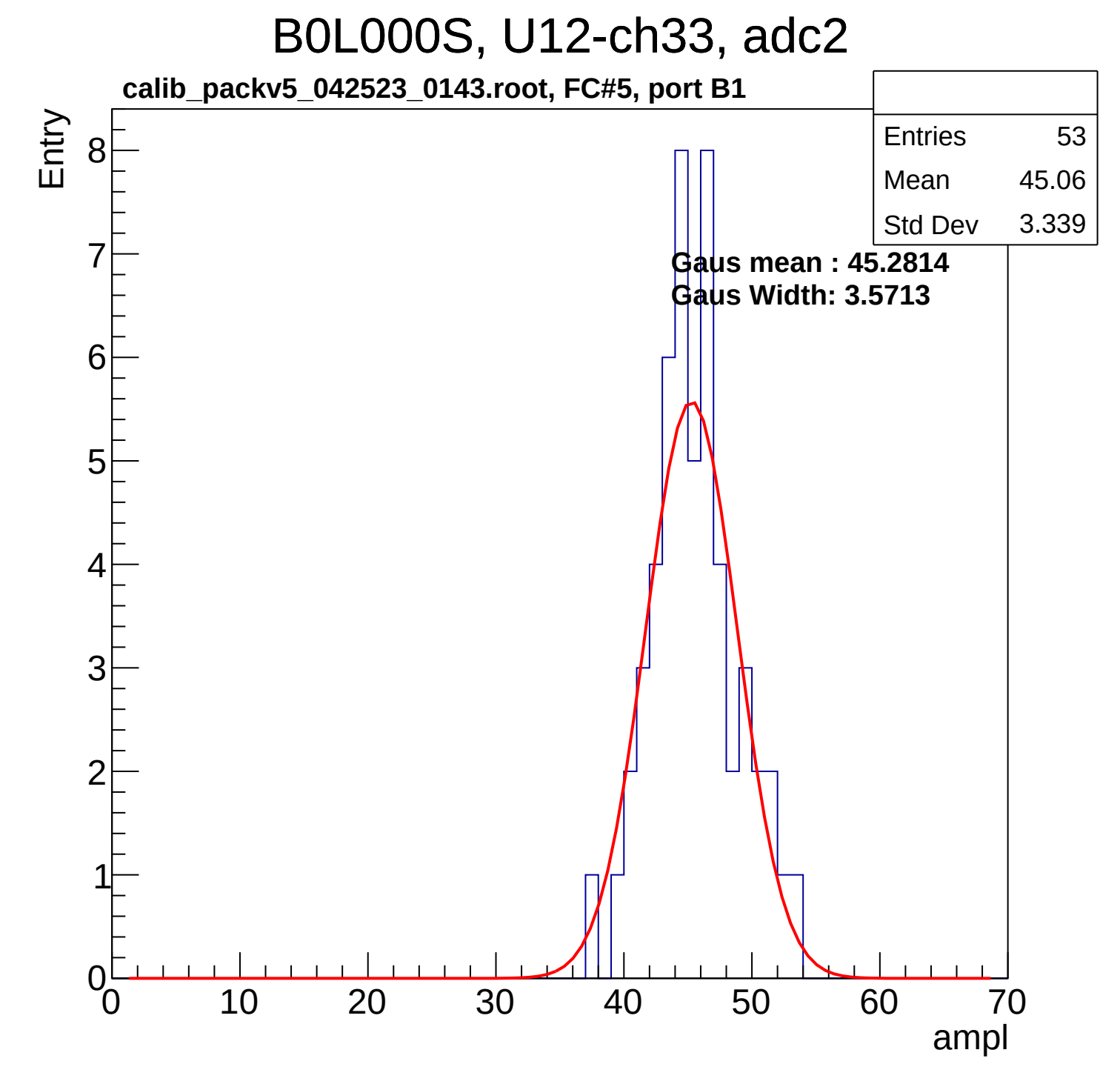
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 45.06
Std Dev 3.339
Gaus mean : 45.2814
Gaus Width: 3.5713
Entry
ampl
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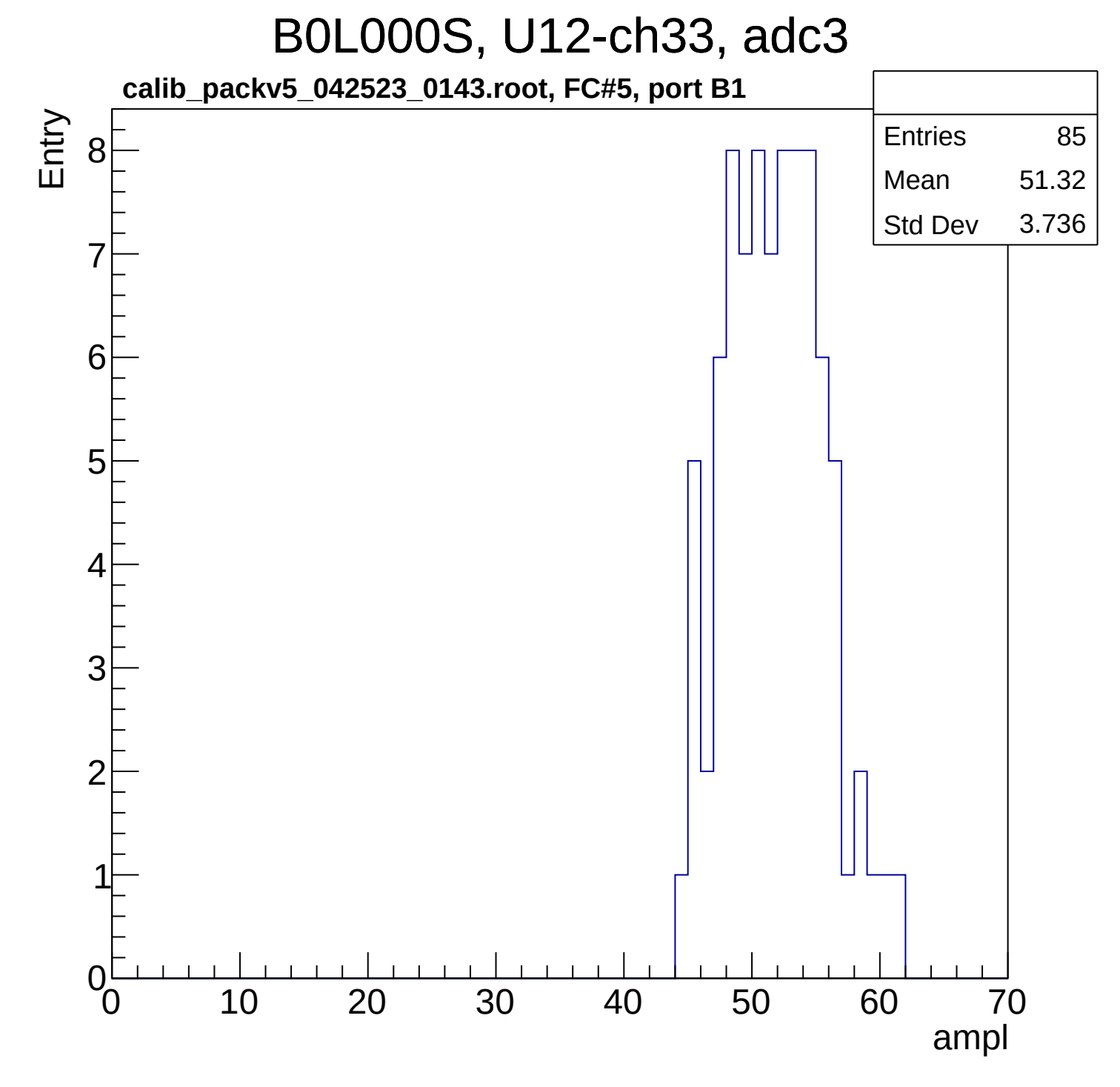
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 51.32
Std Dev 3.736
Entry
ampl
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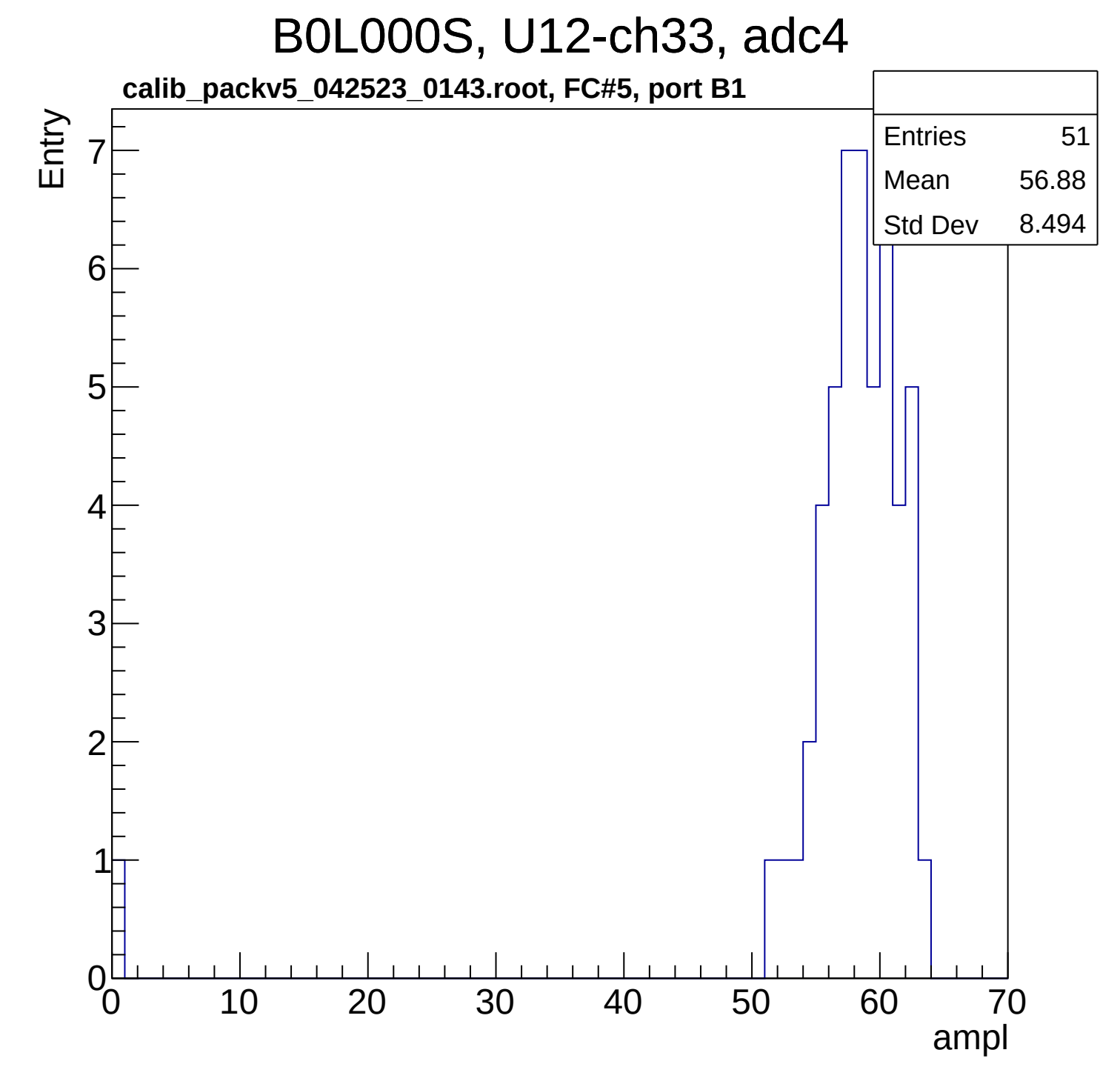
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.88
Std Dev 8.494
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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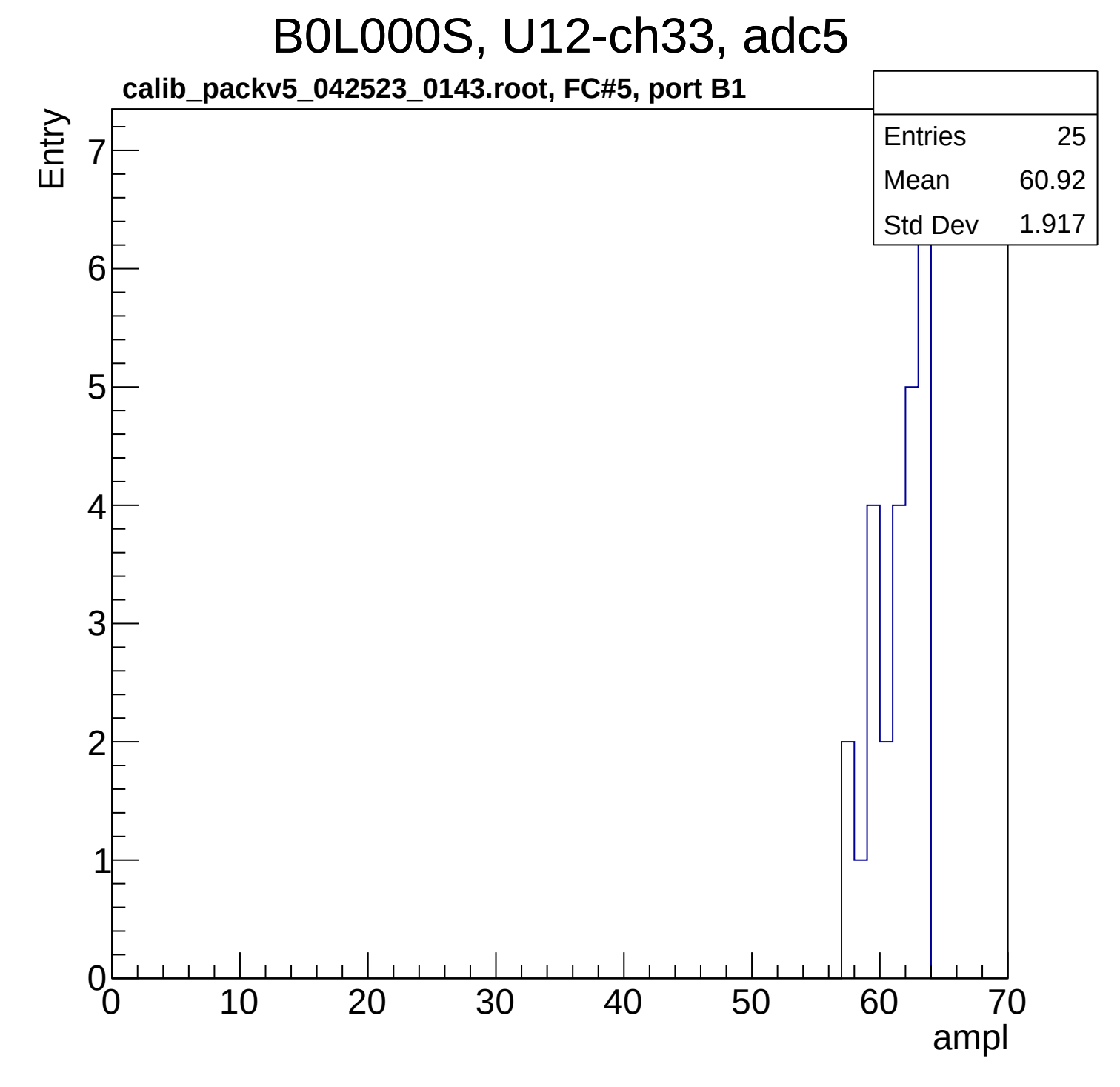
B0L000S, U12-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 60.92
Std Dev 1.917
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
50
60
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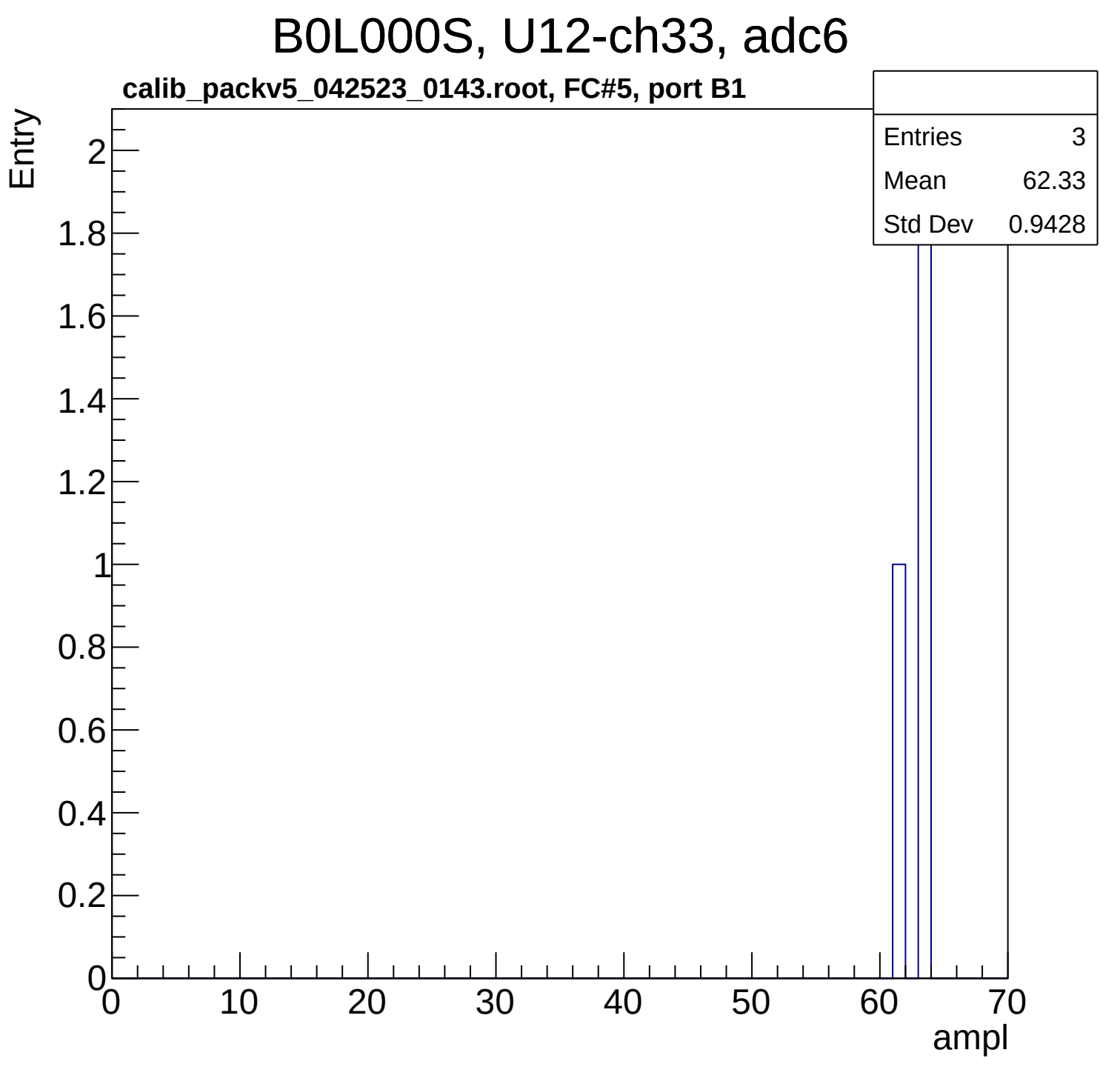
B0L000S, U12-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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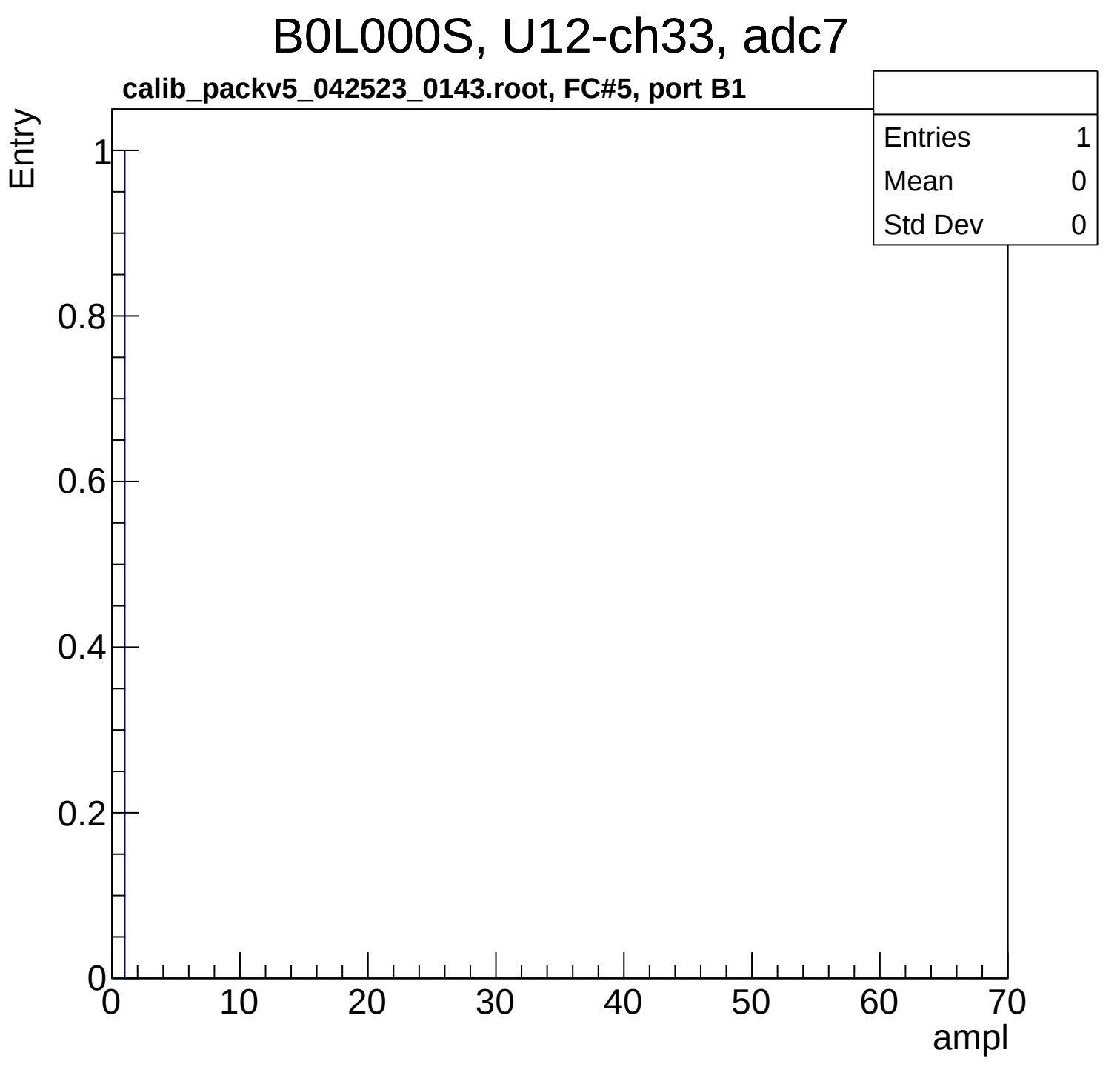
B0L000S, U12-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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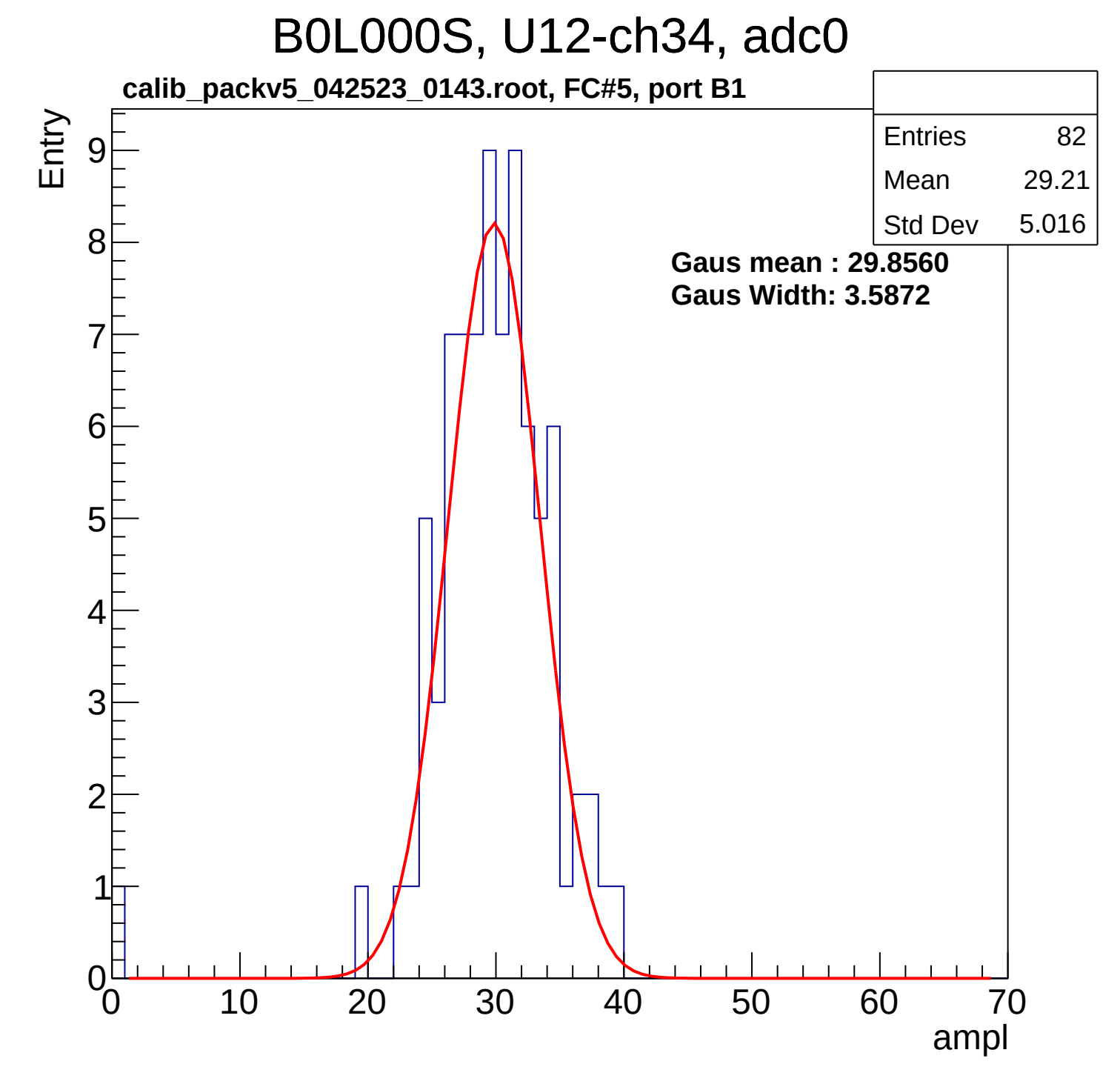
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 29.21
Std Dev 5.016
Gaus mean : 29.8560
Gaus Width: 3.5872
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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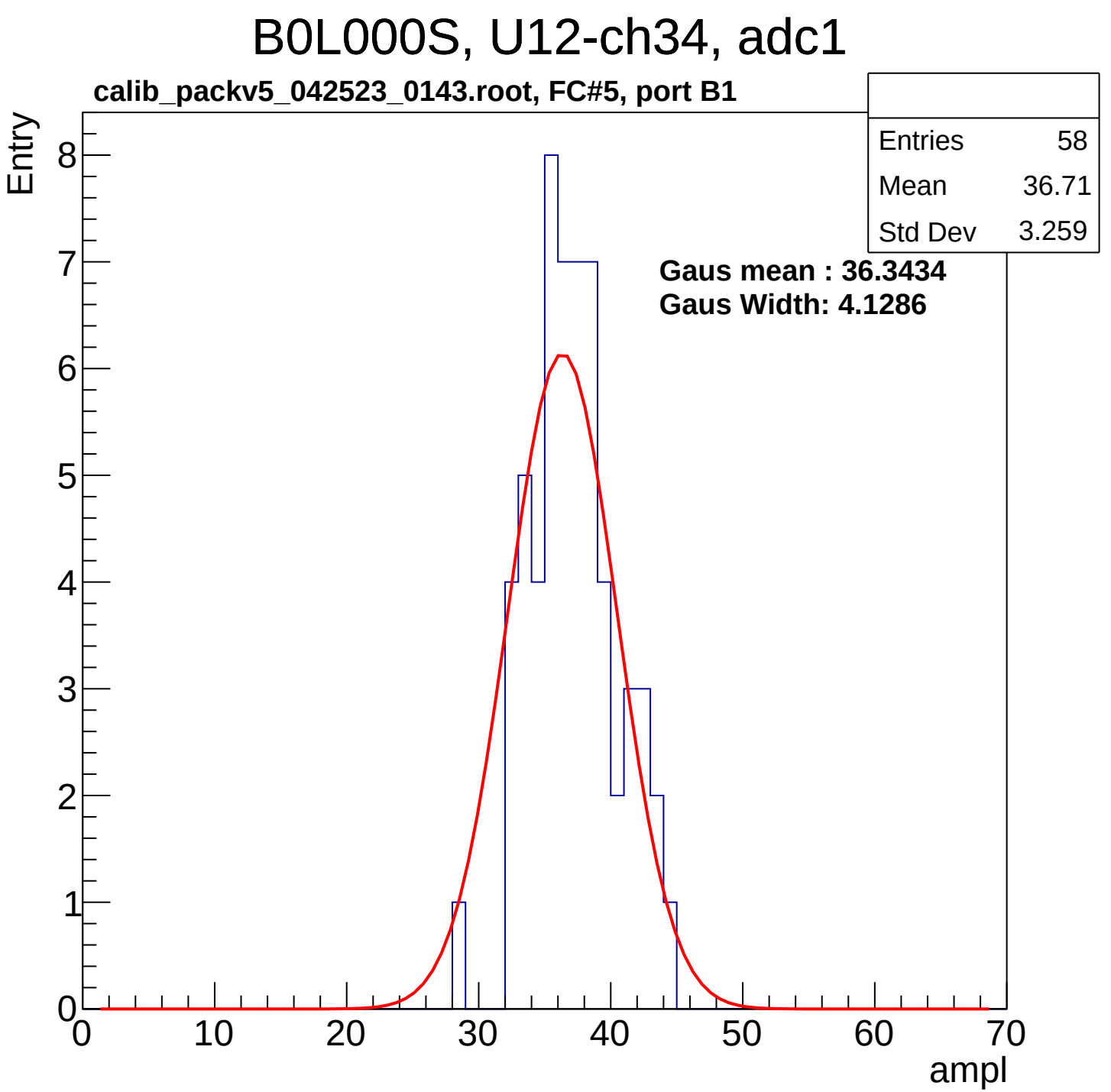
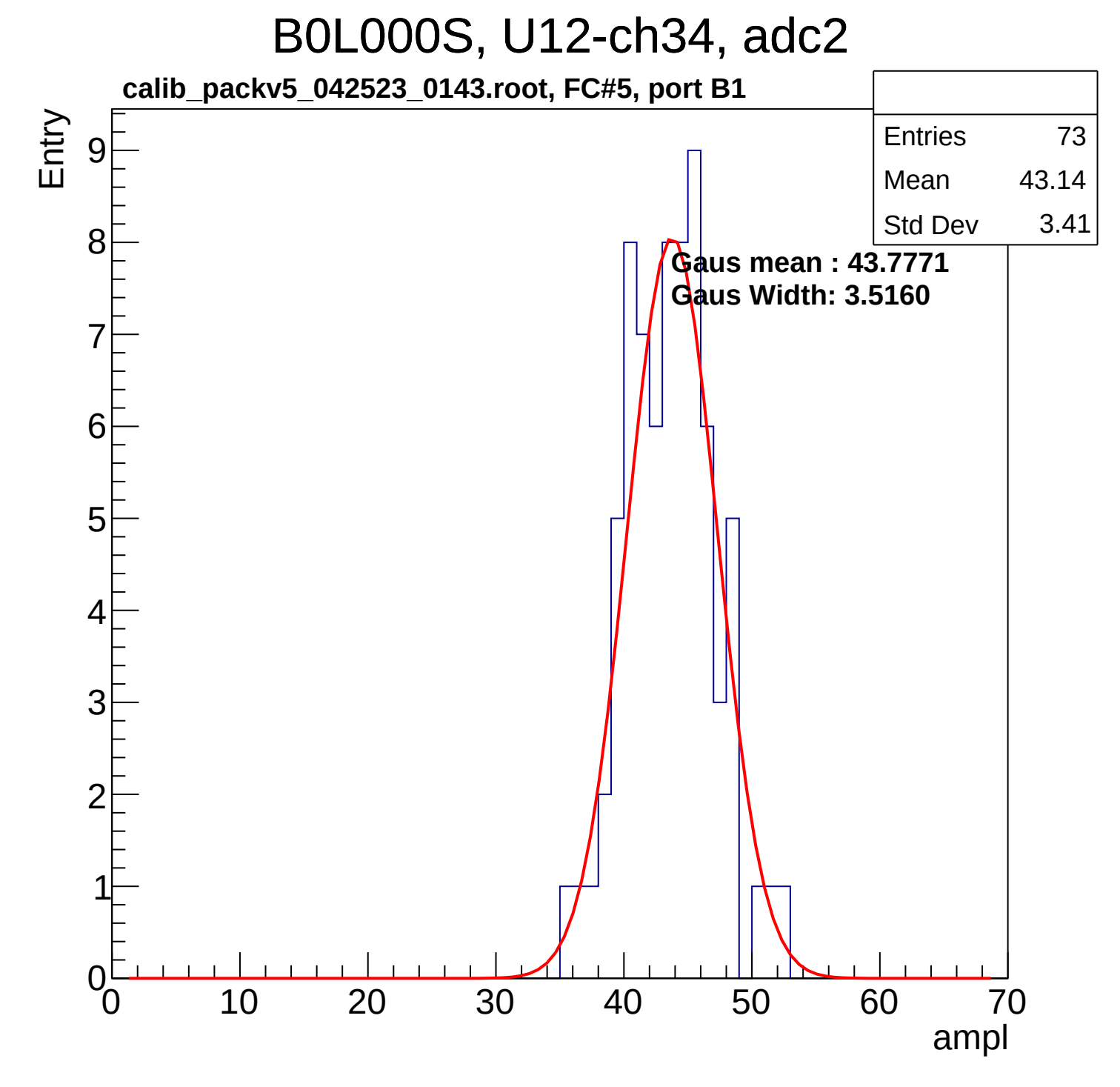

B0L000S, U12-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 36.71
Std Dev 3.259
Gaus mean : 36.3434
Gaus Width: 4.1286
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.14
Std Dev 3.41
Gaus mean : 43.7771
Gaus Width: 3.5160
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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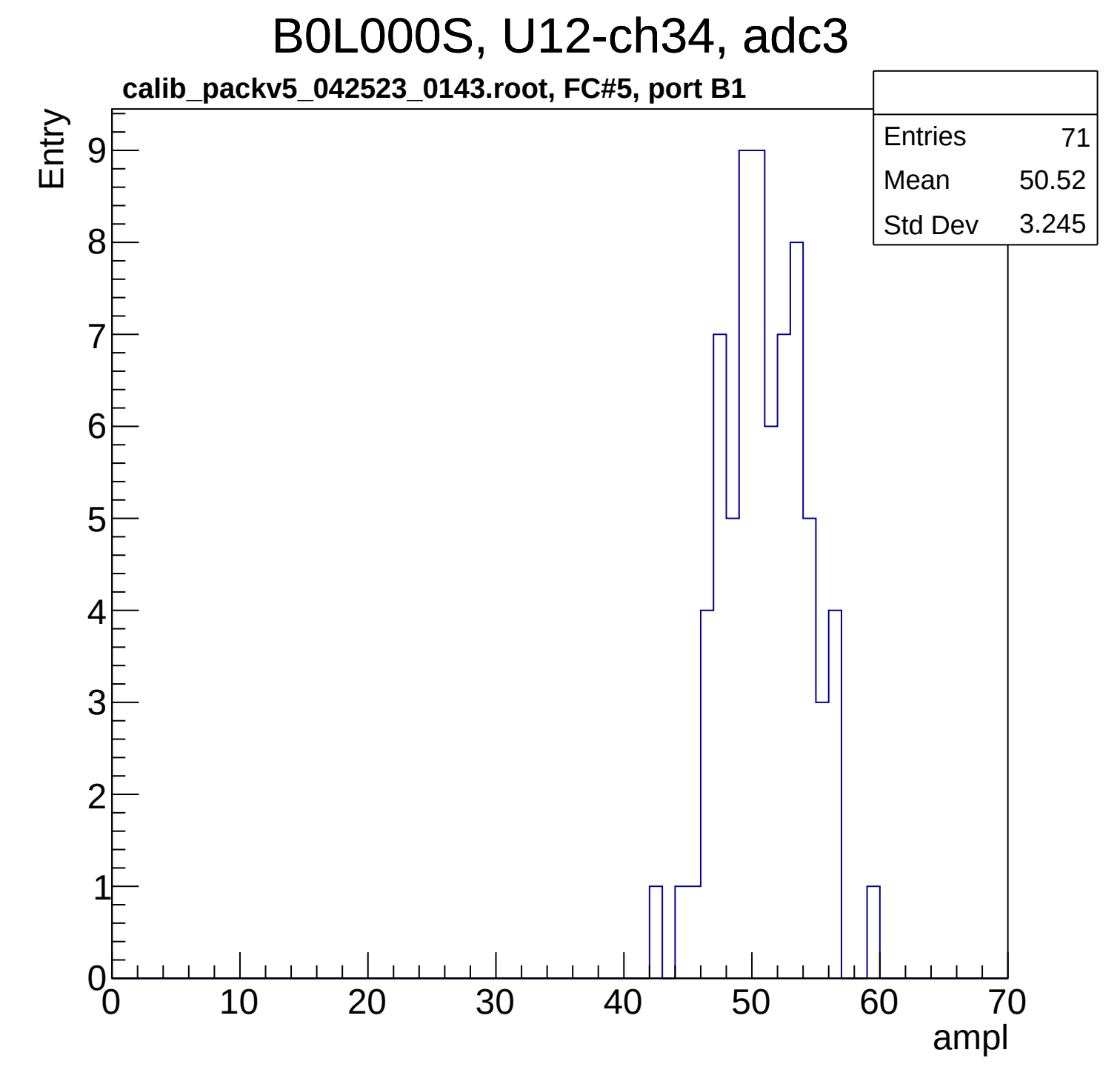
B0L000S, U12-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.52
Std Dev 3.245
Entry
ampl
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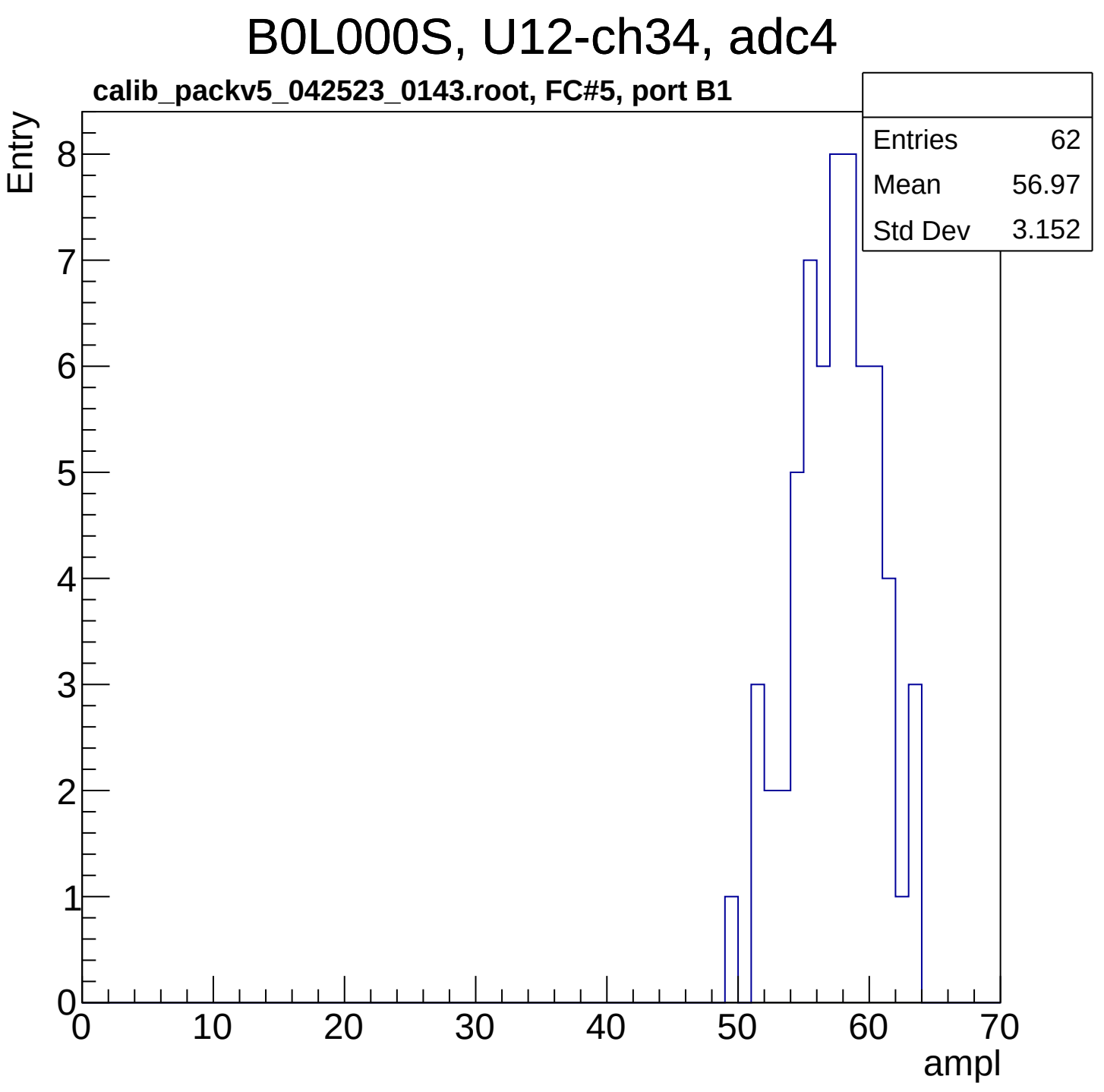
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.97
Std Dev 3.152
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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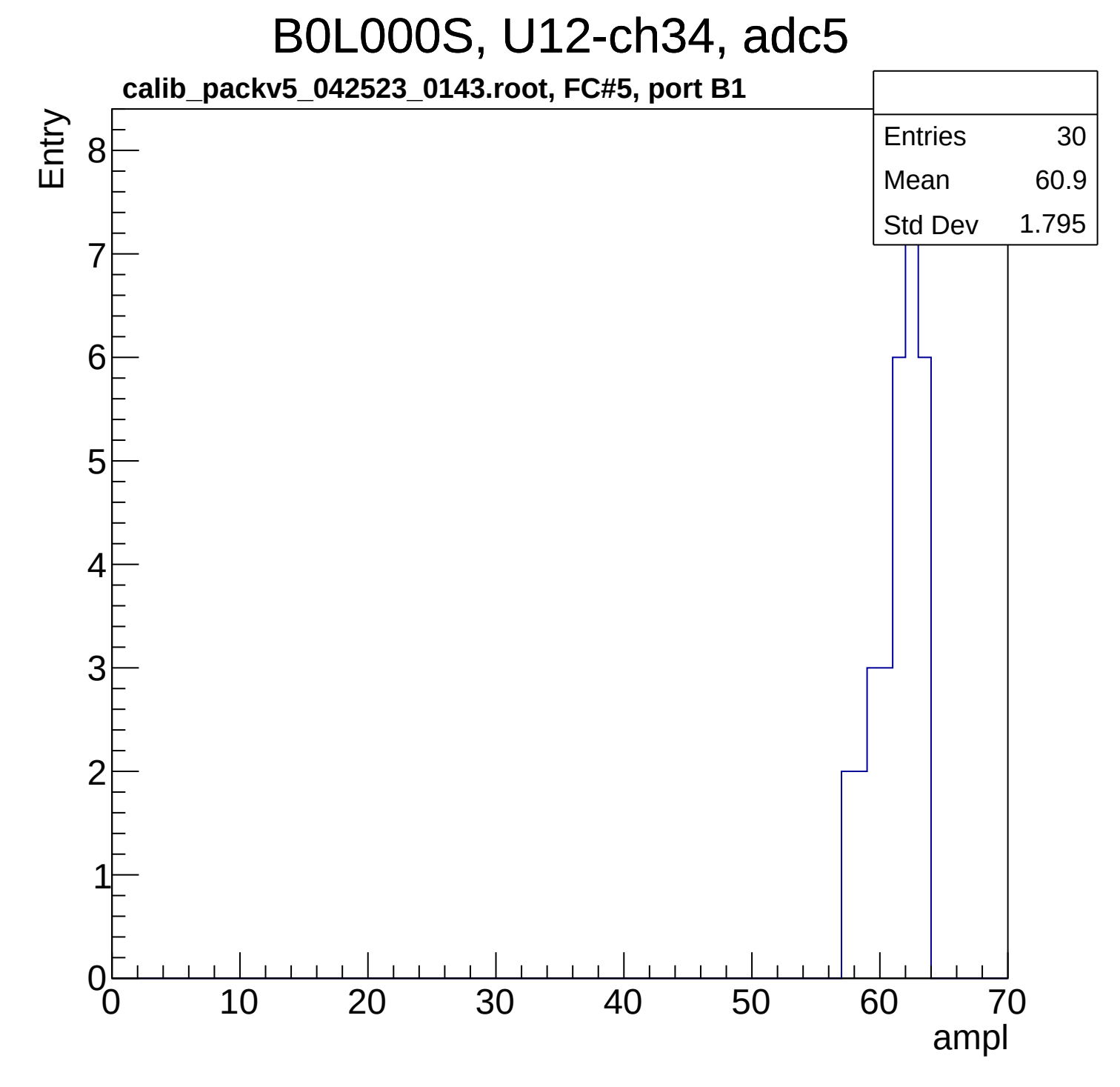
B0L000S, U12-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.9
Std Dev 1.795
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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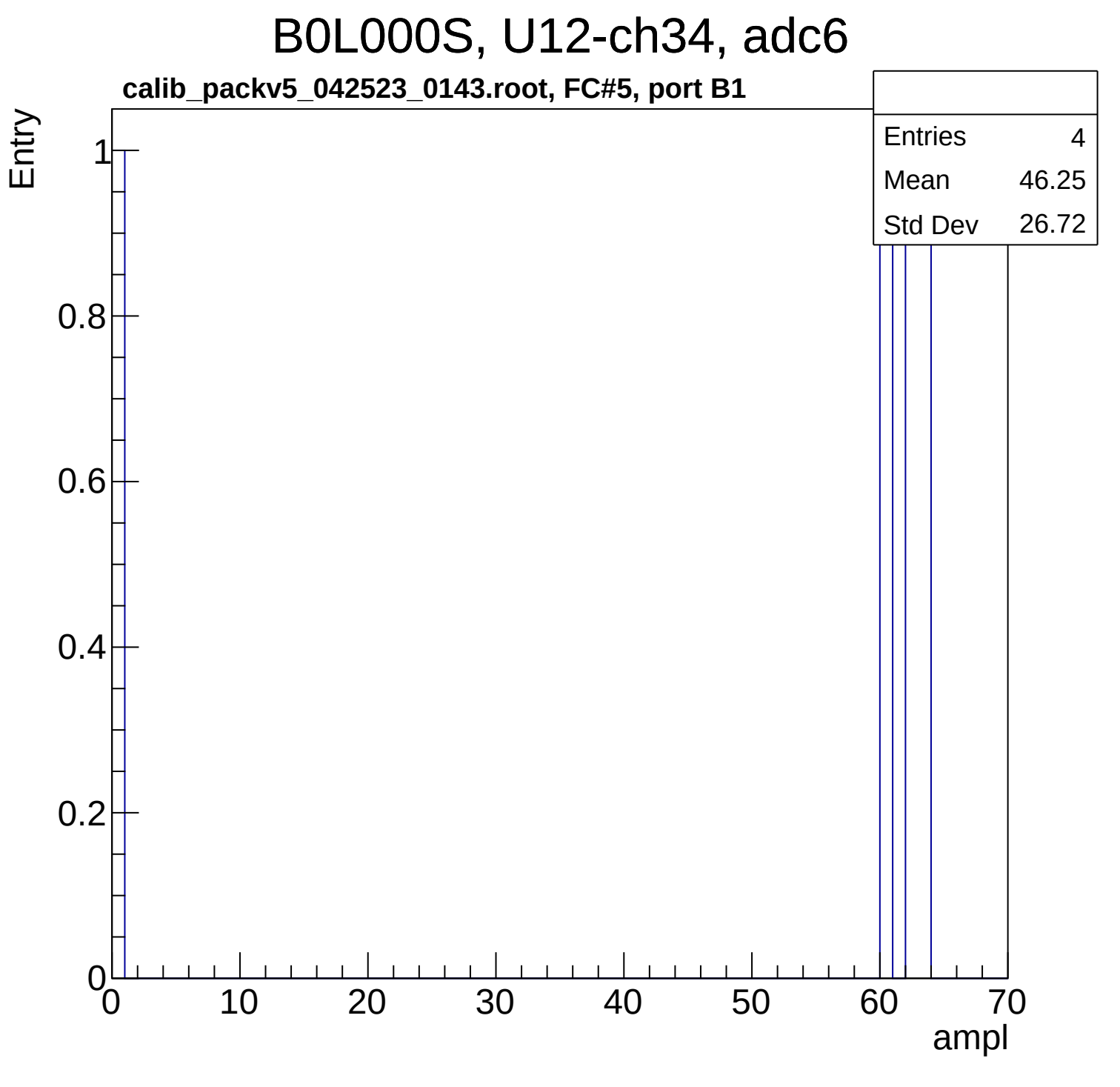
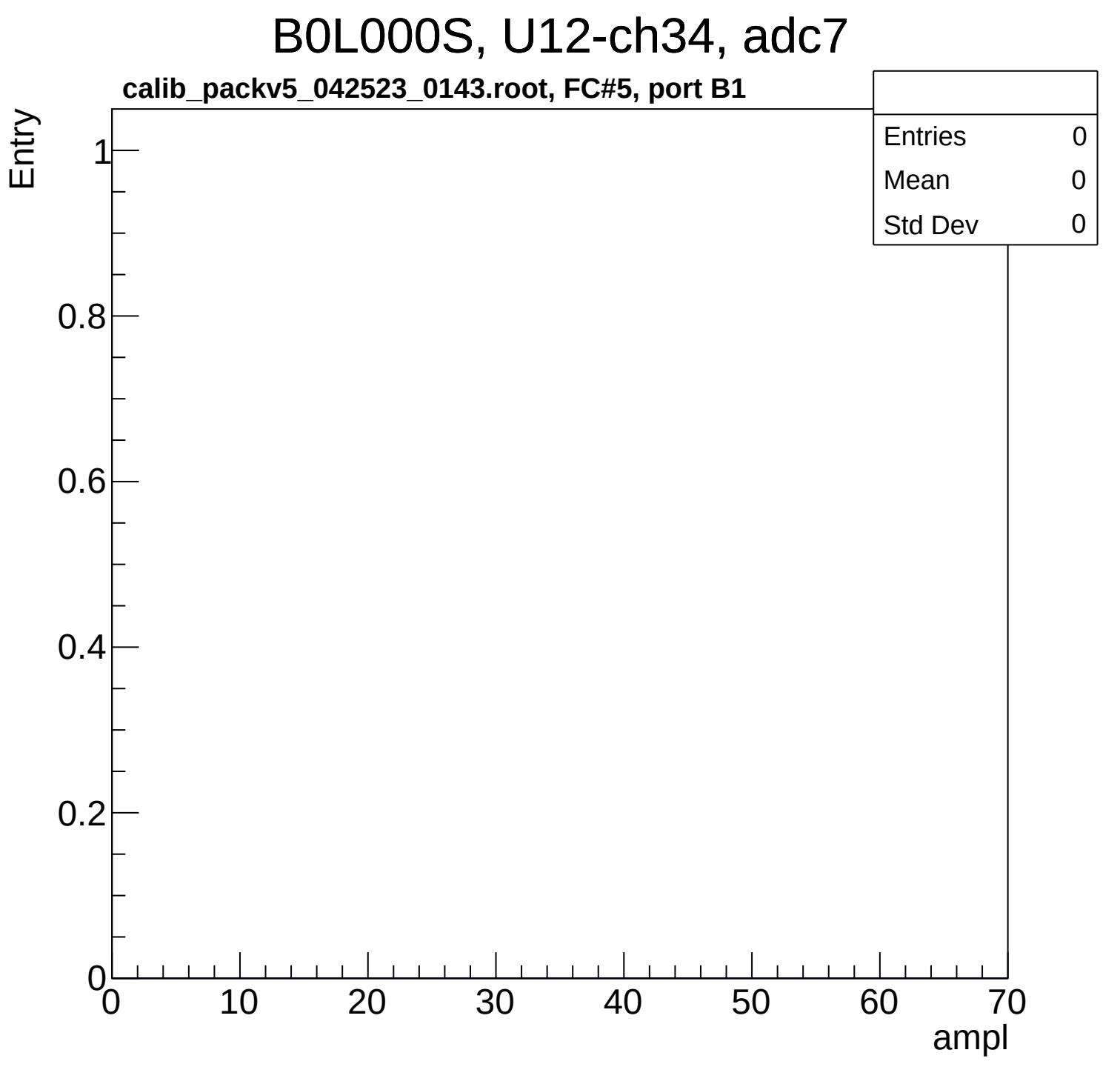

B0L000S, U12-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.25
Std Dev 26.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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10
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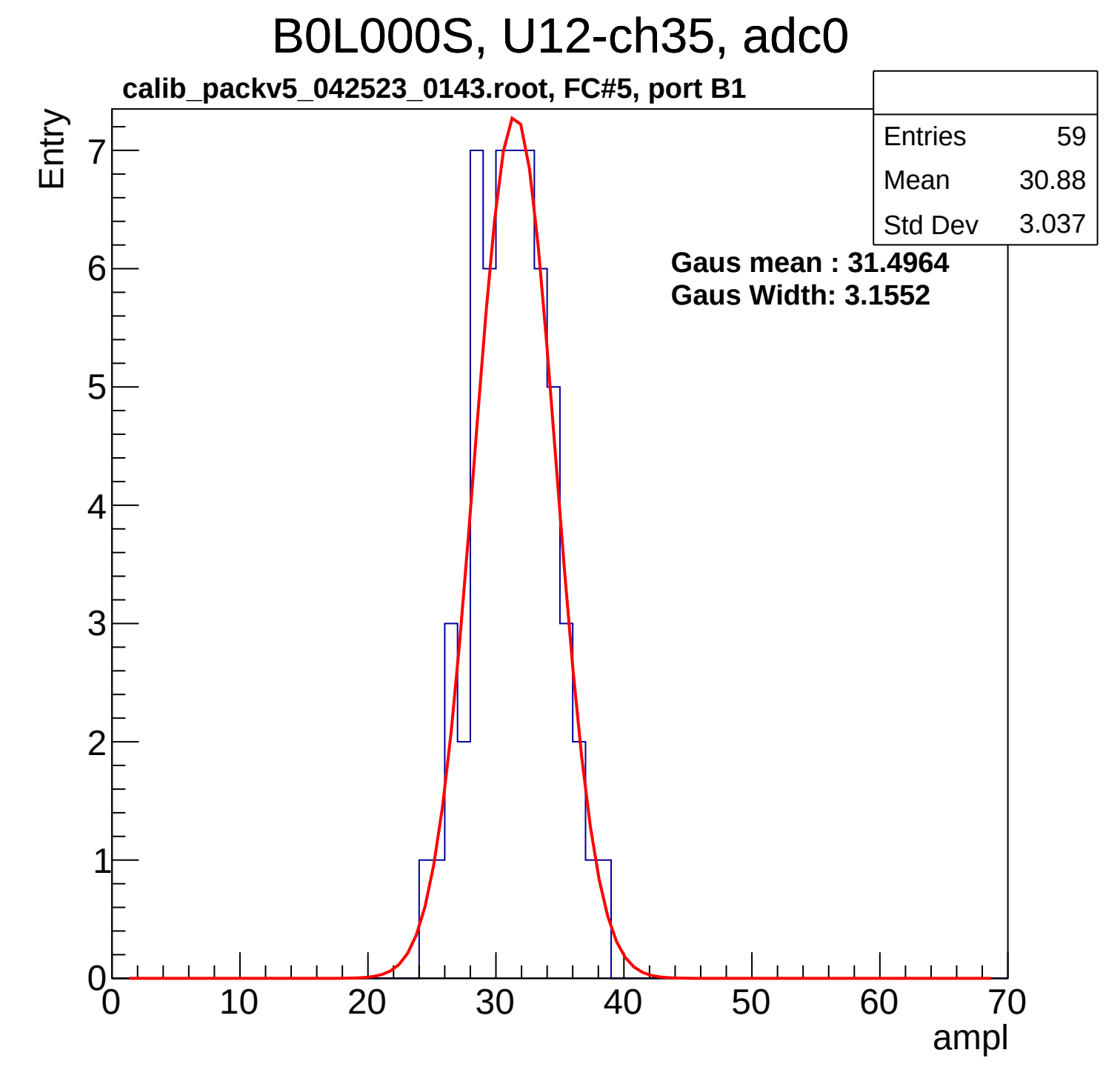
50
60
70
ampl


B0L000S, U12-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 30.88
Std Dev 3.037
Gaus mean : 31.4964
Gaus Width: 3.1552
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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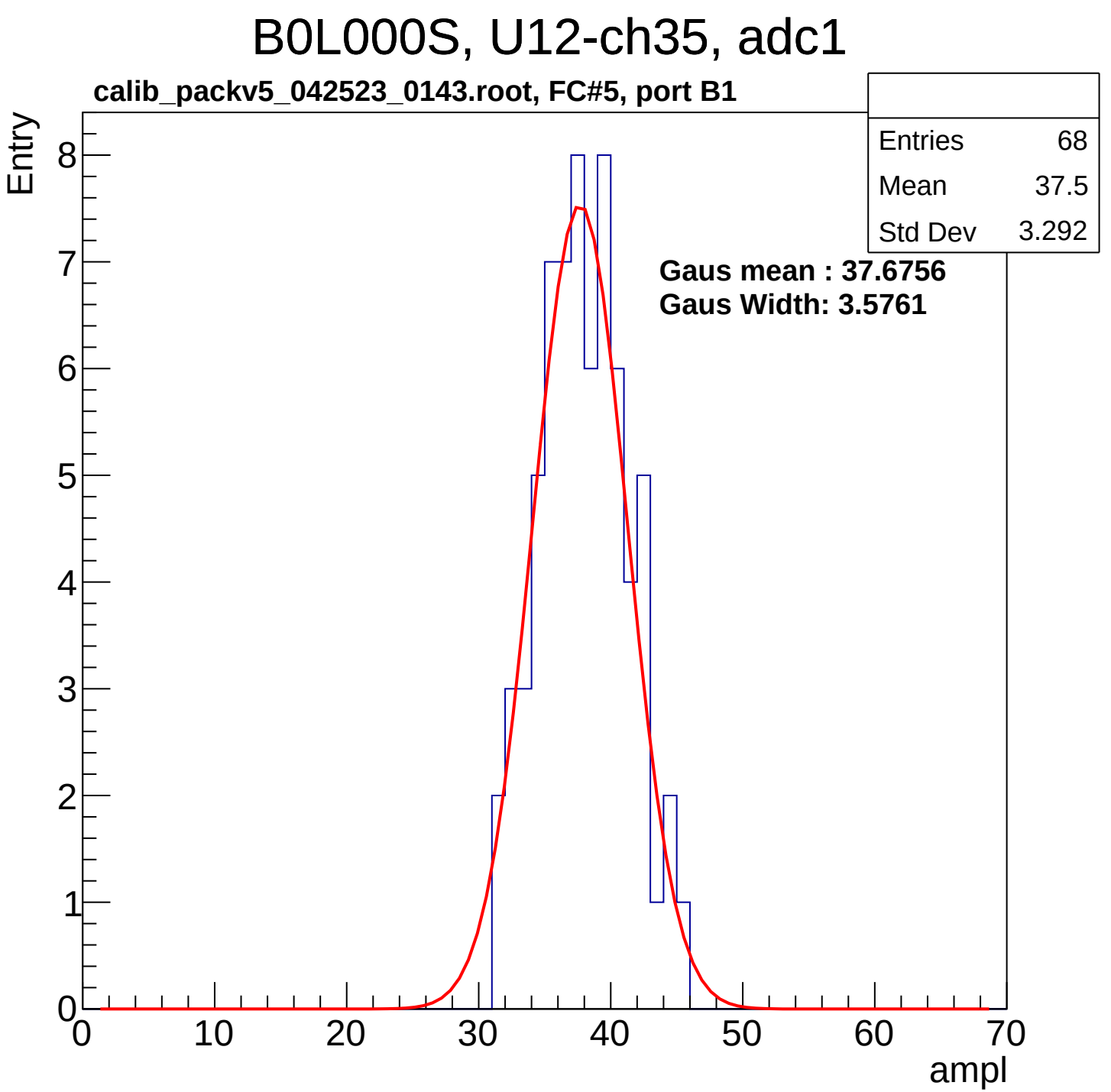
B0L000S, U12-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 37.5
Std Dev 3.292
Gaus mean : 37.6756
Gaus Width: 3.5761
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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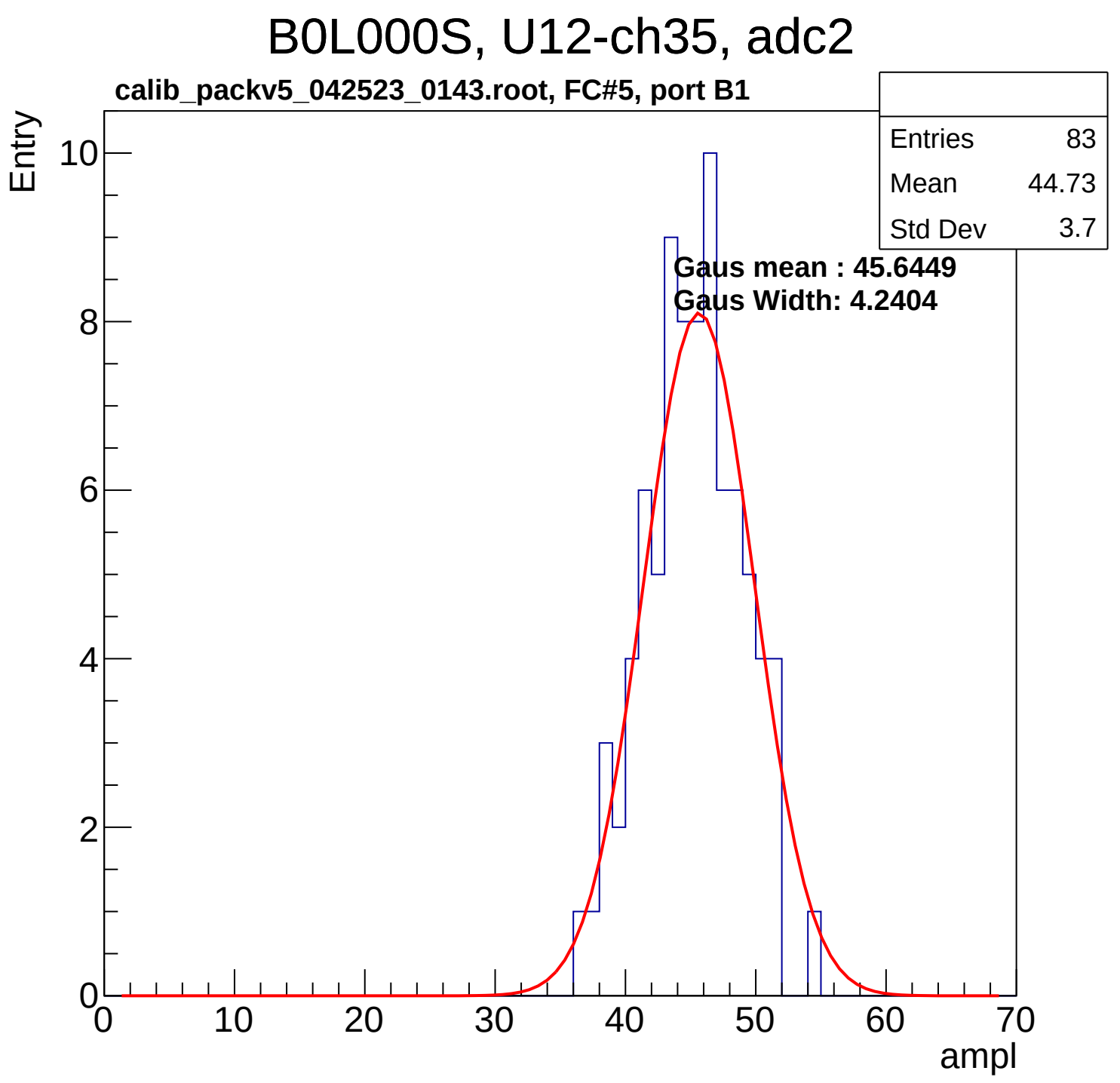
B0L000S, U12-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 44.73
Std Dev 3.7
Gaus mean : 45.6449
Gaus Width: 4.2404
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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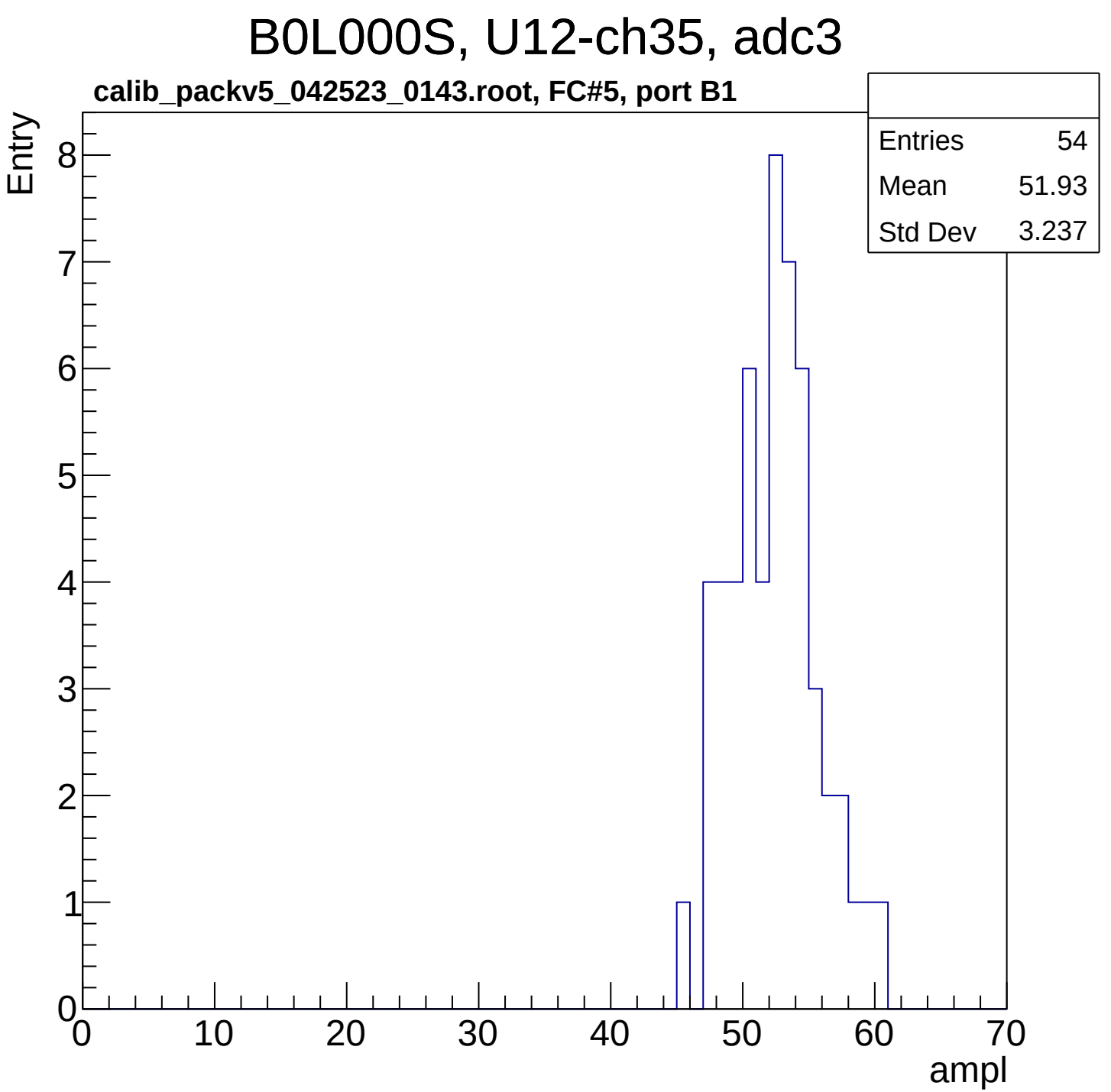
B0L000S, U12-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 51.93
Std Dev 3.237
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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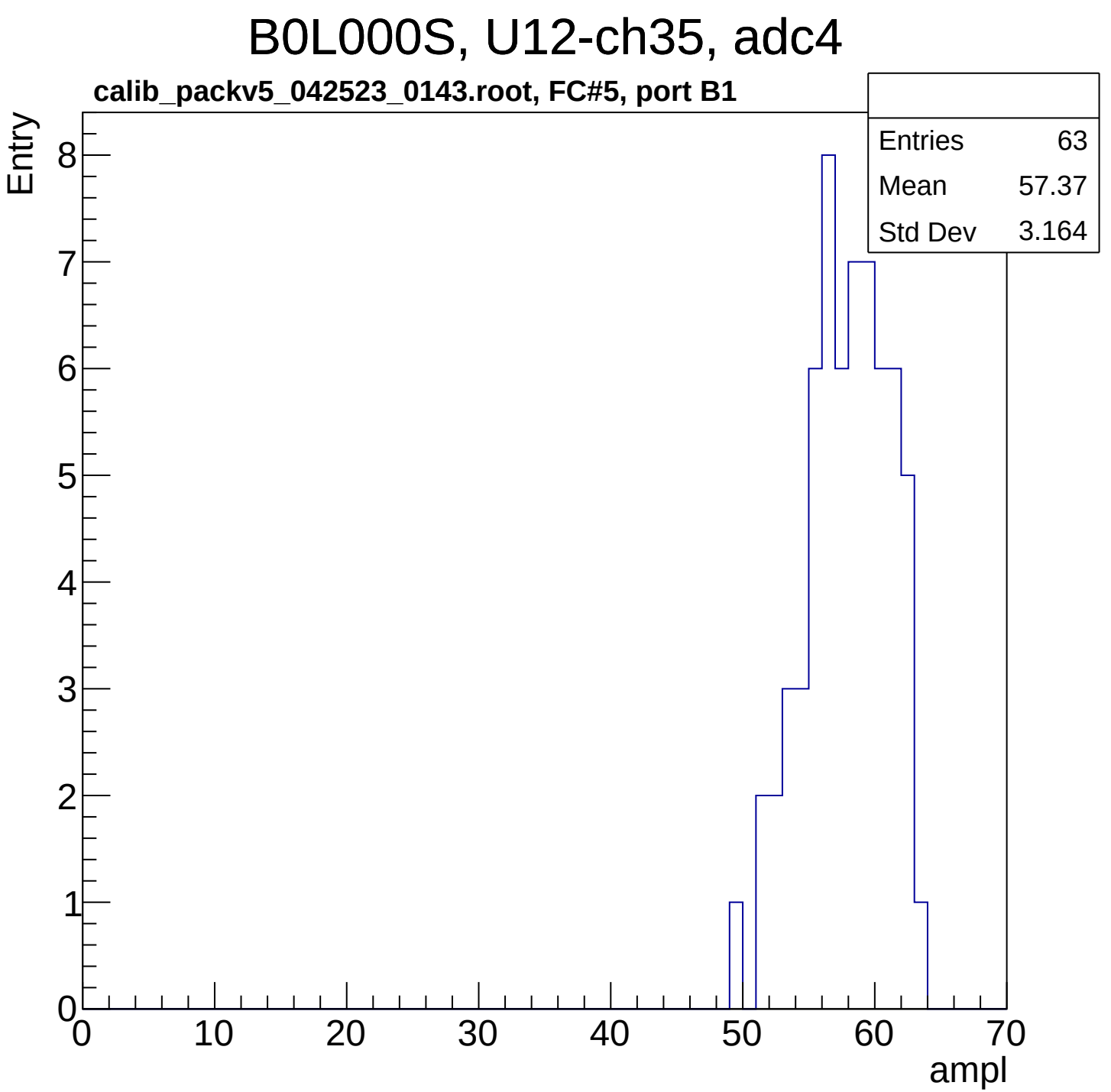
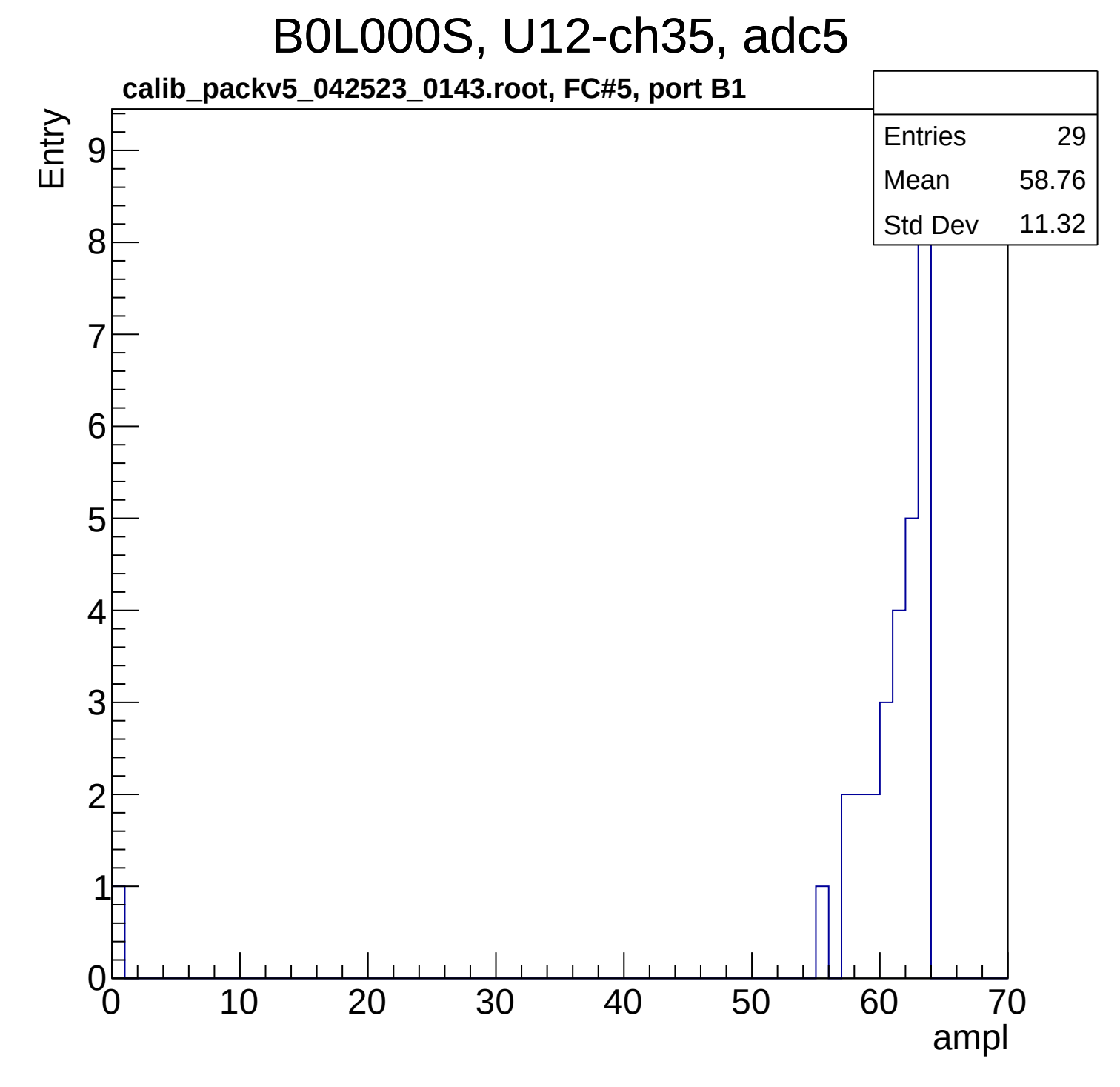

B0L000S, U12-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 57.37
Std Dev 3.164
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58.76
Std Dev 11.32
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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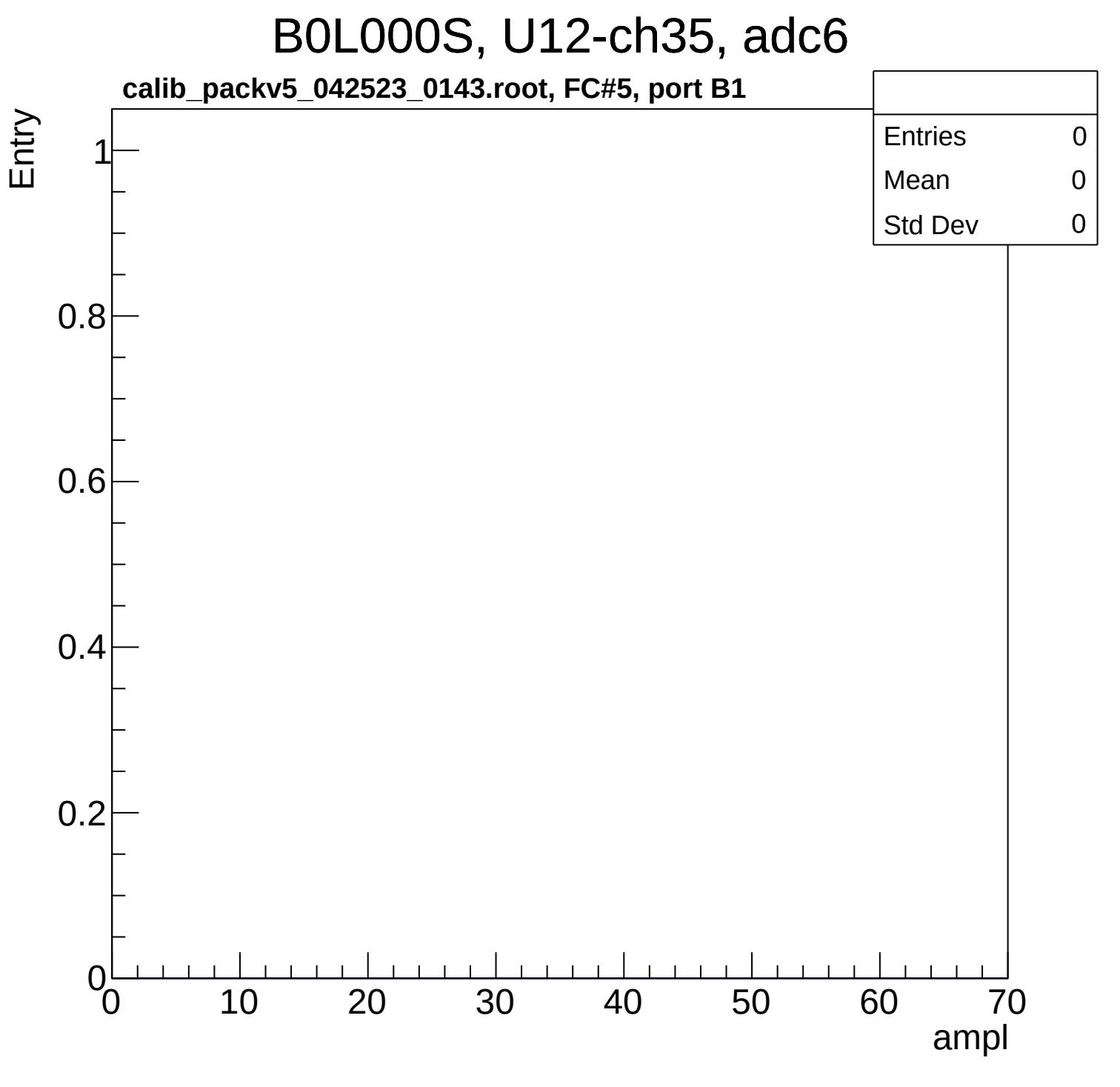
B0L000S, U12-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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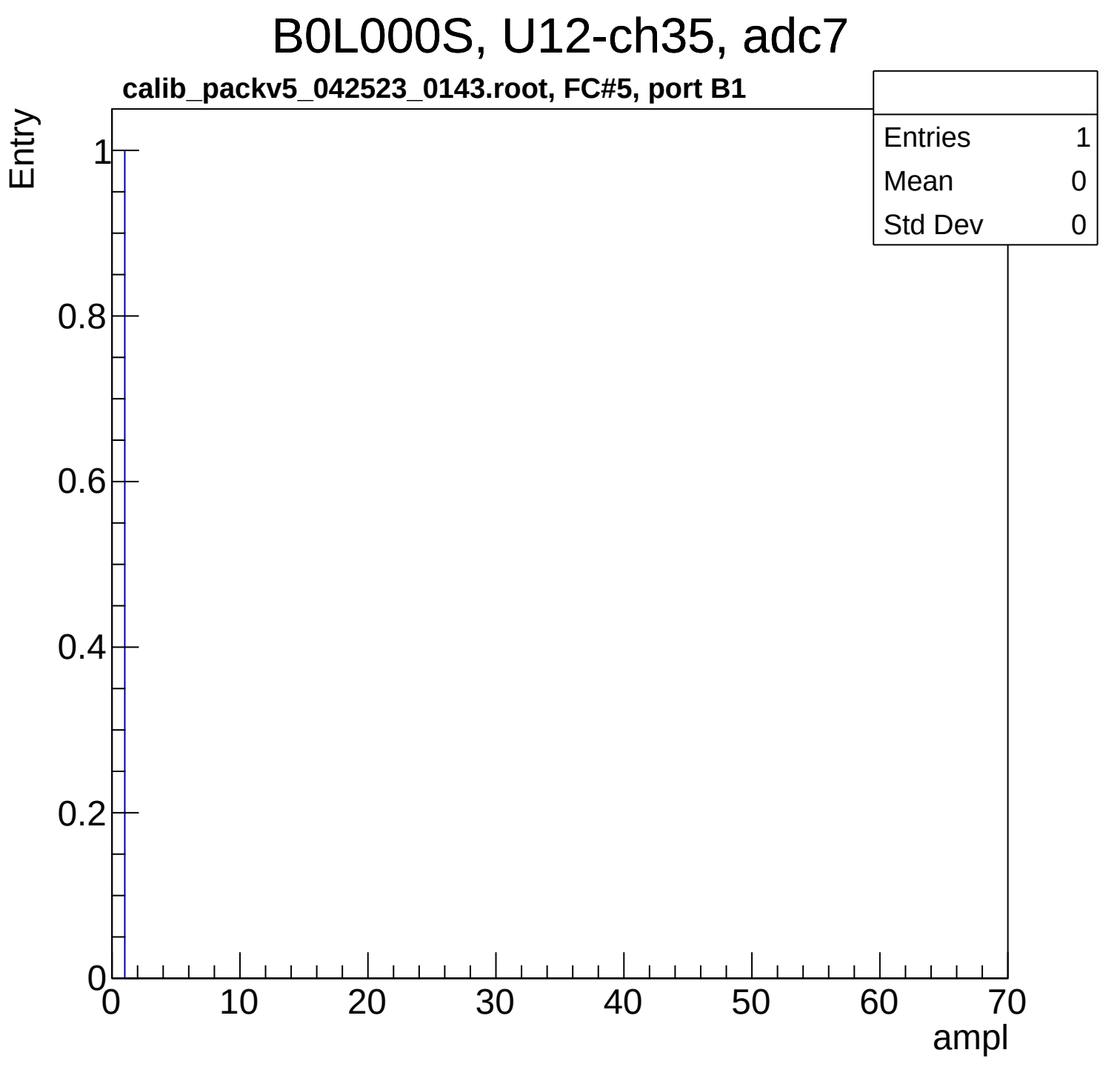
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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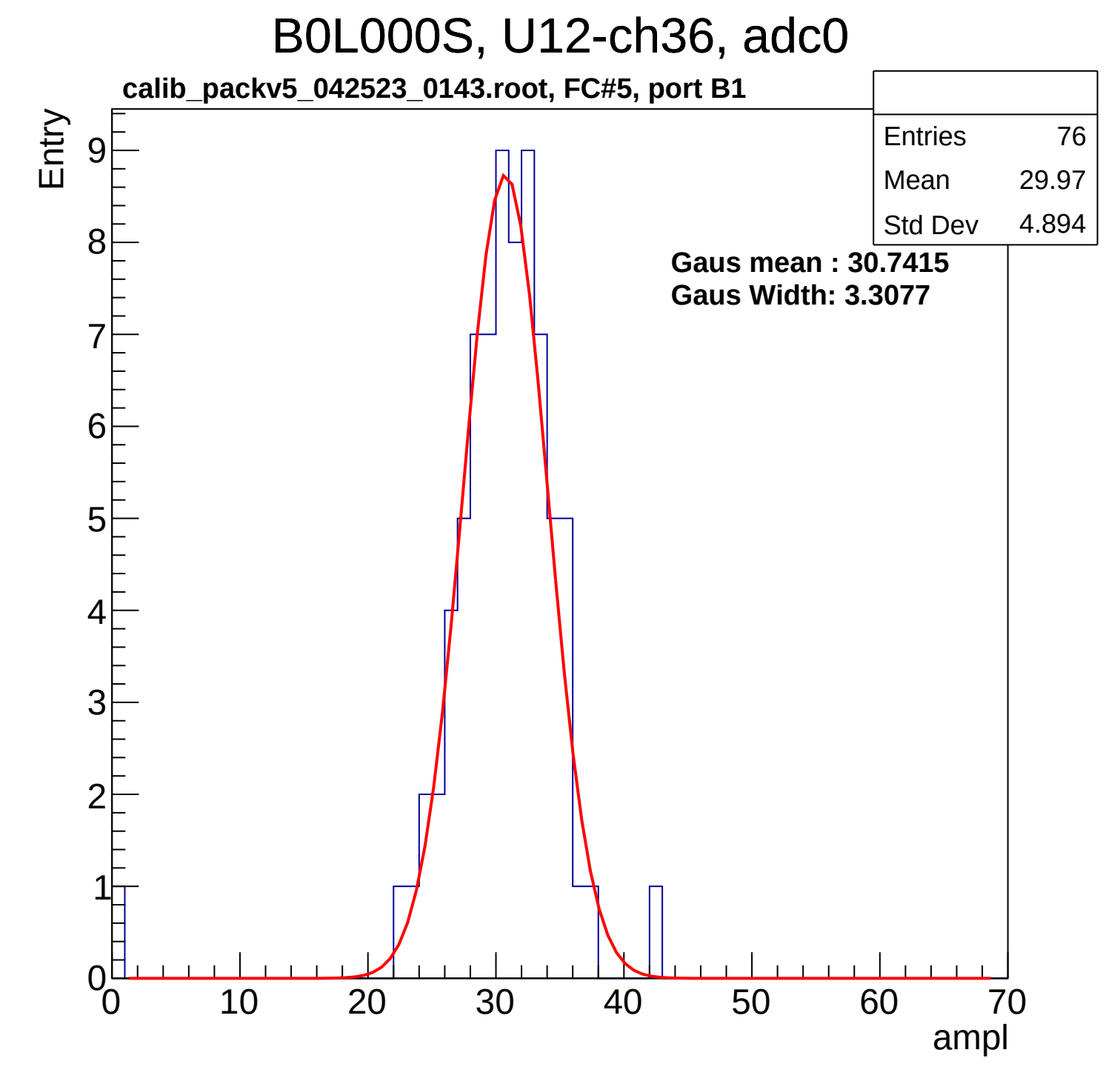
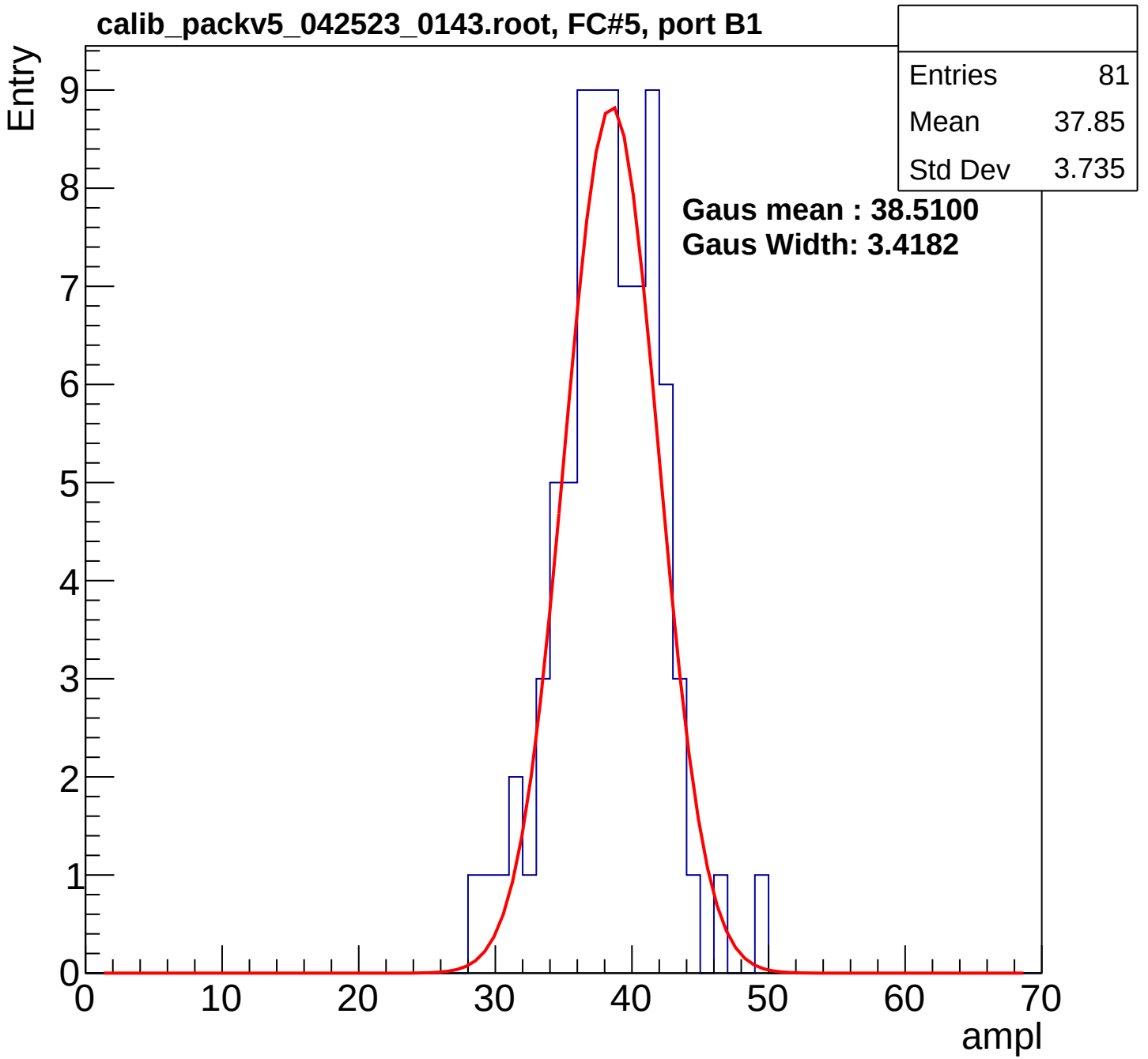
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.97
Std Dev 4.894
Gaus mean : 30.7415
Gaus Width: 3.3077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.85
Std Dev 3.735
Gaus mean : 38.5100
Gaus Width: 3.4182
Entry
ampl
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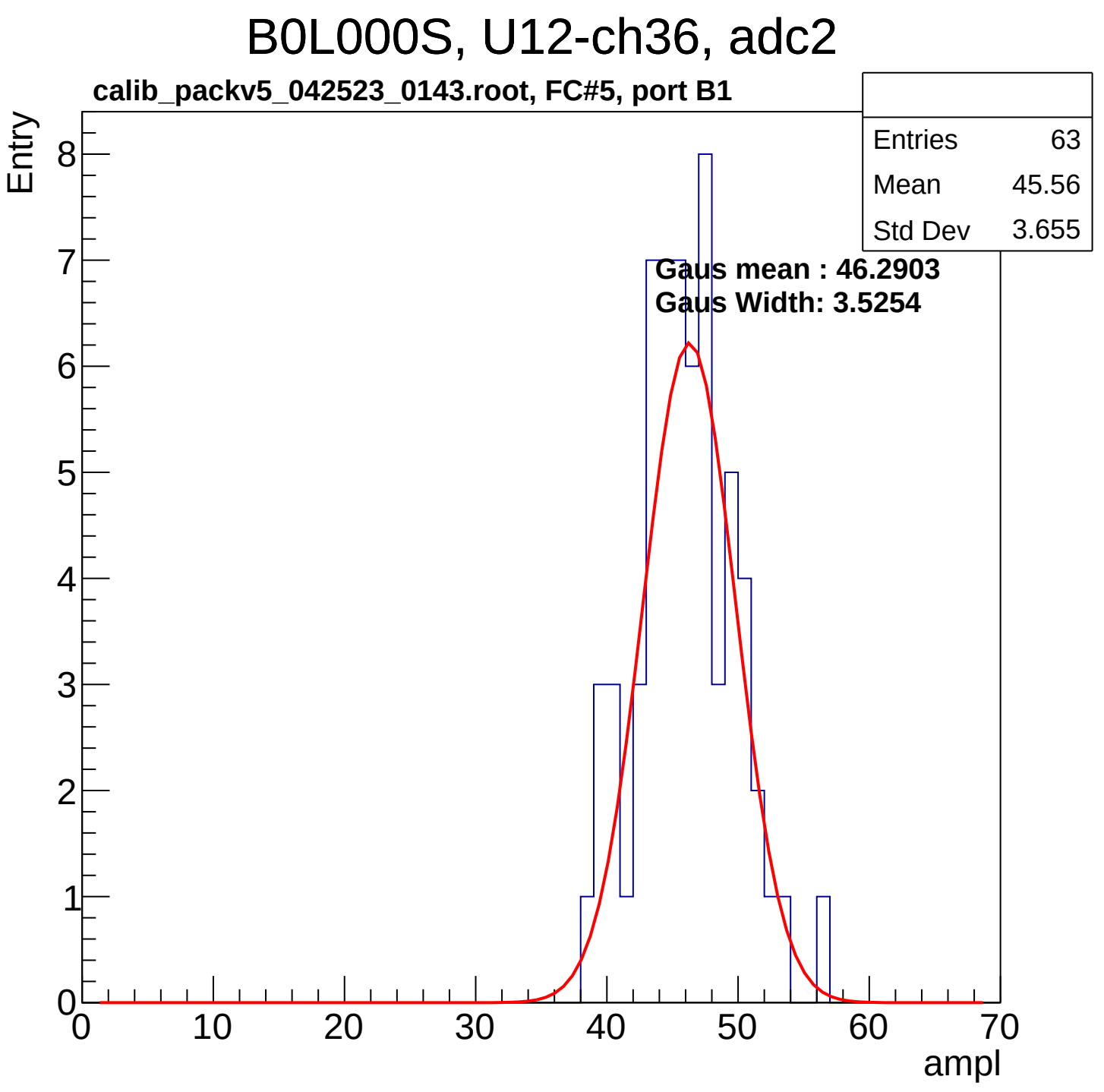
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U12-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 45.56
Std Dev 3.655
Gaus mean : 46.2903
Gaus Width: 3.5254
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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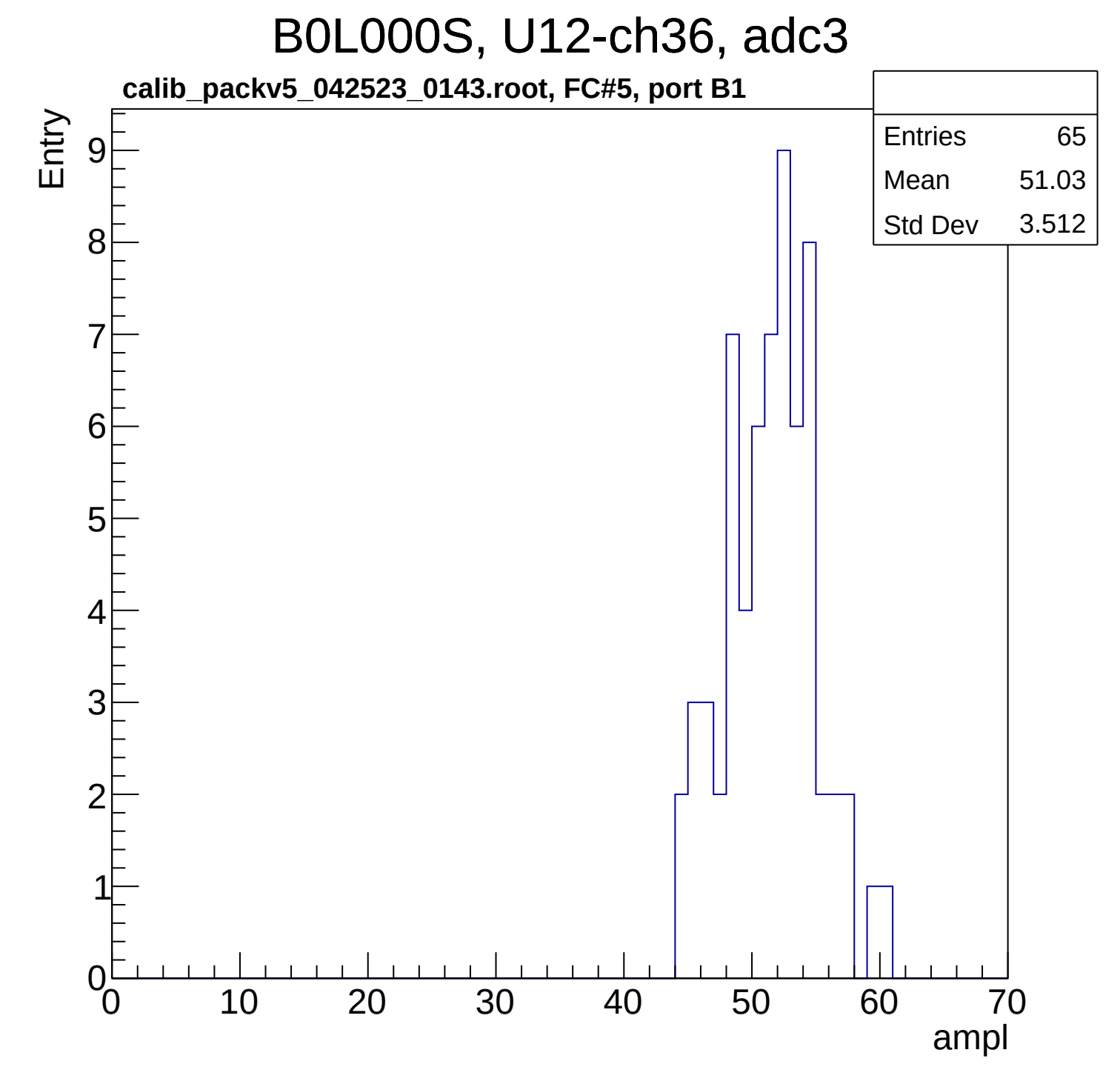
B0L000S, U12-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 51.03
Std Dev 3.512
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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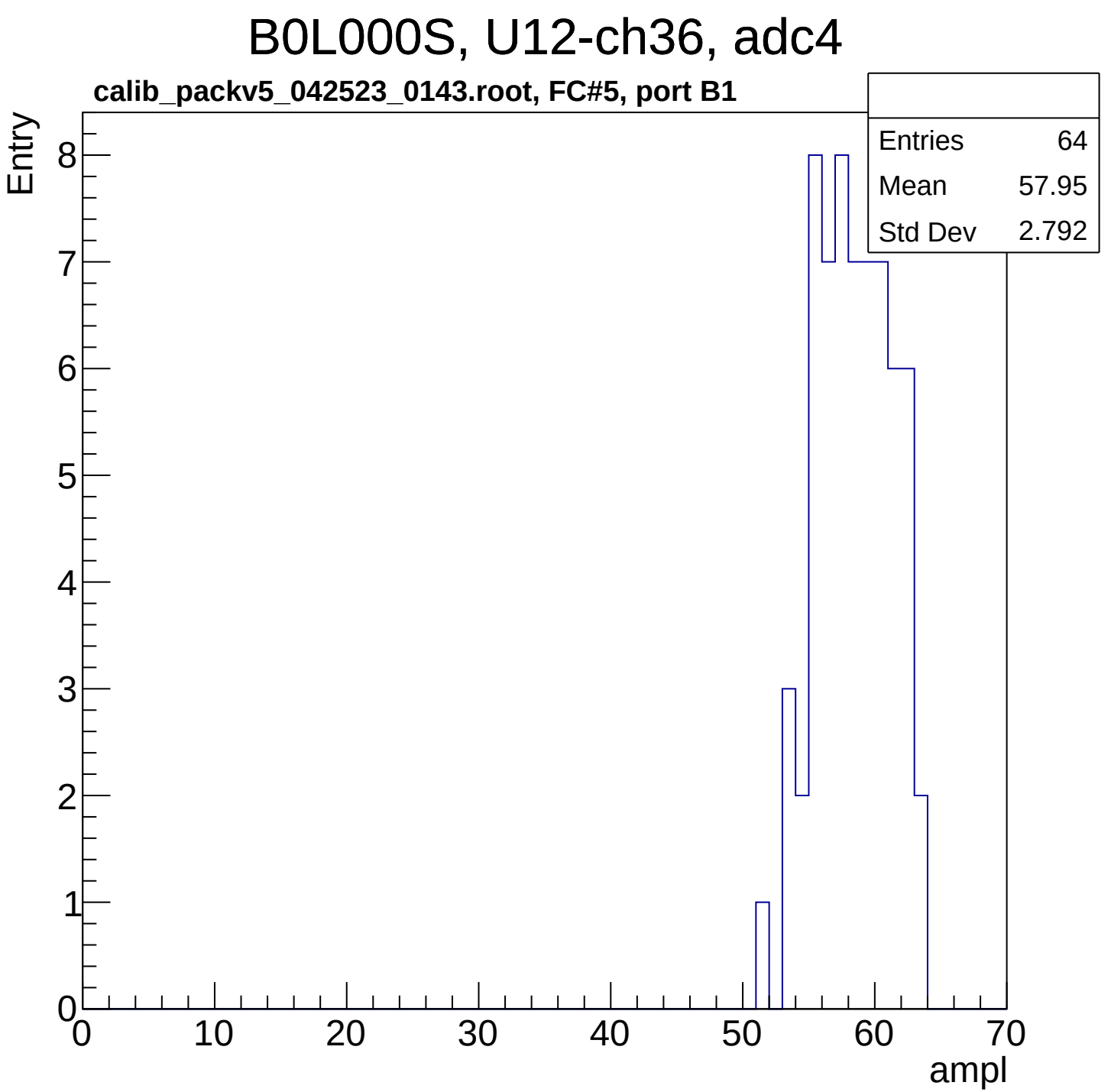
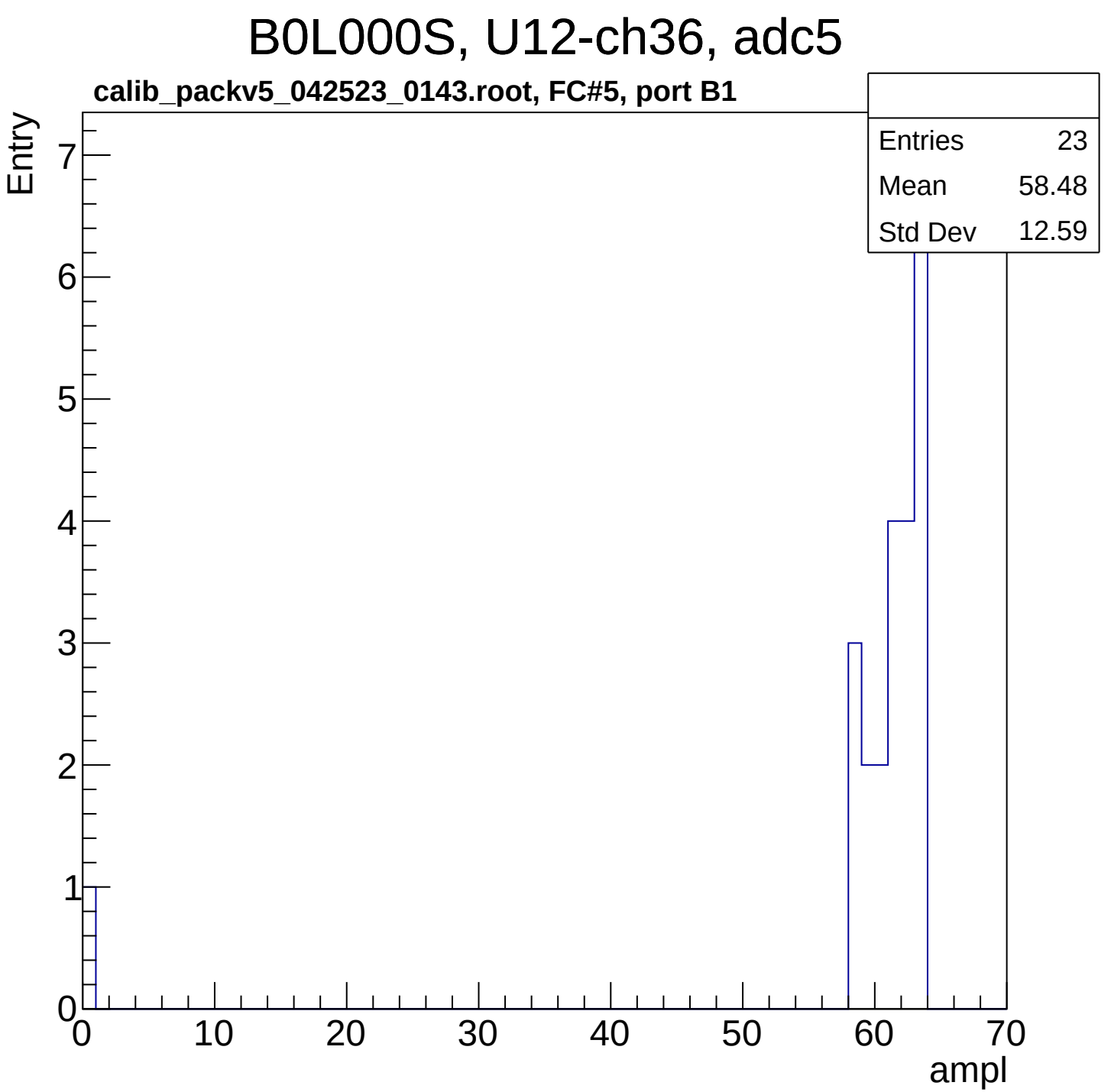

B0L000S, U12-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 57.95
Std Dev 2.792
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.48
Std Dev 12.59
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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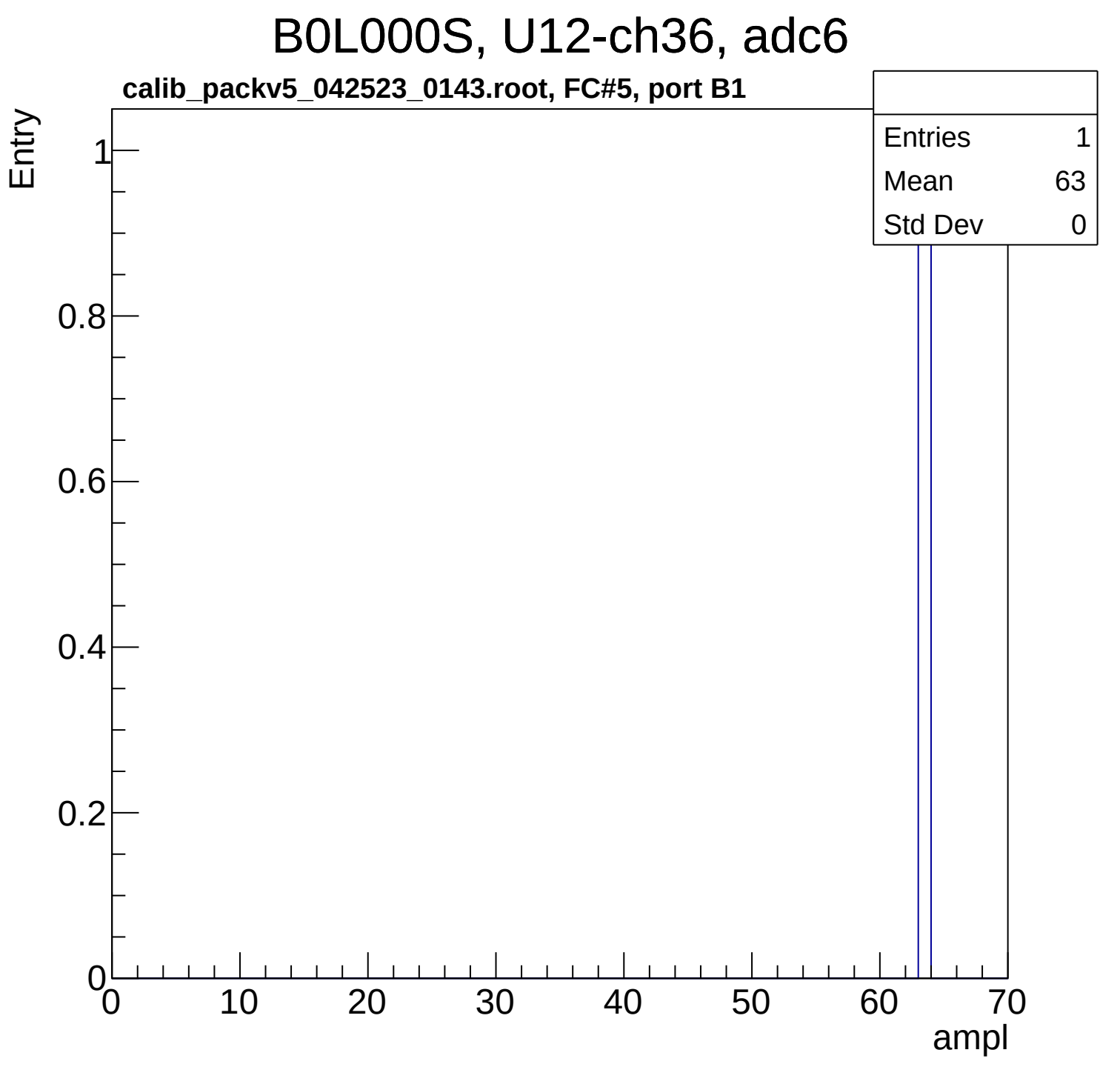
B0L000S, U12-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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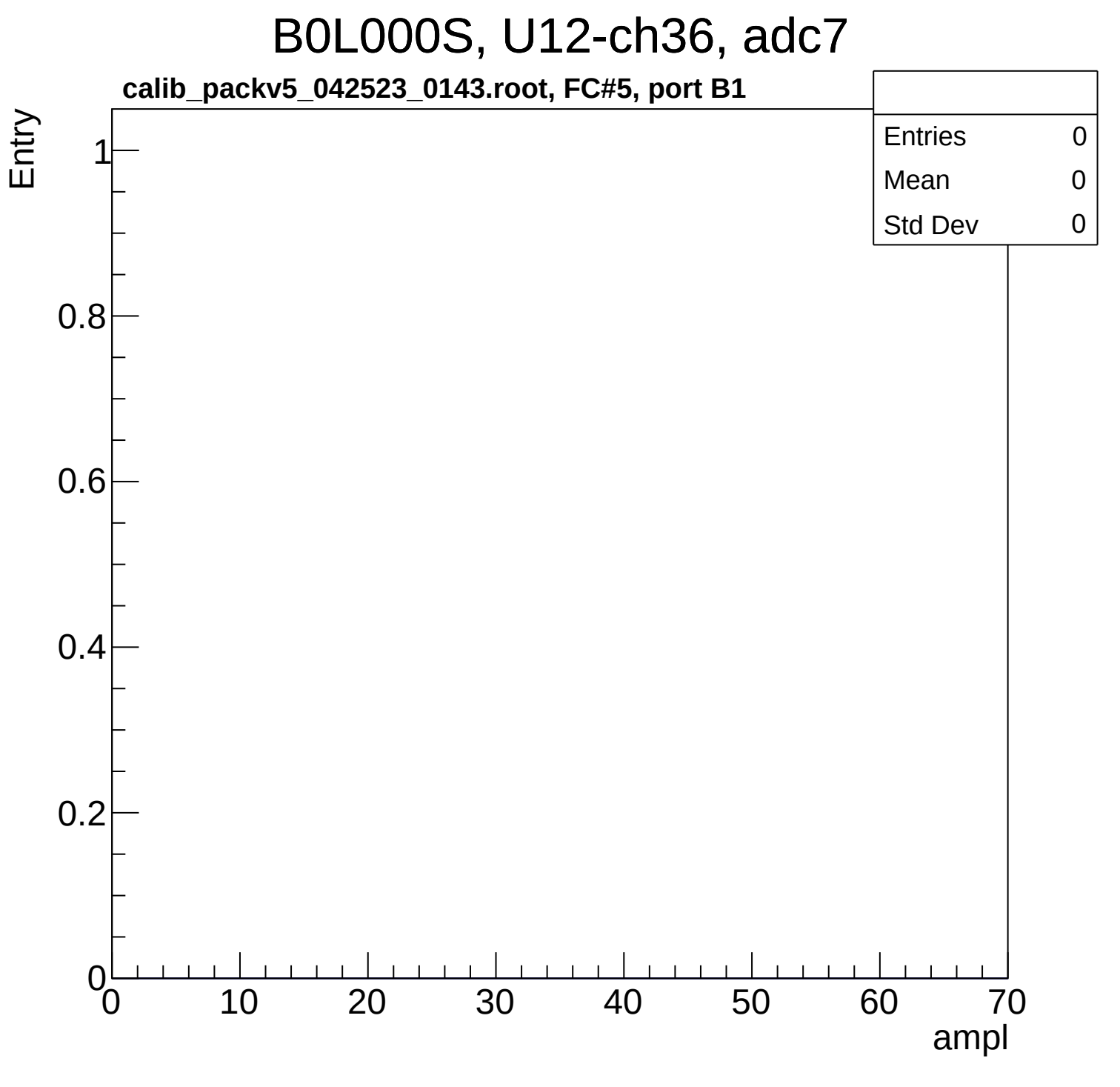
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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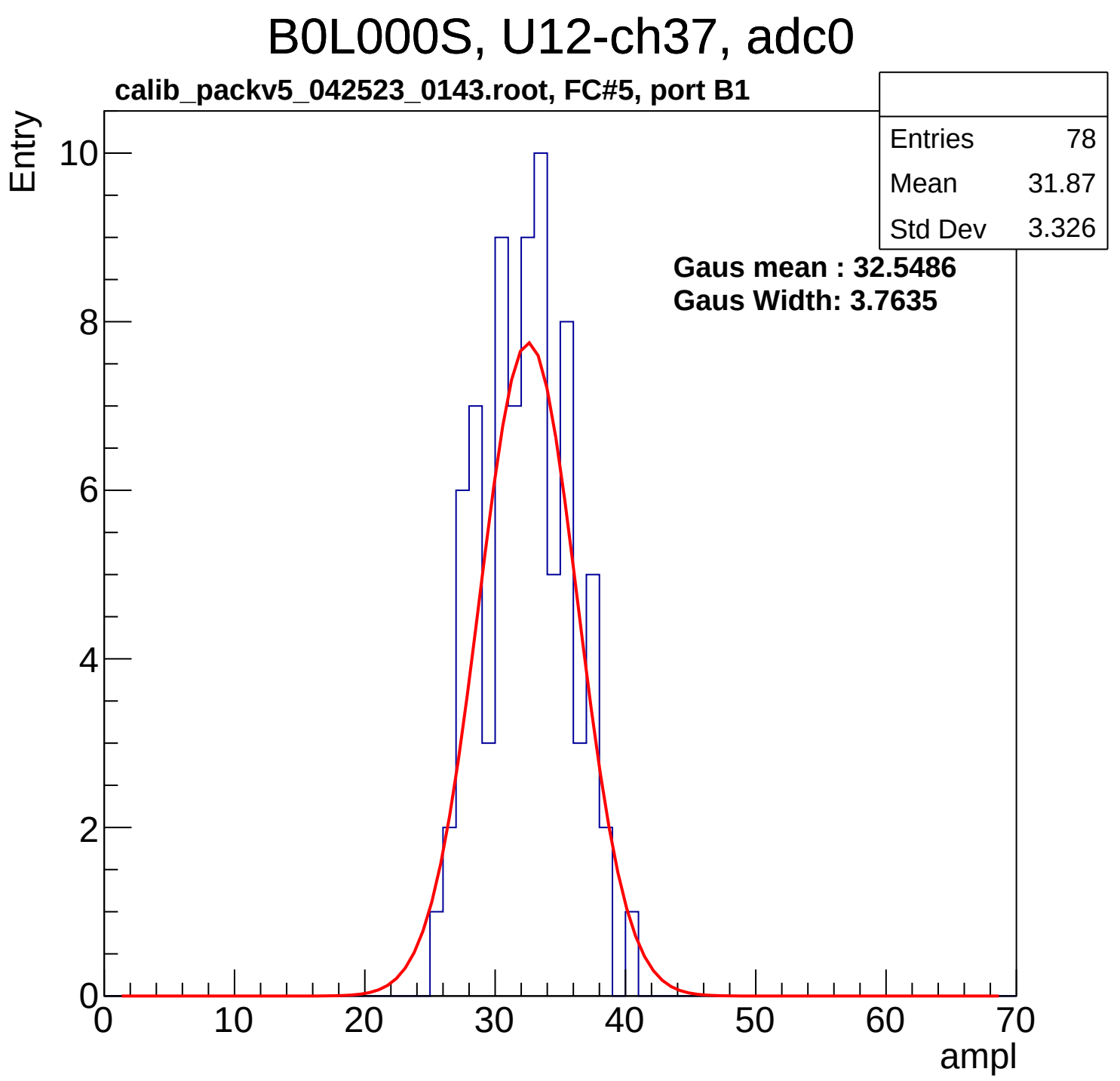
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 31.87
Std Dev 3.326
Gaus mean : 32.5486
Gaus Width: 3.7635
Entry
ampl
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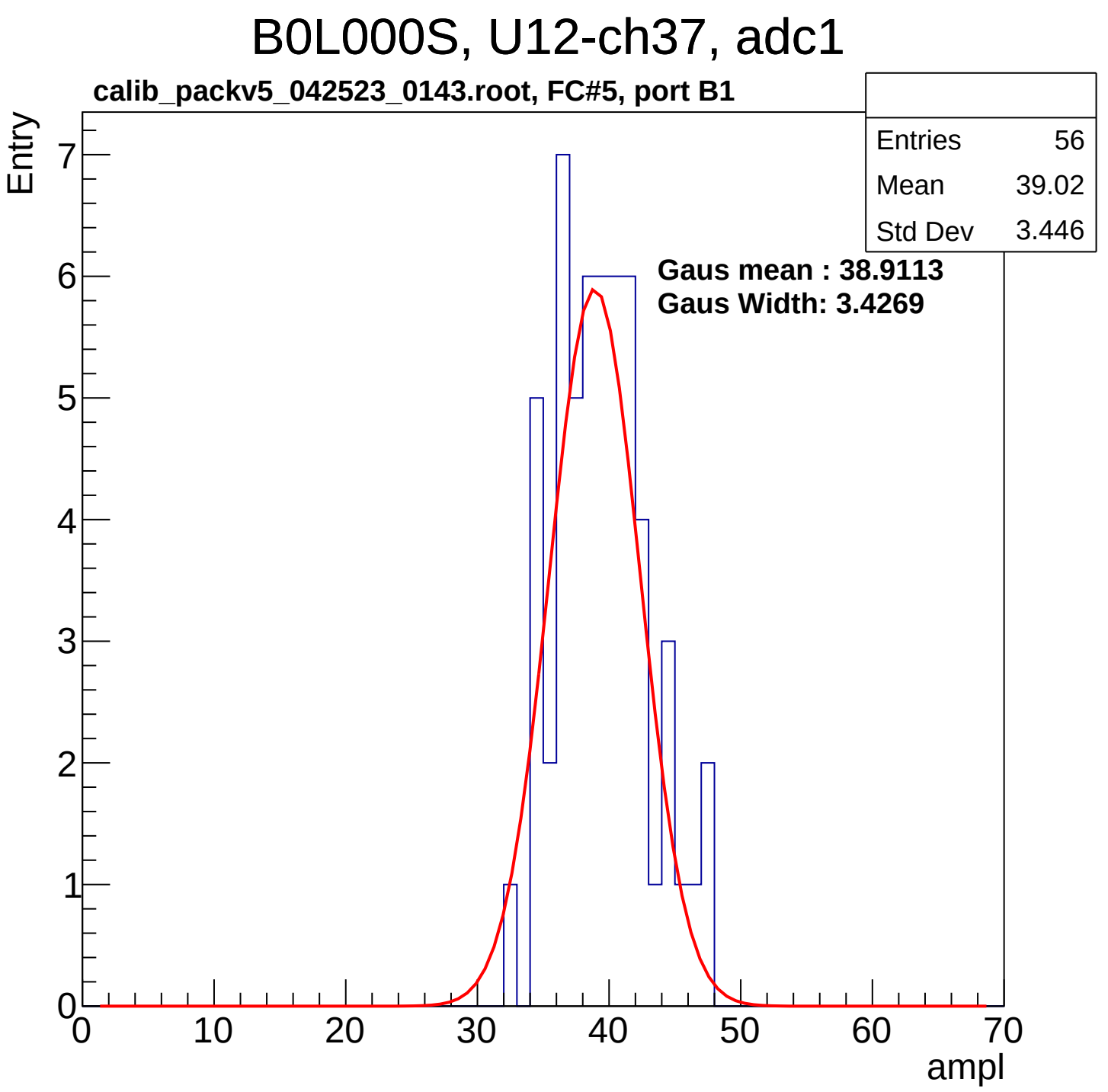
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 39.02
Std Dev 3.446
Gaus mean : 38.9113
Gaus Width: 3.4269
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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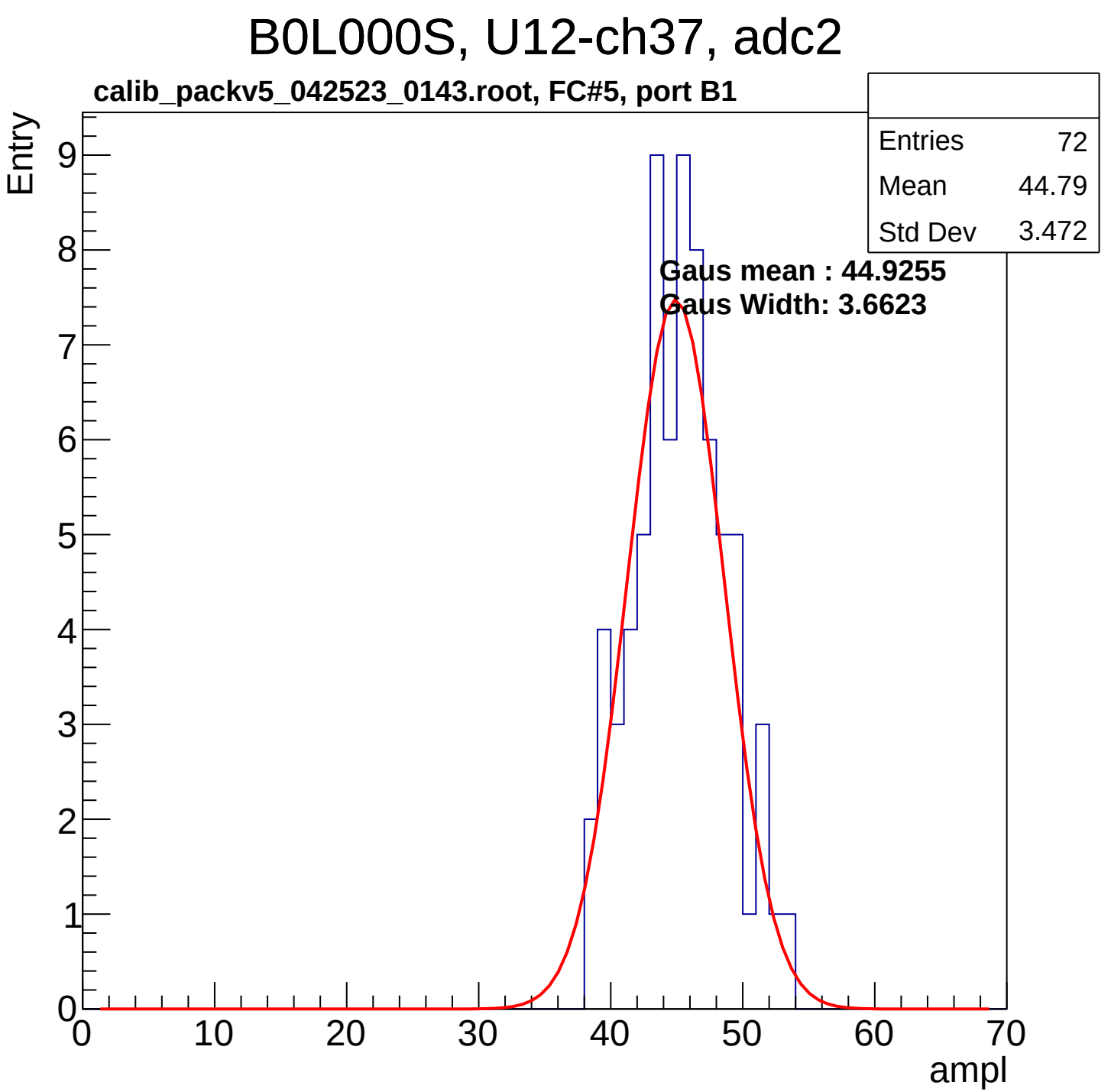
B0L000S, U12-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 44.79
Std Dev 3.472
Gaus mean : 44.9255
Gaus Width: 3.6623
Entry
ampl
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3
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5
6
7
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9
0
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60
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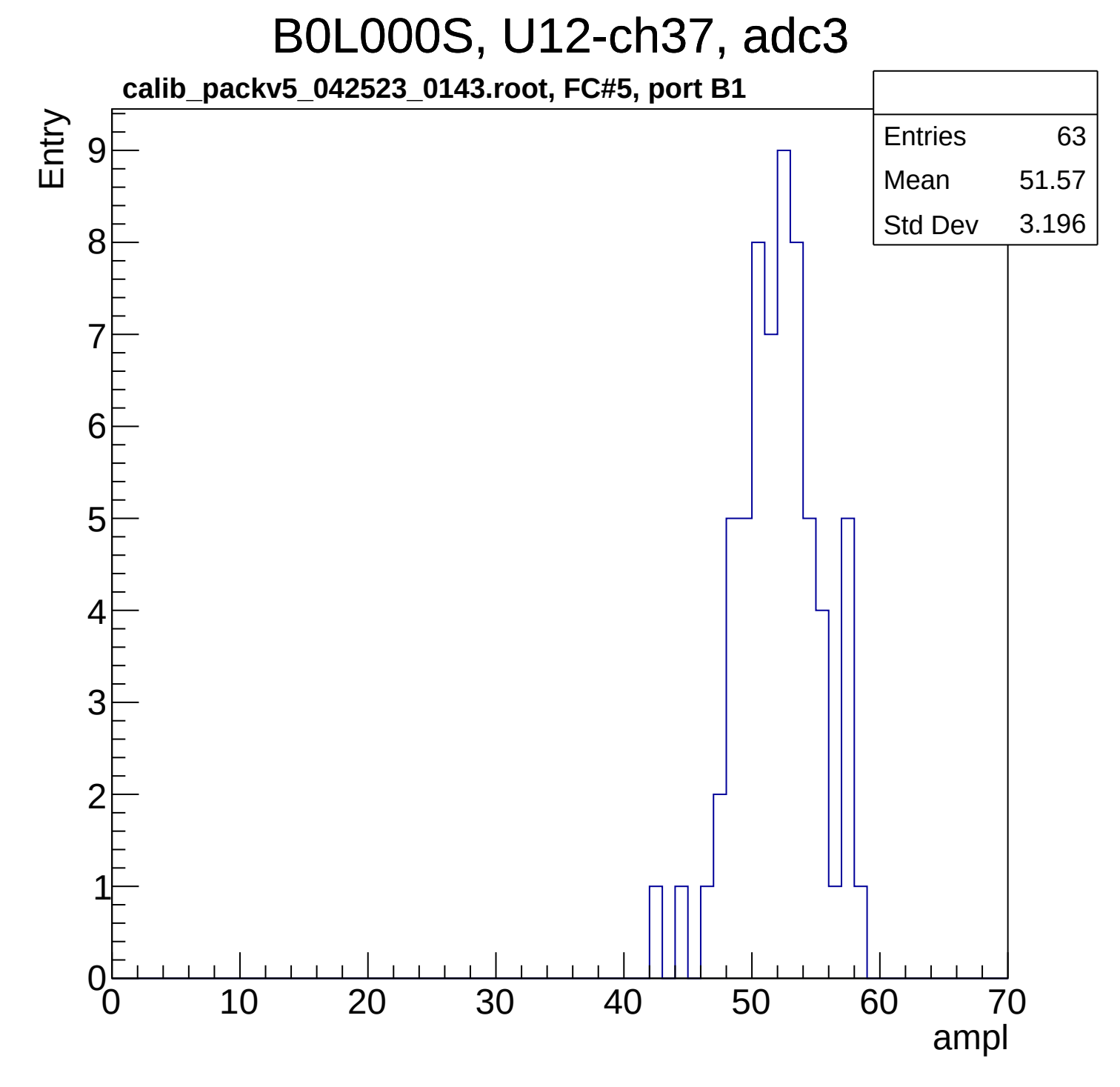
B0L000S, U12-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.57
Std Dev 3.196
Entry
ampl
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3
4
5
6
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8
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0
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60
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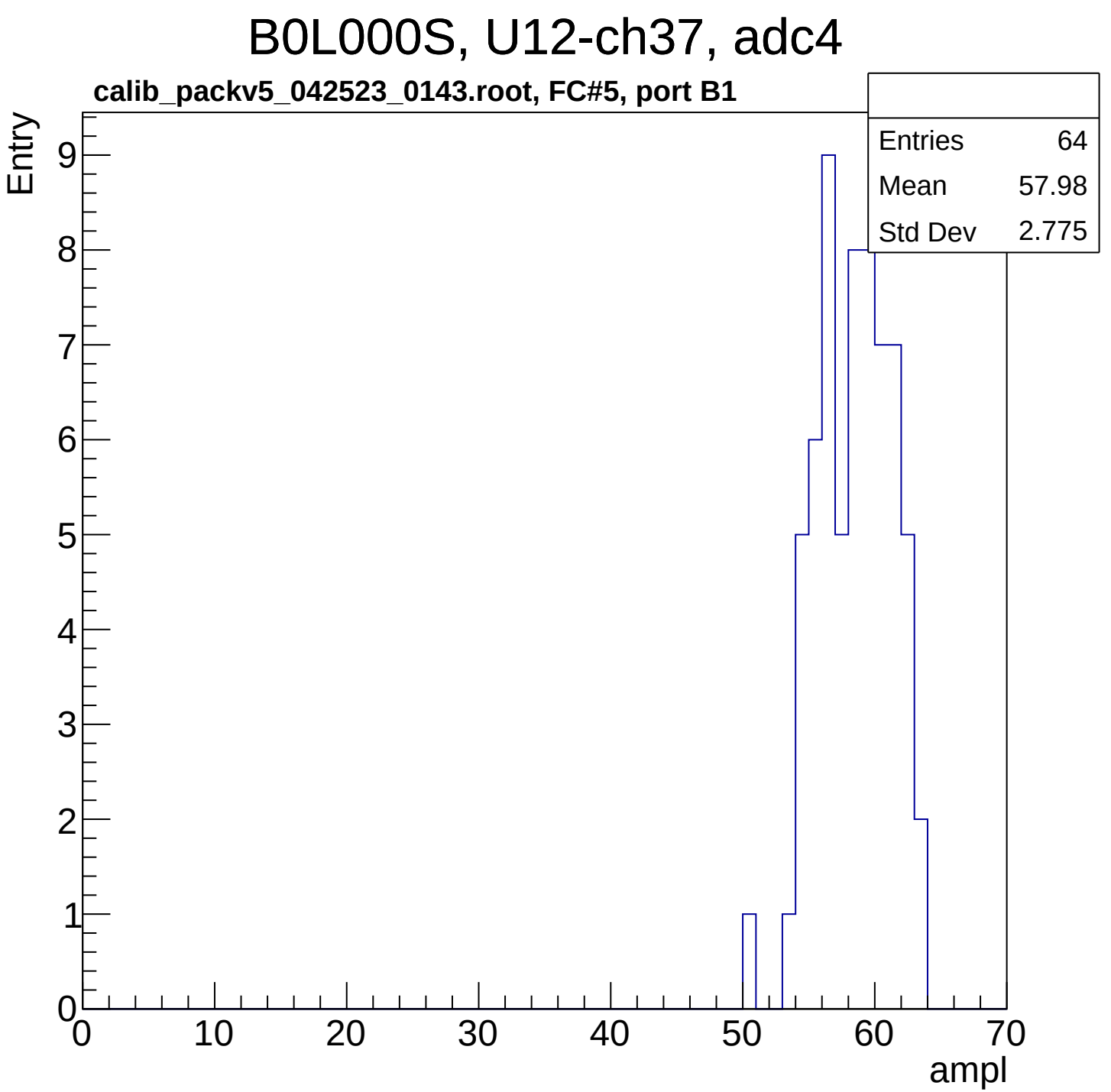
B0L000S, U12-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 57.98
Std Dev 2.775
Entry
ampl
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3
4
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6
7
8
9
0
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60
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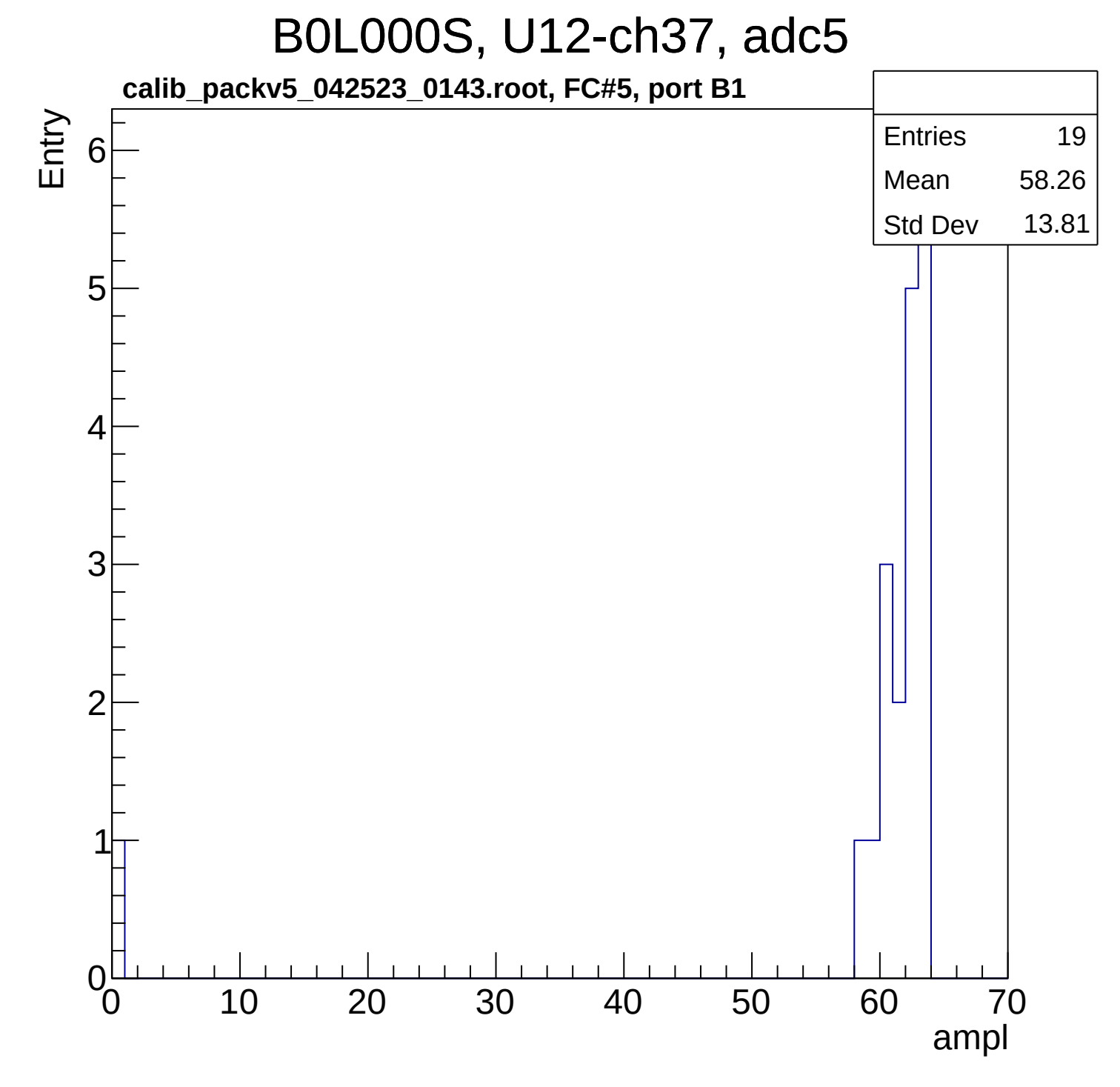
B0L000S, U12-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.26
Std Dev 13.81
Entry
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3
4
5
6
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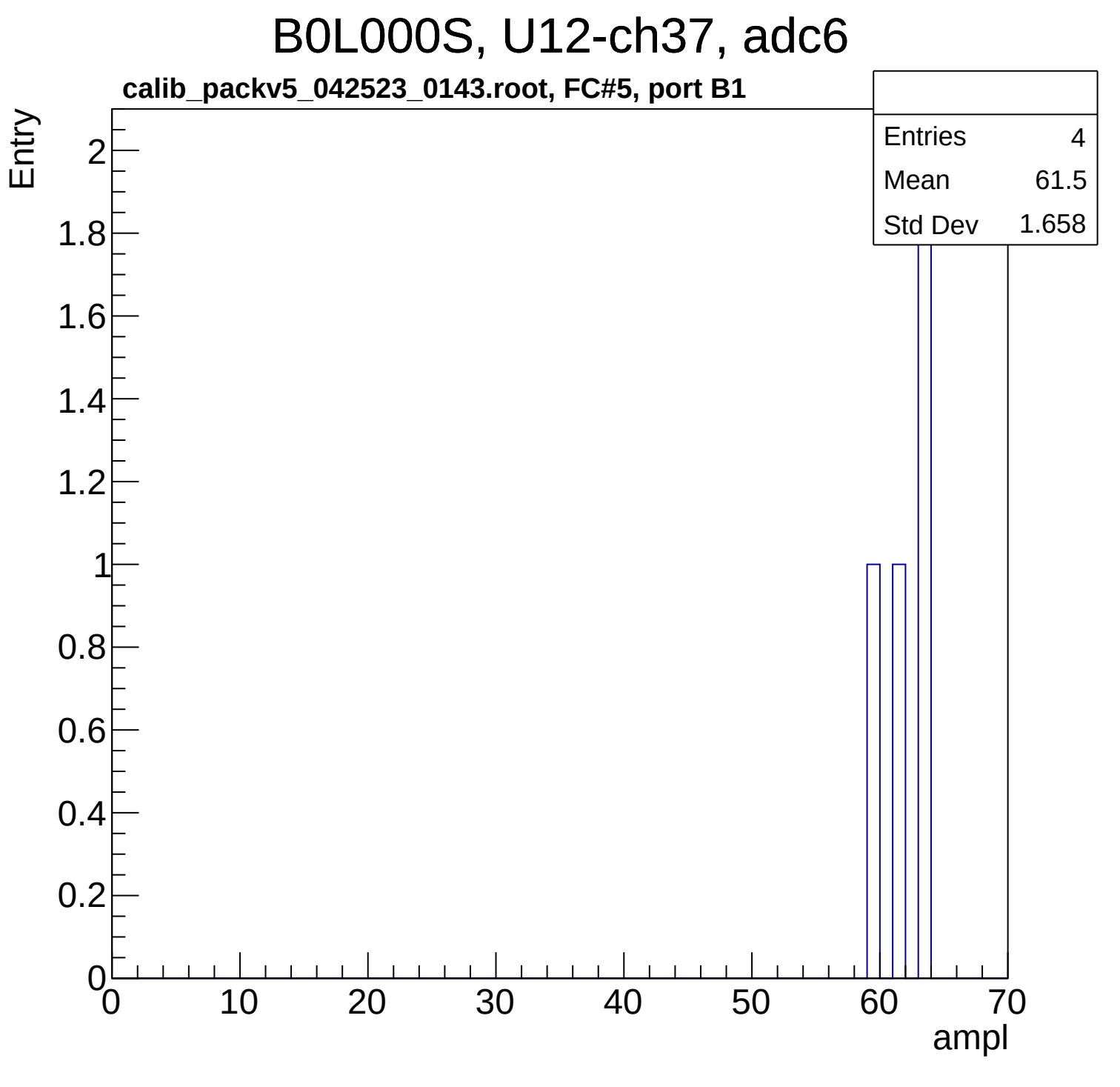
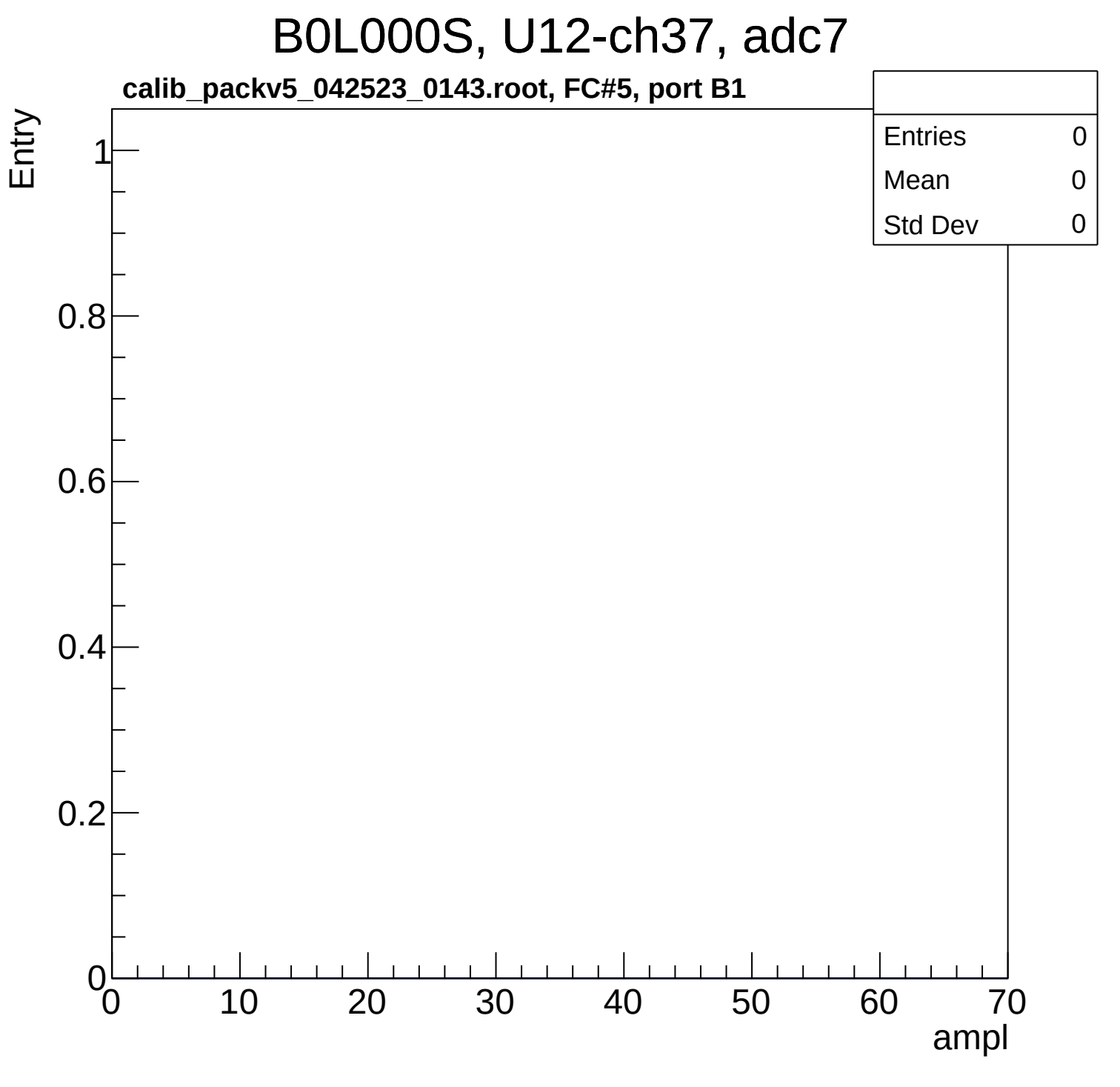
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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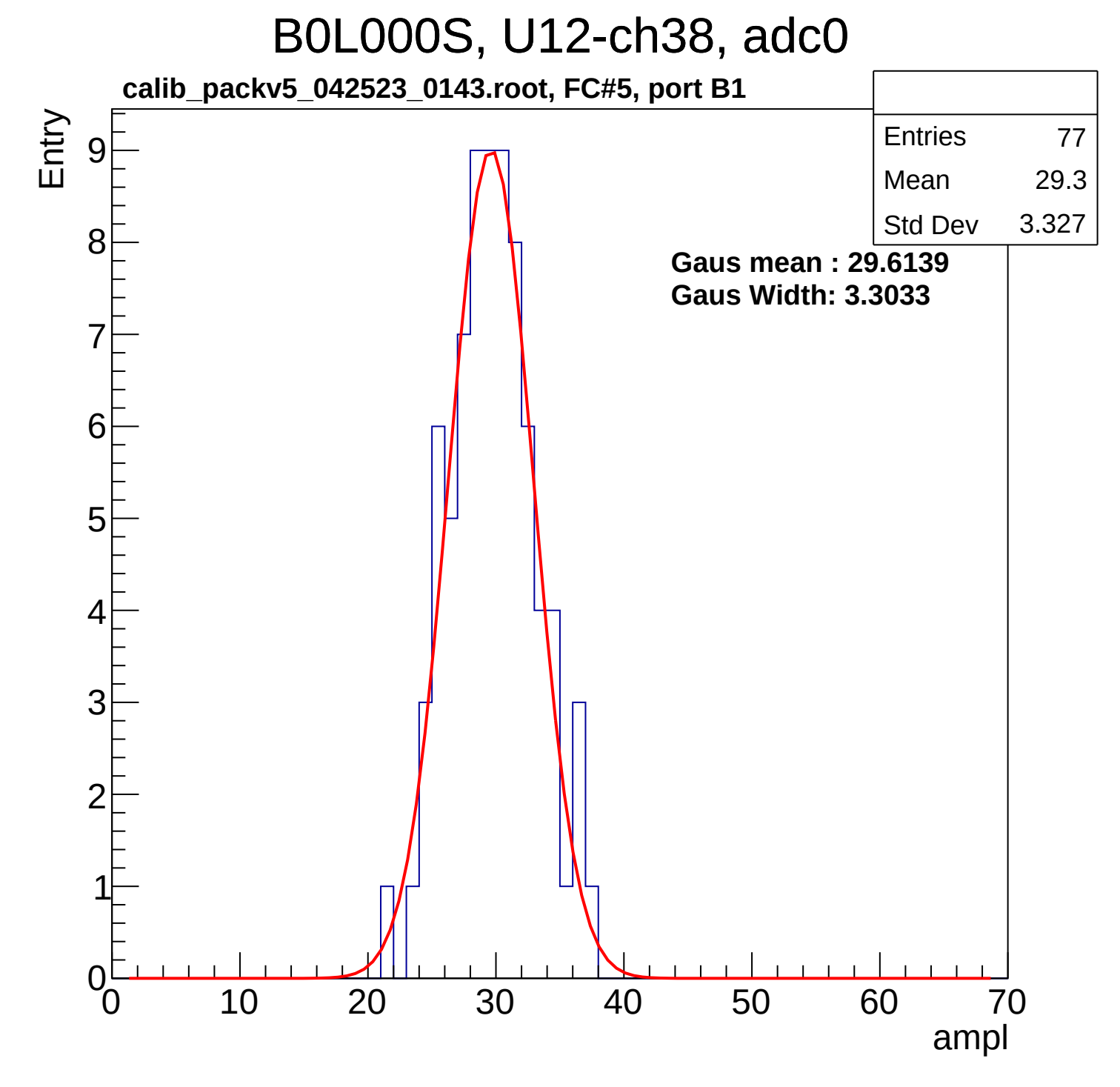
50
60
70
ampl


B0L000S, U12-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.3
Std Dev 3.327
Gaus mean : 29.6139
Gaus Width: 3.3033
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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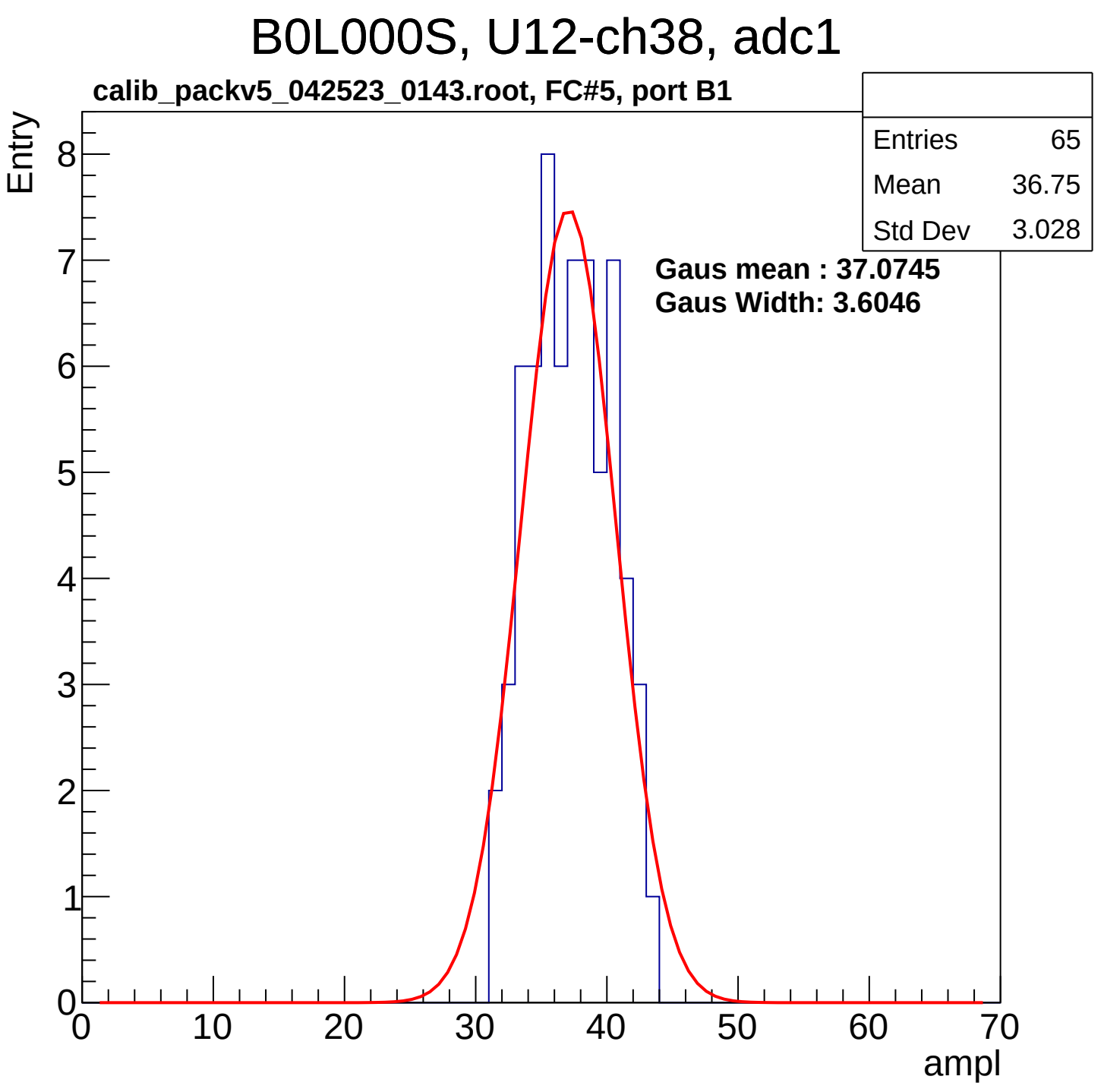
B0L000S, U12-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.75
Std Dev 3.028
Gaus mean : 37.0745
Gaus Width: 3.6046
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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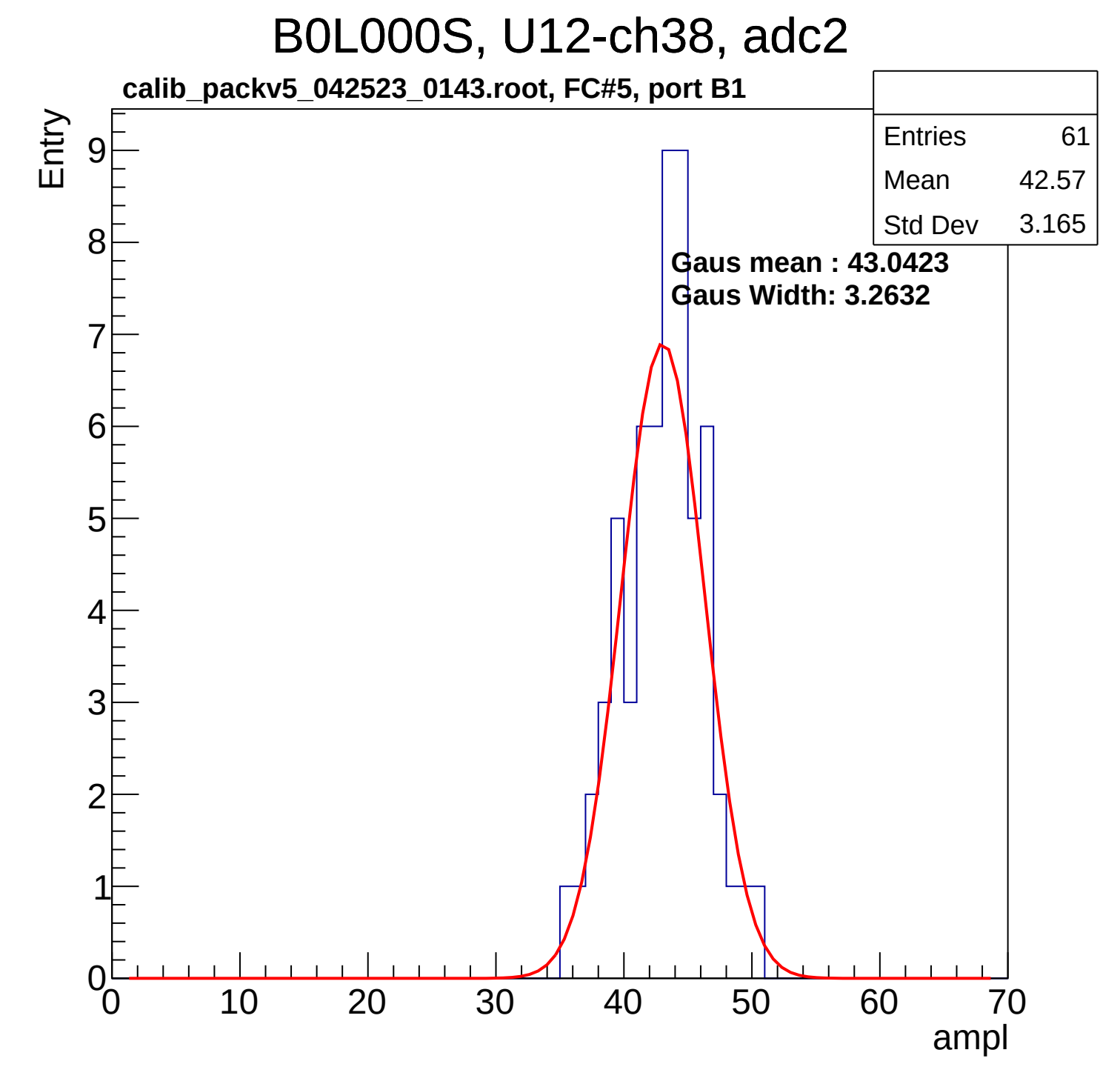
40
50
60
70


B0L000S, U12-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.57
Std Dev 3.165
Gaus mean : 43.0423
Gaus Width: 3.2632
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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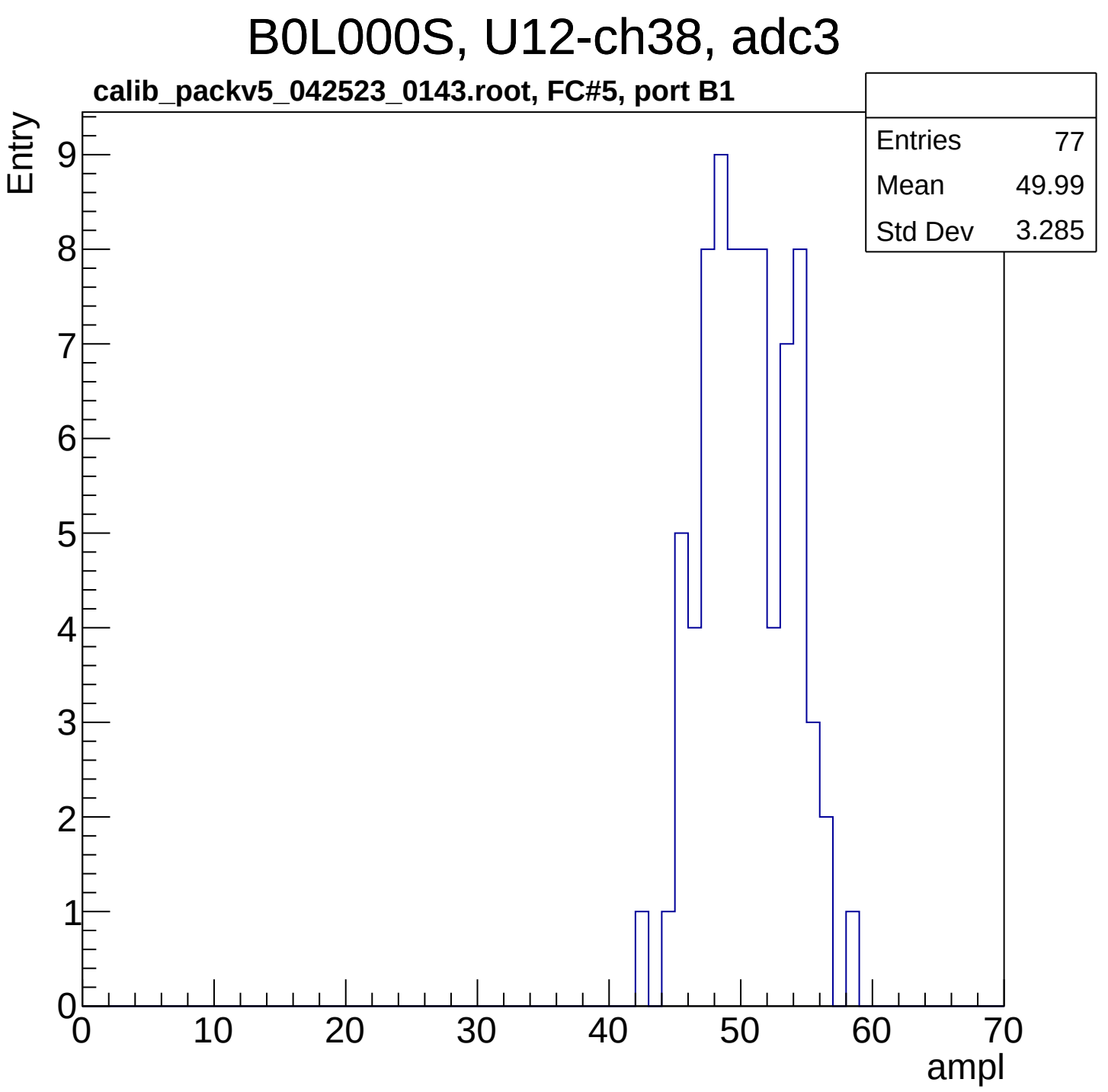
B0L000S, U12-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.99
Std Dev 3.285
Entry
ampl
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8
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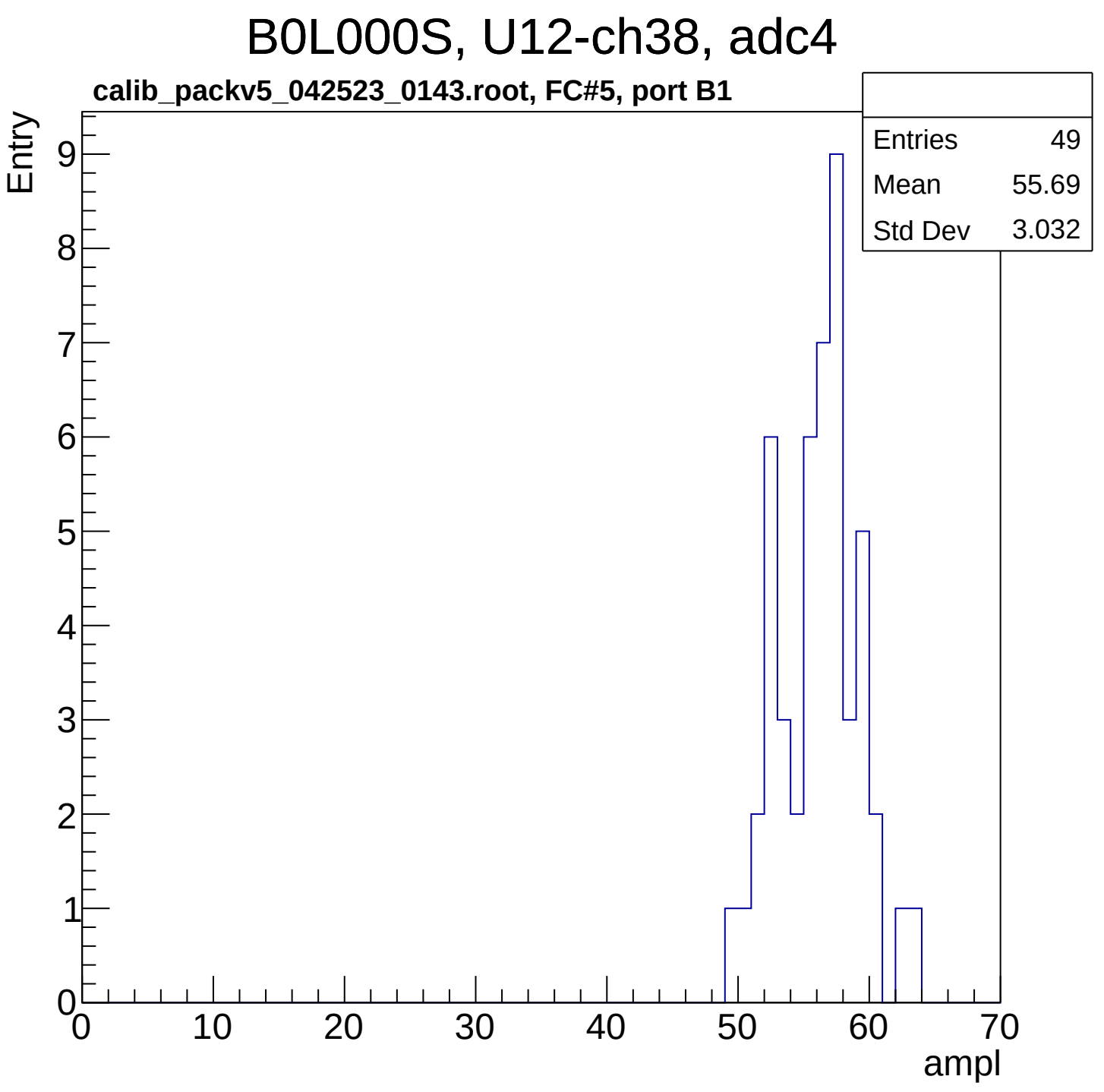
B0L000S, U12-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.69
Std Dev 3.032
Entry
ampl
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7
8
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0
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60
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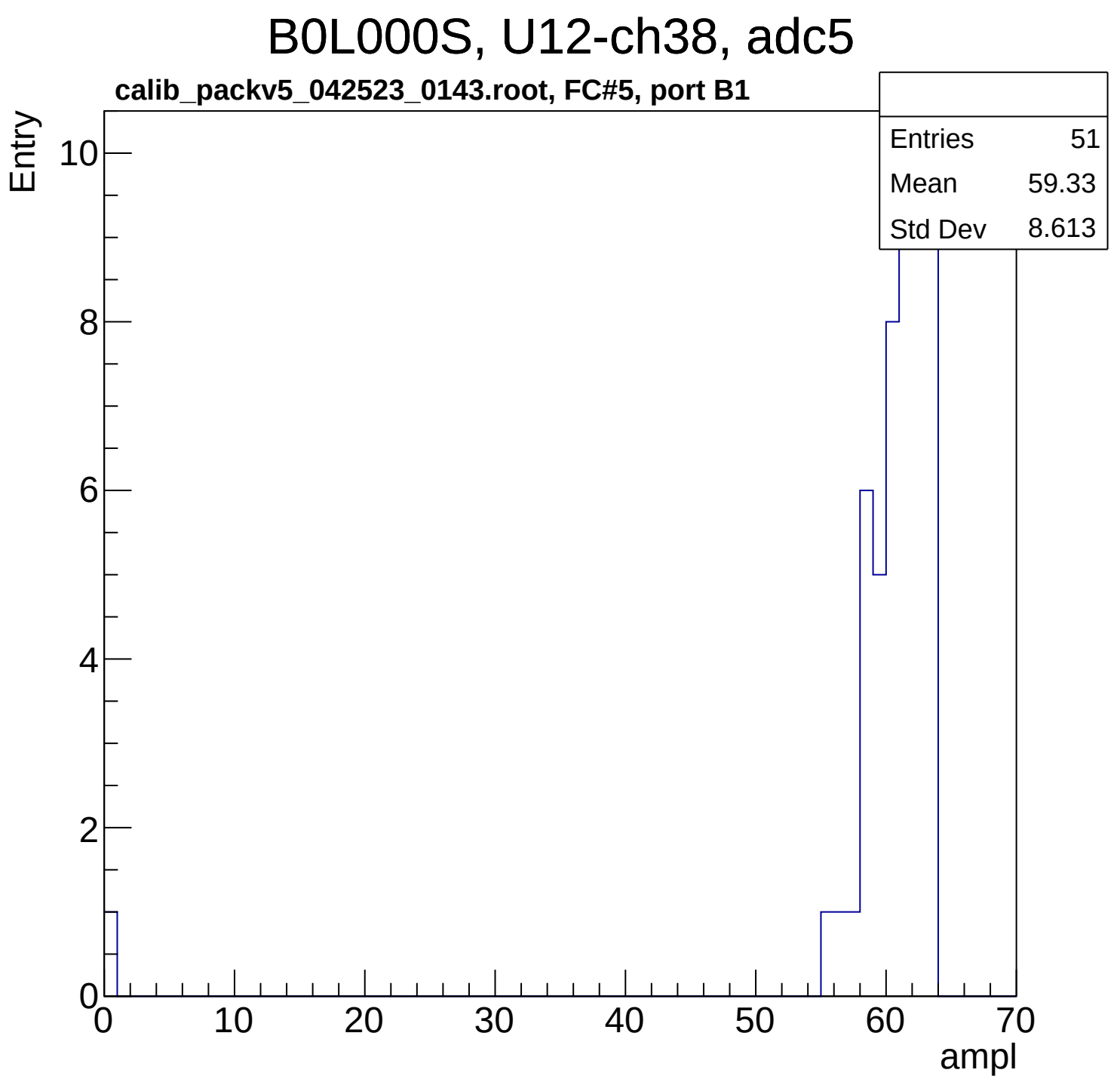
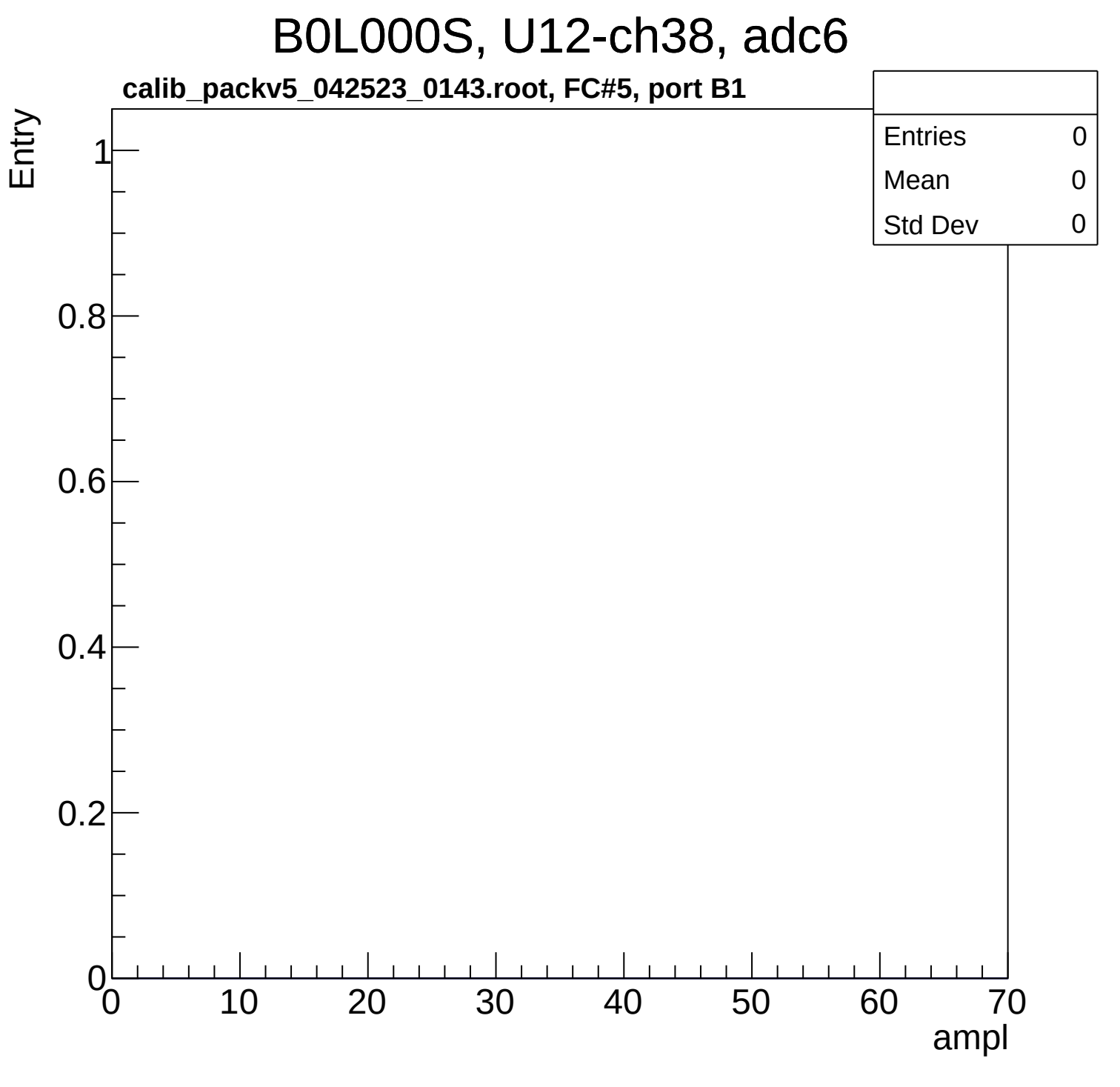
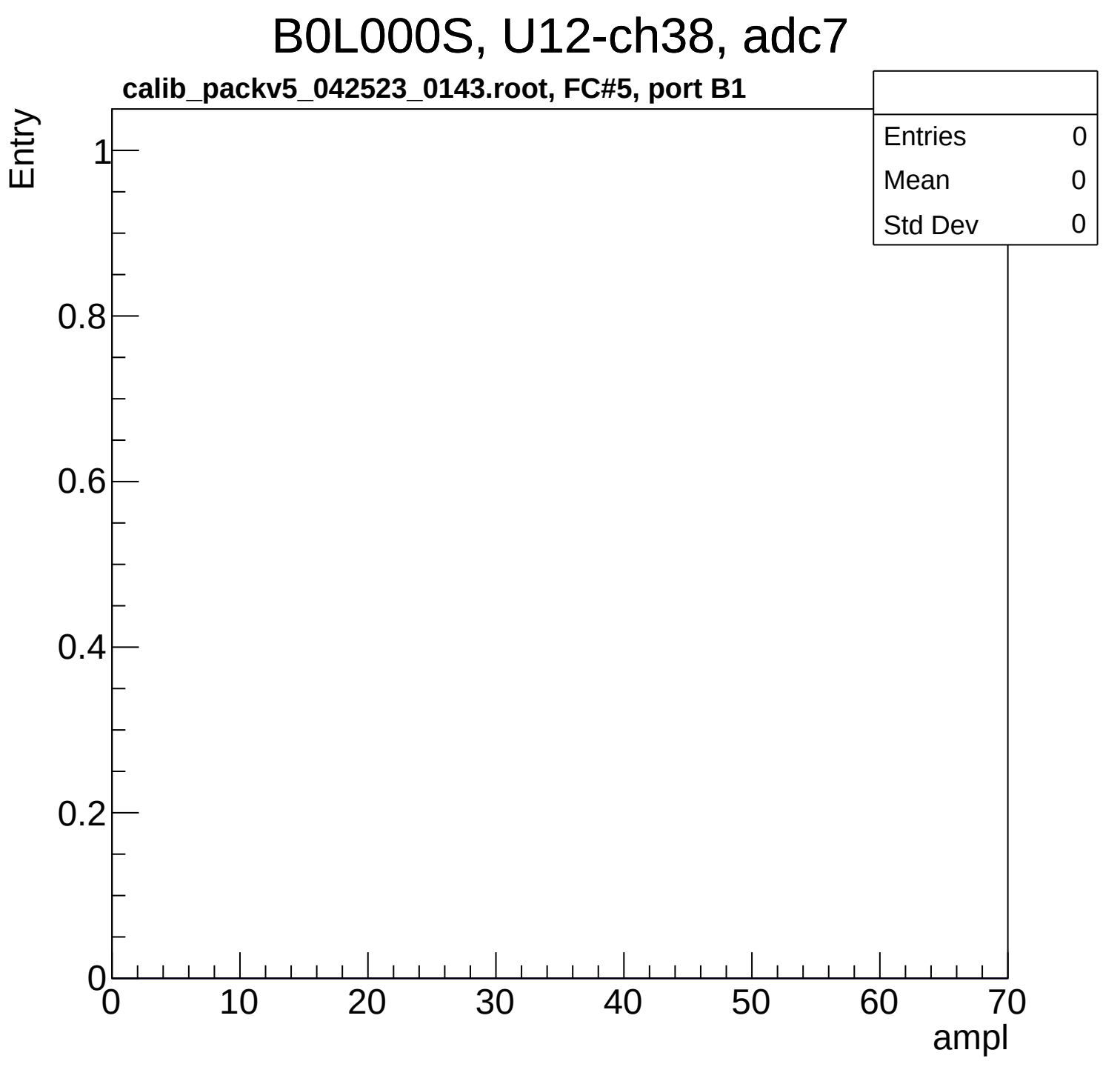
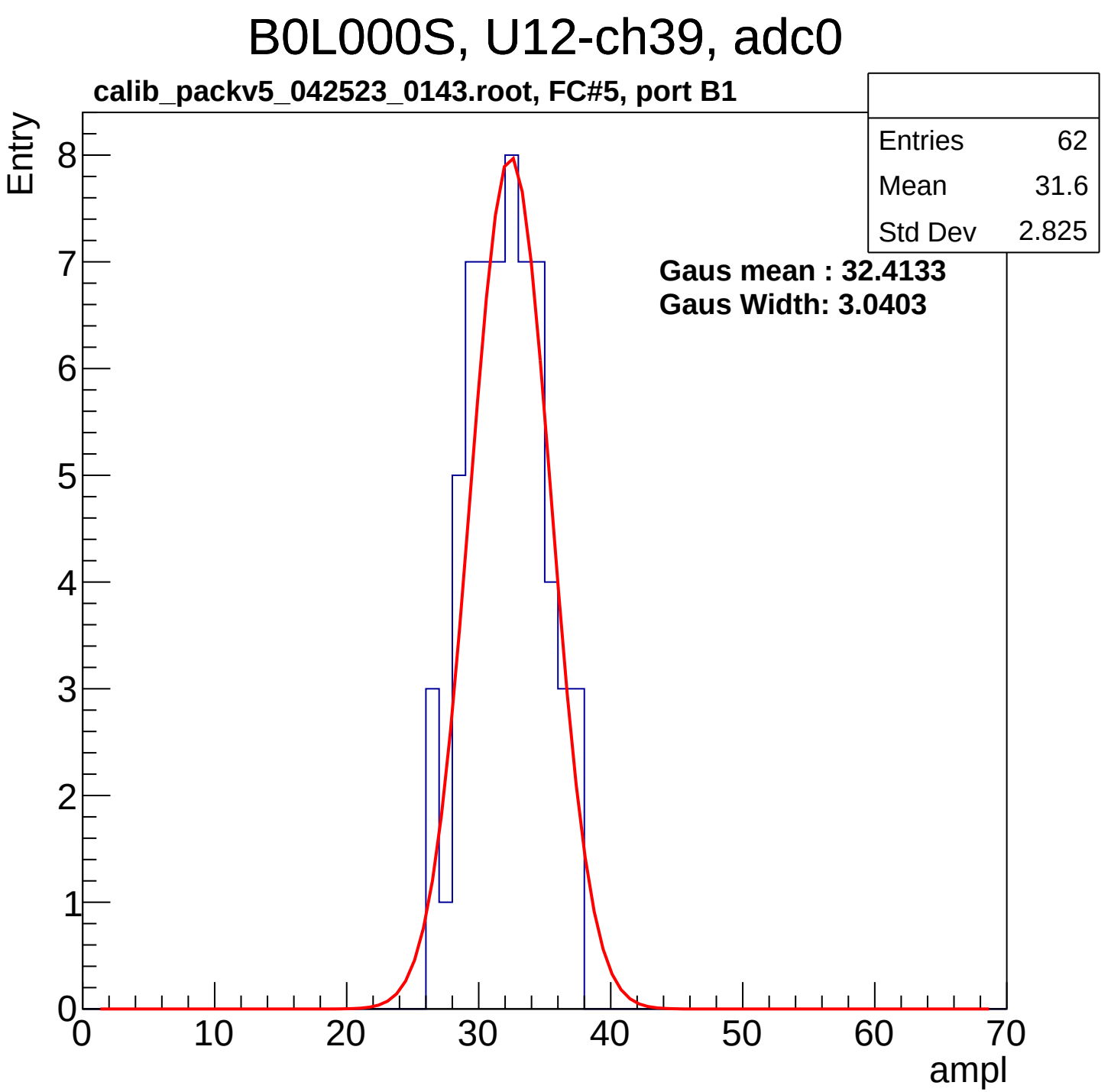
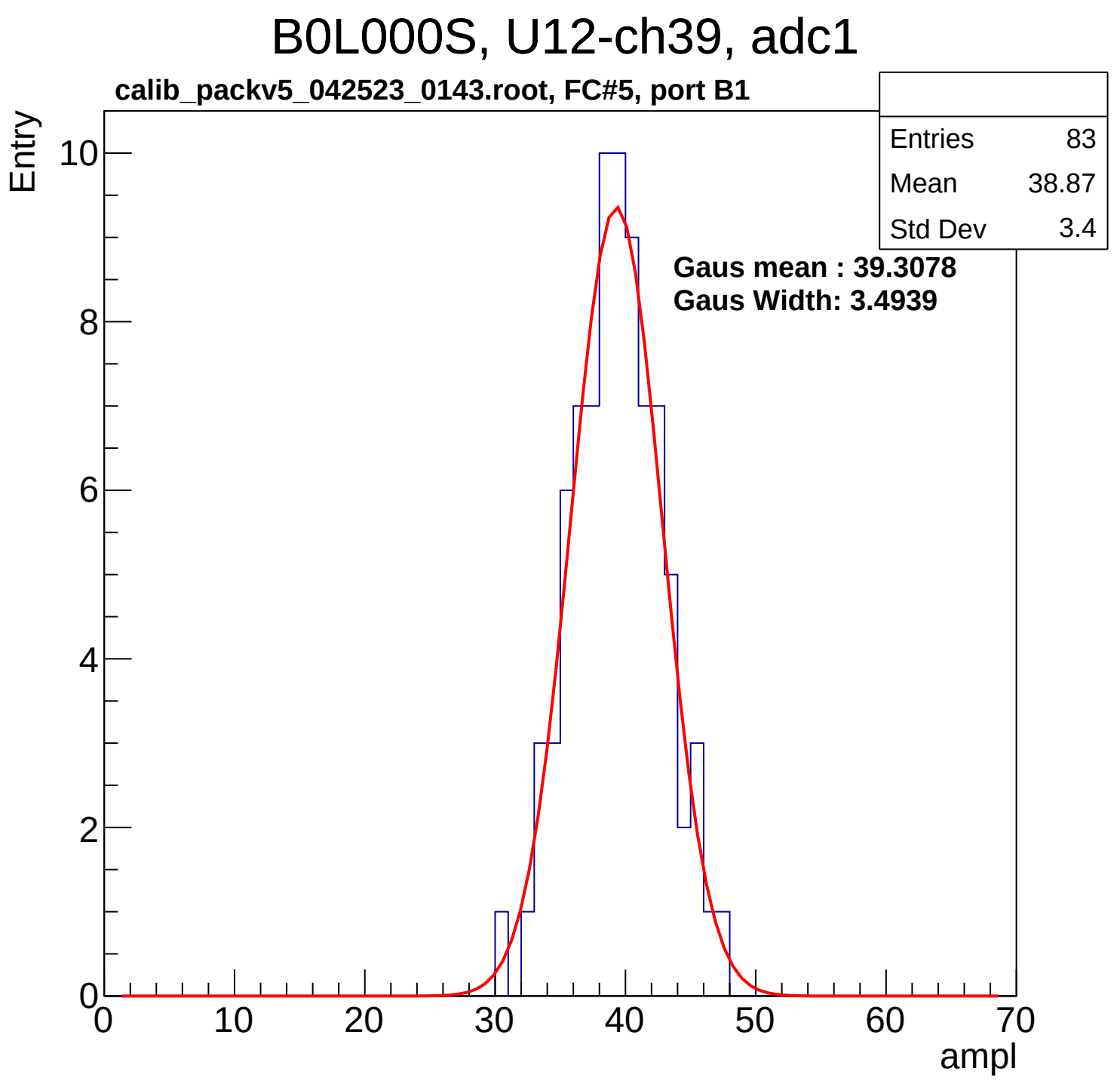
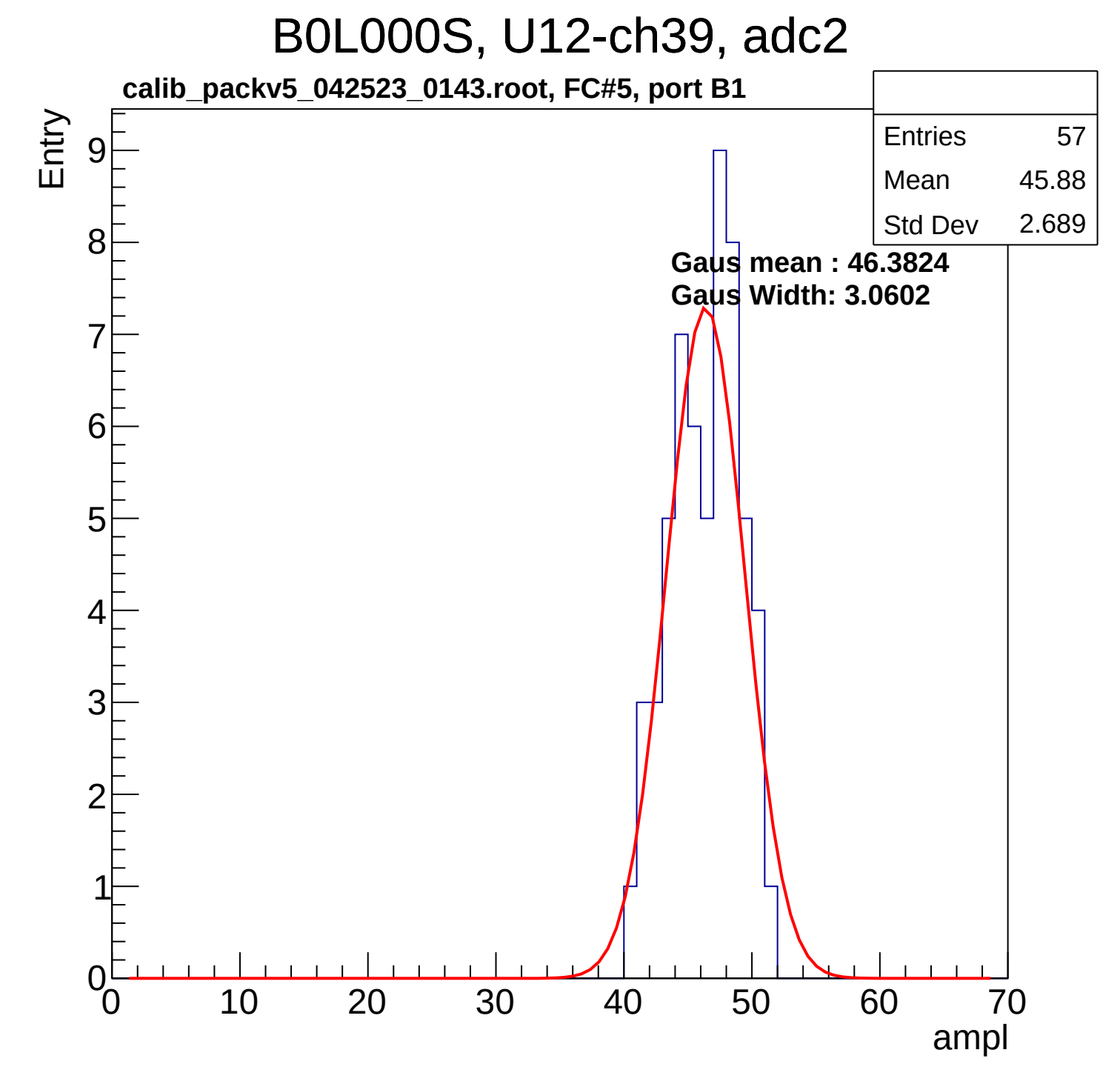

B0L000S, U12-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.33
Std Dev 8.613
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

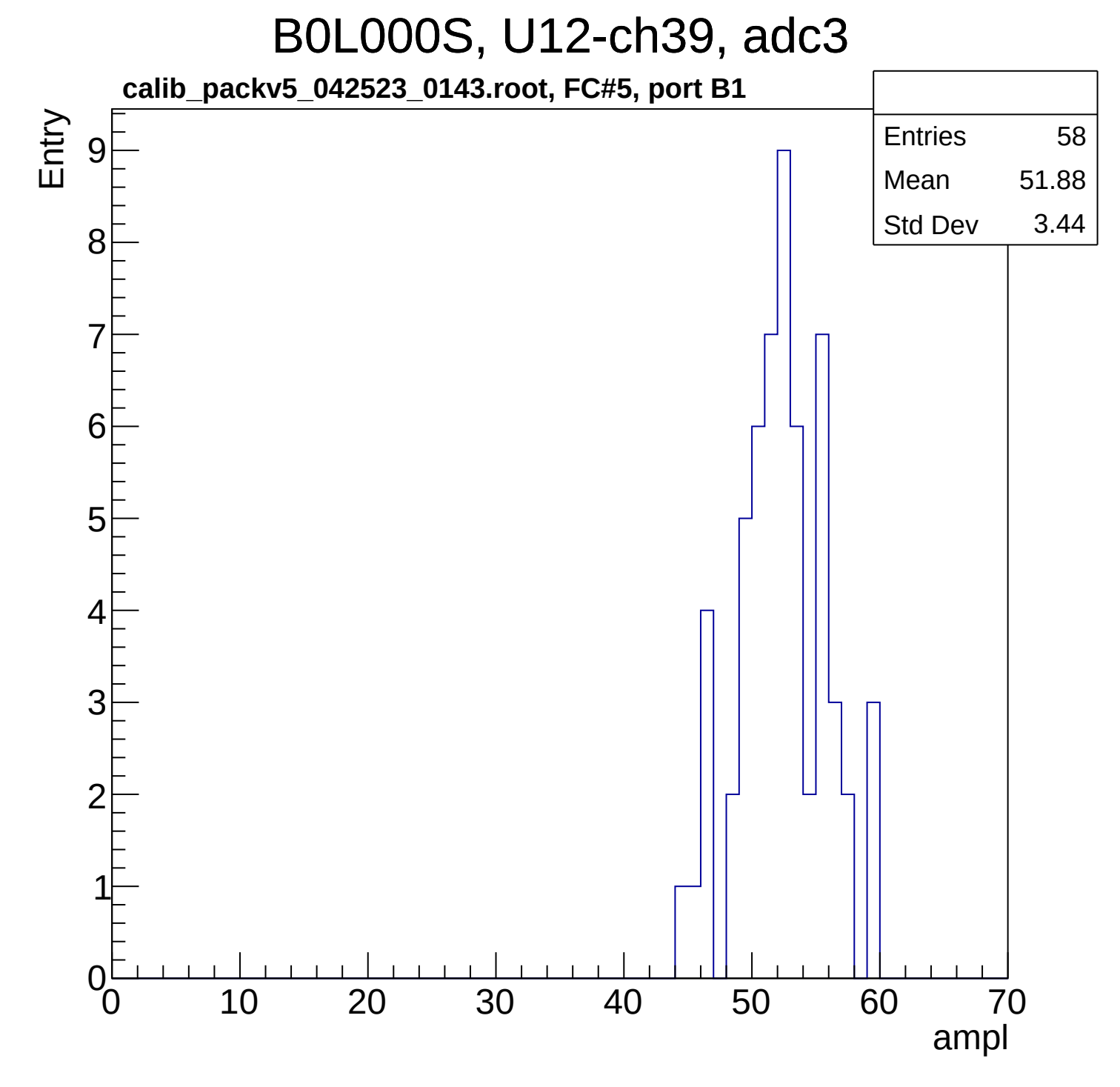

B0L000S, U12-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

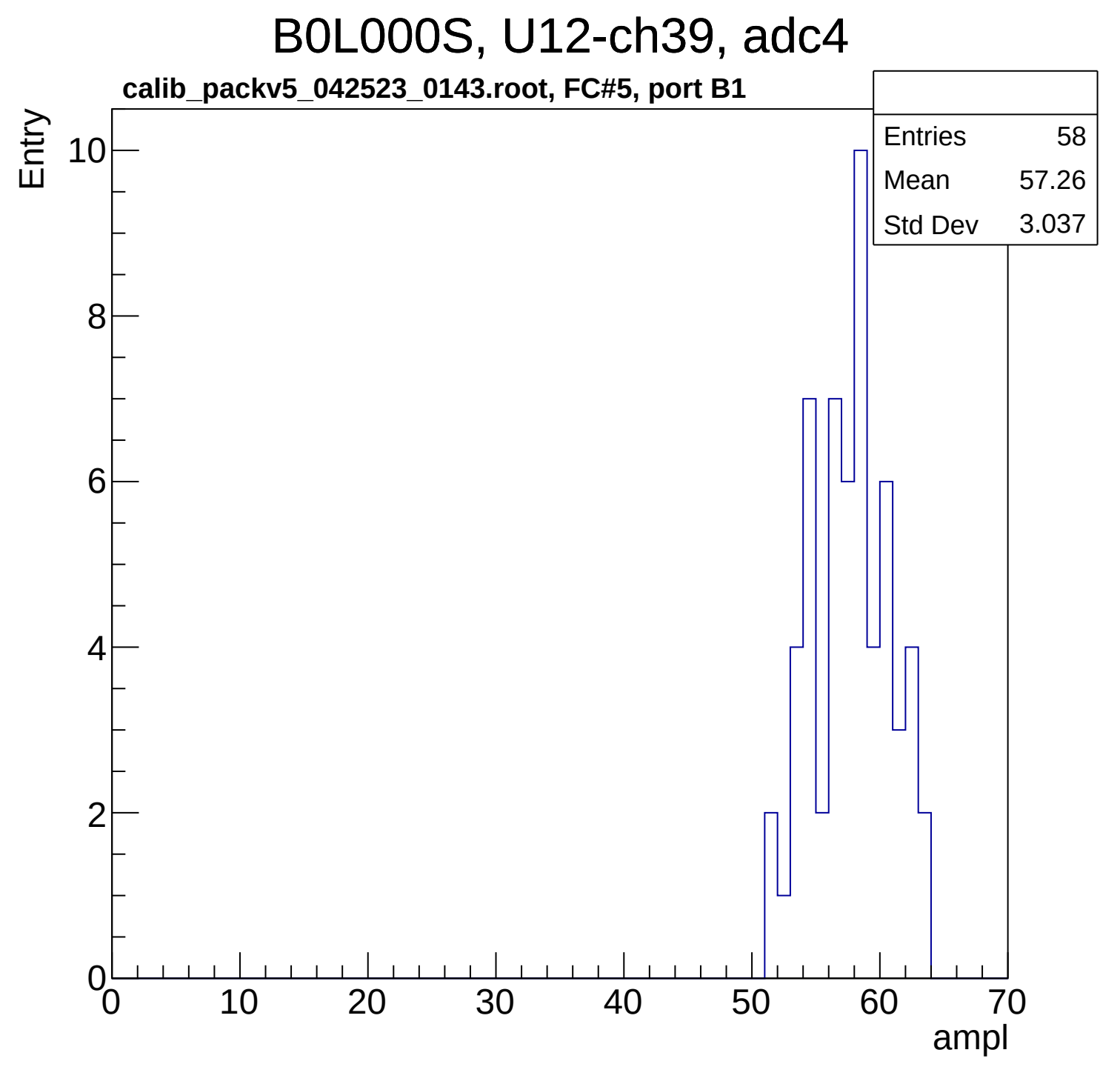

B0L000S, U12-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

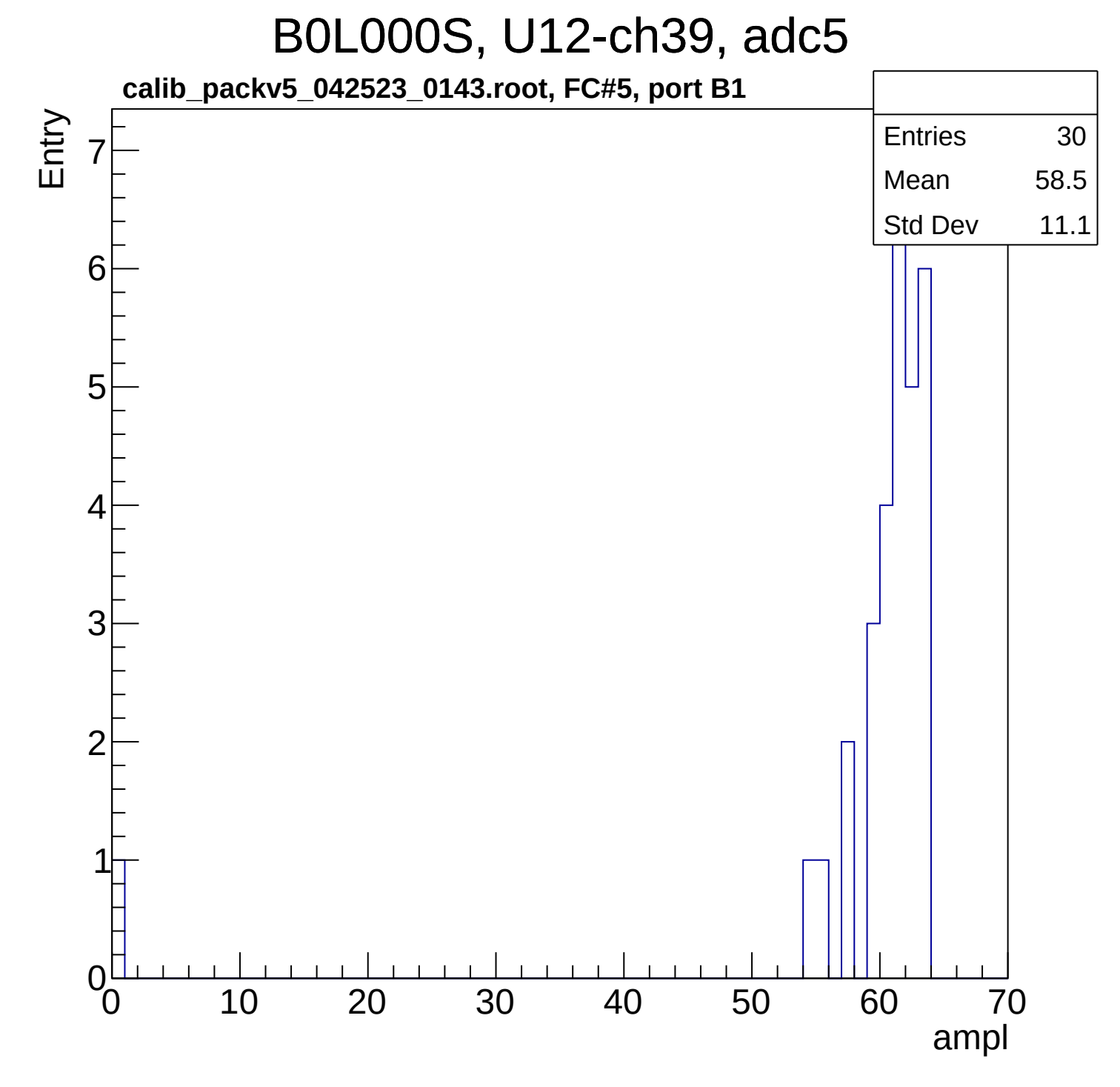

B0L000S, U12-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 31.6
Std Dev 2.825
Gaus mean : 32.4133
Gaus Width: 3.0403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

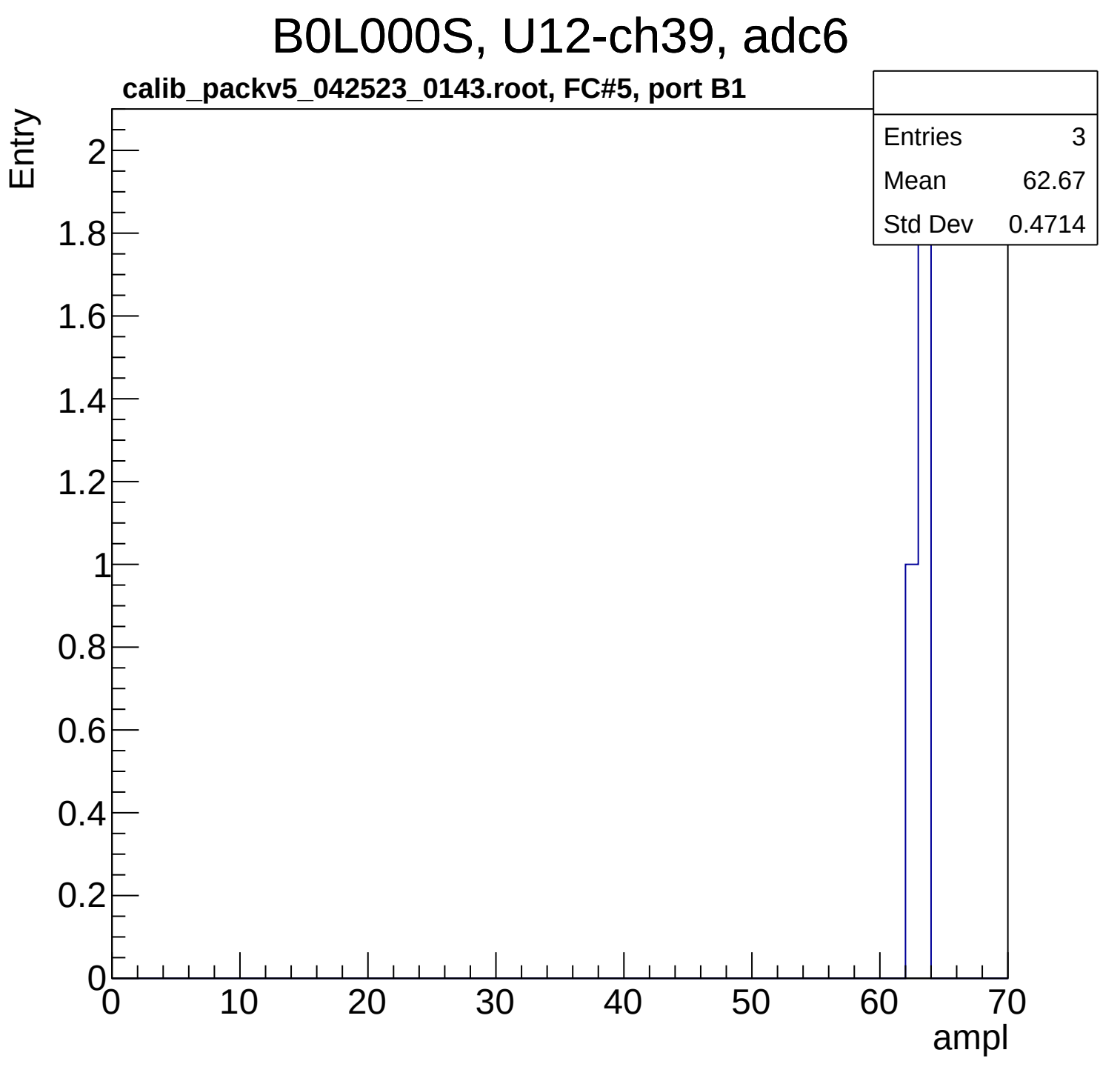

B0L000S, U12-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 38.87
Std Dev 3.4
Gaus mean : 39.3078
Gaus Width: 3.4939
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

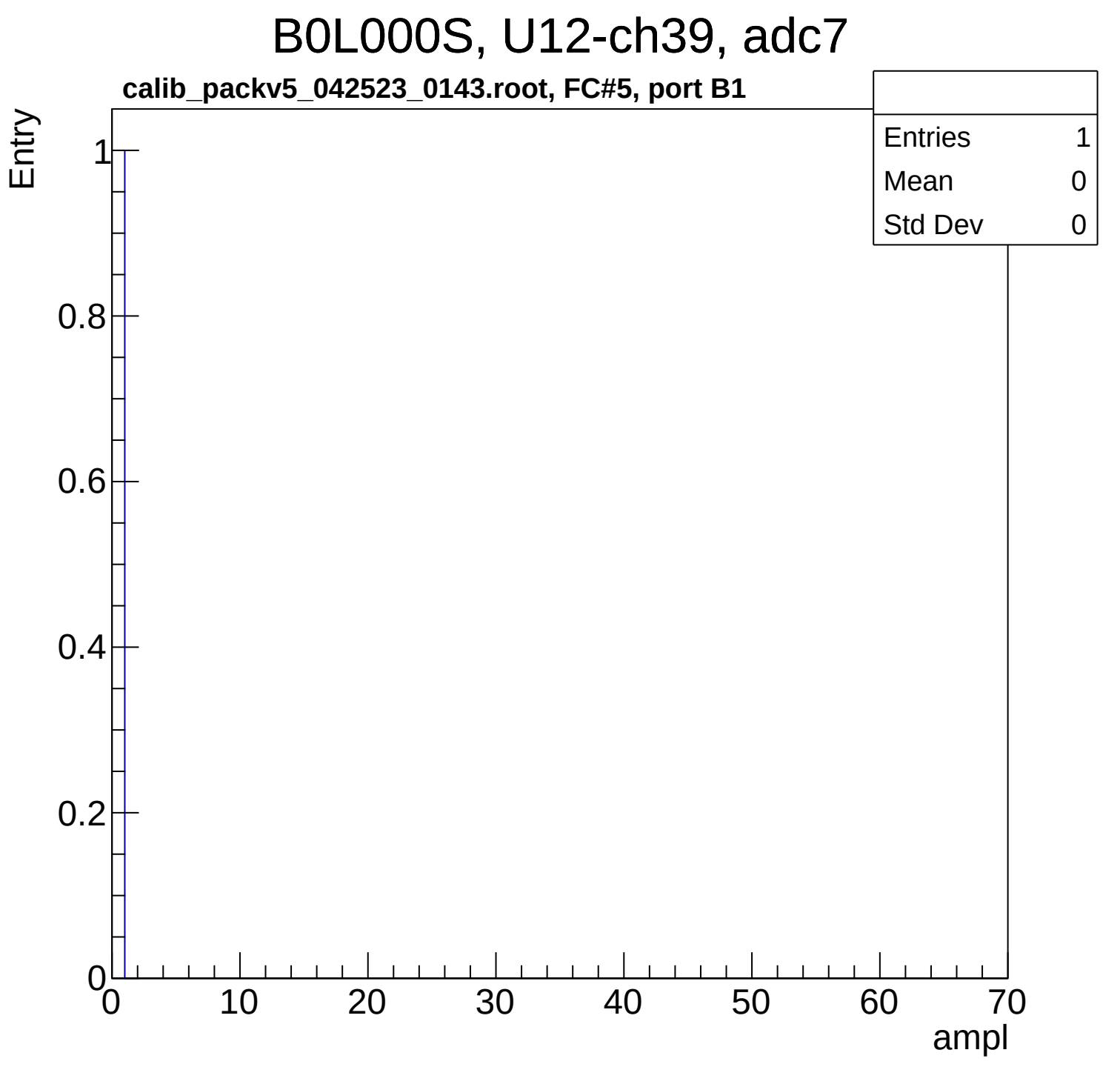

B0L000S, U12-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 45.88
Std Dev 2.689
Gaus mean : 46.3824
Gaus Width: 3.0602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

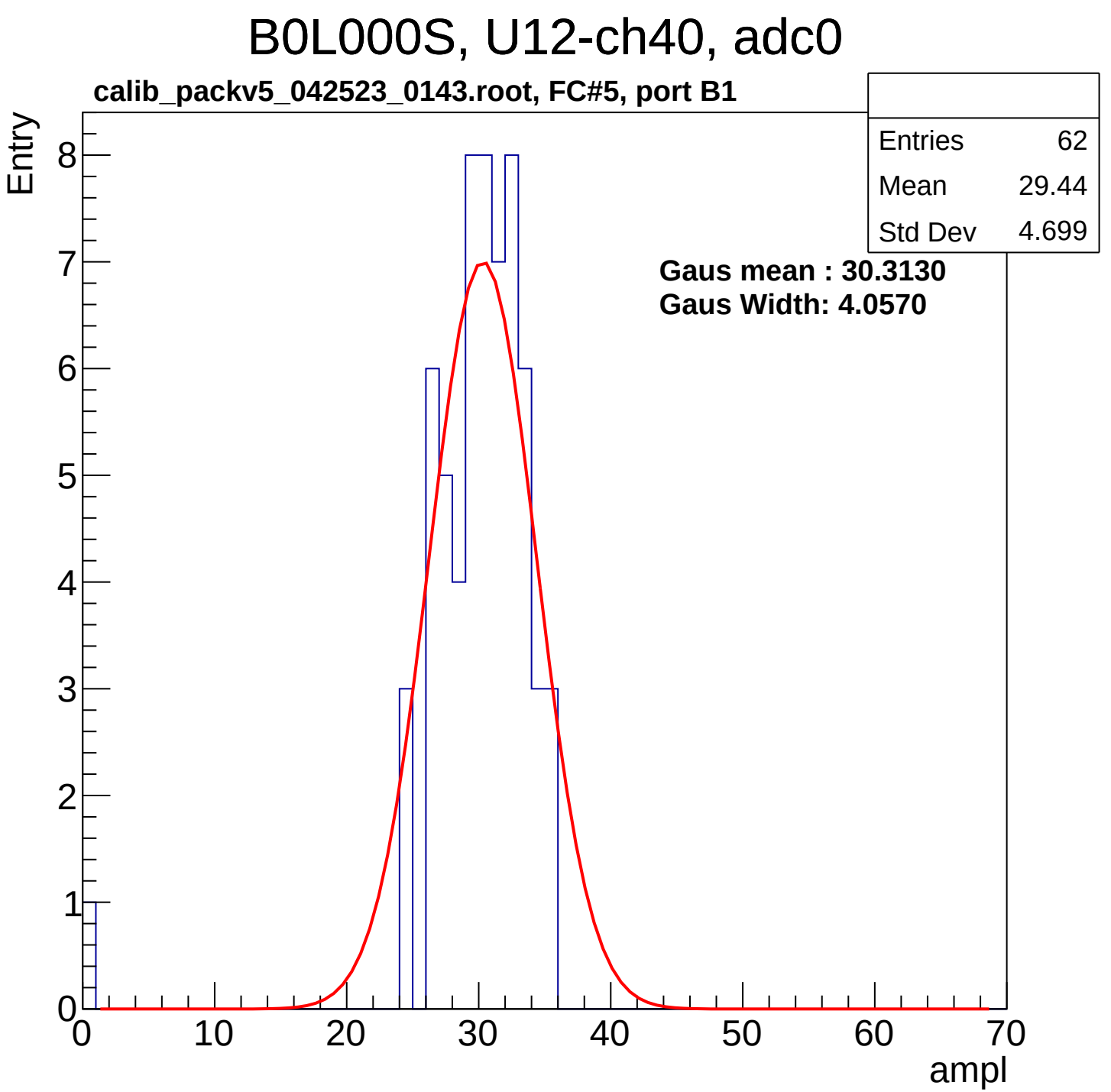

B0L000S, U12-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.88
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

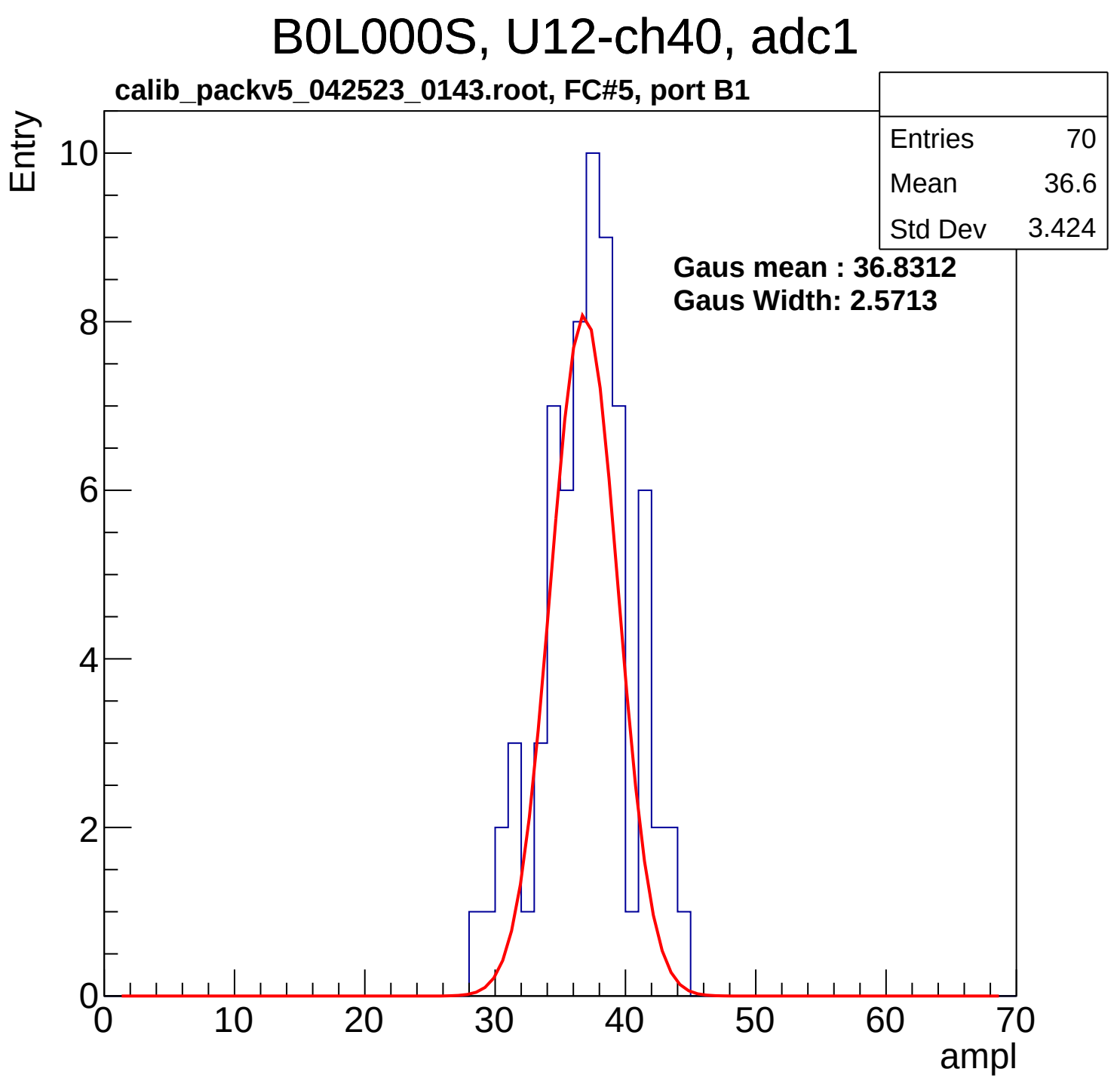

B0L000S, U12-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.26
Std Dev 3.037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

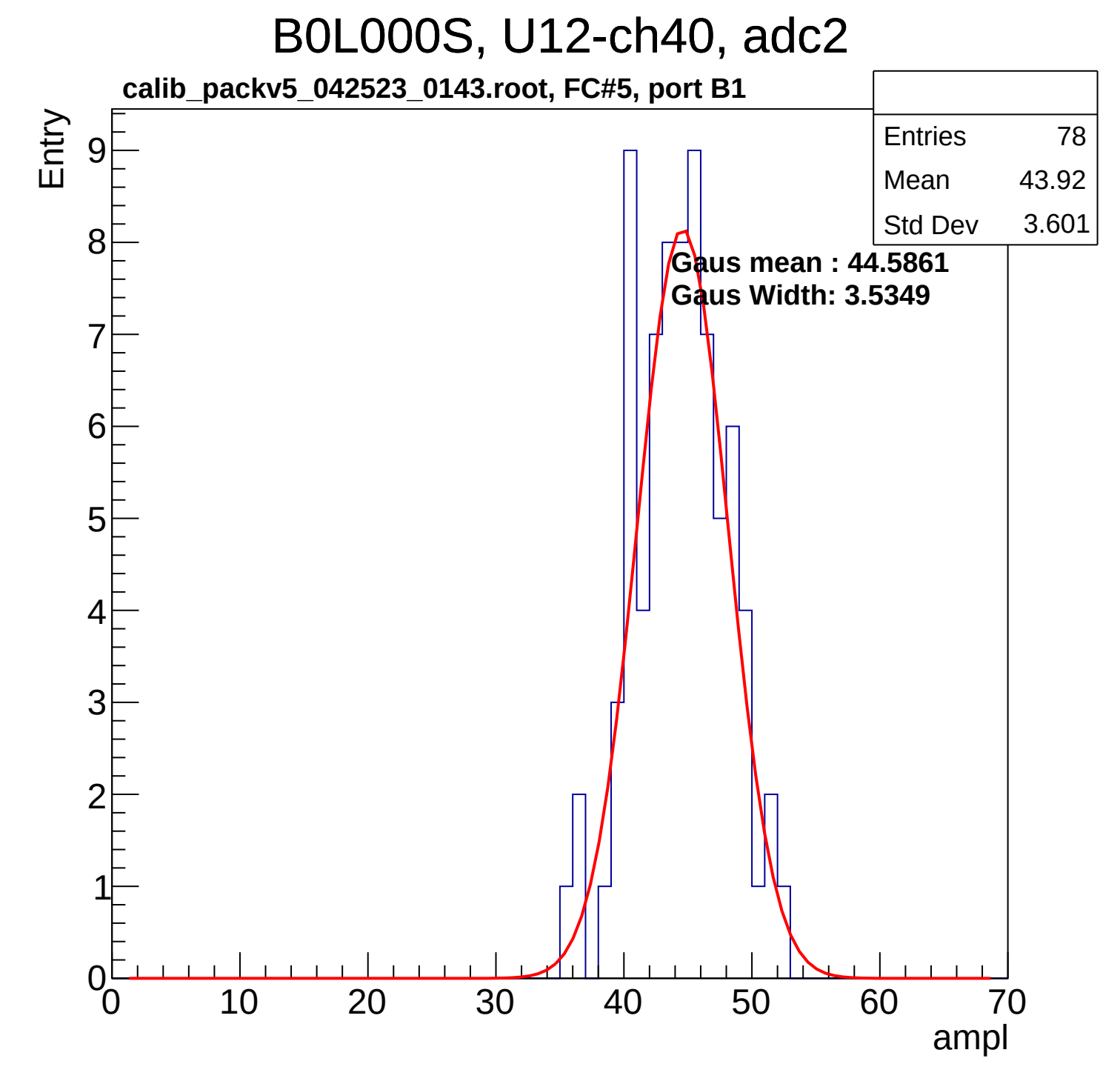

B0L000S, U12-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.5
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

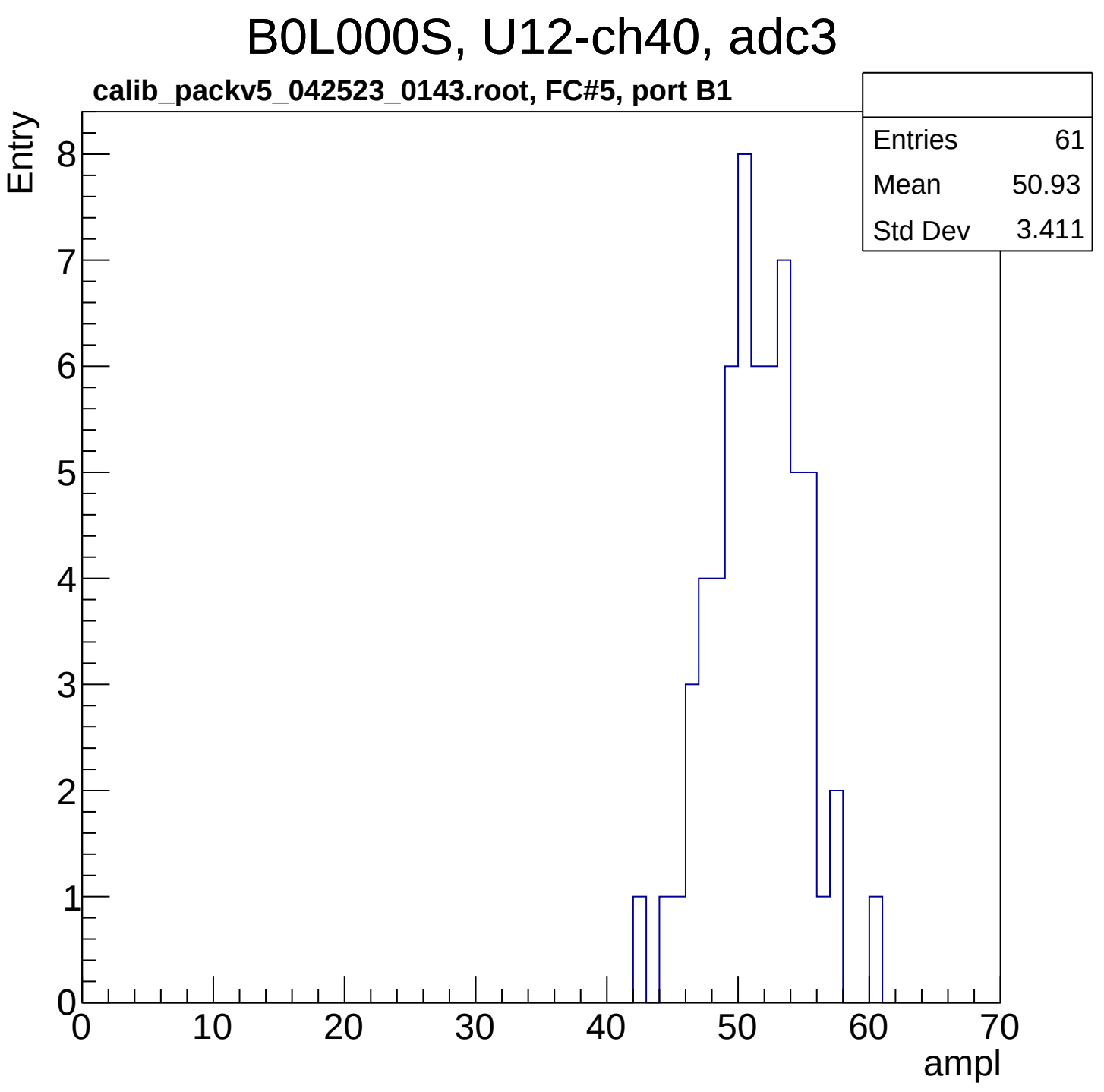

B0L000S, U12-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

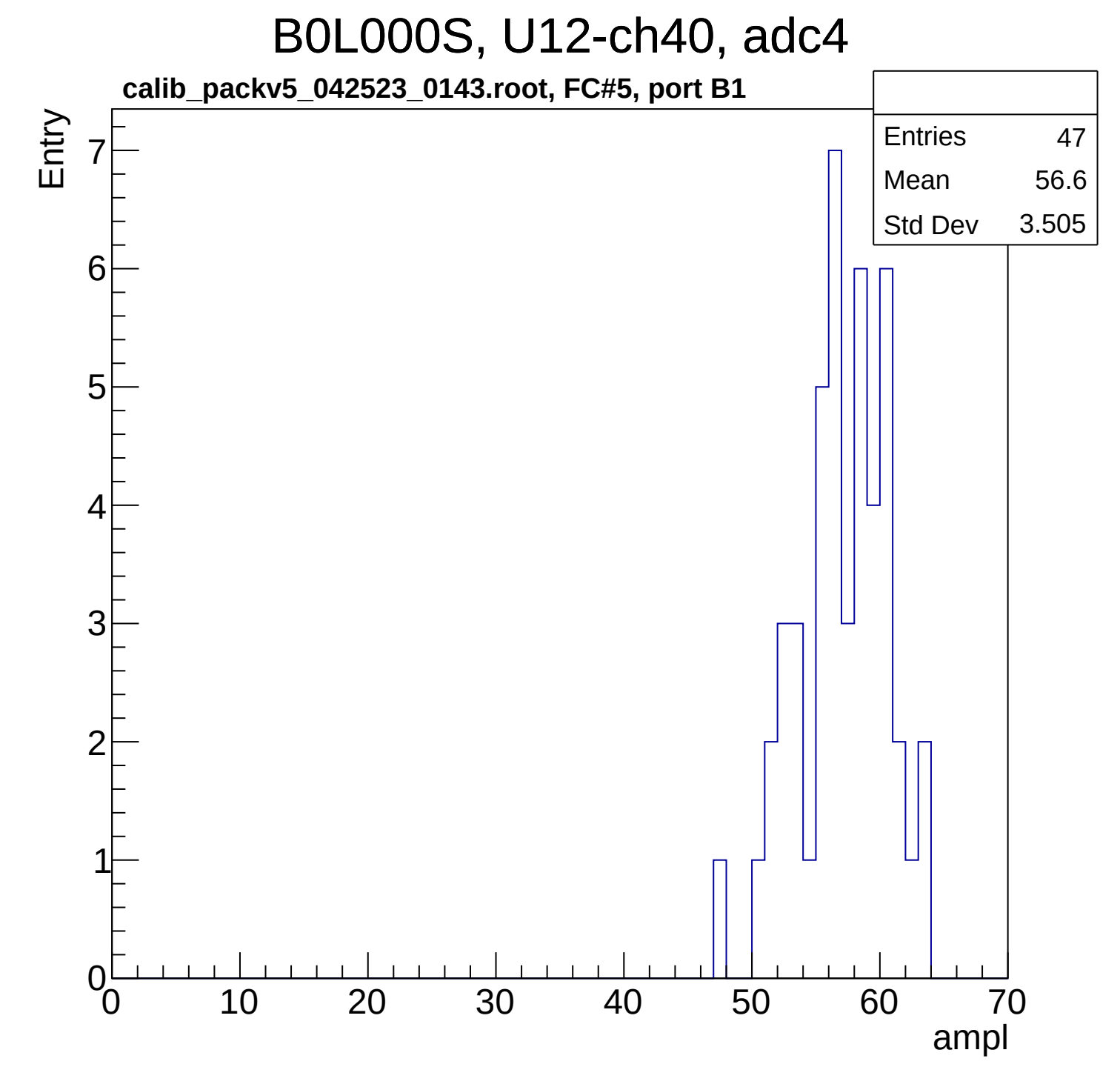

B0L000S, U12-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

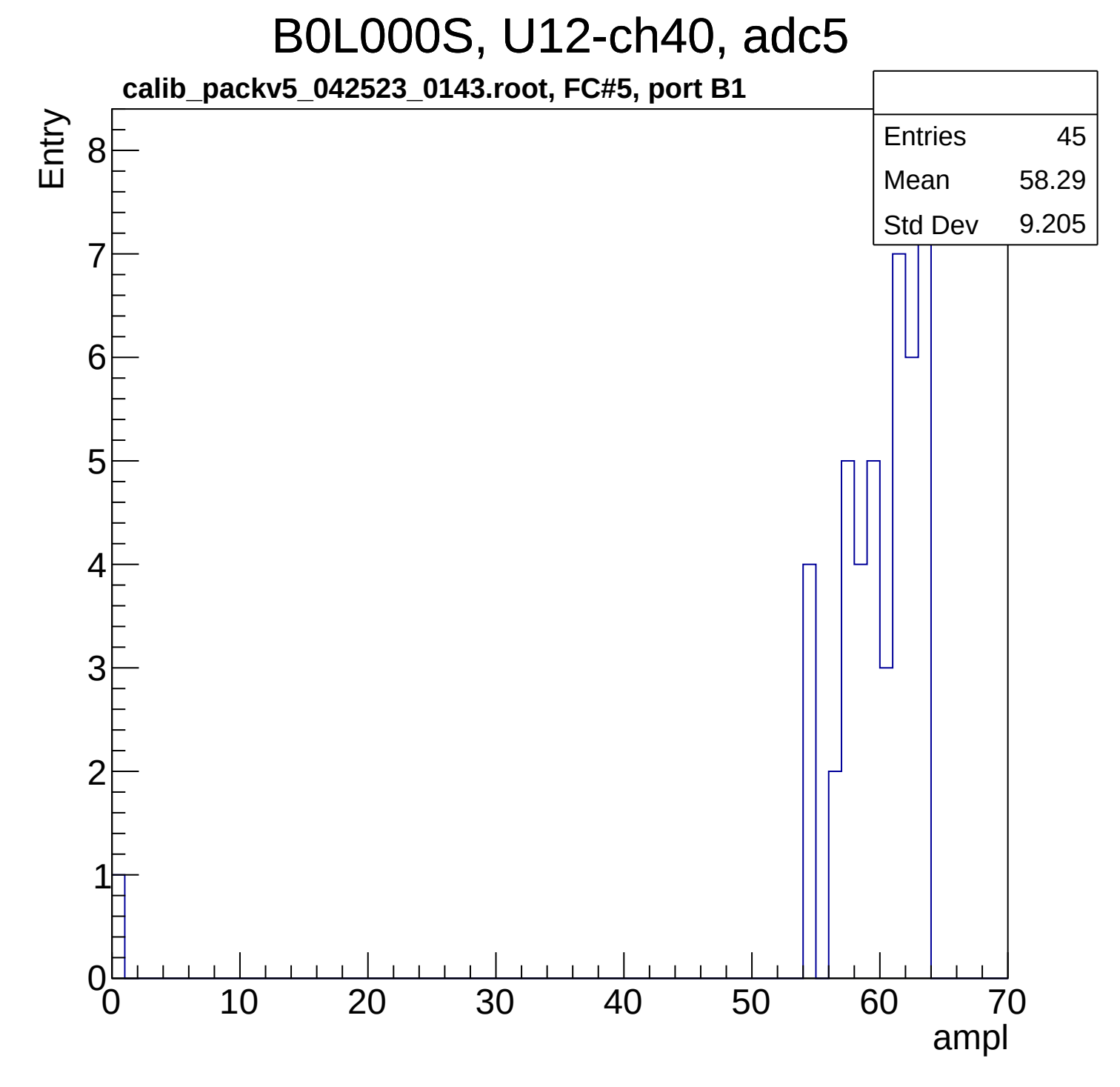

B0L000S, U12-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.44
Std Dev 4.699
Gaus mean : 30.3130
Gaus Width: 4.0570
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

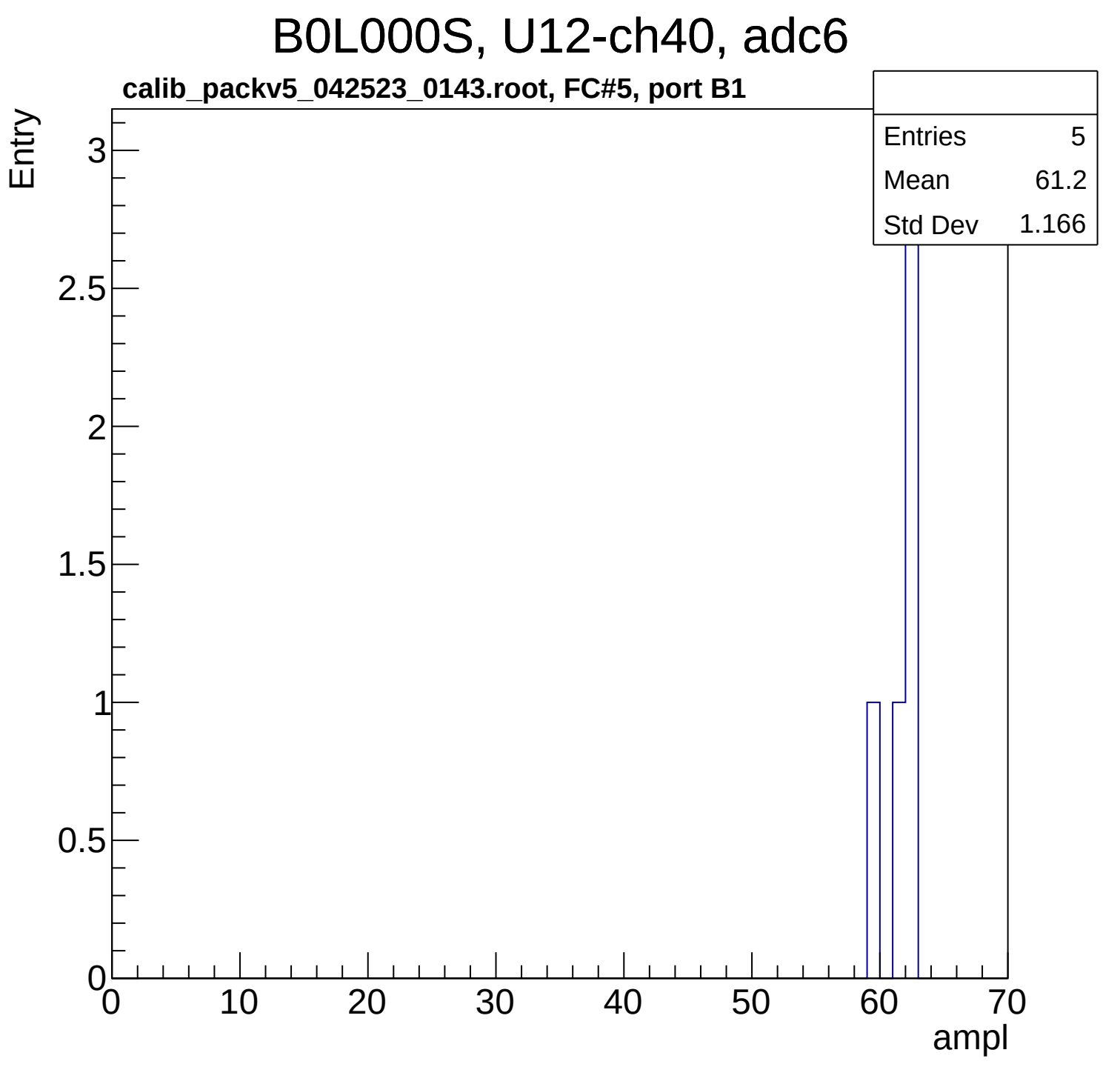

B0L000S, U12-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.6
Std Dev 3.424
Gaus mean : 36.8312
Gaus Width: 2.5713
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.92
Std Dev 3.601
Gaus mean : 44.5861
Gaus Width: 3.5349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.93
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 56.6
Std Dev 3.505
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.29
Std Dev 9.205
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.2
Std Dev 1.166
Entry
ampl
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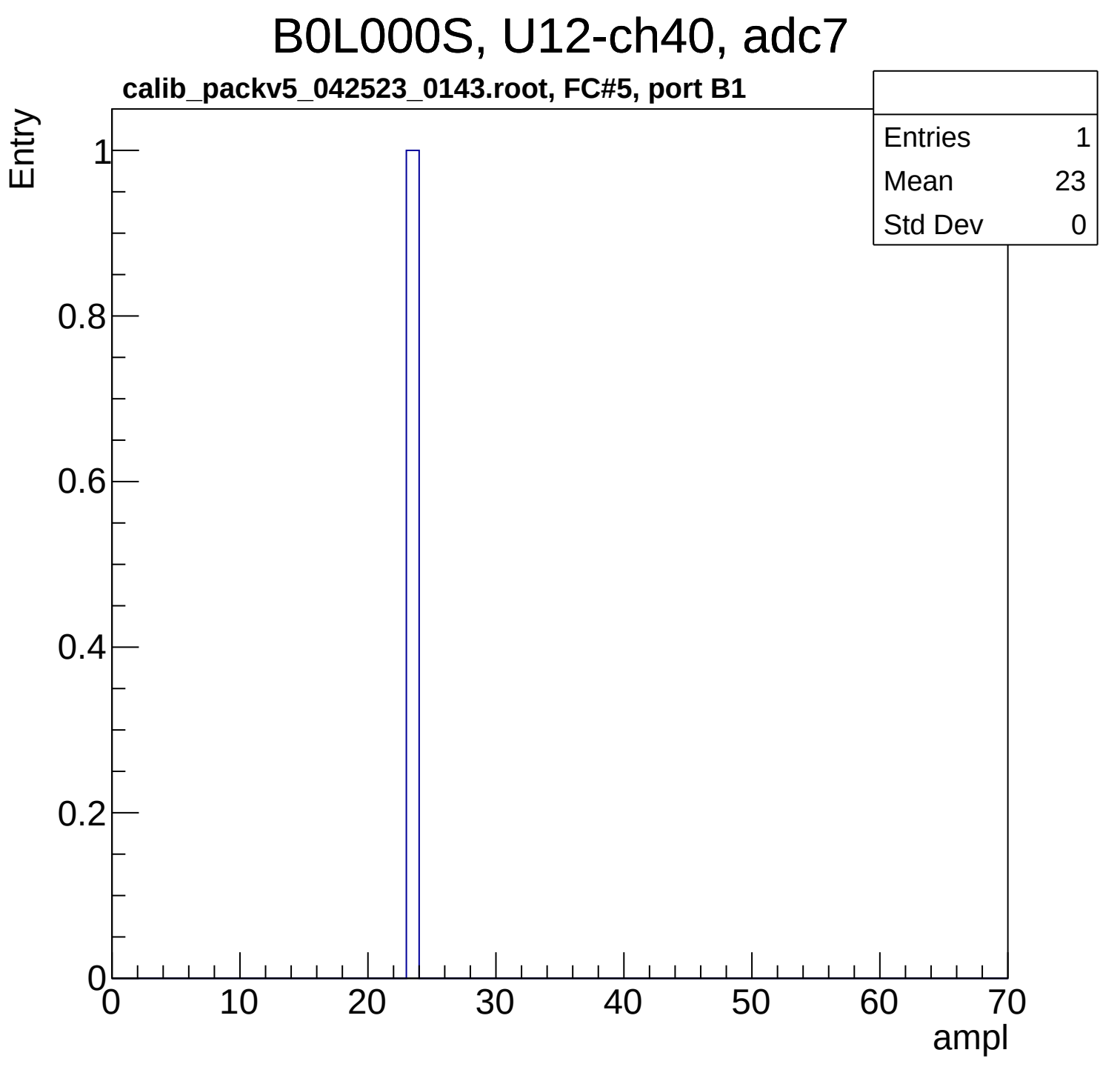
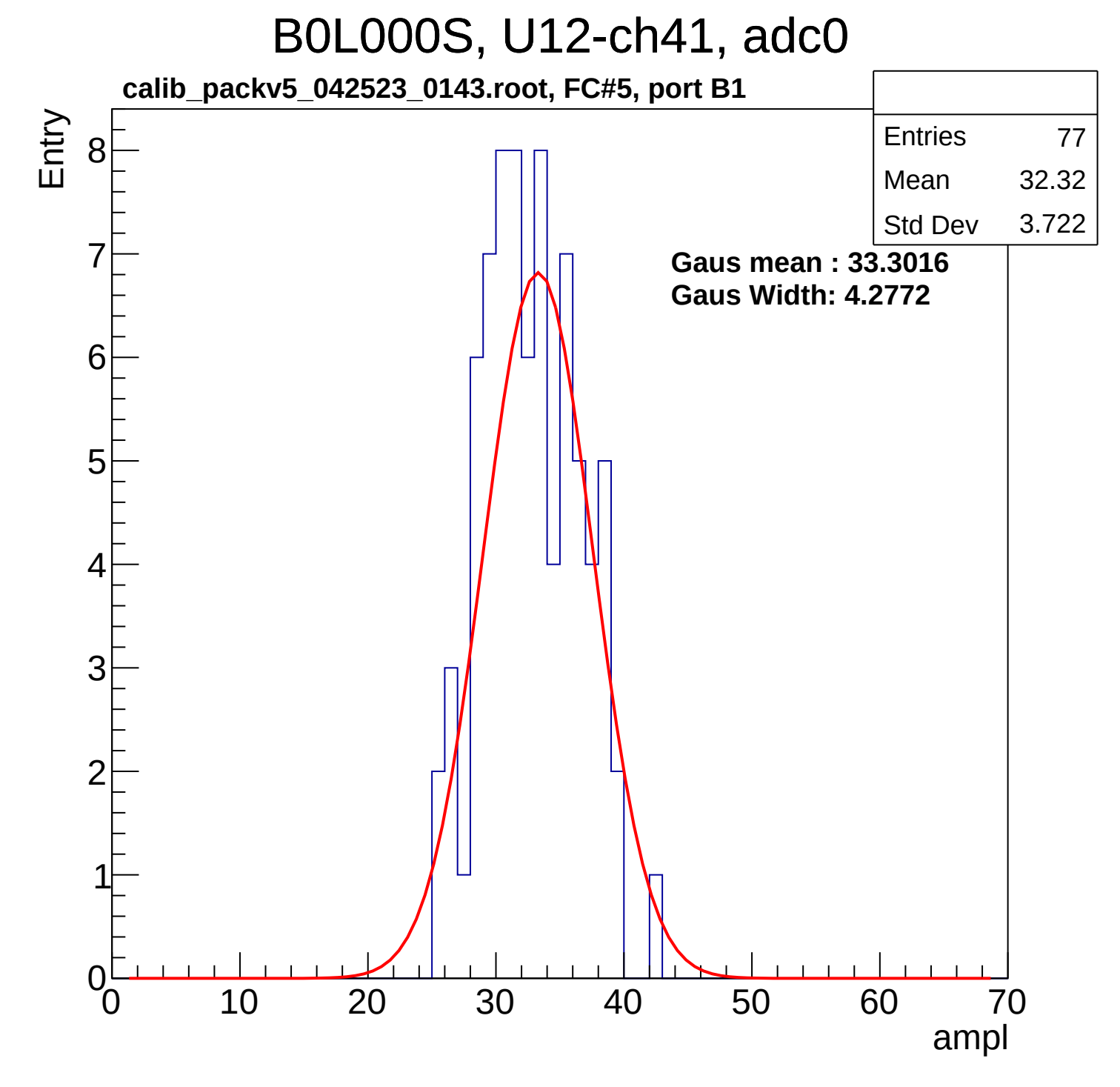
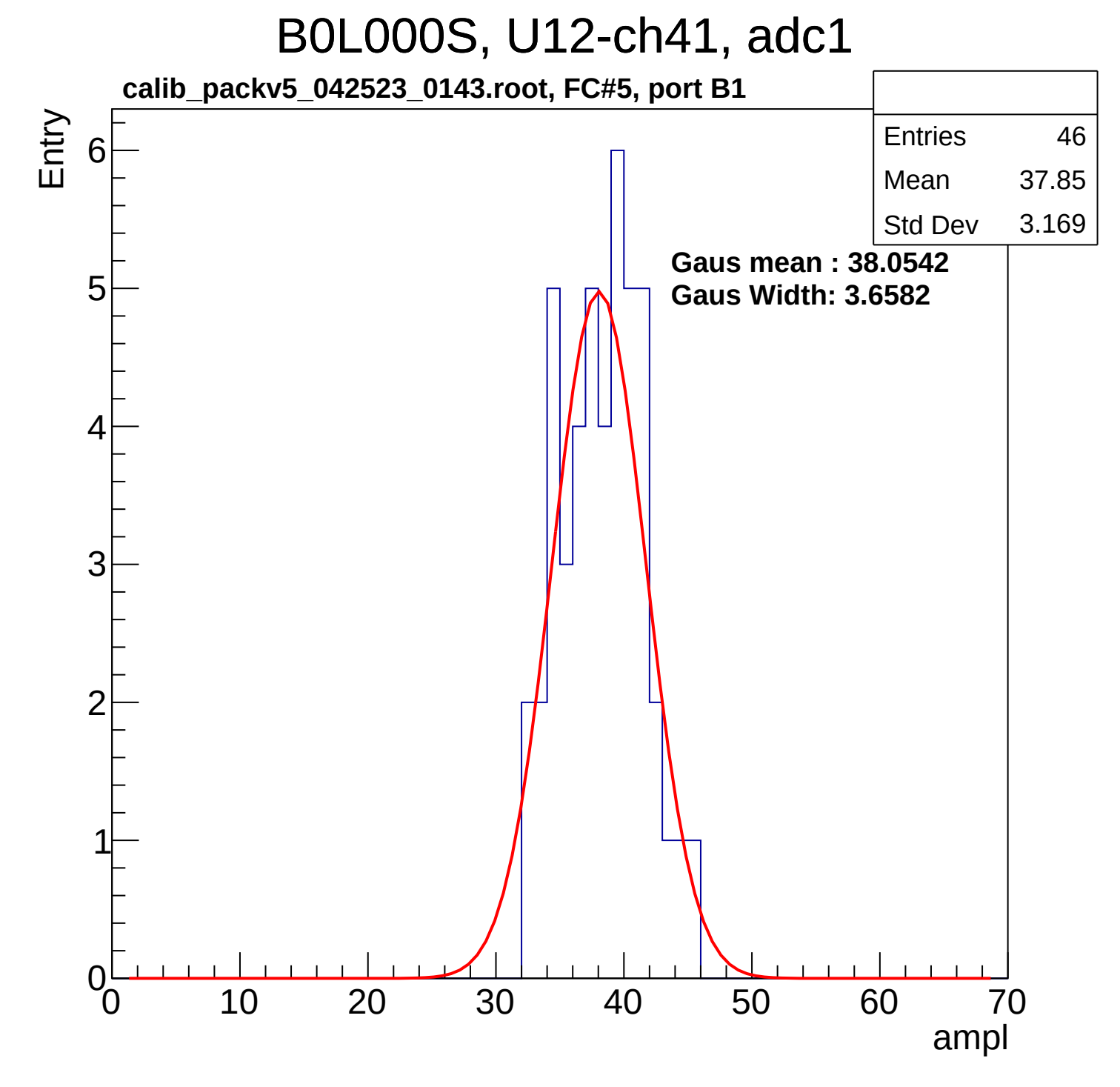
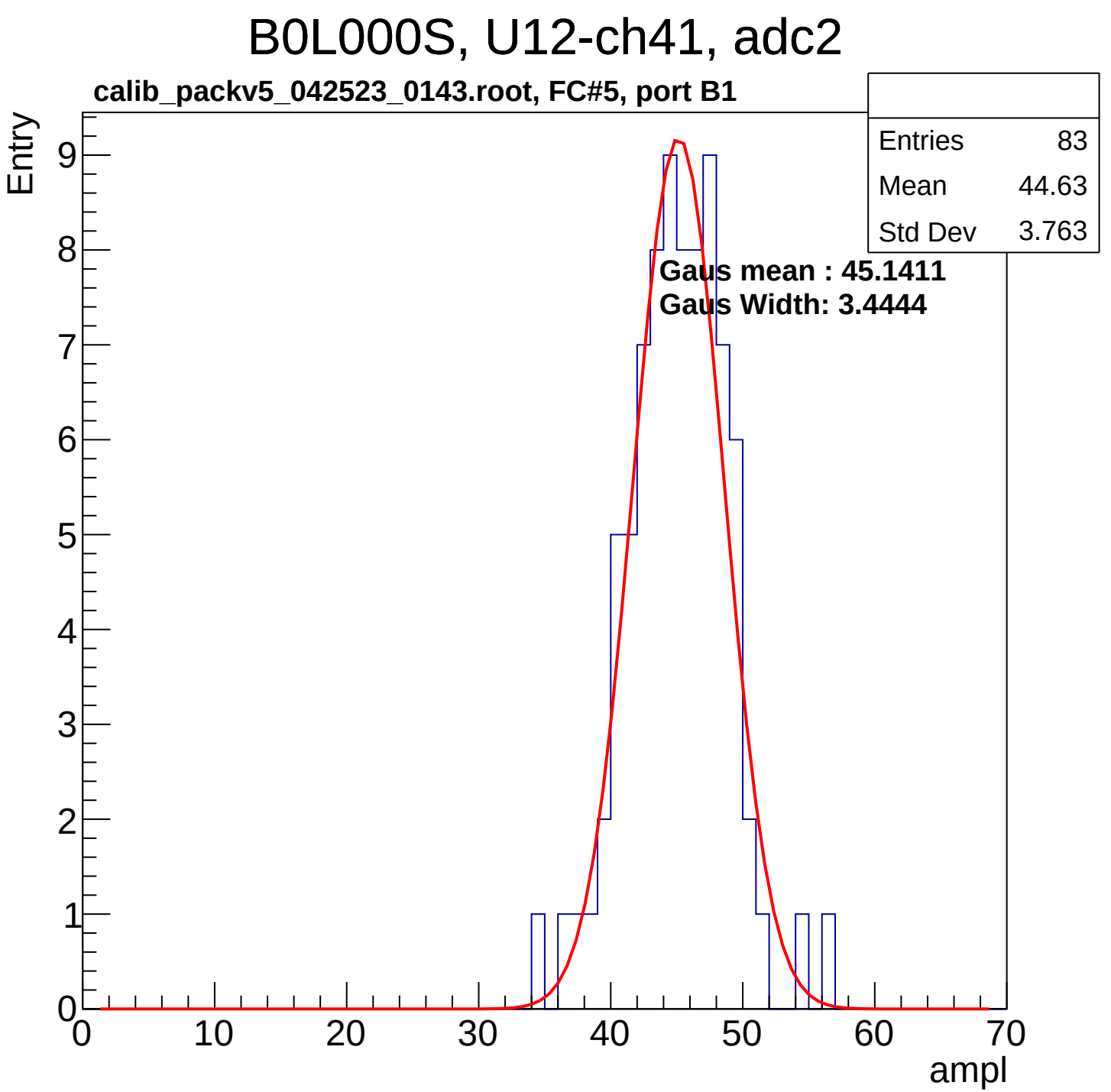
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 32.32
Std Dev 3.722
Gaus mean : 33.3016
Gaus Width: 4.2772
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 37.85
Std Dev 3.169
Gaus mean : 38.0542
Gaus Width: 3.6582
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U12-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 44.63
Std Dev 3.763
Gaus mean : 45.1411
Gaus Width: 3.4444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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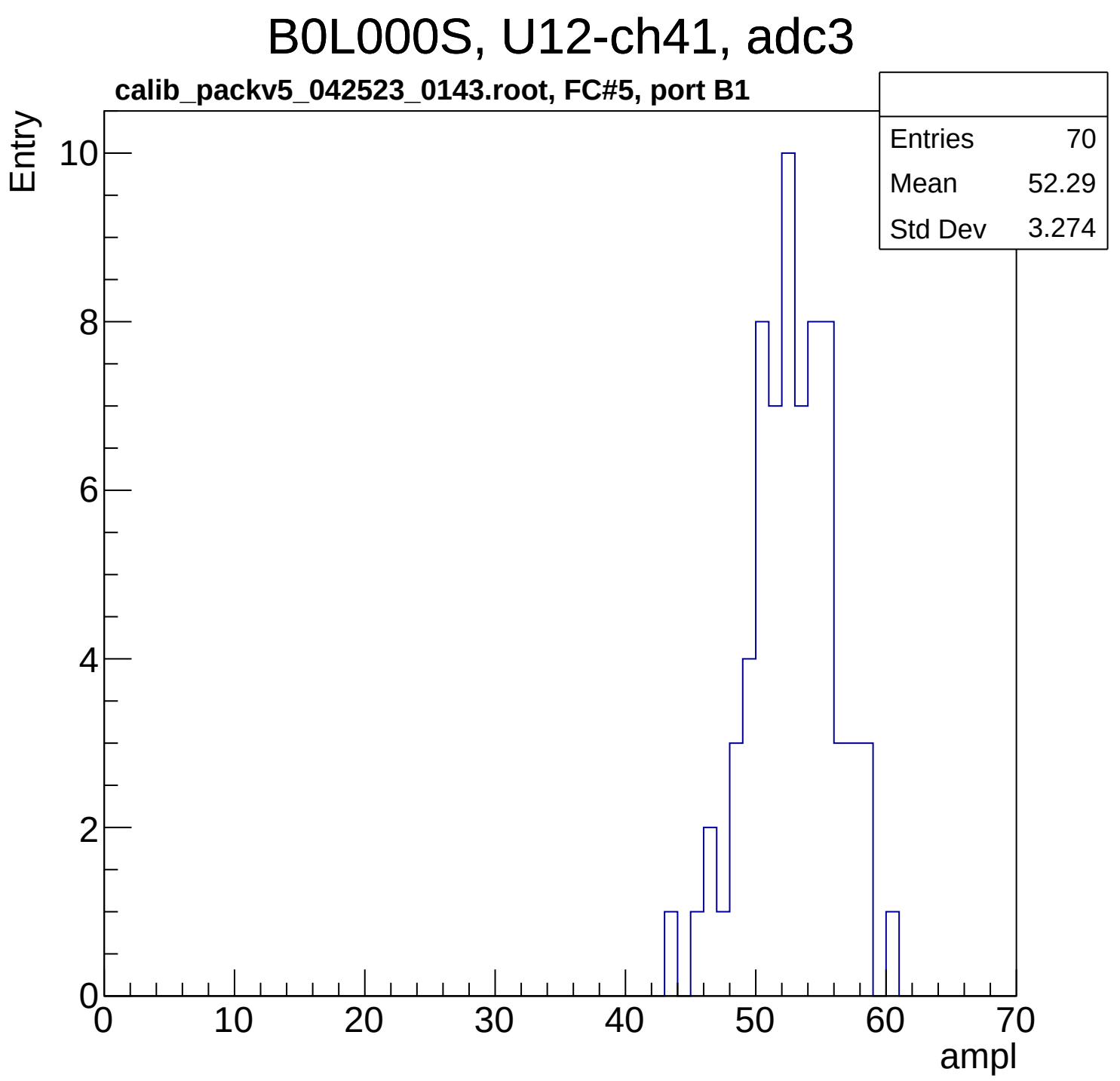
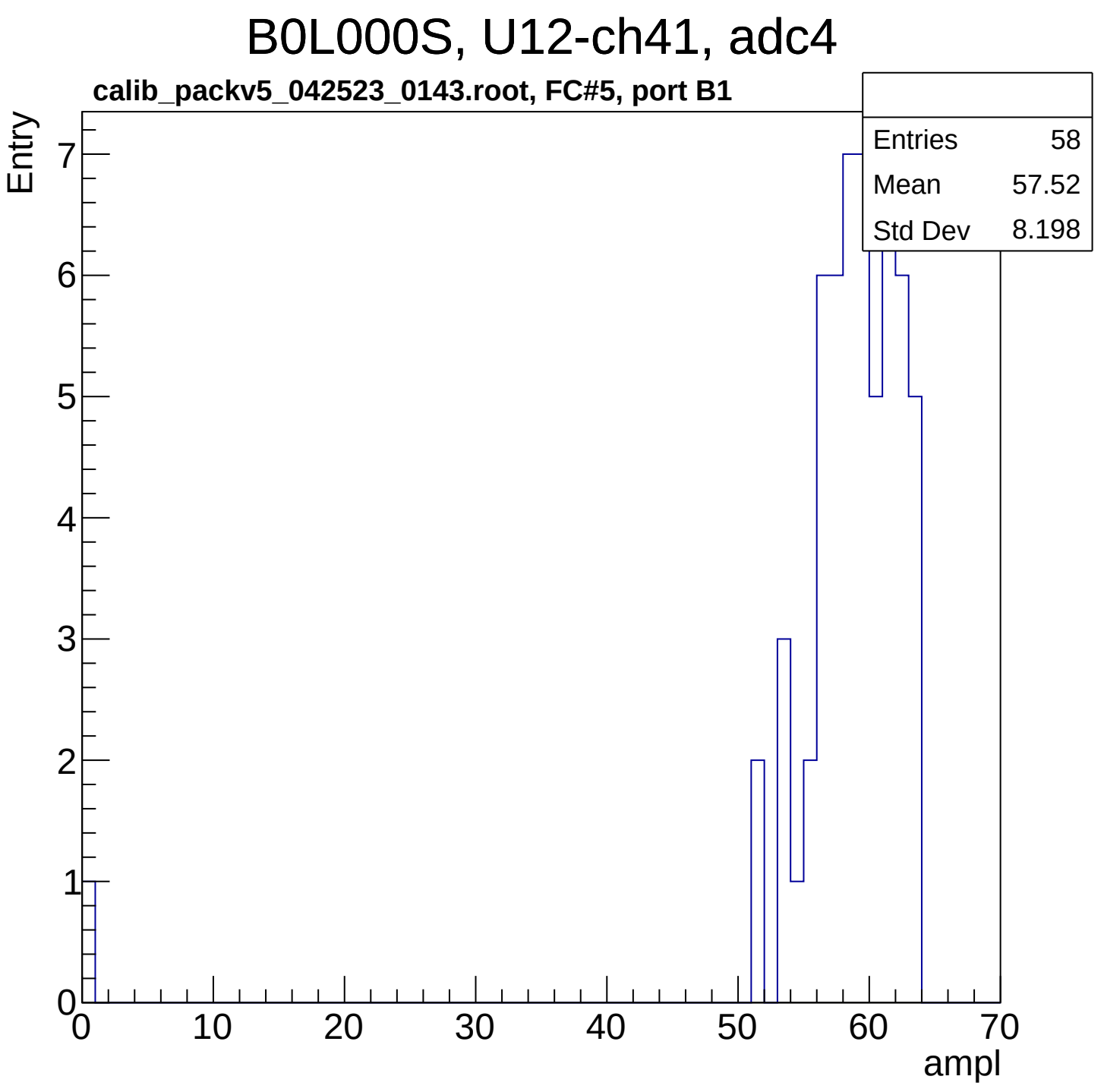
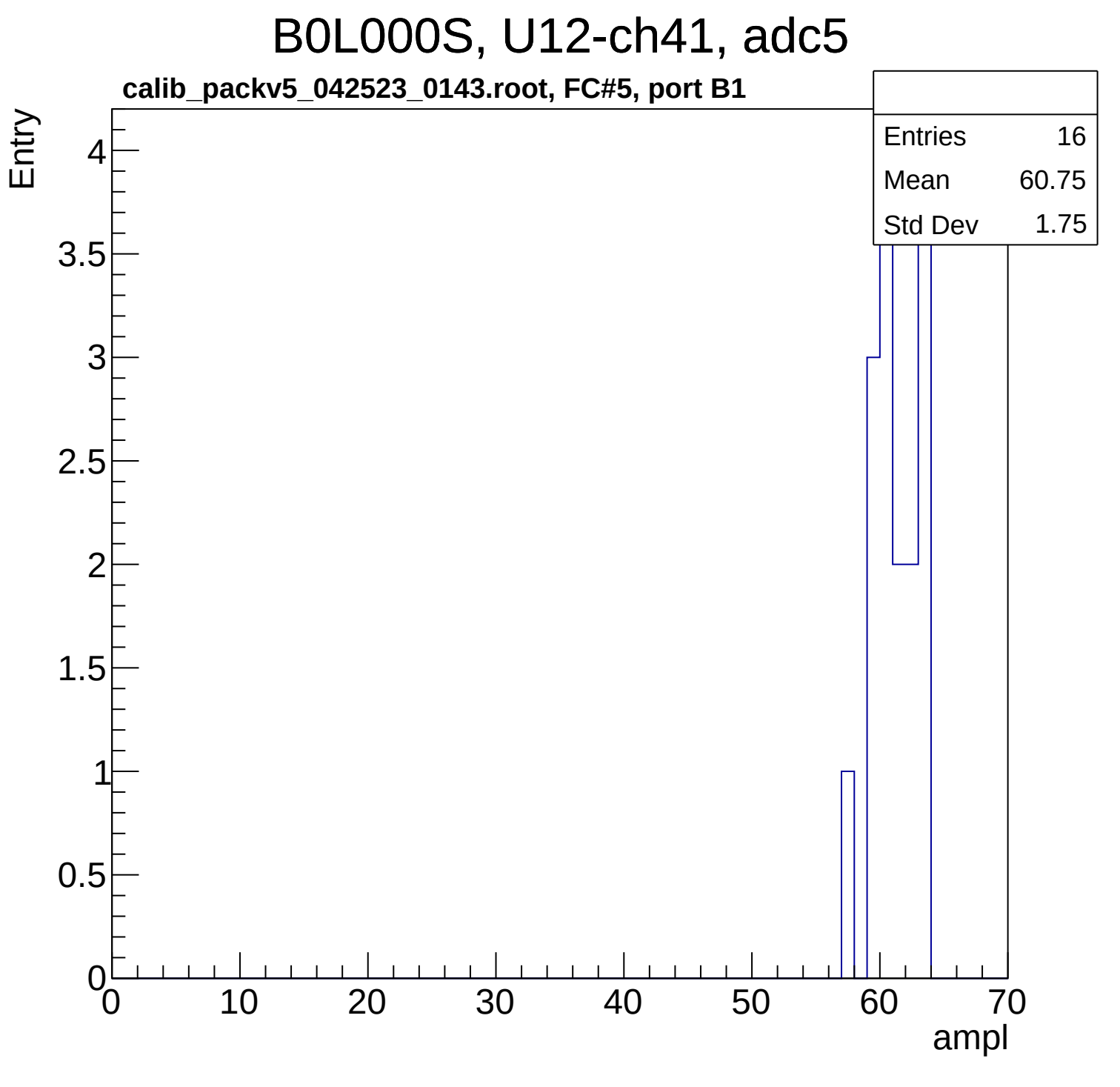

B0L000S, U12-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.29
Std Dev 3.274
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.52
Std Dev 8.198
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 60.75
Std Dev 1.75
Entry
ampl
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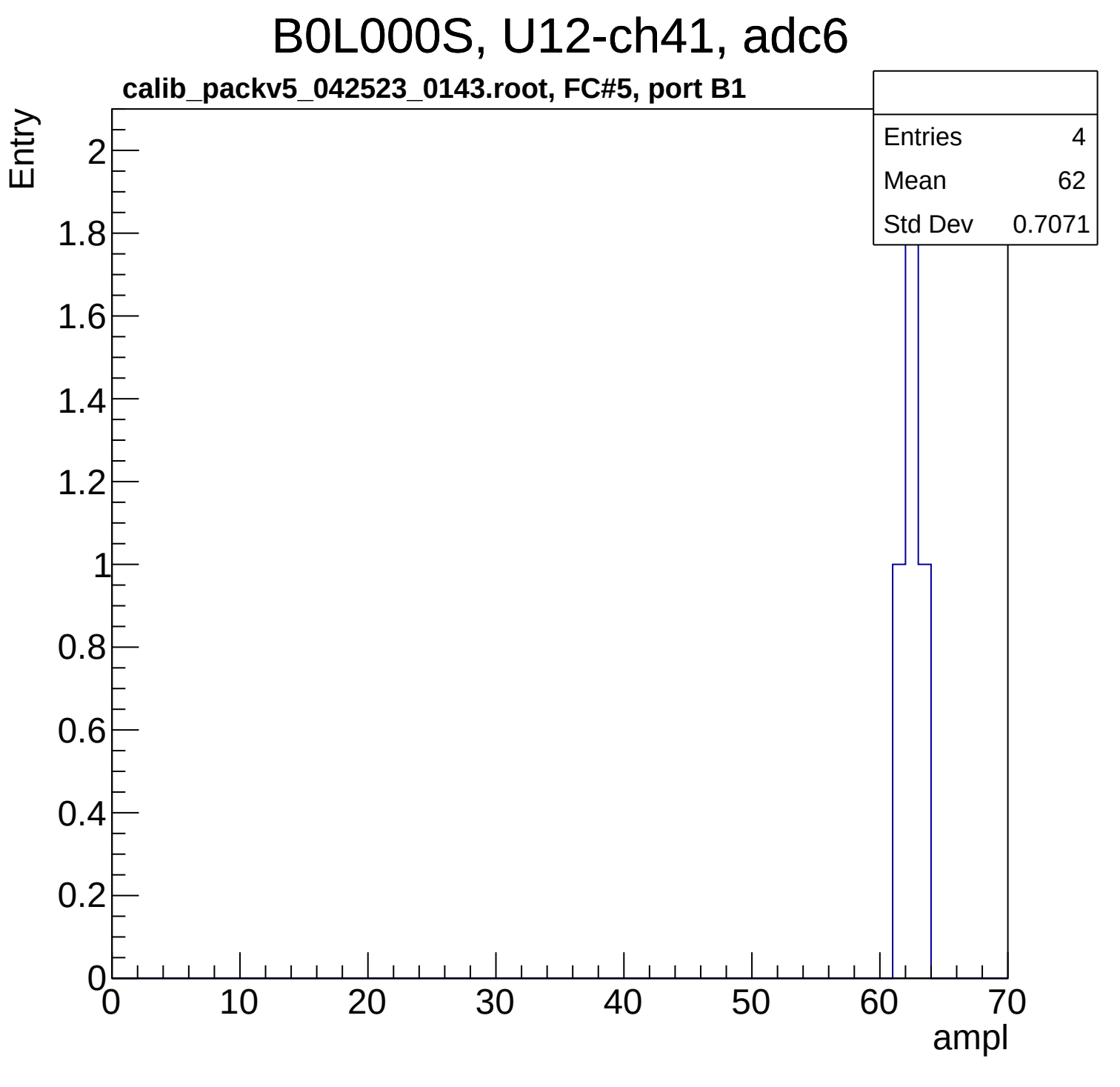
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U12-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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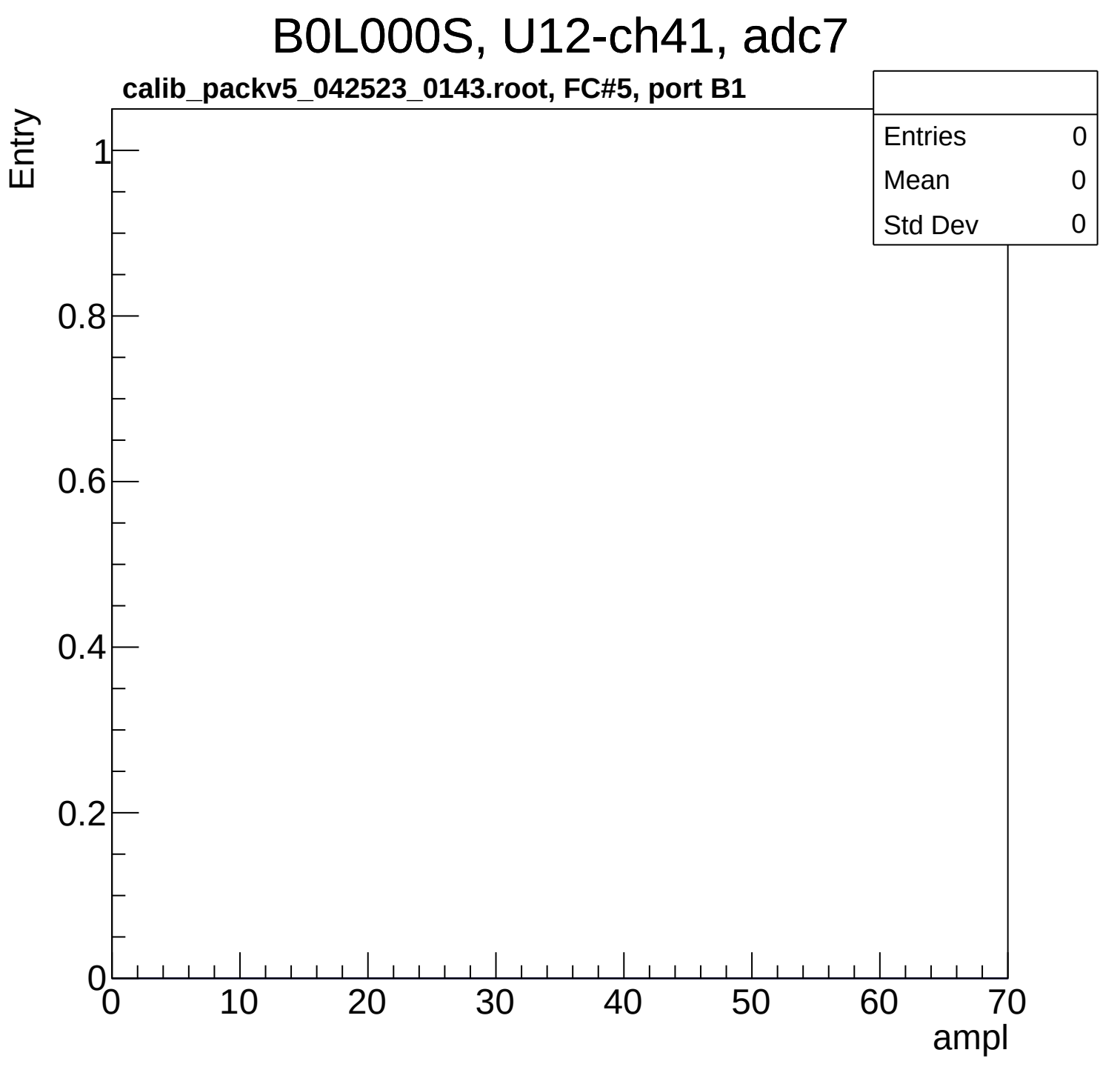
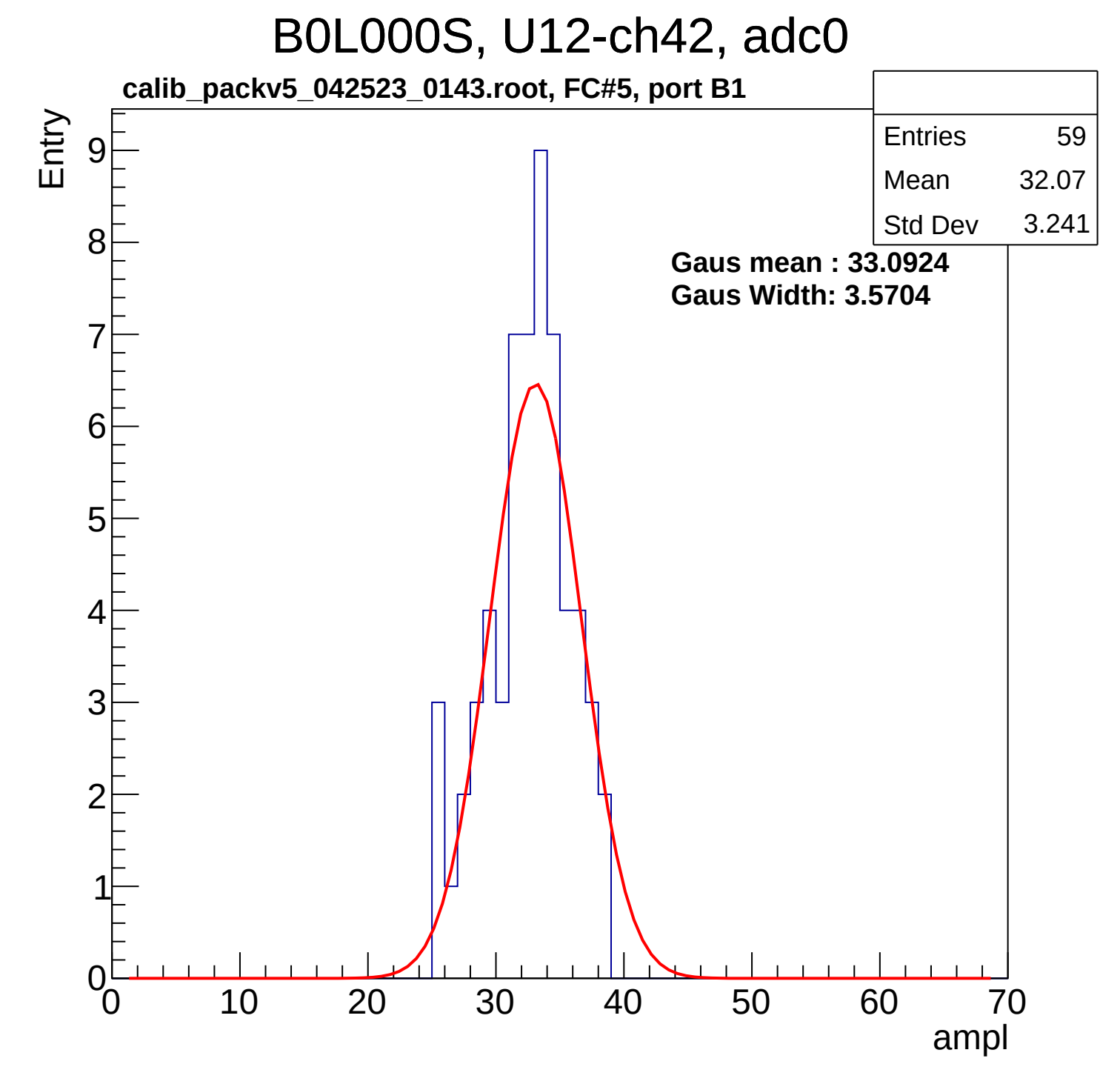
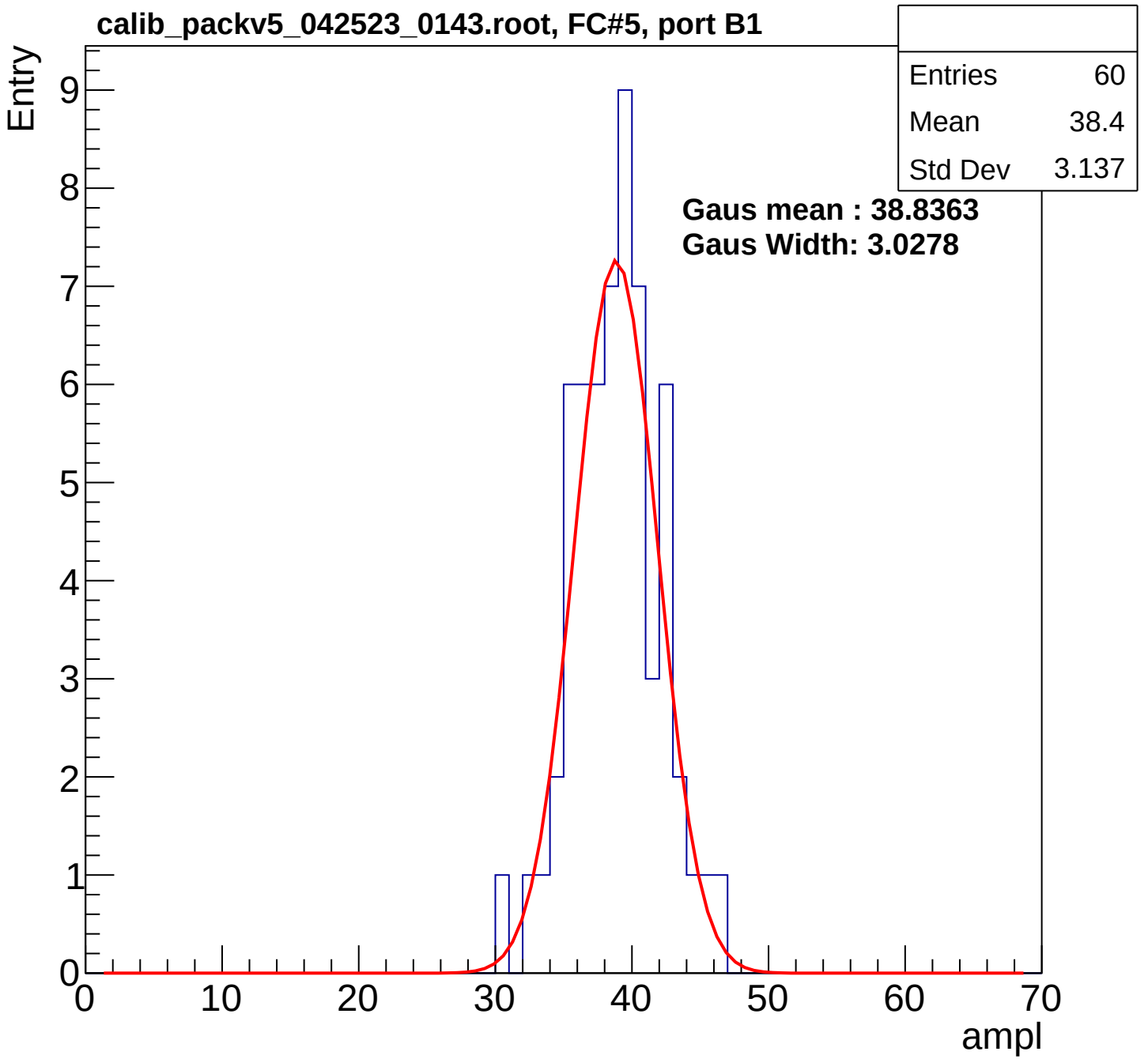

B0L000S, U12-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 32.07
Std Dev 3.241
Gaus mean : 33.0924
Gaus Width: 3.5704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 38.4
Std Dev 3.137
Gaus mean : 38.8363
Gaus Width: 3.0278
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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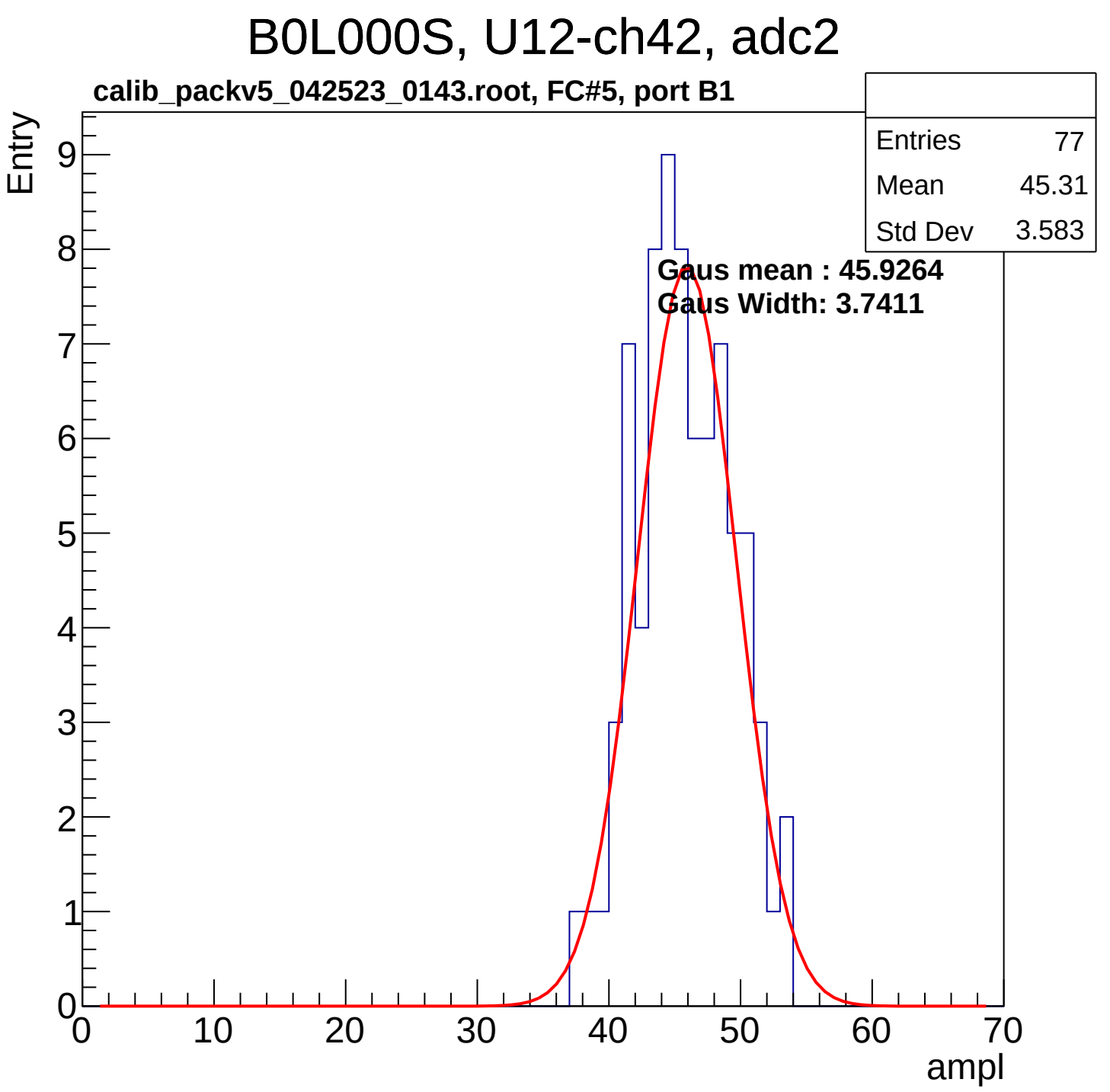
B0L000S, U12-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 45.31
Std Dev 3.583
Gaus mean : 45.9264
Gaus Width: 3.7411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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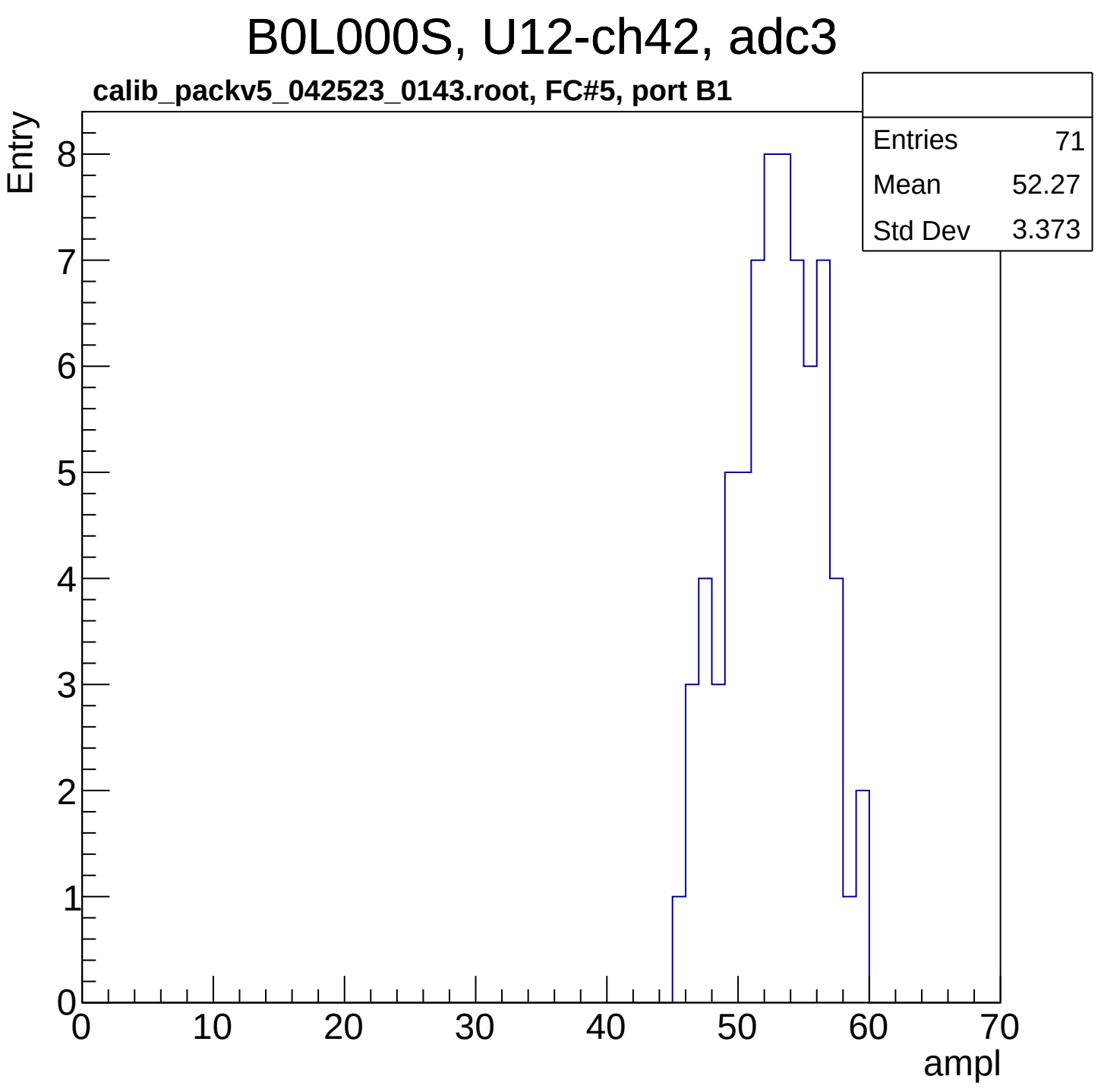
B0L000S, U12-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 52.27
Std Dev 3.373
Entry
ampl
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2
3
4
5
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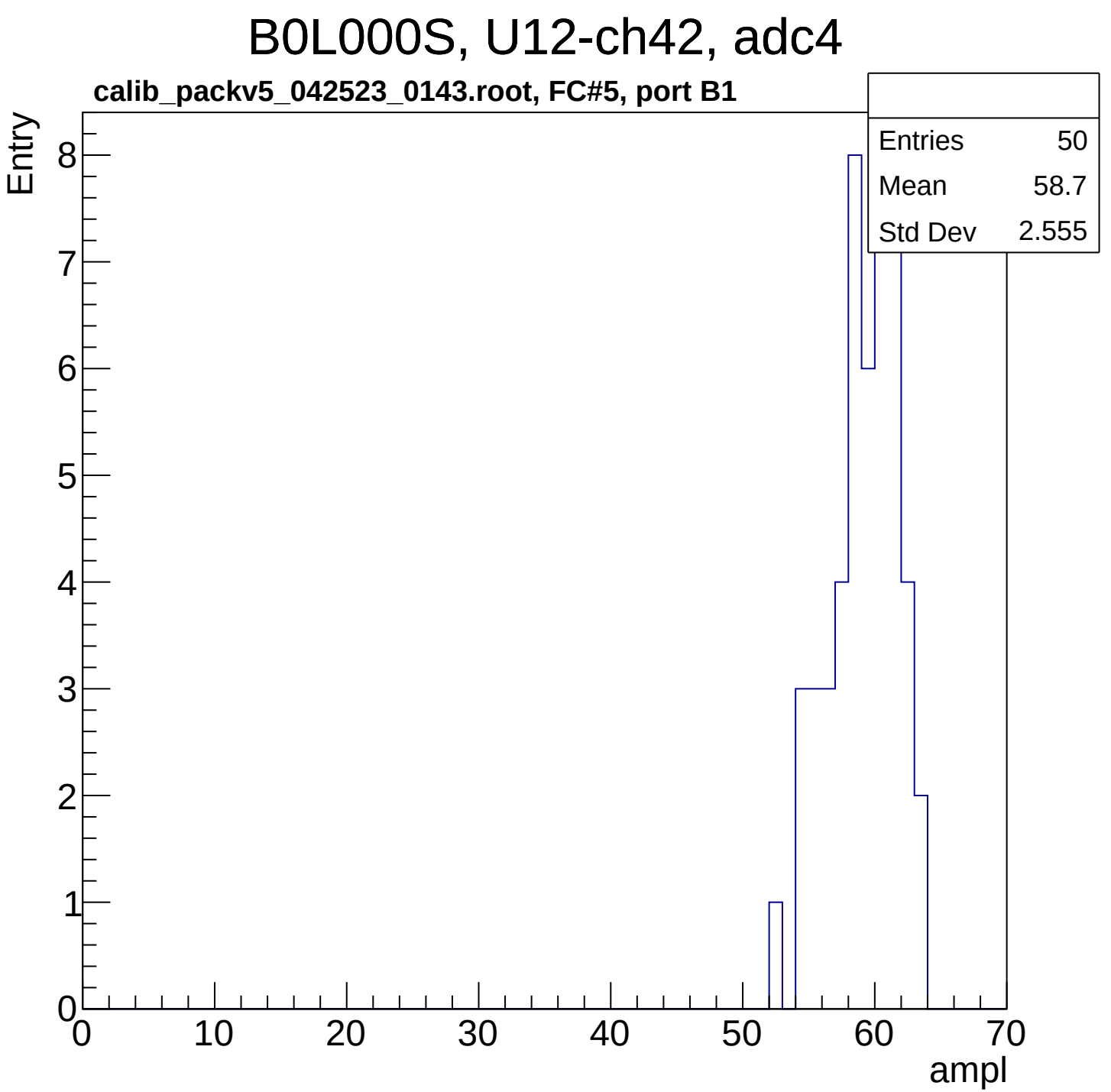
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.7
Std Dev 2.555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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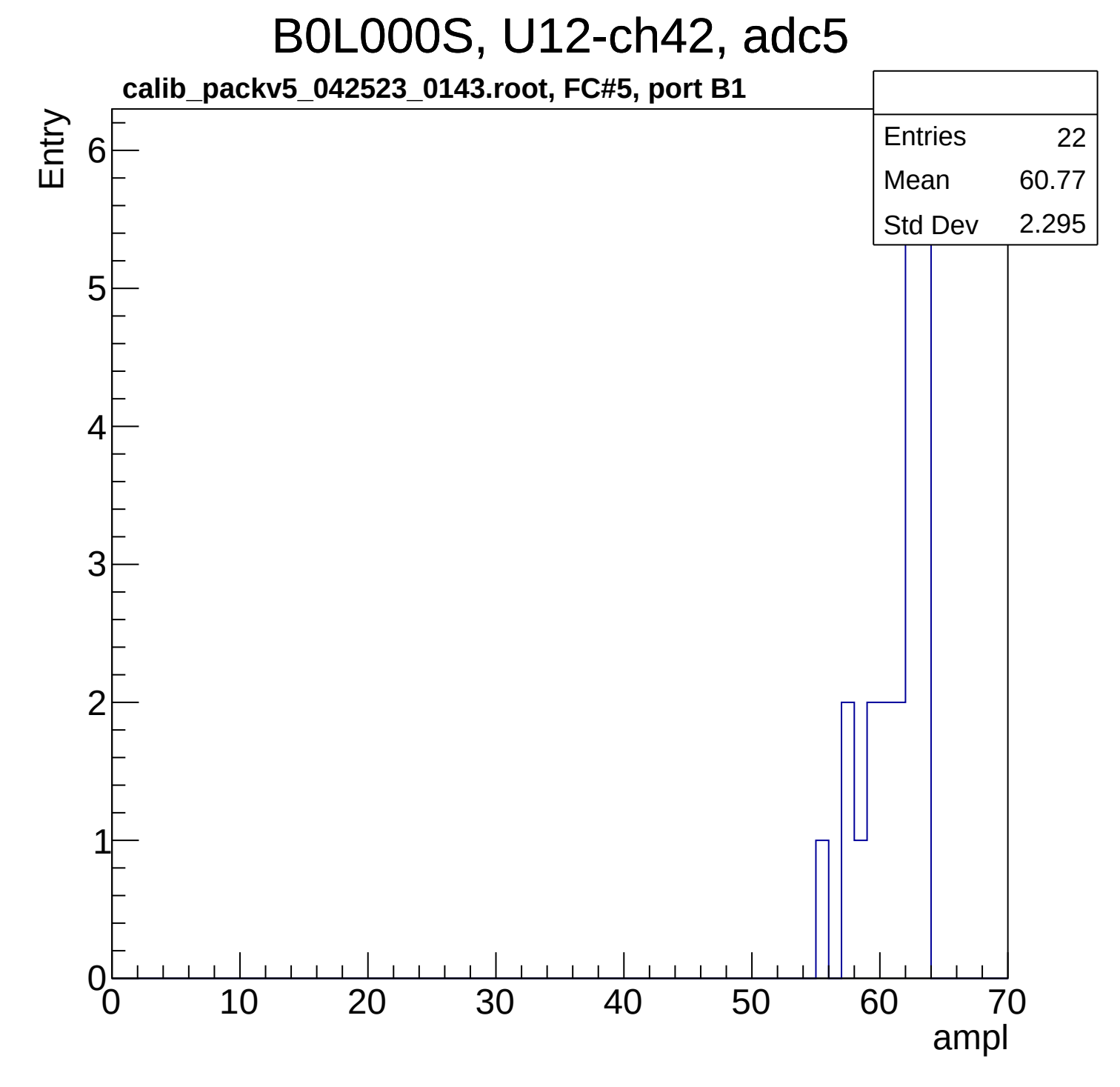
40
50
60
70


B0L000S, U12-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 60.77
Std Dev 2.295
Entry
0
1
2
3
4
5
6
0
10
20
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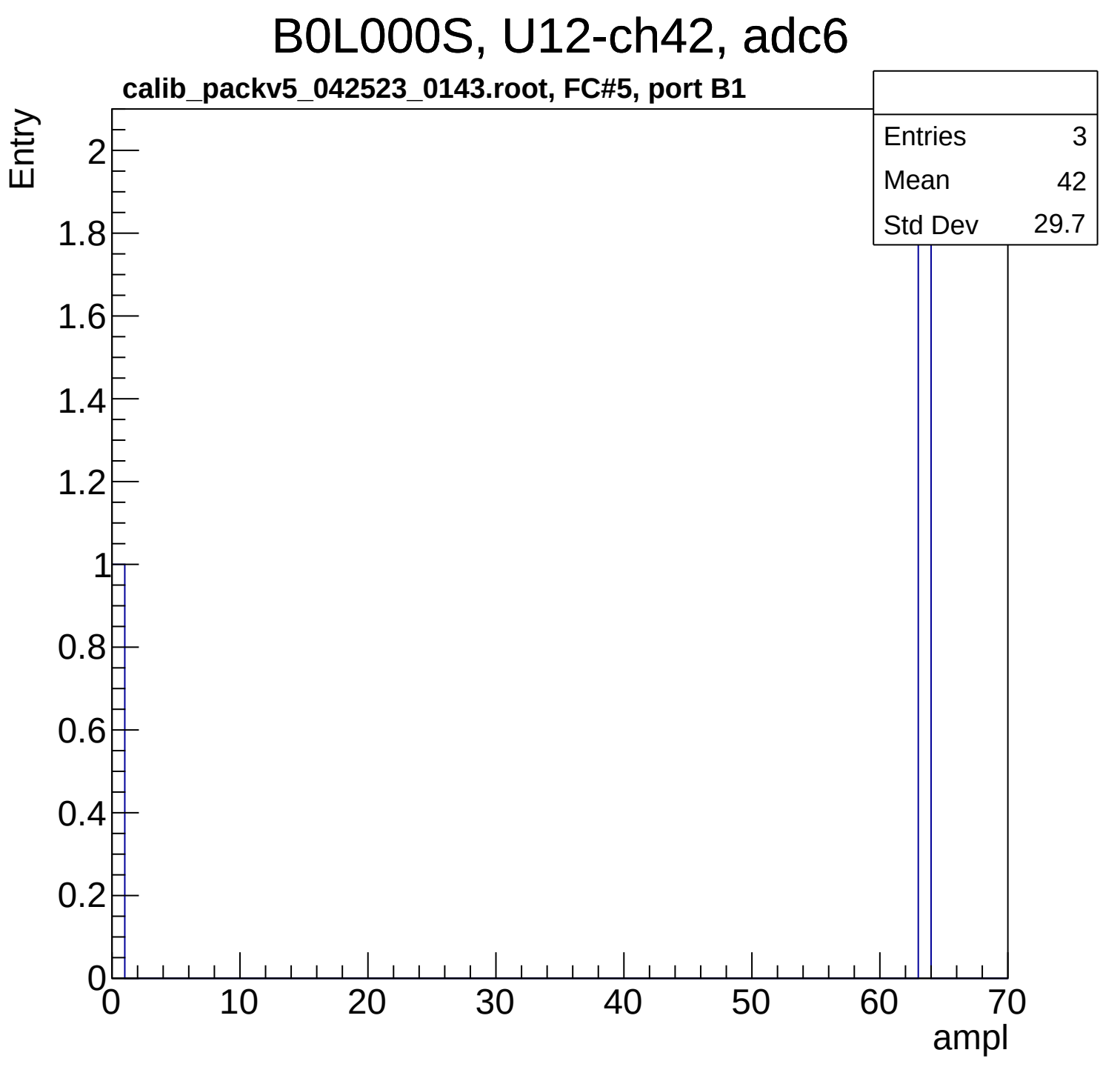
50
60
70
ampl


B0L000S, U12-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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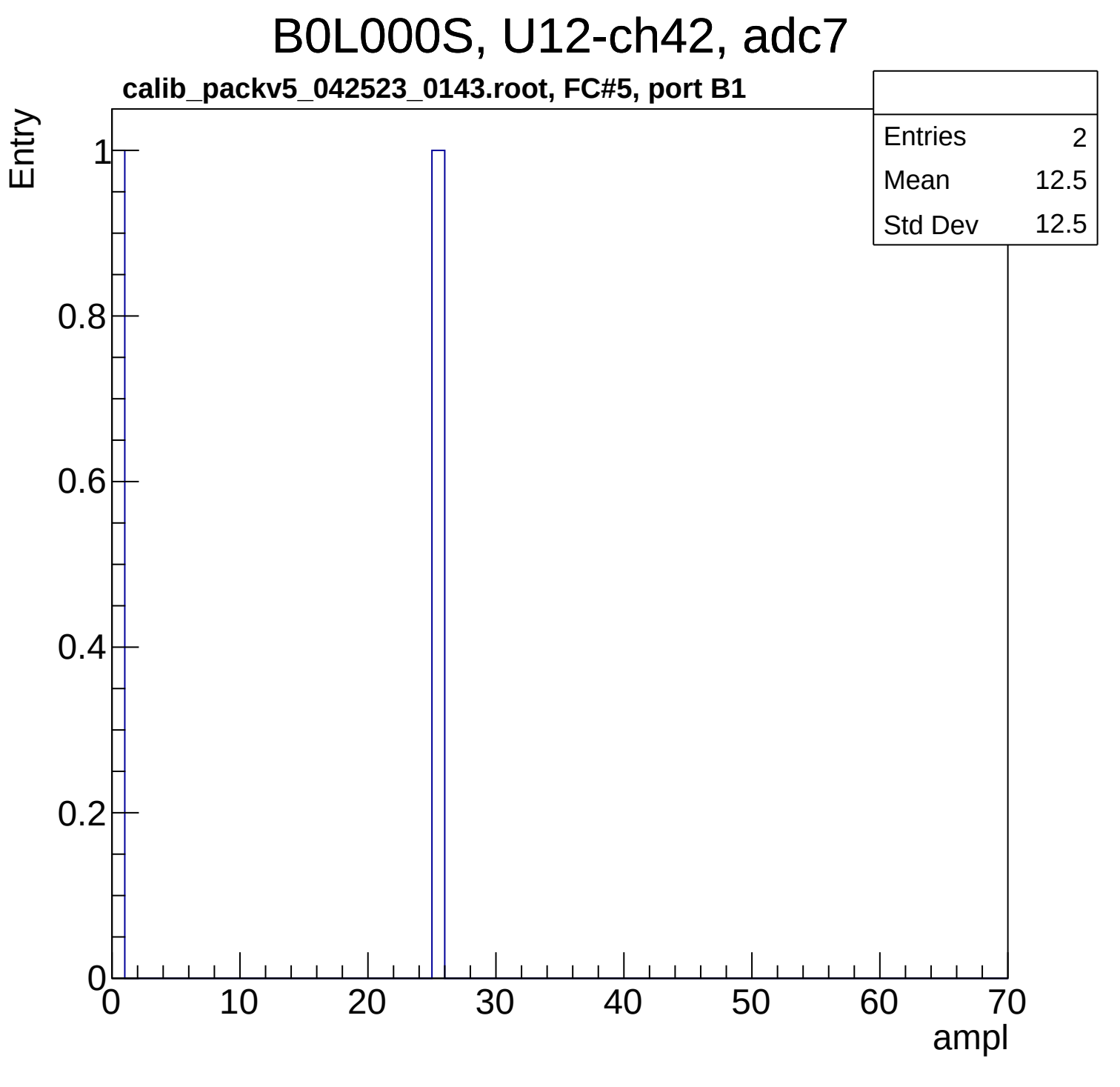
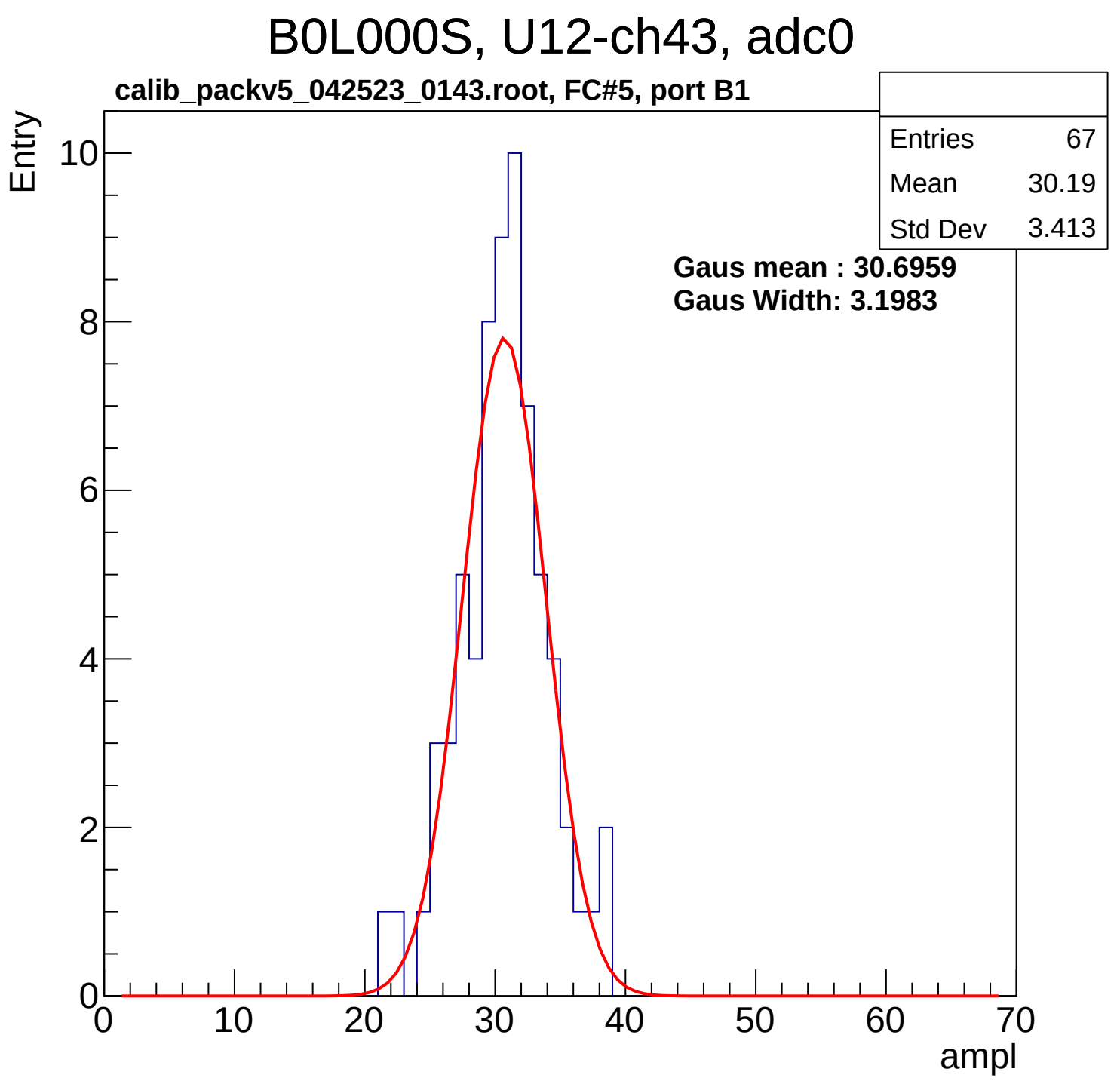
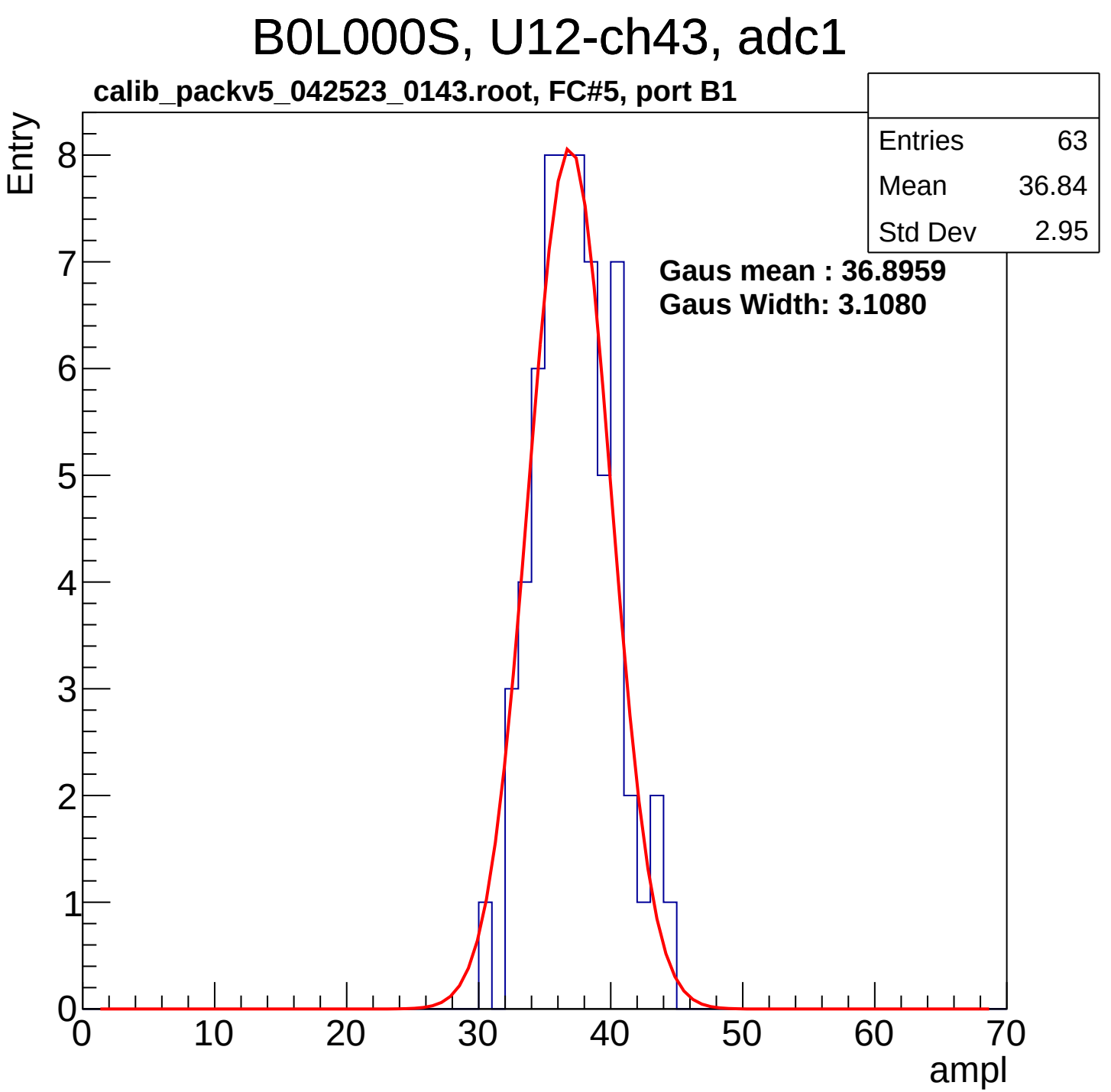

B0L000S, U12-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.19
Std Dev 3.413
Gaus mean : 30.6959
Gaus Width: 3.1983
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.84
Std Dev 2.95
Gaus mean : 36.8959
Gaus Width: 3.1080
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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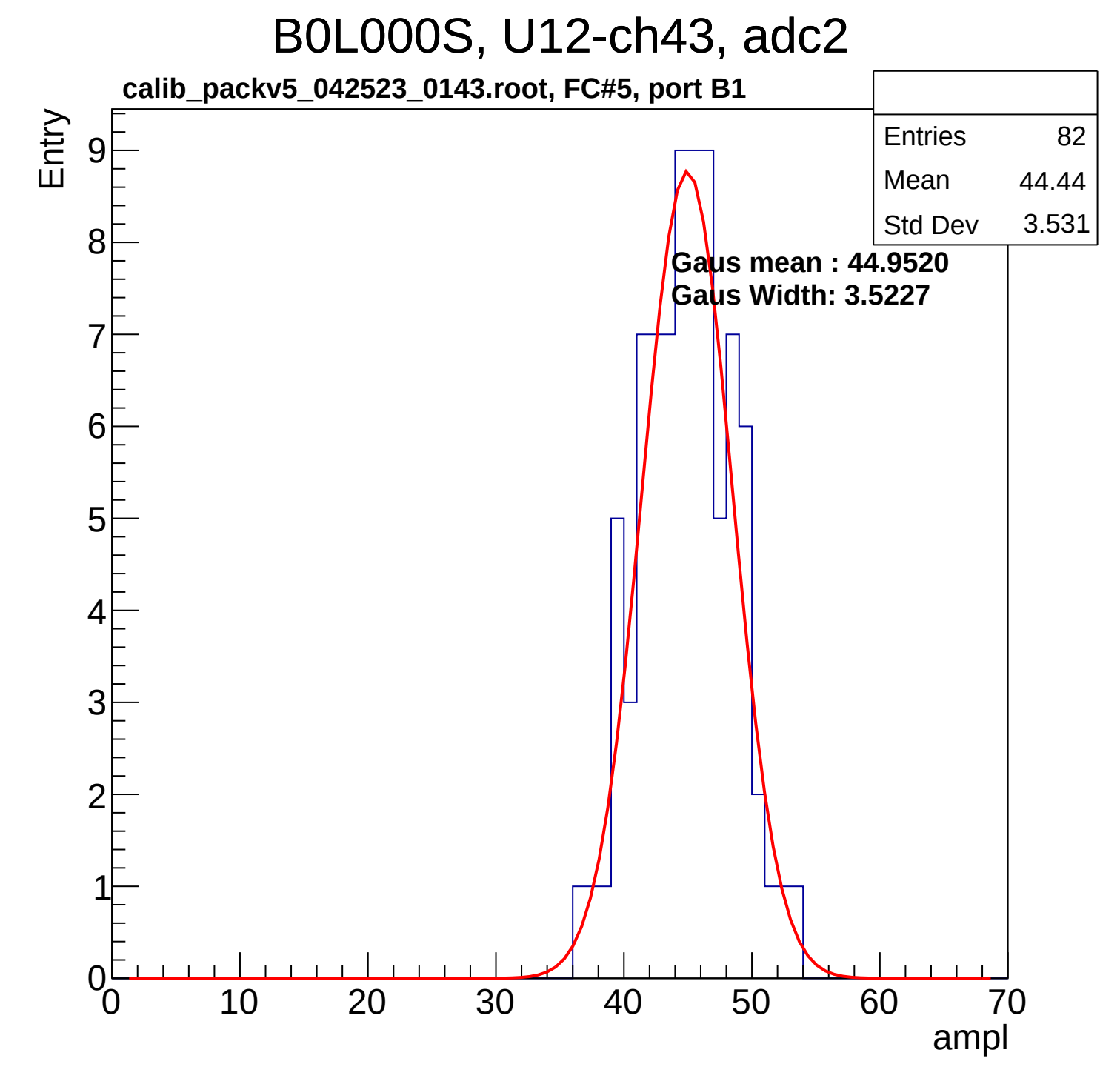
B0L000S, U12-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 44.44
Std Dev 3.531
Gaus mean : 44.9520
Gaus Width: 3.5227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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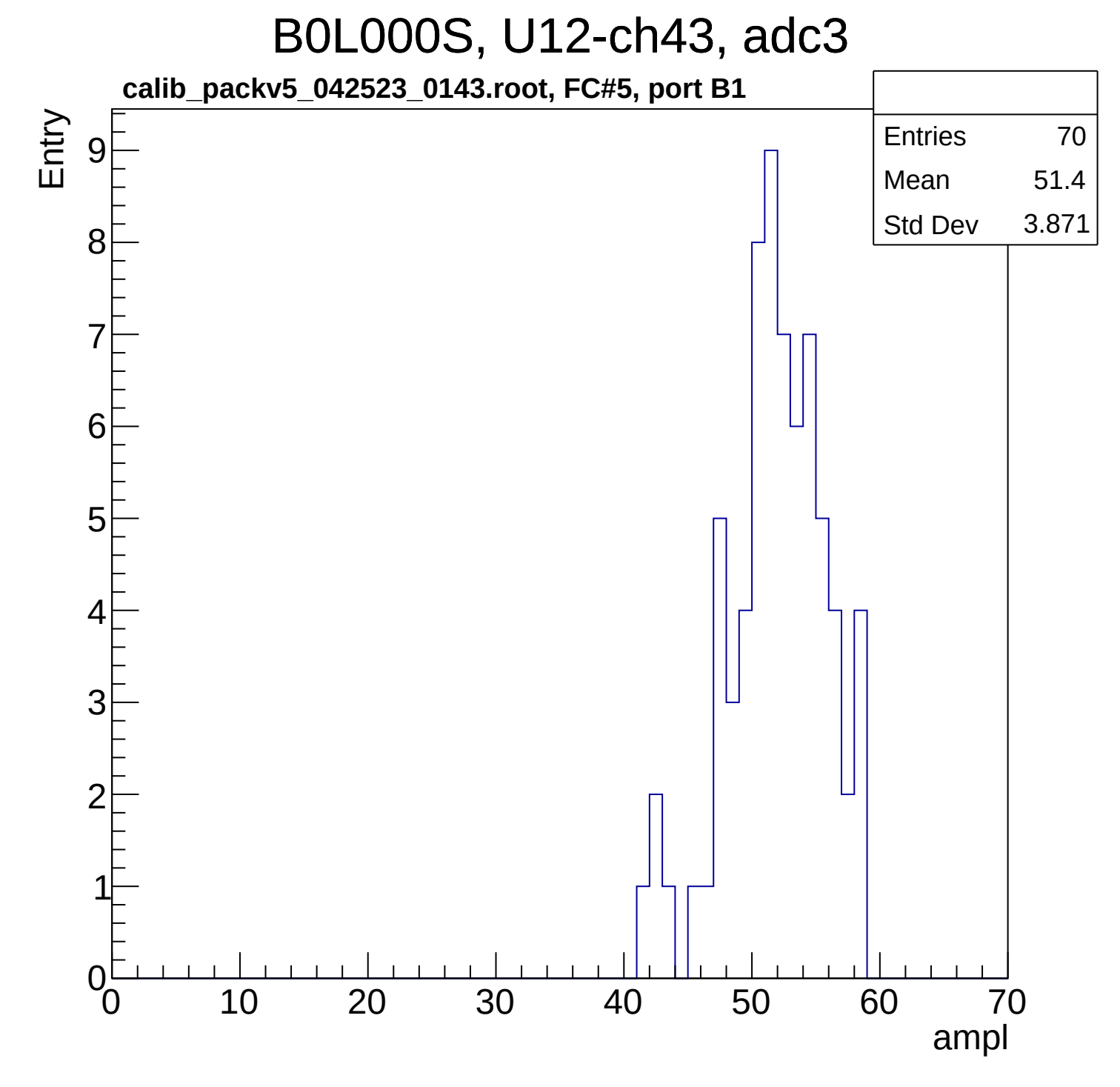
B0L000S, U12-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 51.4
Std Dev 3.871
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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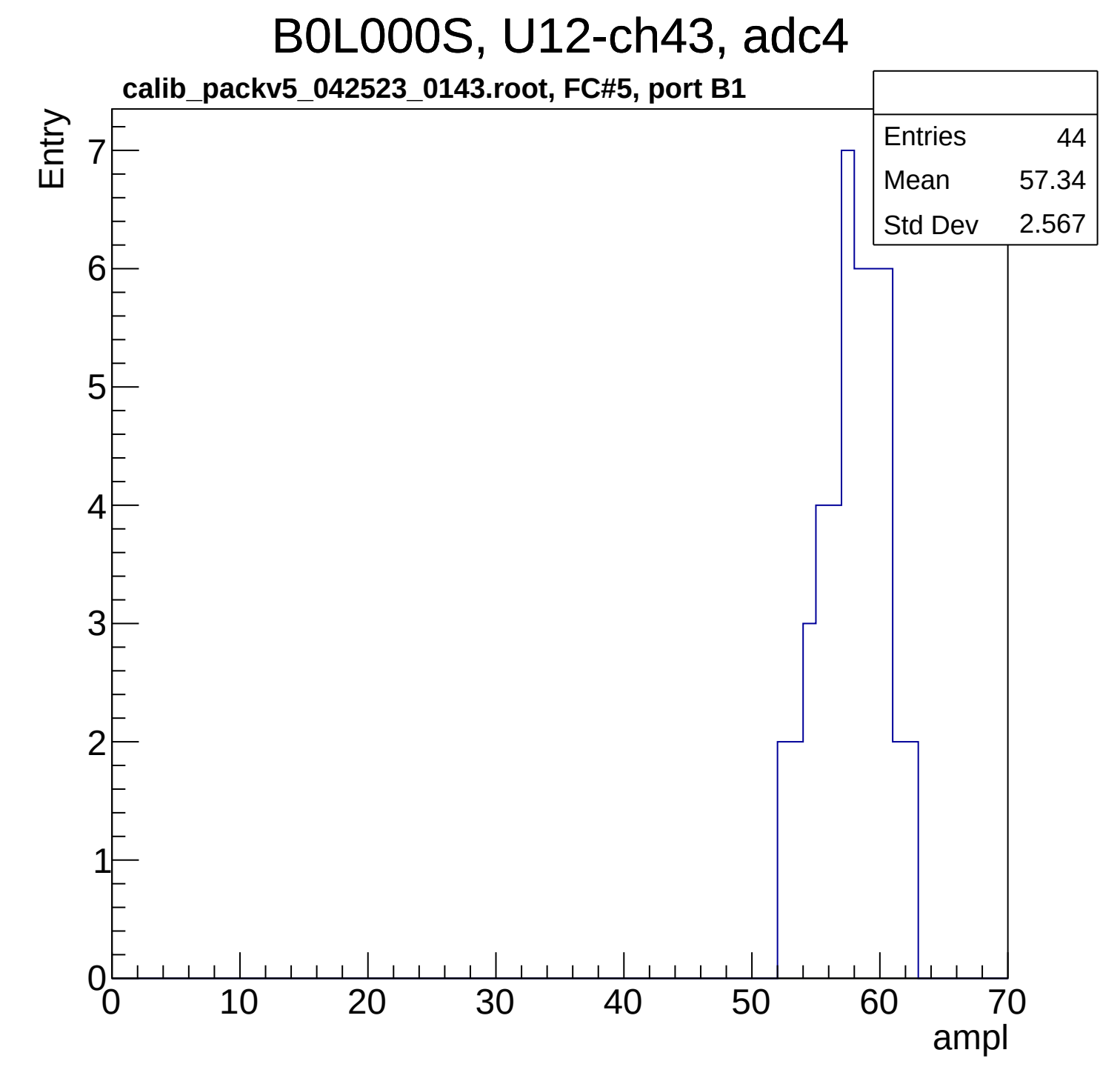
B0L000S, U12-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 57.34
Std Dev 2.567
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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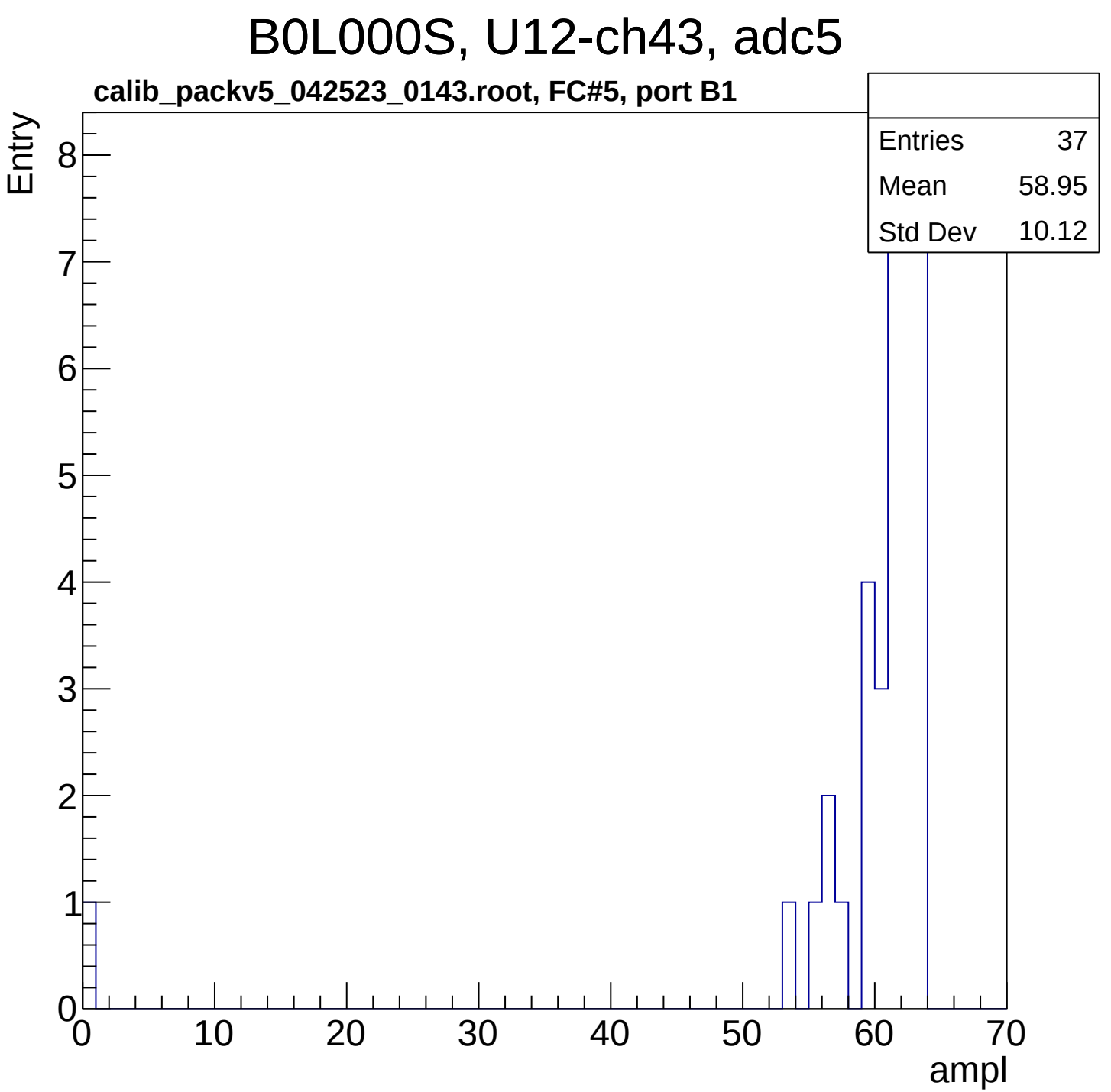
60
70


B0L000S, U12-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.95
Std Dev 10.12
Entry
ampl
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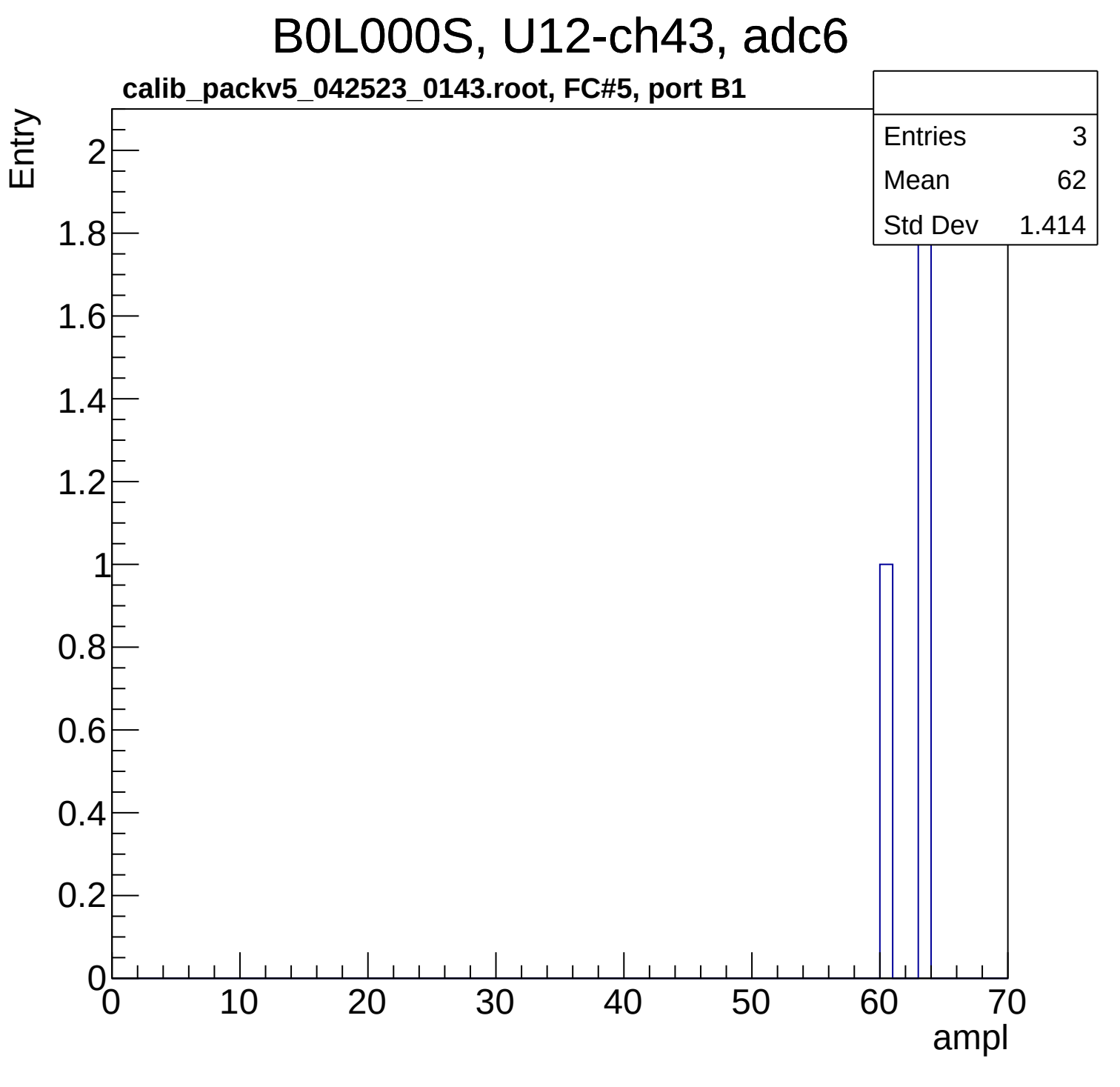
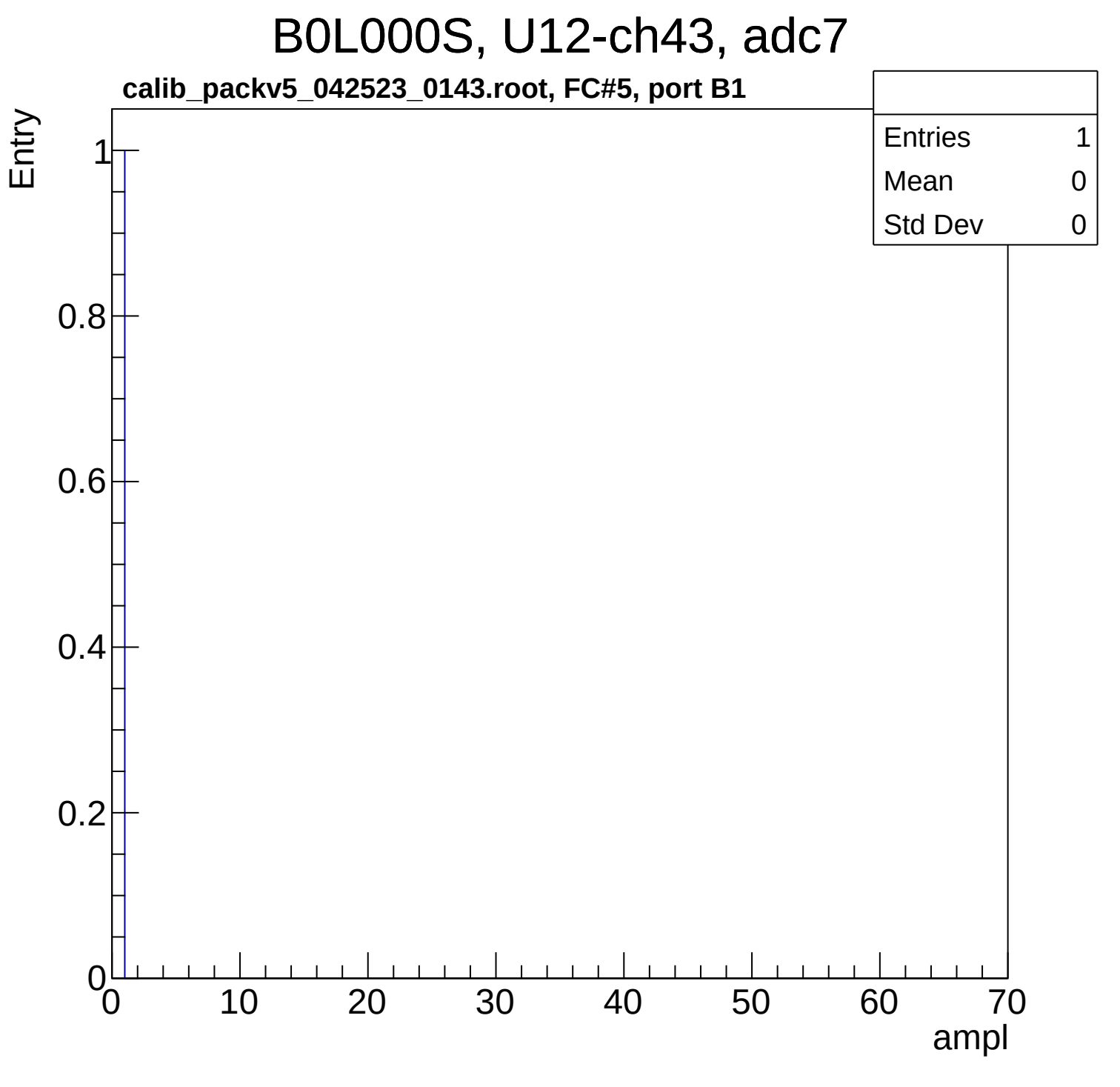
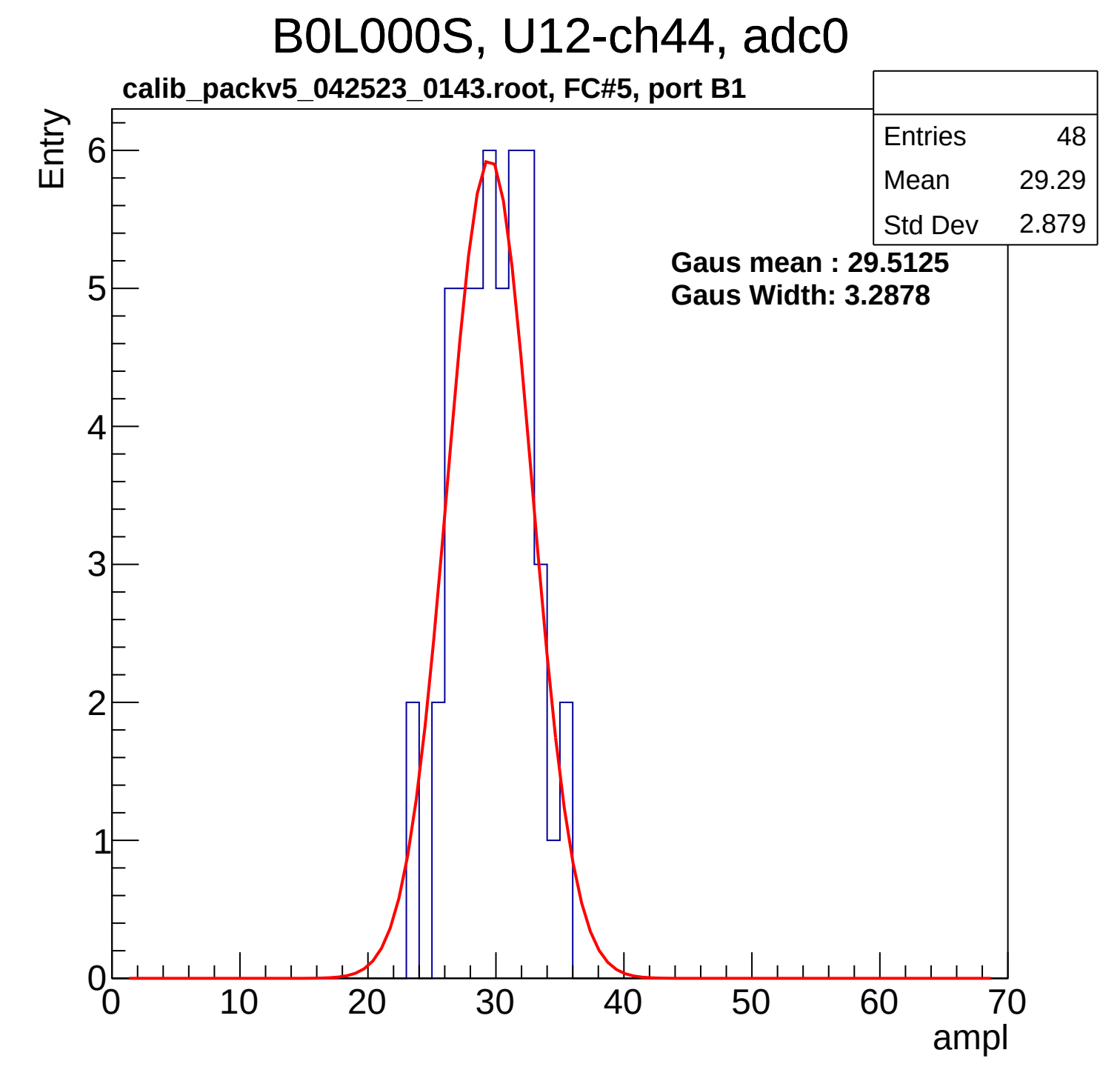
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 29.29
Std Dev 2.879
Gaus mean : 29.5125
Gaus Width: 3.2878
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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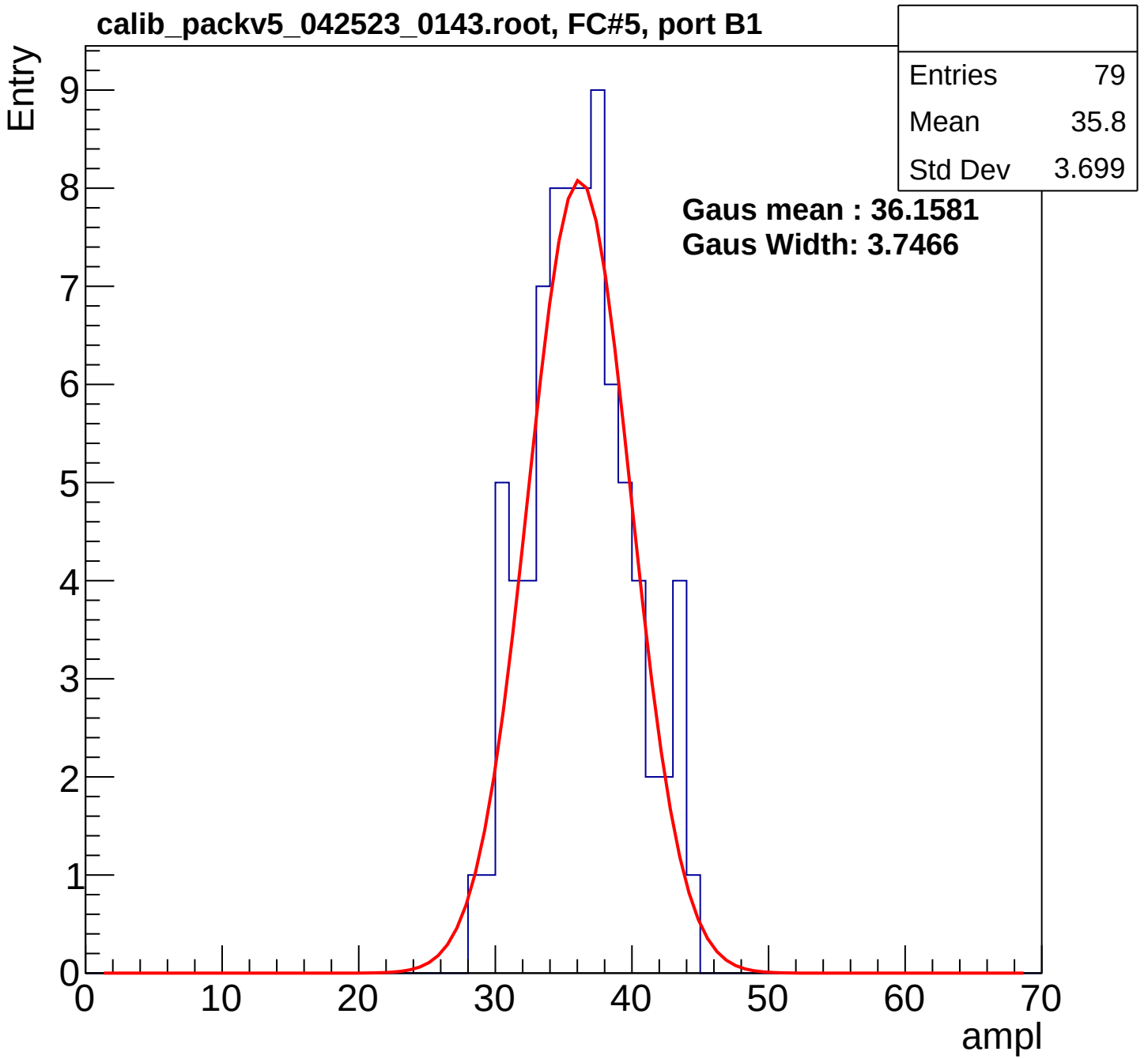
B0L000S, U12-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 35.8
Std Dev 3.699
Gaus mean : 36.1581
Gaus Width: 3.7466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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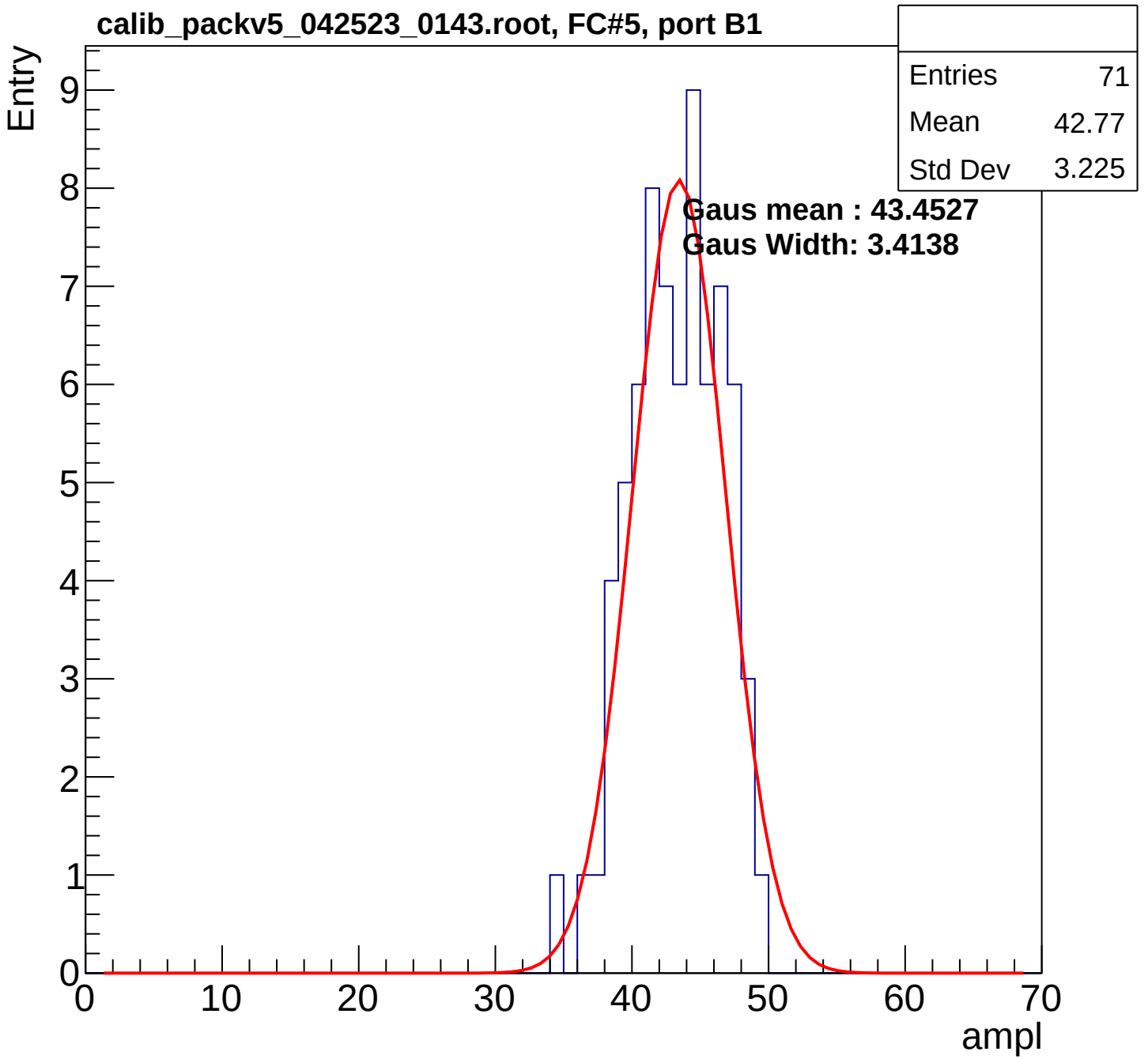
60
70


B0L000S, U12-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.77
Std Dev 3.225
Gaus mean : 43.4527
Gaus Width: 3.4138
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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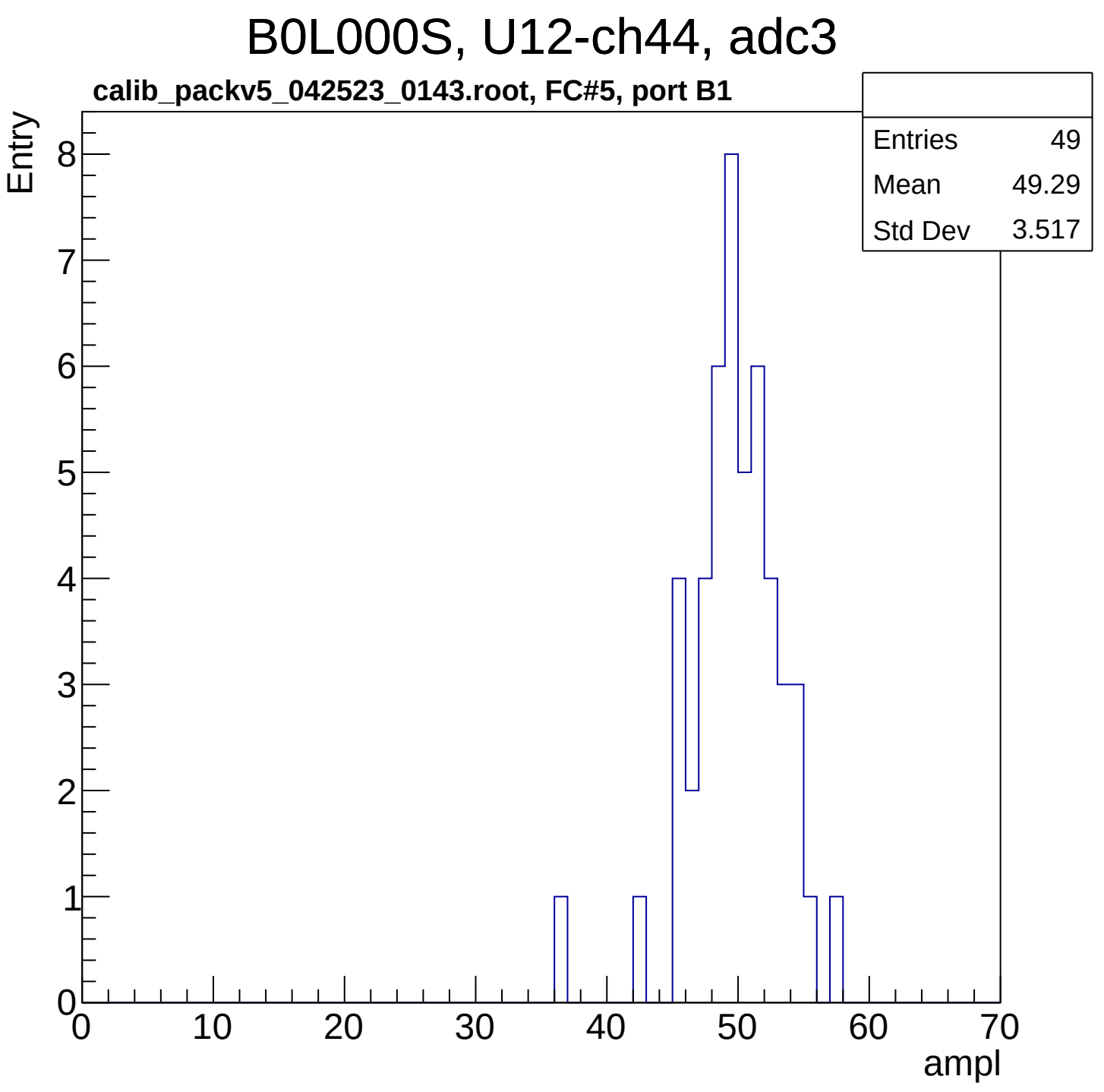
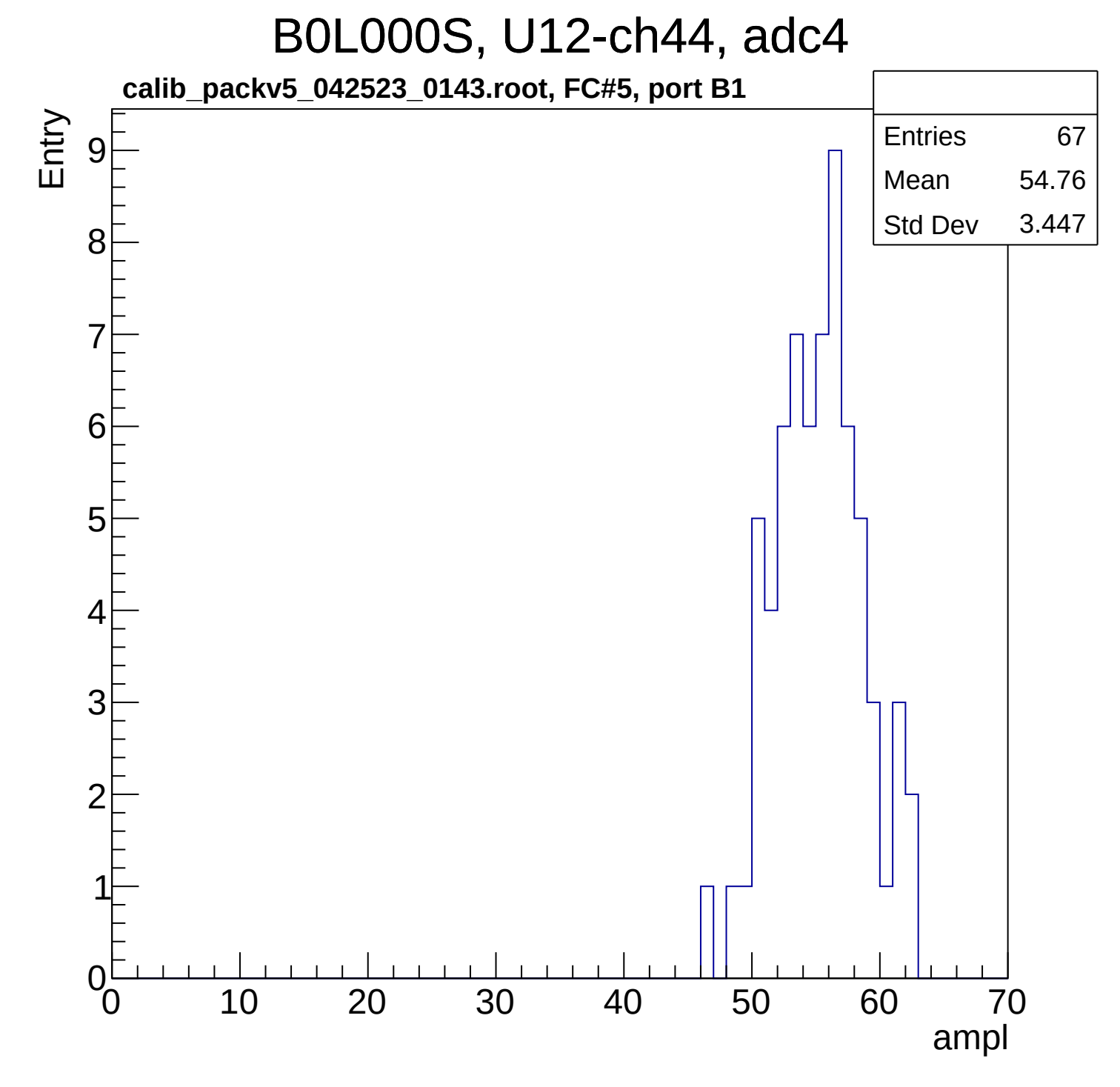
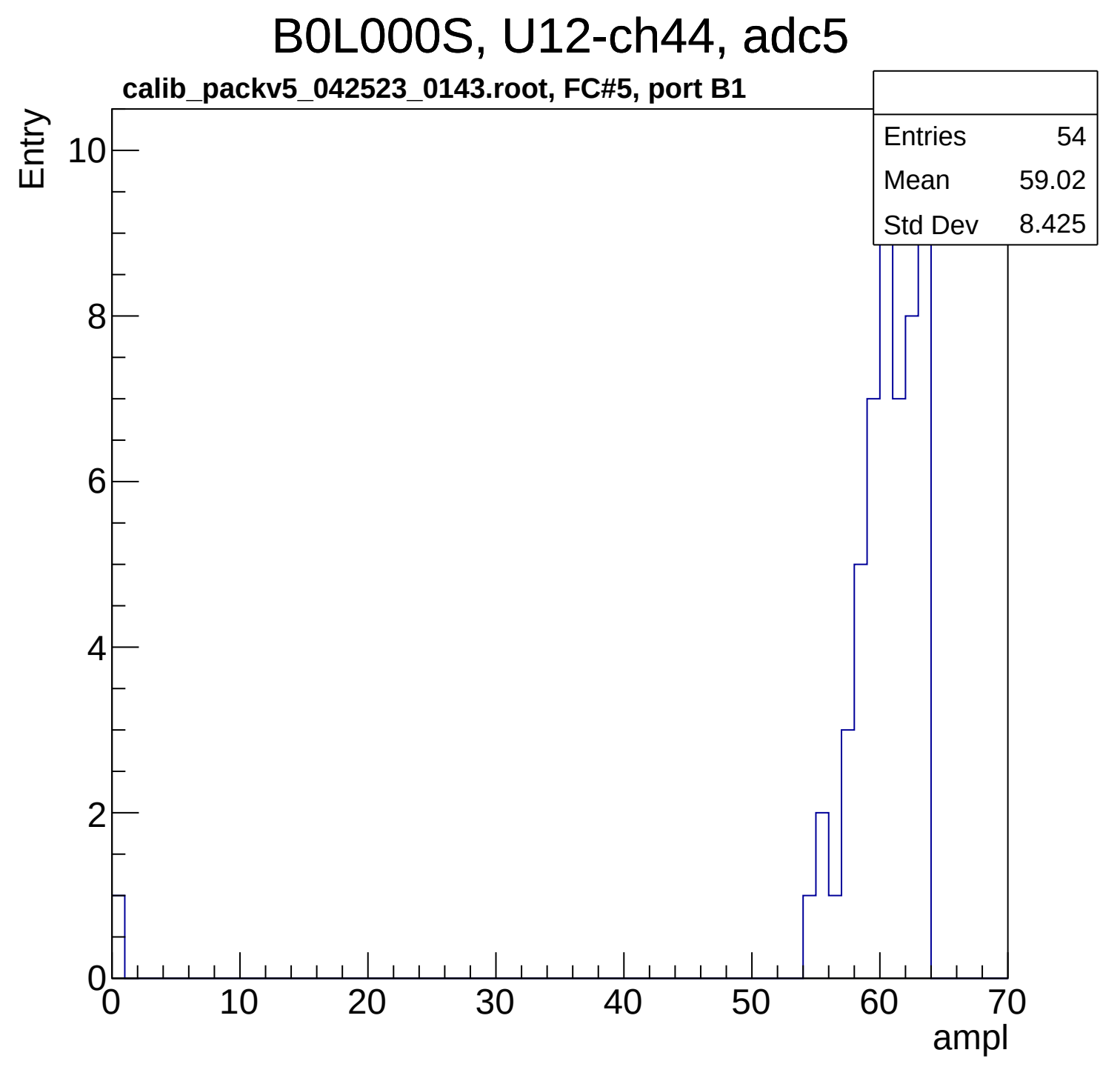
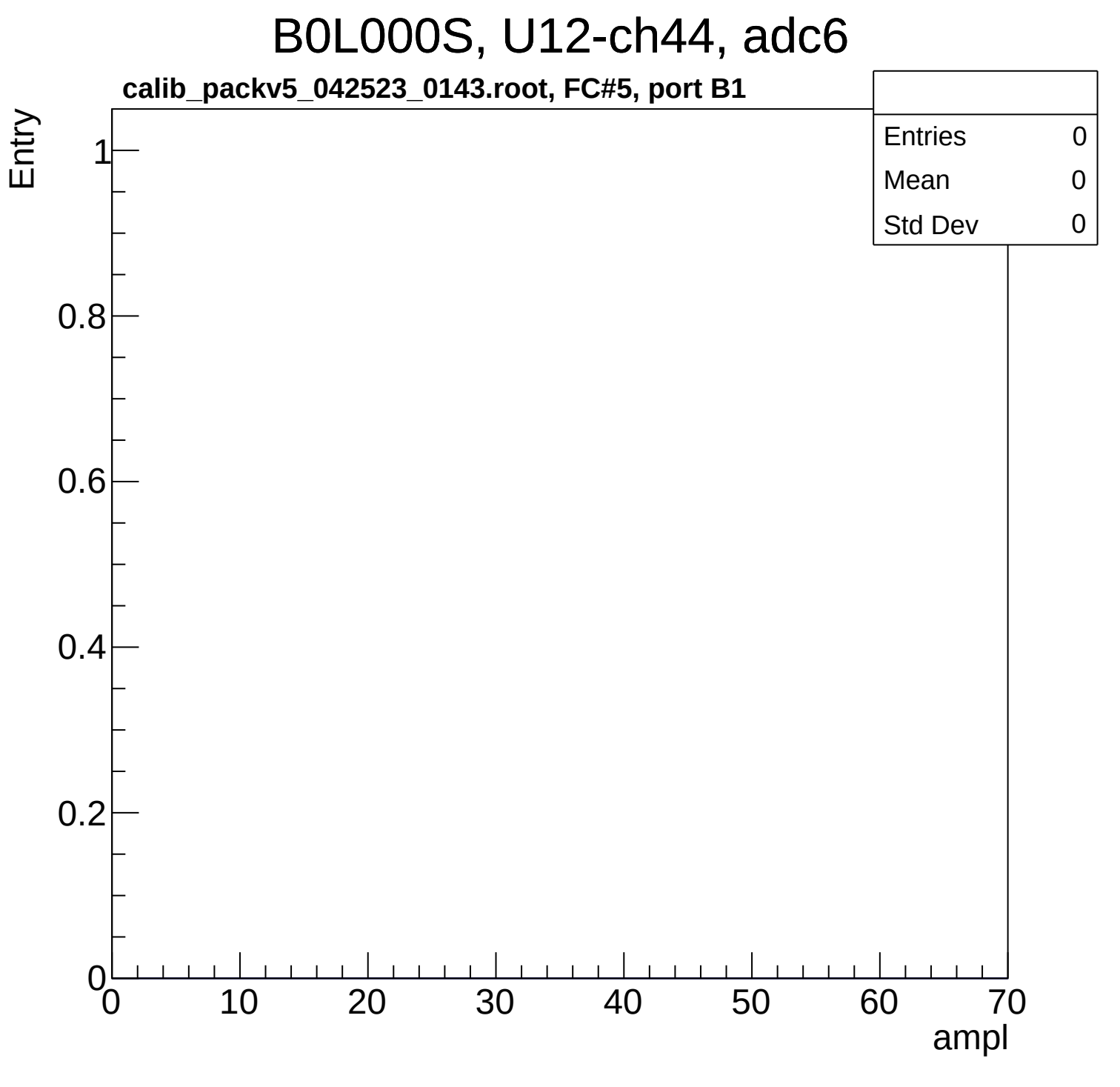
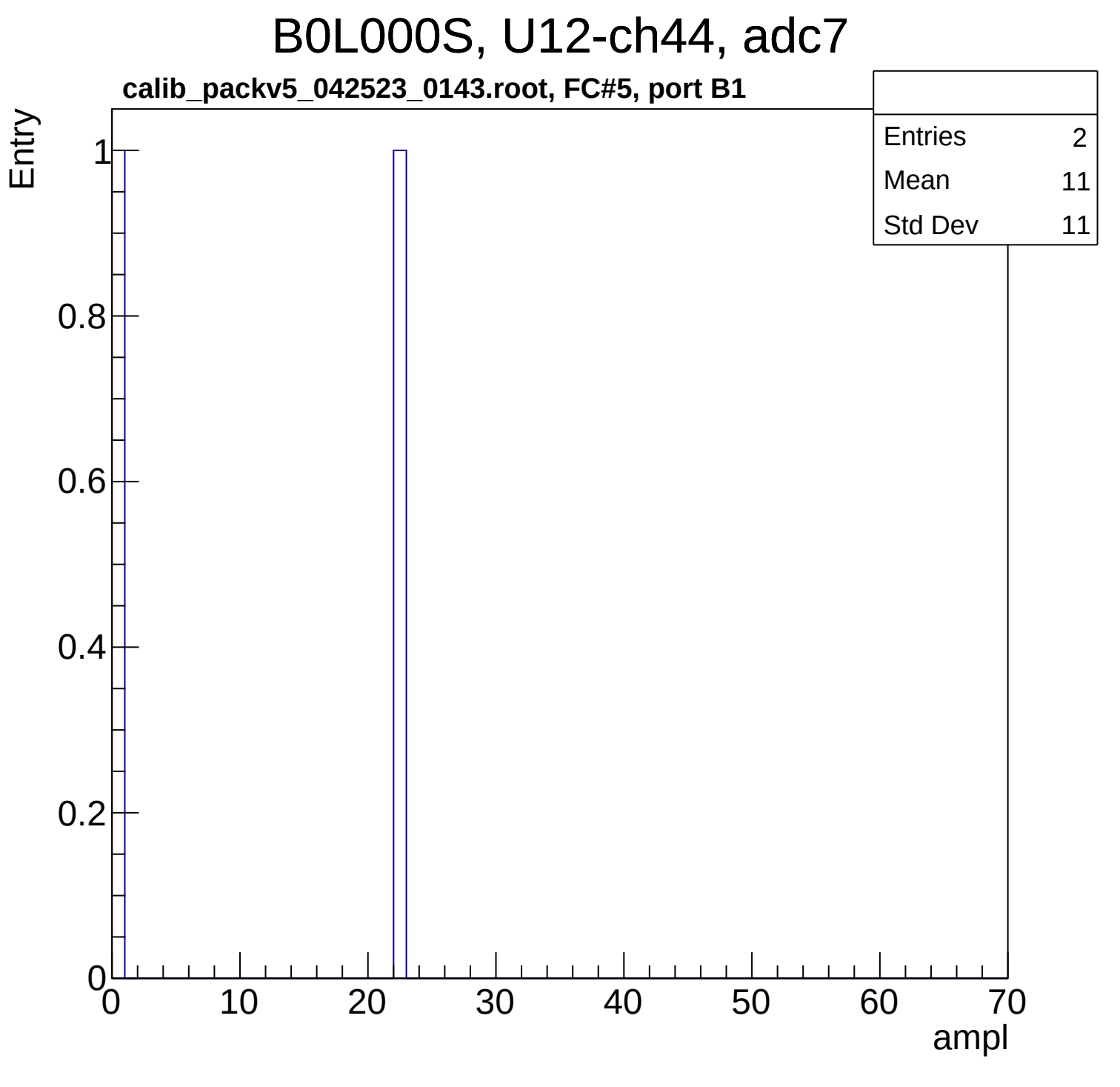
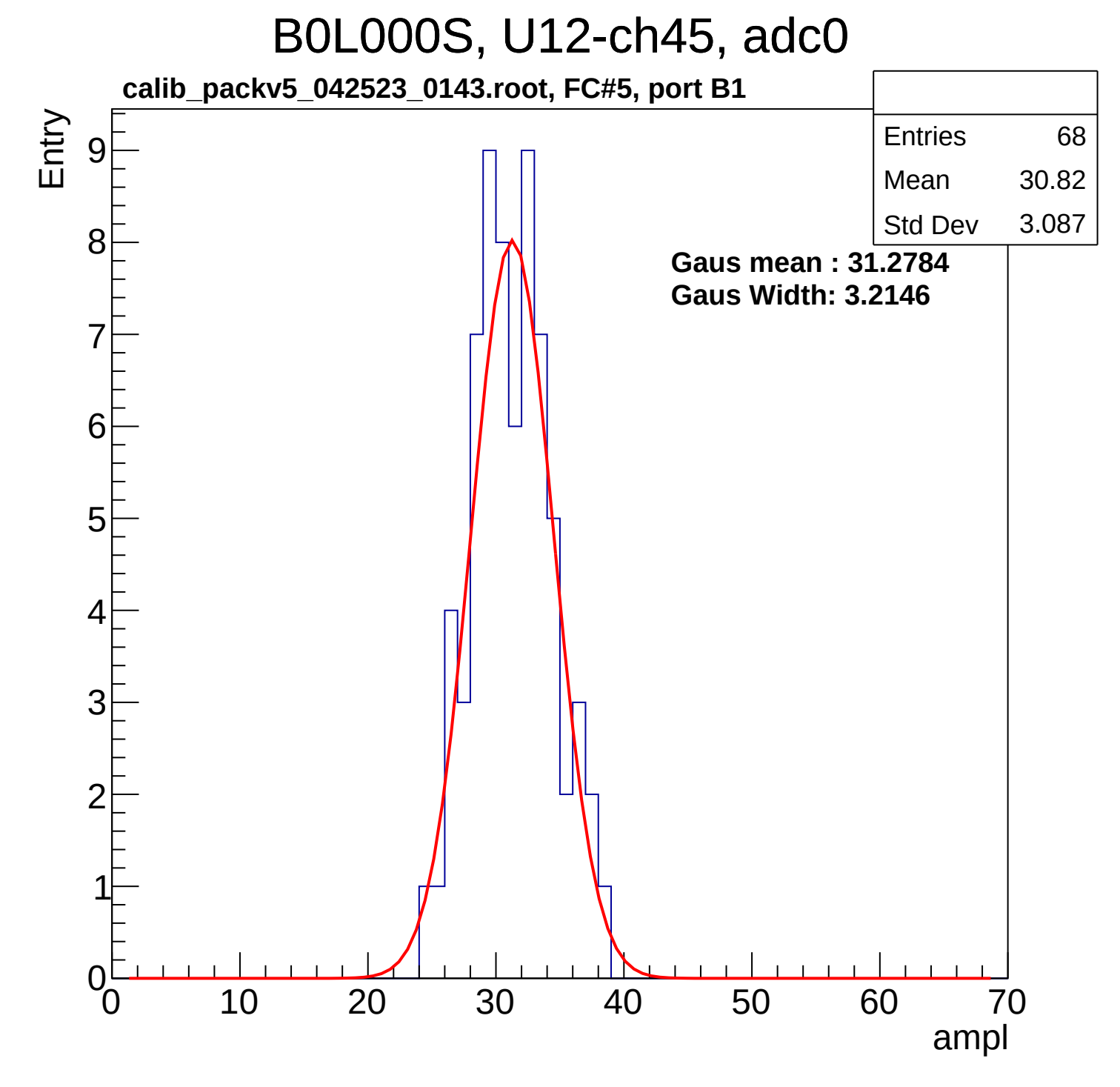

B0L000S, U12-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 49.29
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

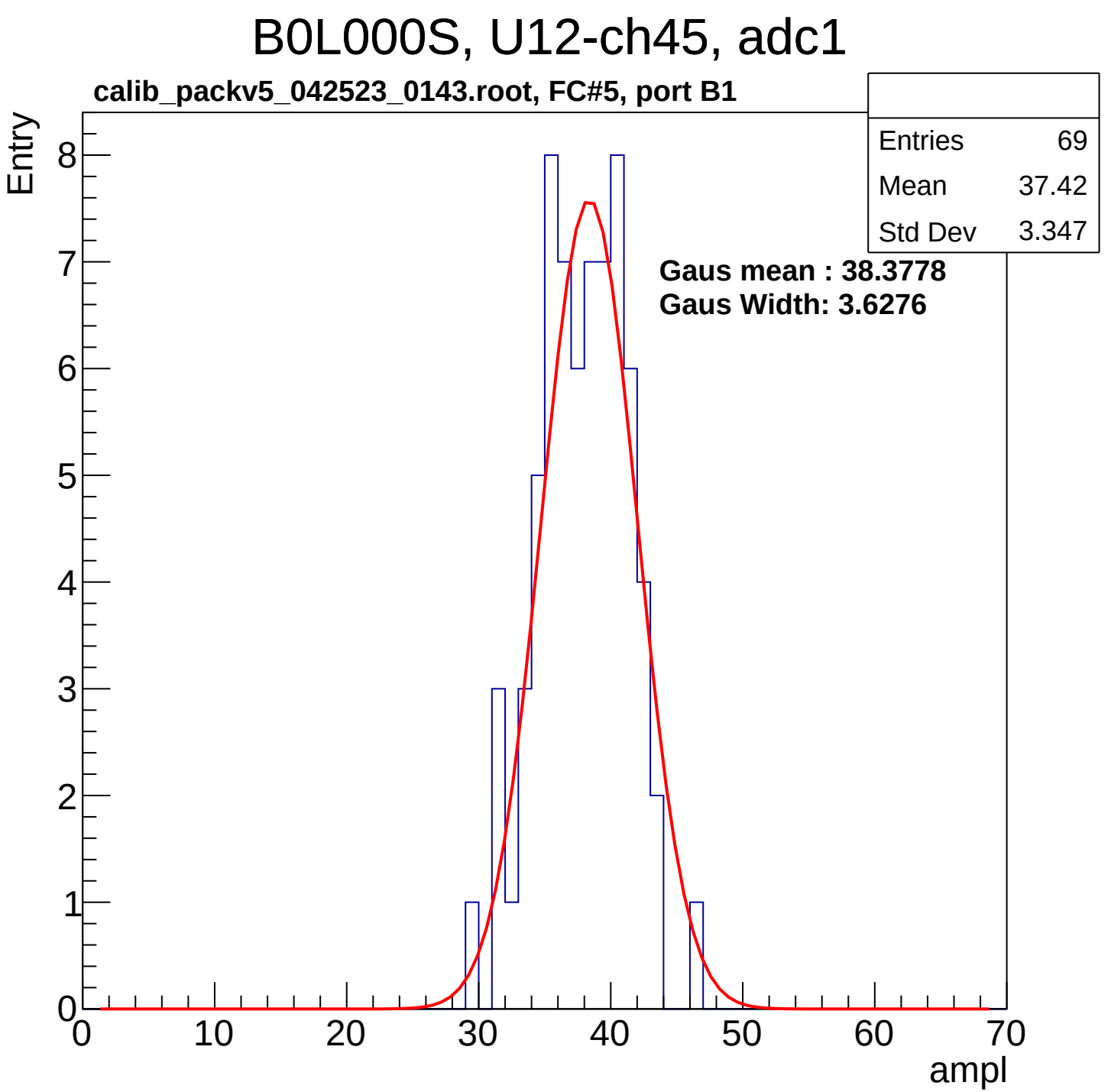

B0L000S, U12-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 54.76
Std Dev 3.447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

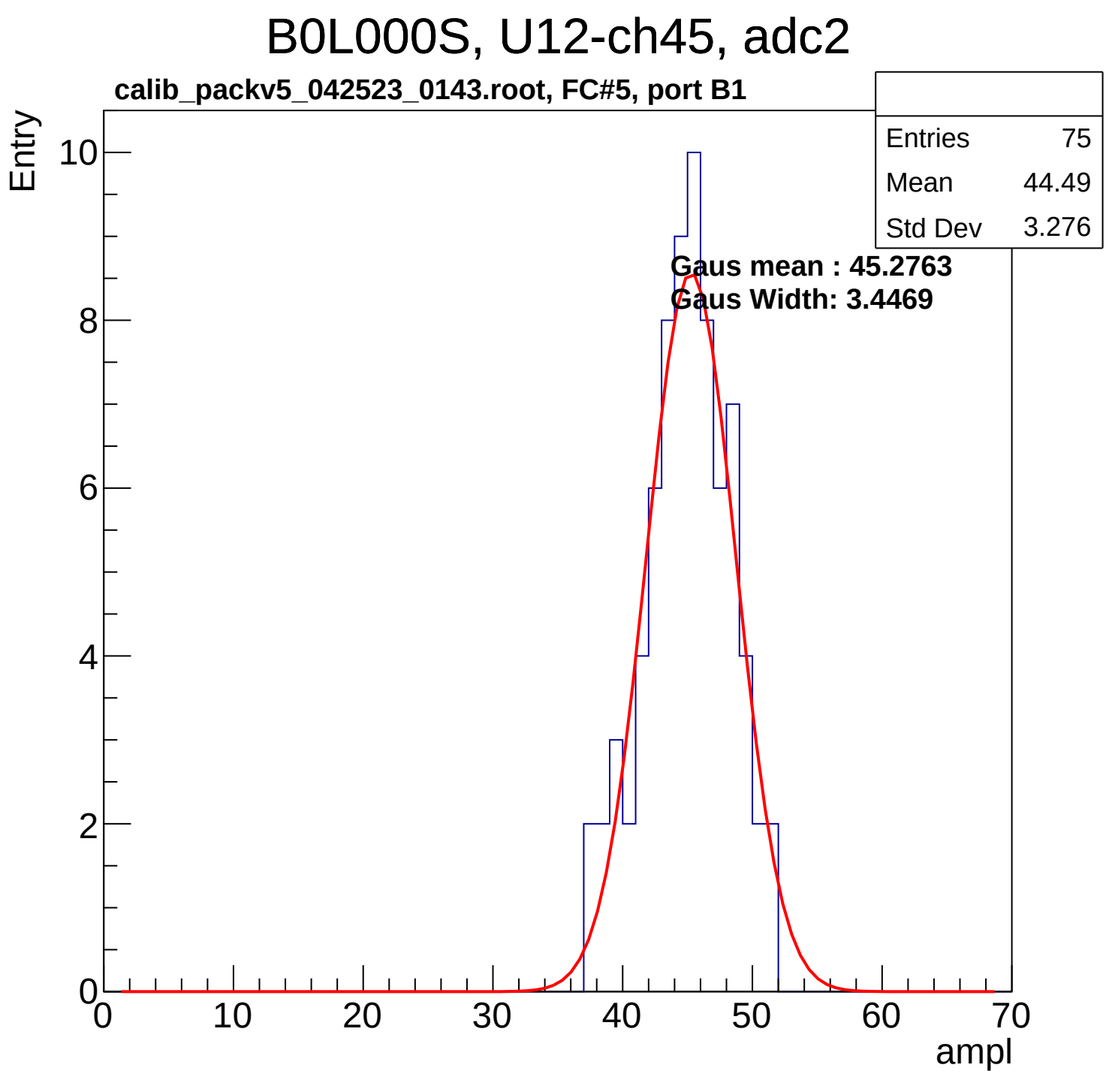

B0L000S, U12-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.02
Std Dev 8.425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

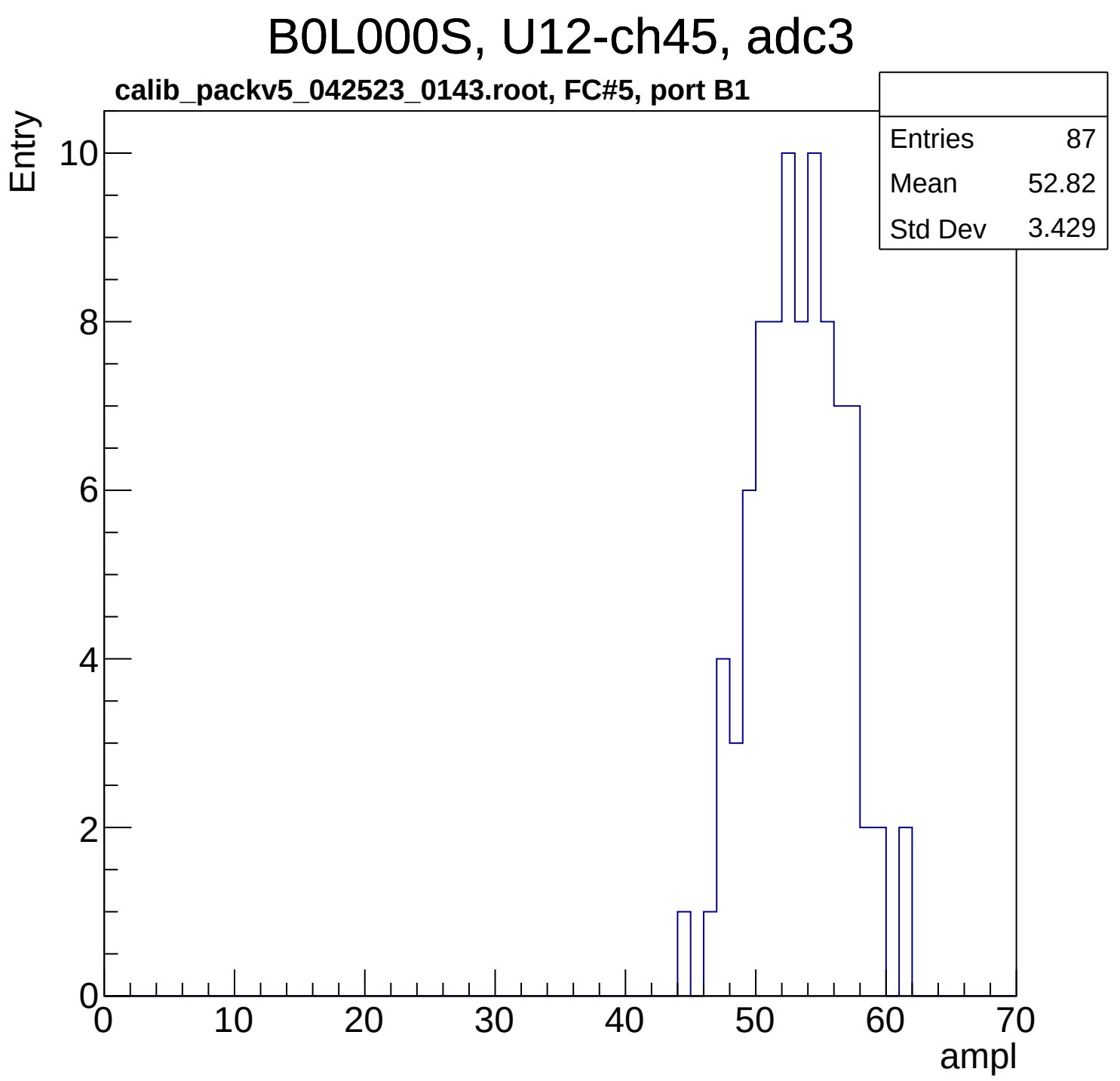

B0L000S, U12-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

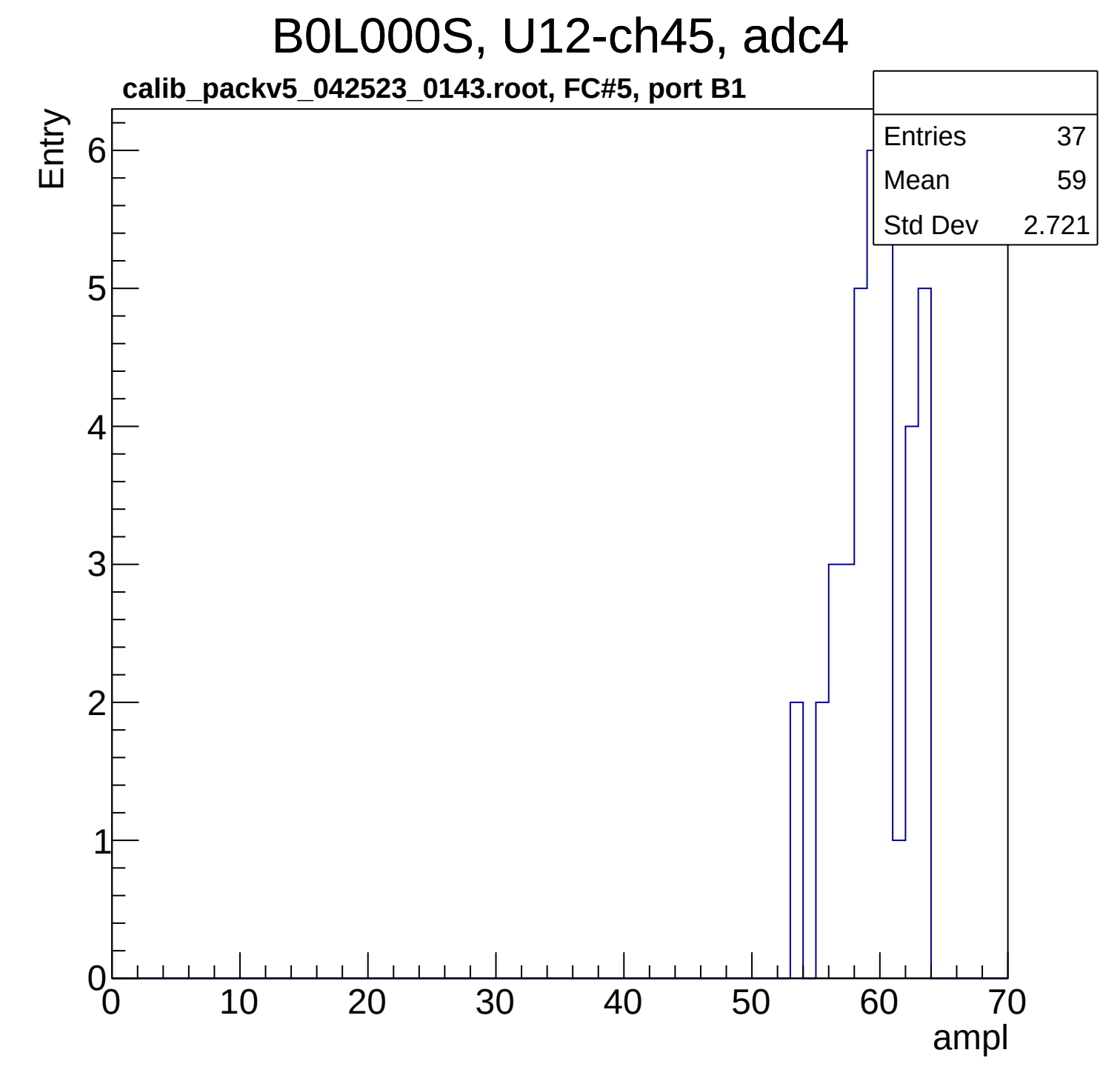

B0L000S, U12-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

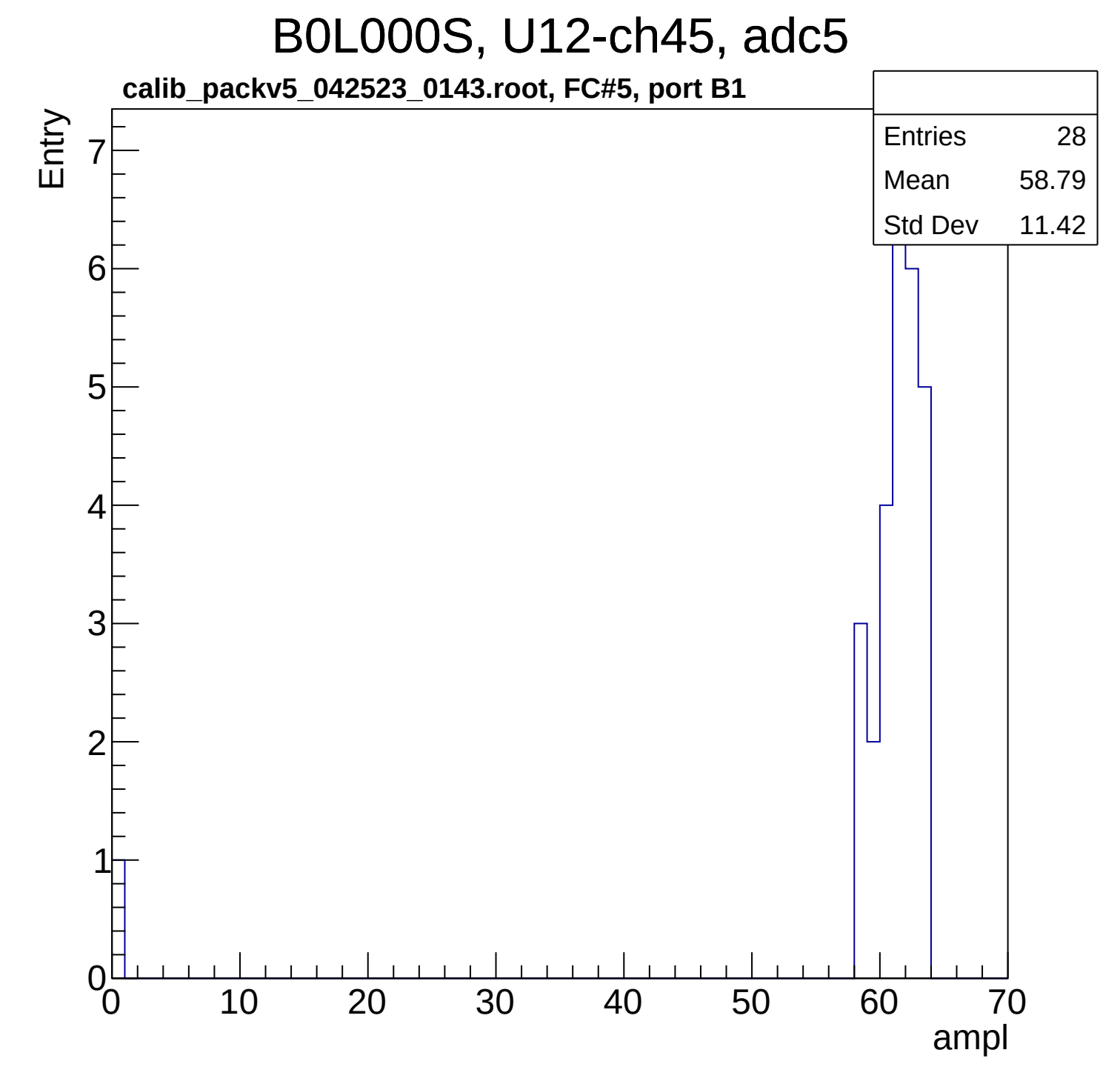

B0L000S, U12-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.82
Std Dev 3.087
Gaus mean : 31.2784
Gaus Width: 3.2146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.42
Std Dev 3.347
Gaus mean : 38.3778
Gaus Width: 3.6276
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 44.49
Std Dev 3.276
Gaus mean : 45.2763
Gaus Width: 3.4469
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 52.82
Std Dev 3.429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59
Std Dev 2.721
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.79
Std Dev 11.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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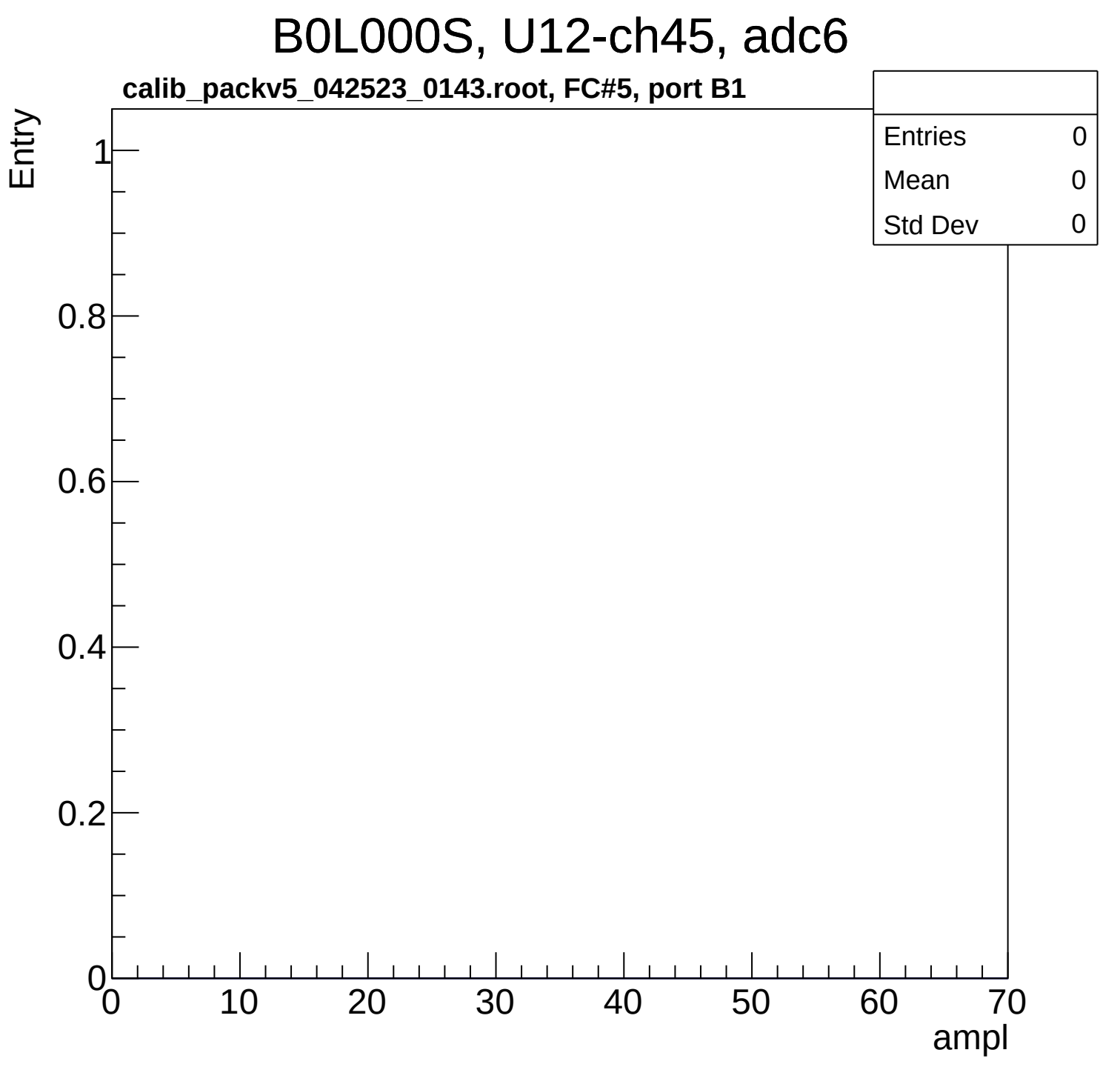
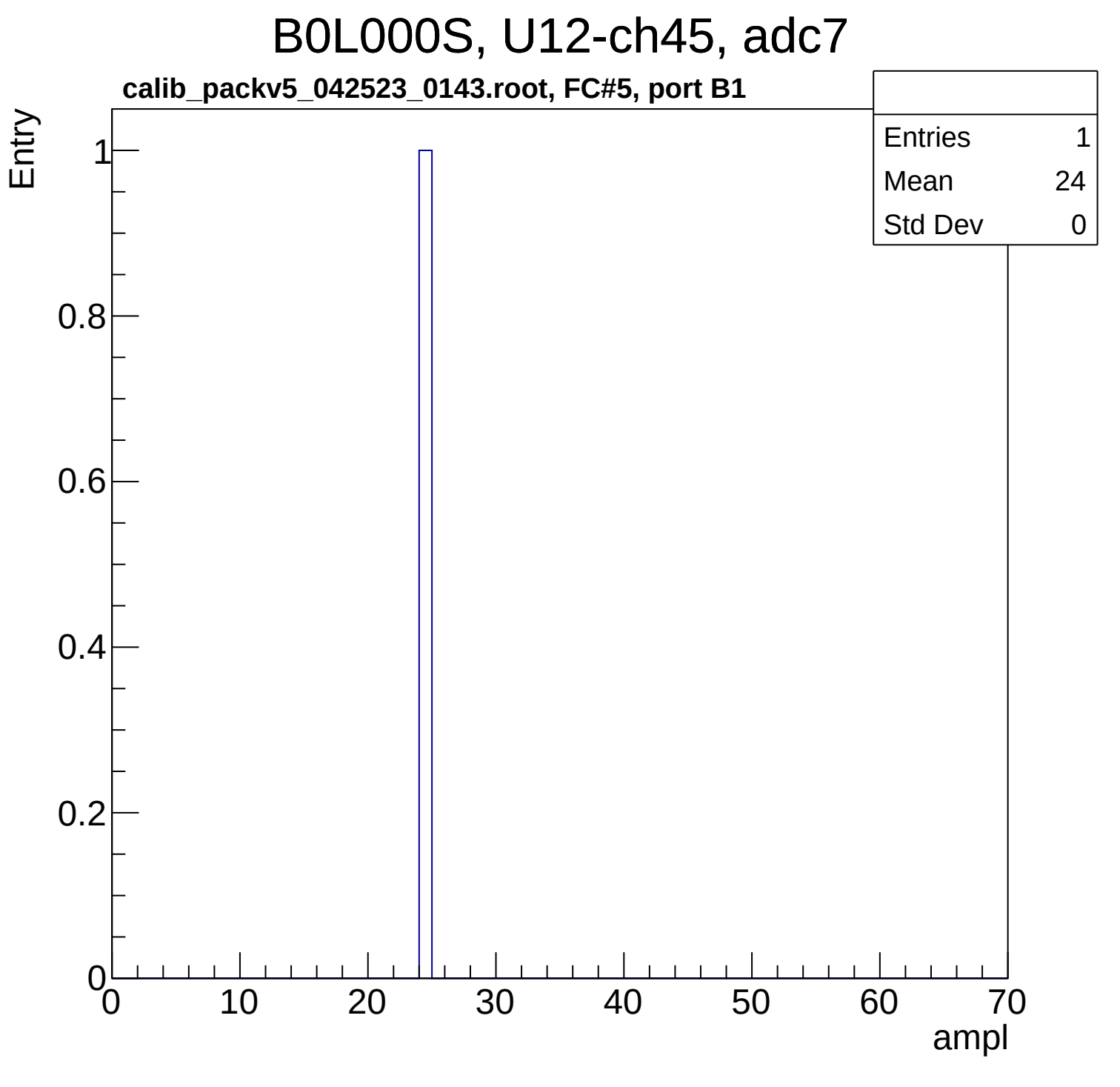

B0L000S, U12-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
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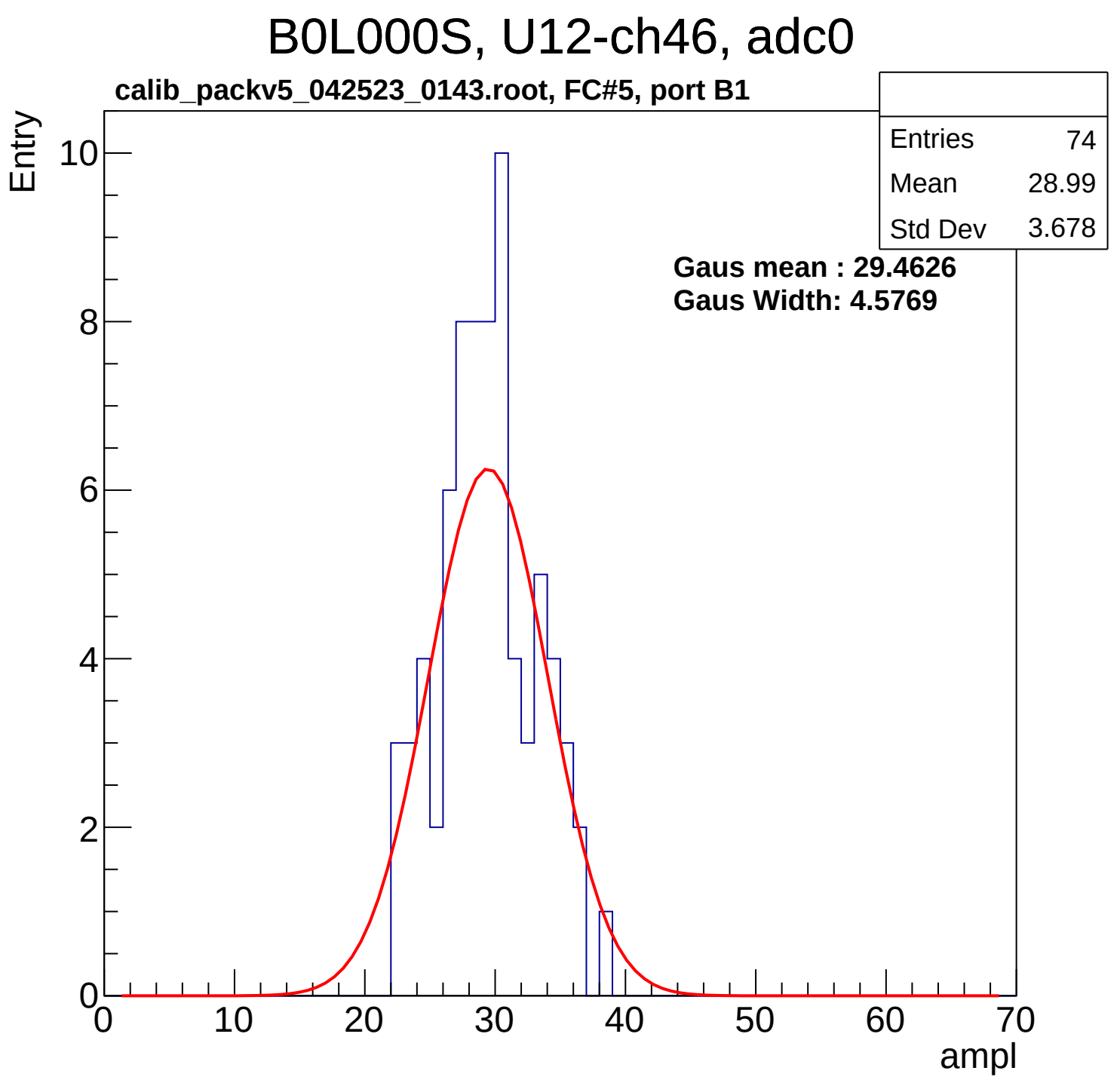
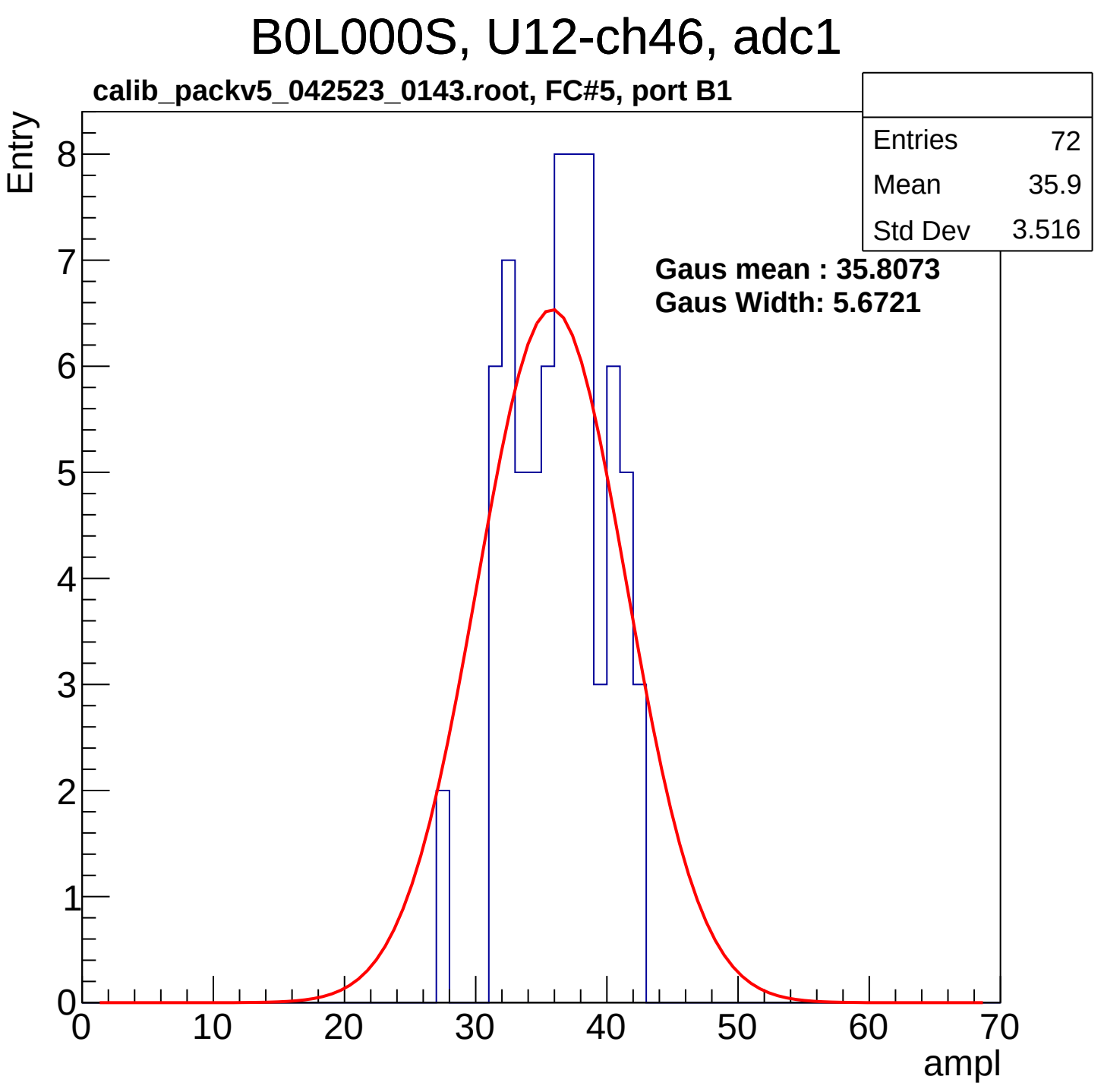
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.99
Std Dev 3.678
Gaus mean : 29.4626
Gaus Width: 4.5769
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.9
Std Dev 3.516
Gaus mean : 35.8073
Gaus Width: 5.6721
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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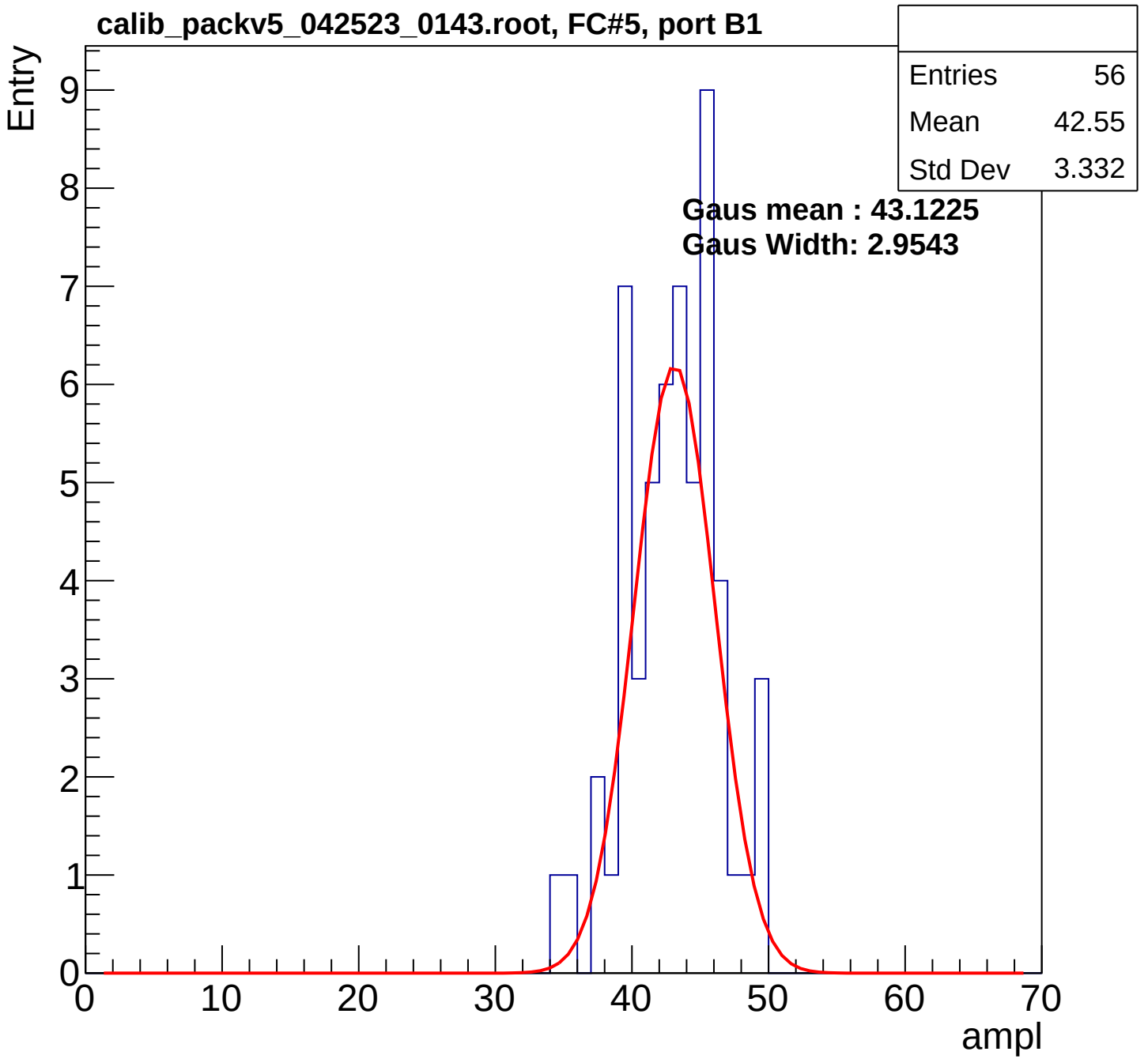
B0L000S, U12-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.55
Std Dev 3.332
Gaus mean : 43.1225
Gaus Width: 2.9543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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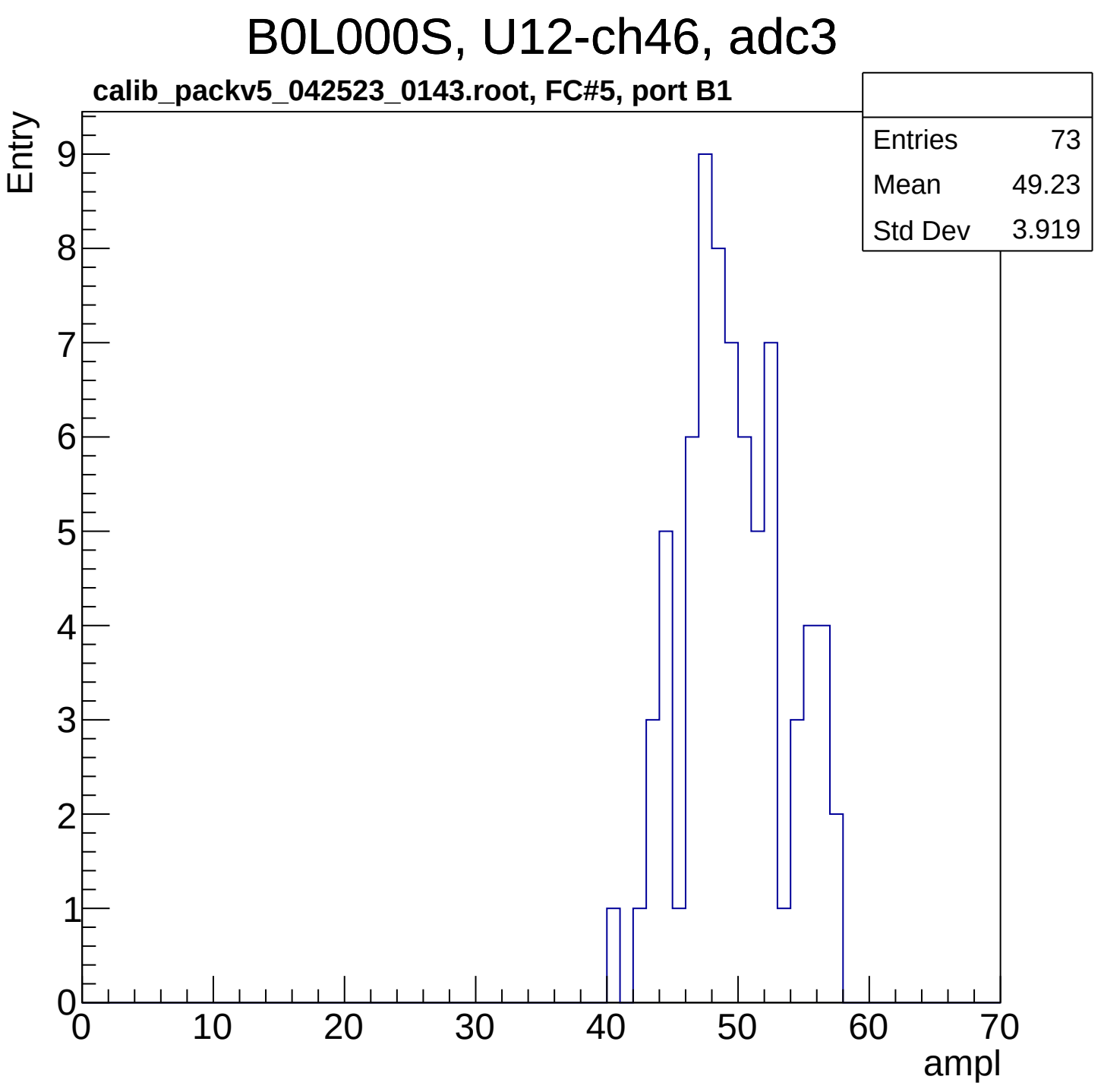
B0L000S, U12-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.23
Std Dev 3.919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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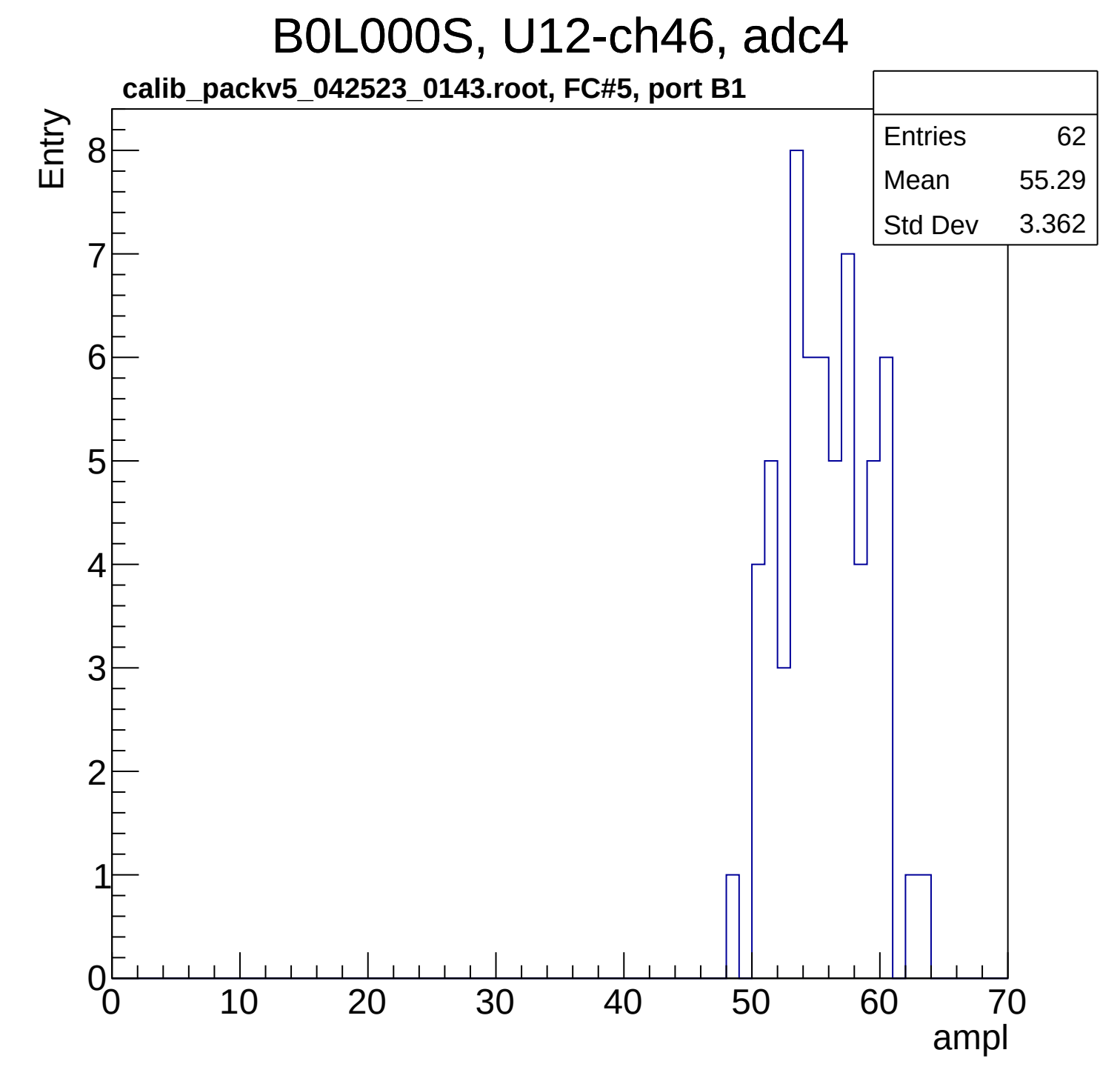
B0L000S, U12-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.29
Std Dev 3.362
Entry
ampl
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1
2
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4
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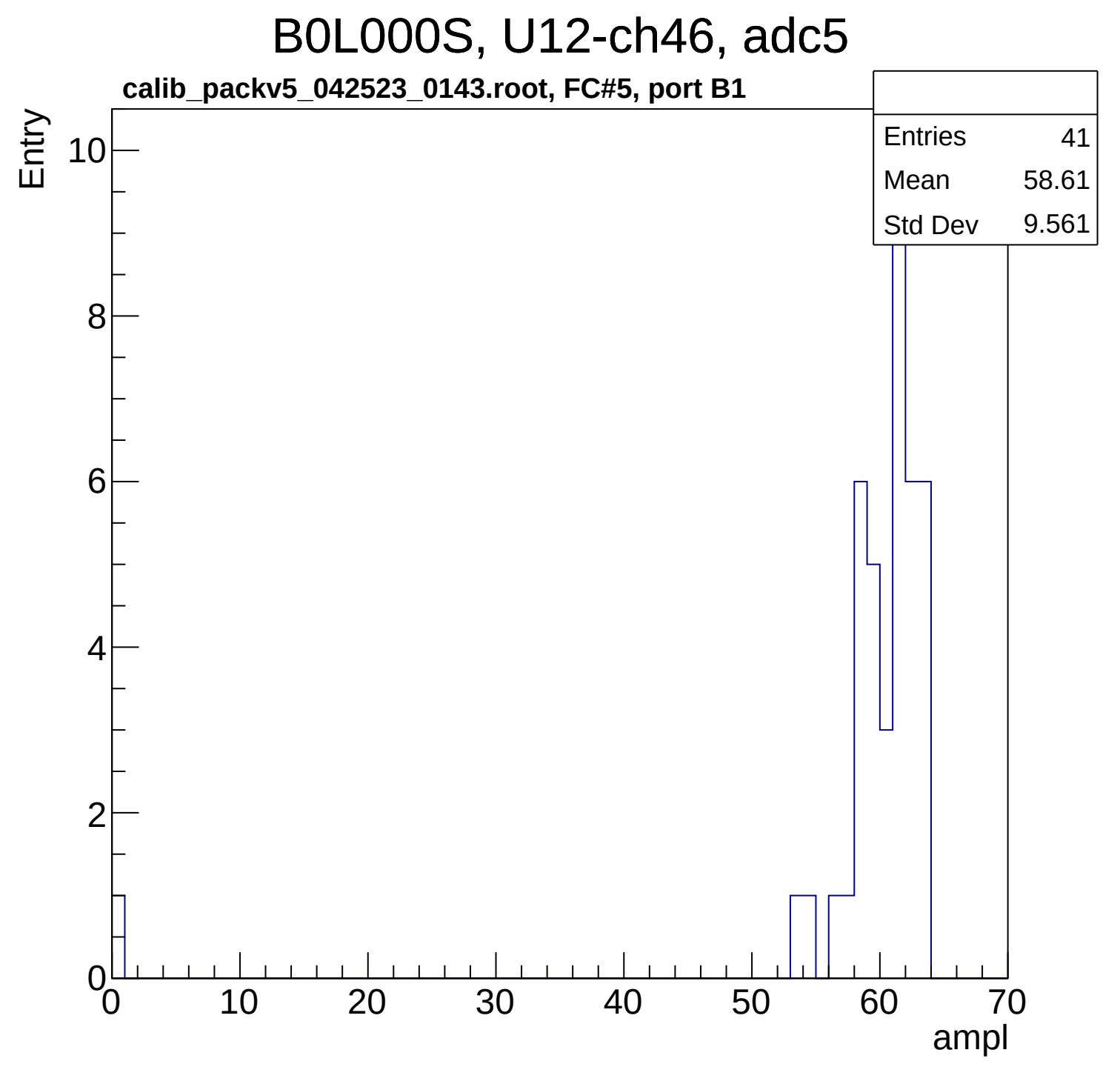
B0L000S, U12-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.61
Std Dev 9.561
Entry
0
2
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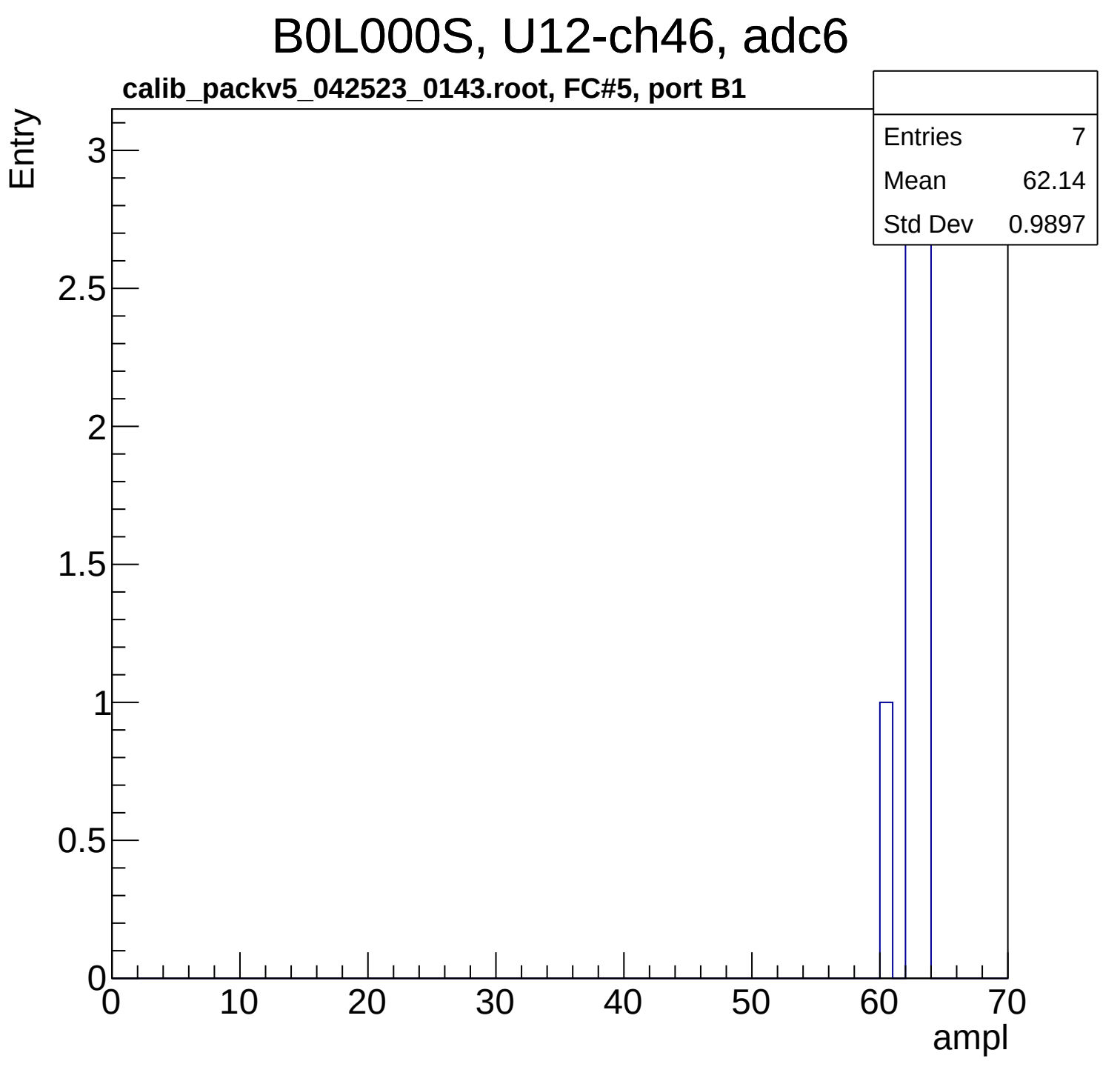
50
60
70
ampl


B0L000S, U12-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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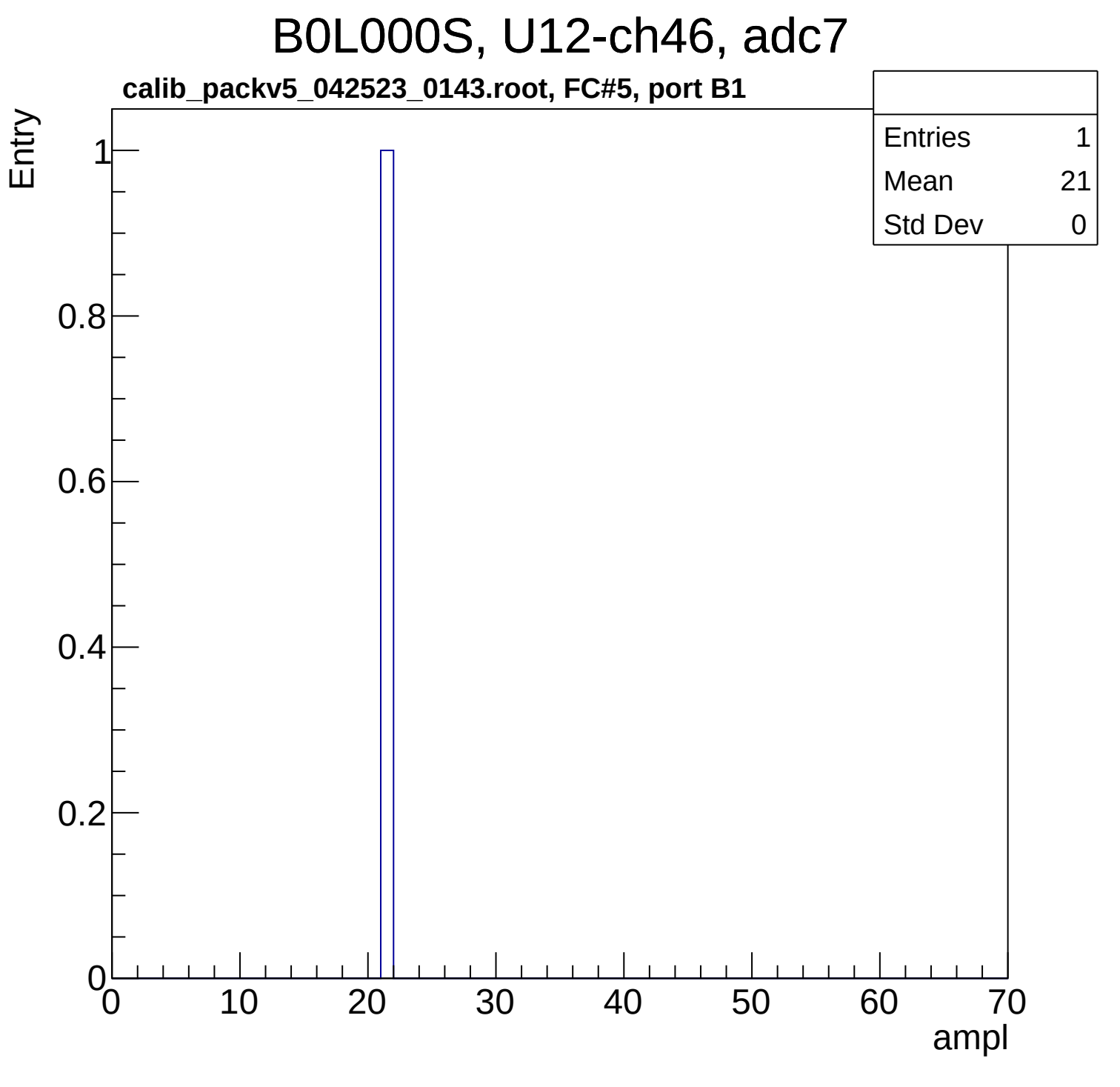
40
50
60
70


B0L000S, U12-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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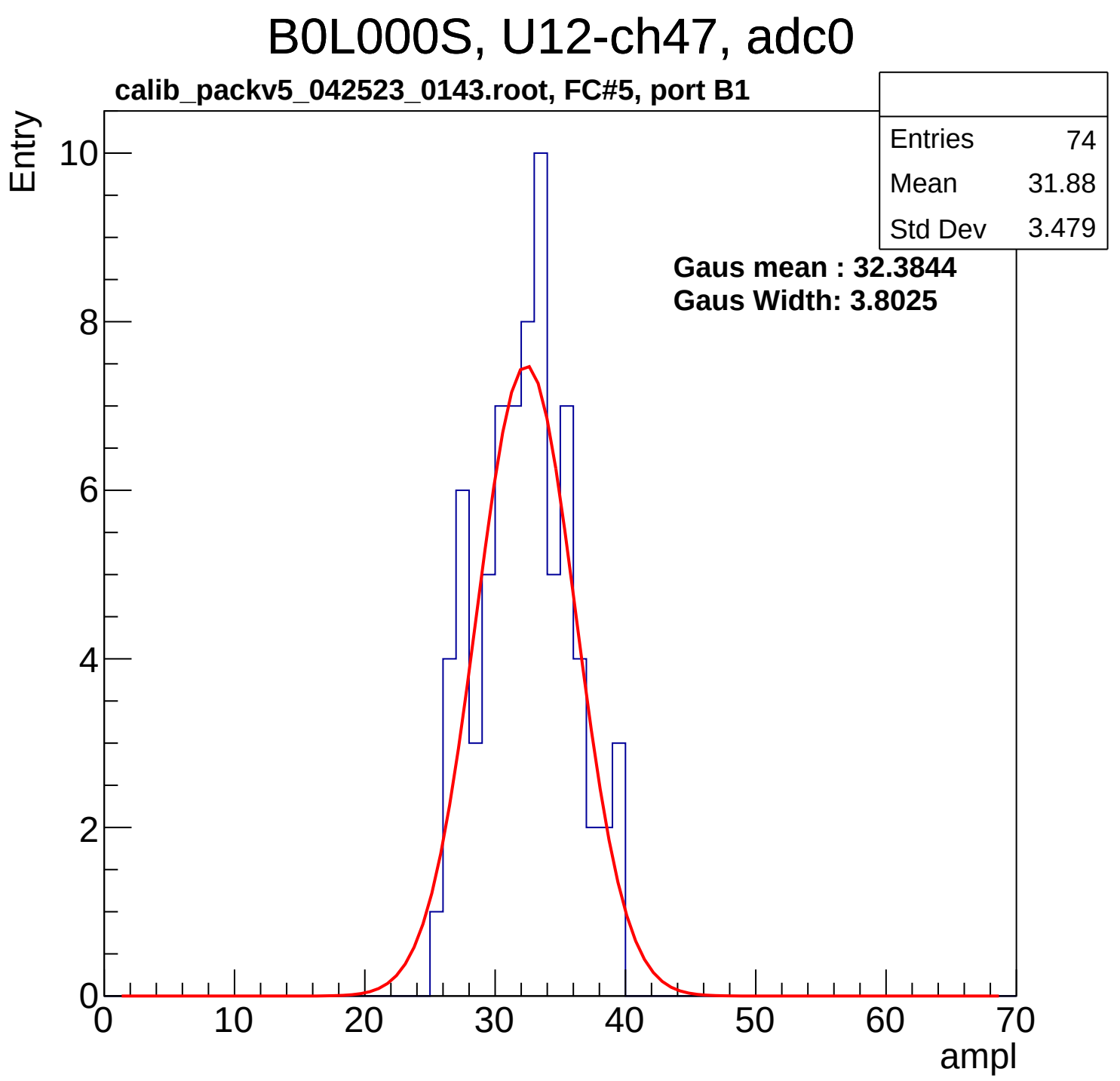
40
50
60
70
ampl


B0L000S, U12-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 31.88
Std Dev 3.479
Gaus mean : 32.3844
Gaus Width: 3.8025
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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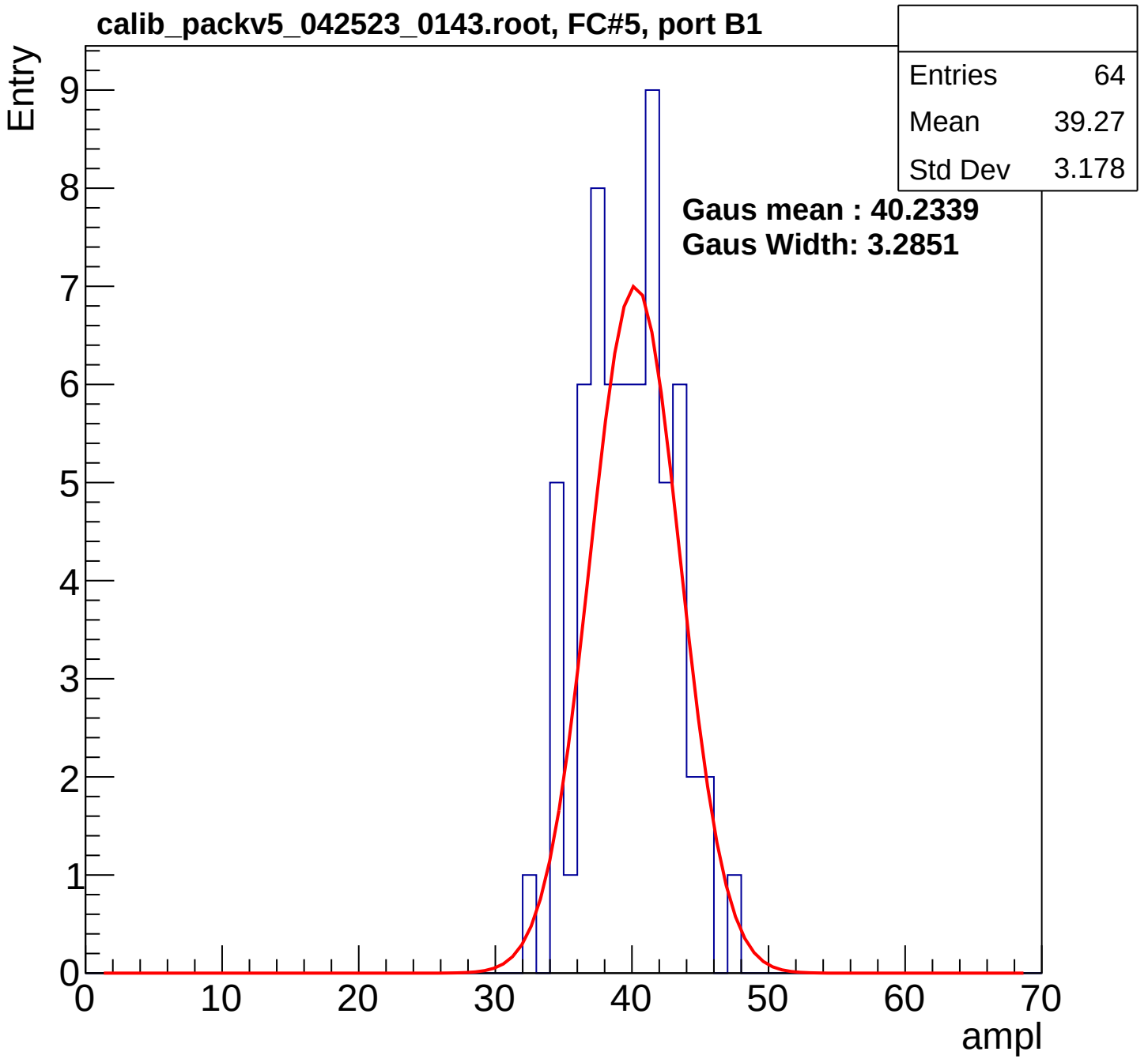
B0L000S, U12-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 39.27
Std Dev 3.178
Gaus mean : 40.2339
Gaus Width: 3.2851
Entry
ampl
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3
4
5
6
7
8
9
0
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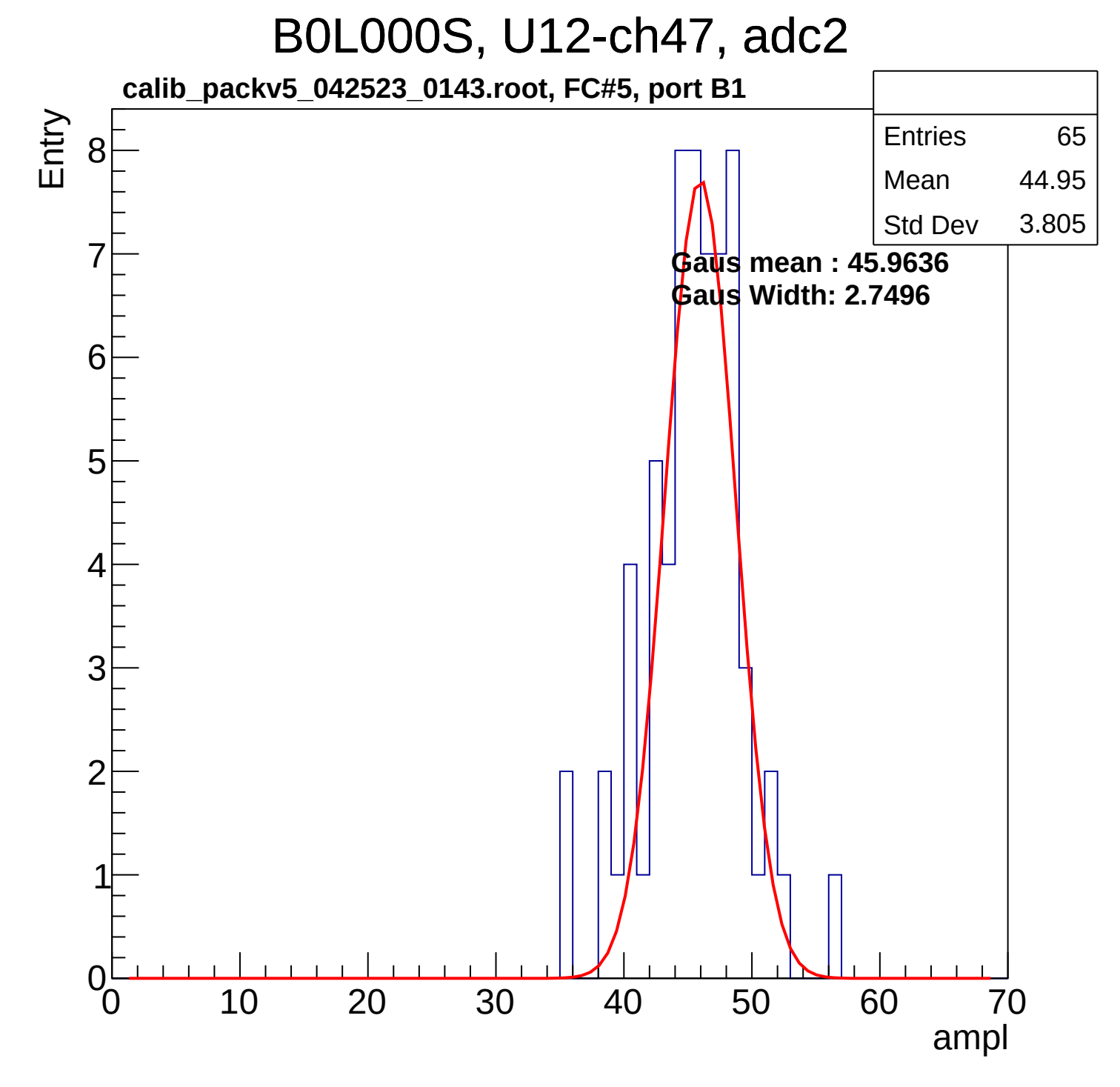
B0L000S, U12-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 44.95
Std Dev 3.805
Gaus mean : 45.9636
Gaus Width: 2.7496
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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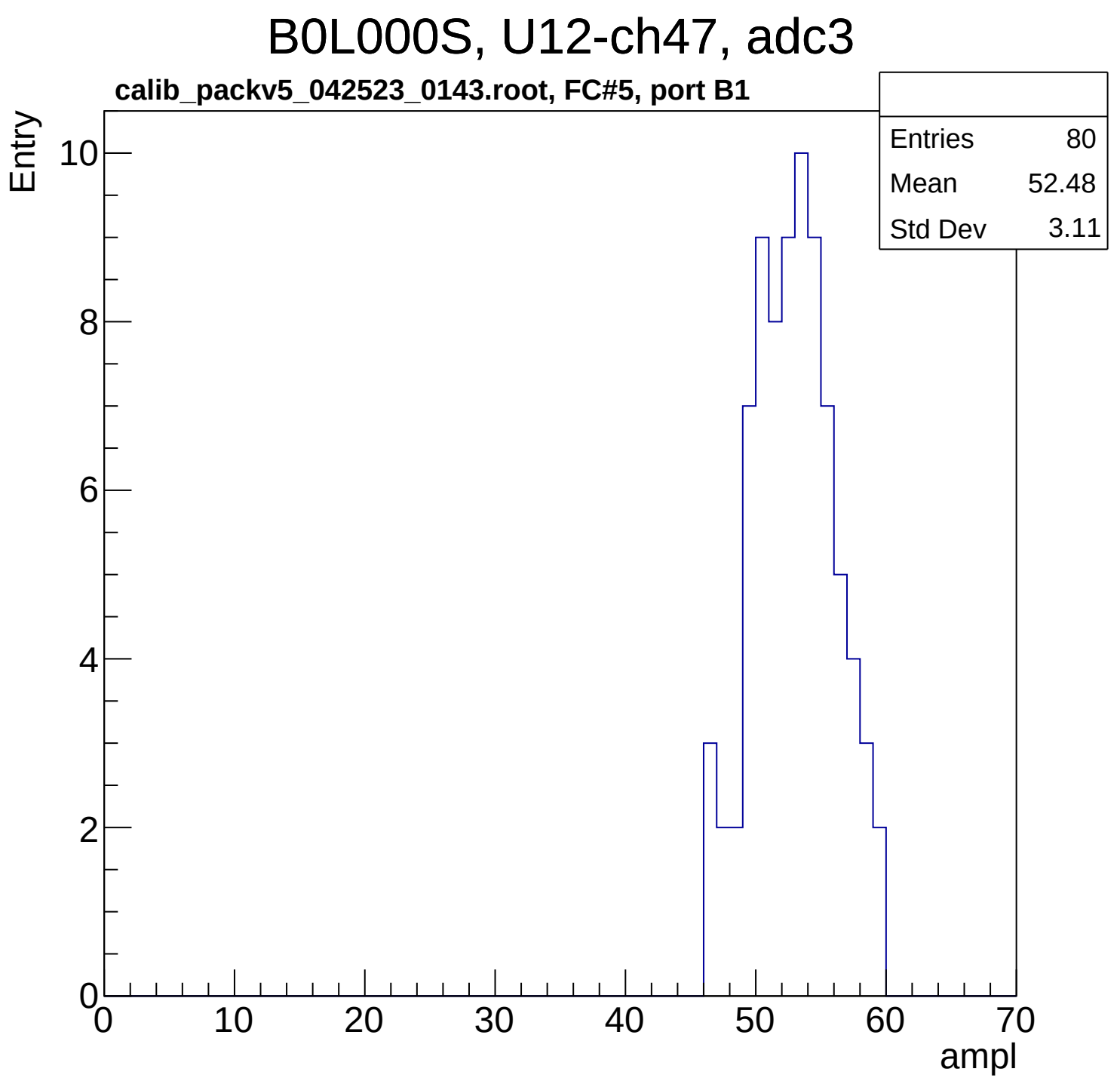
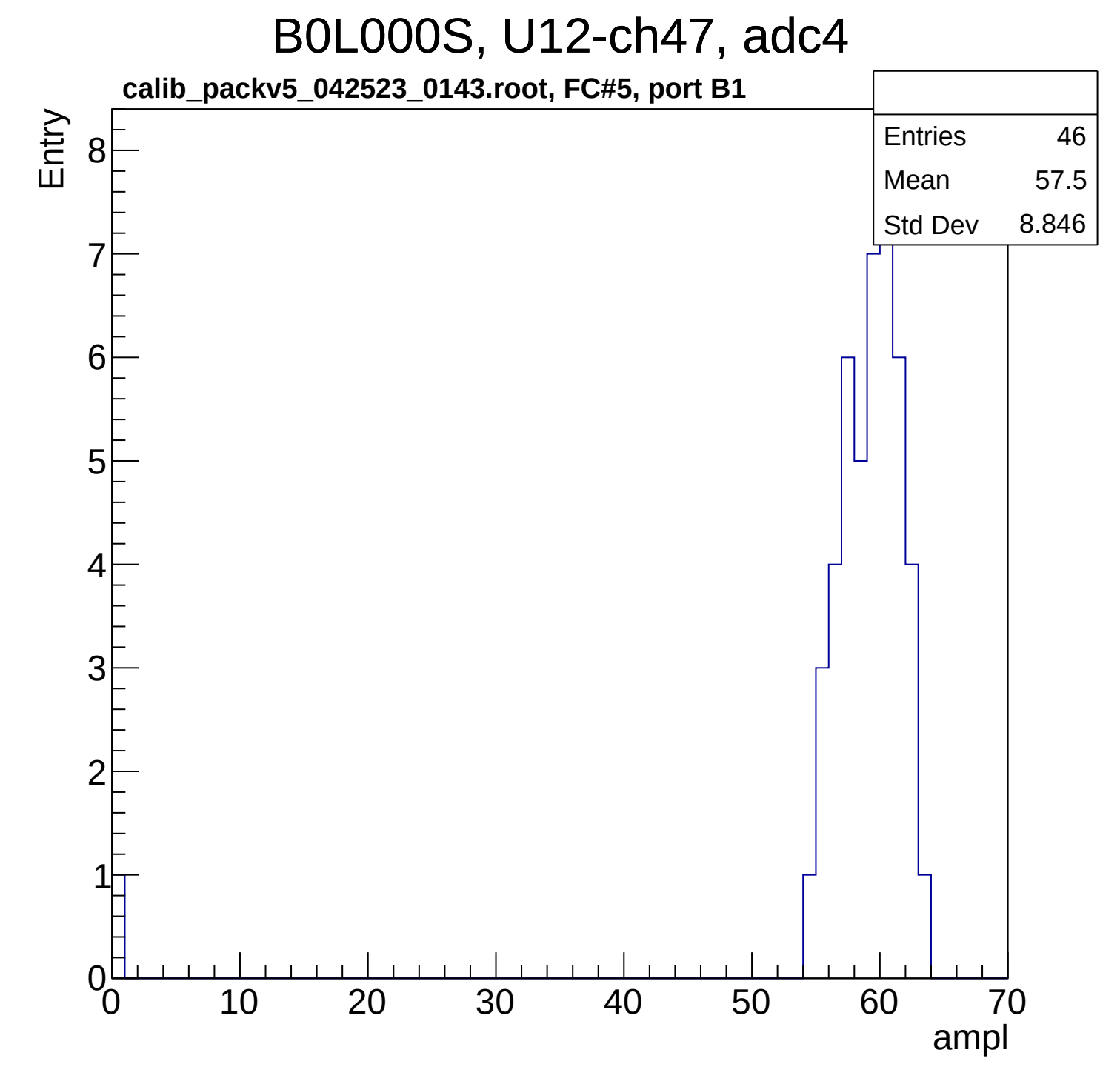
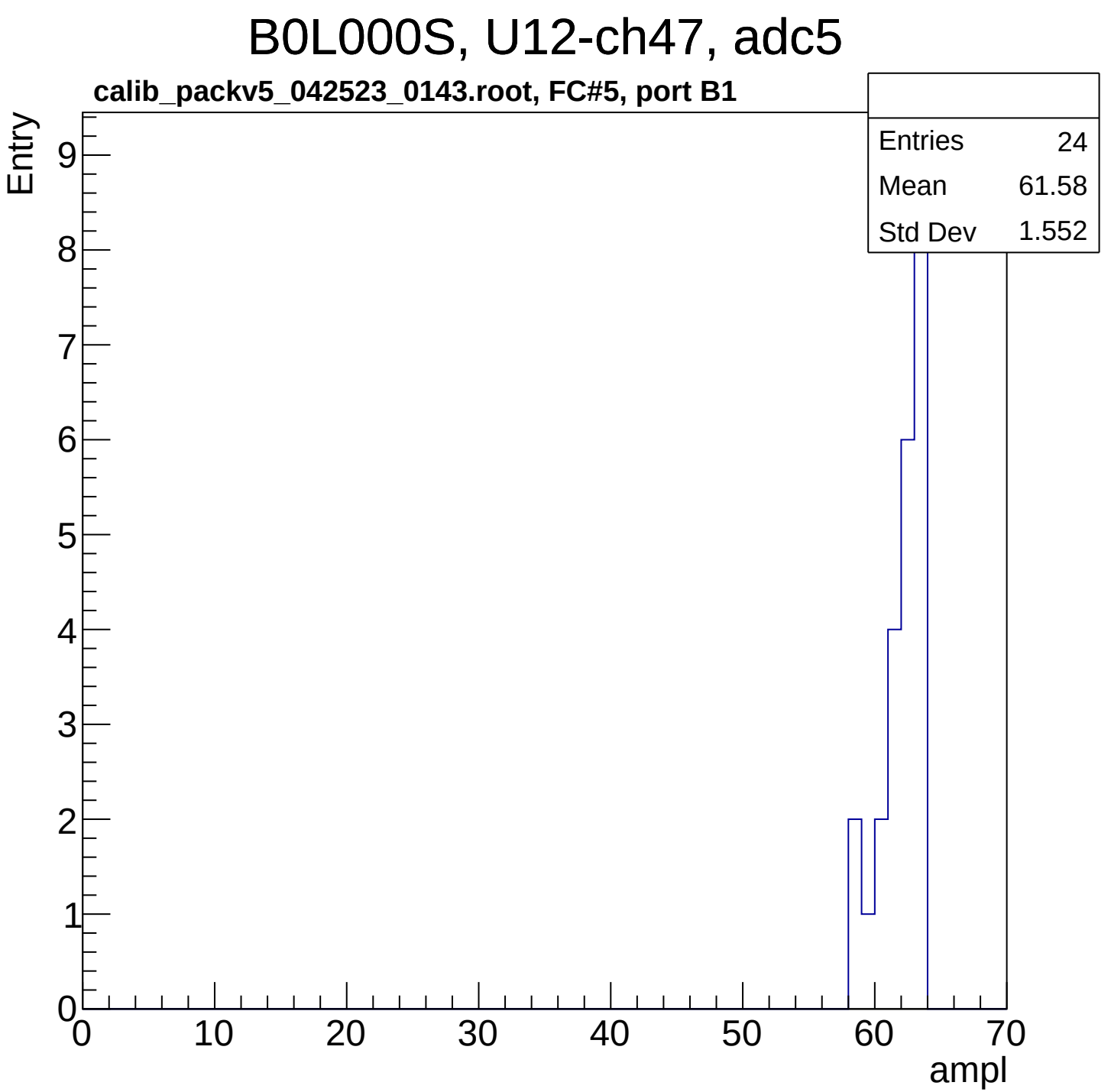
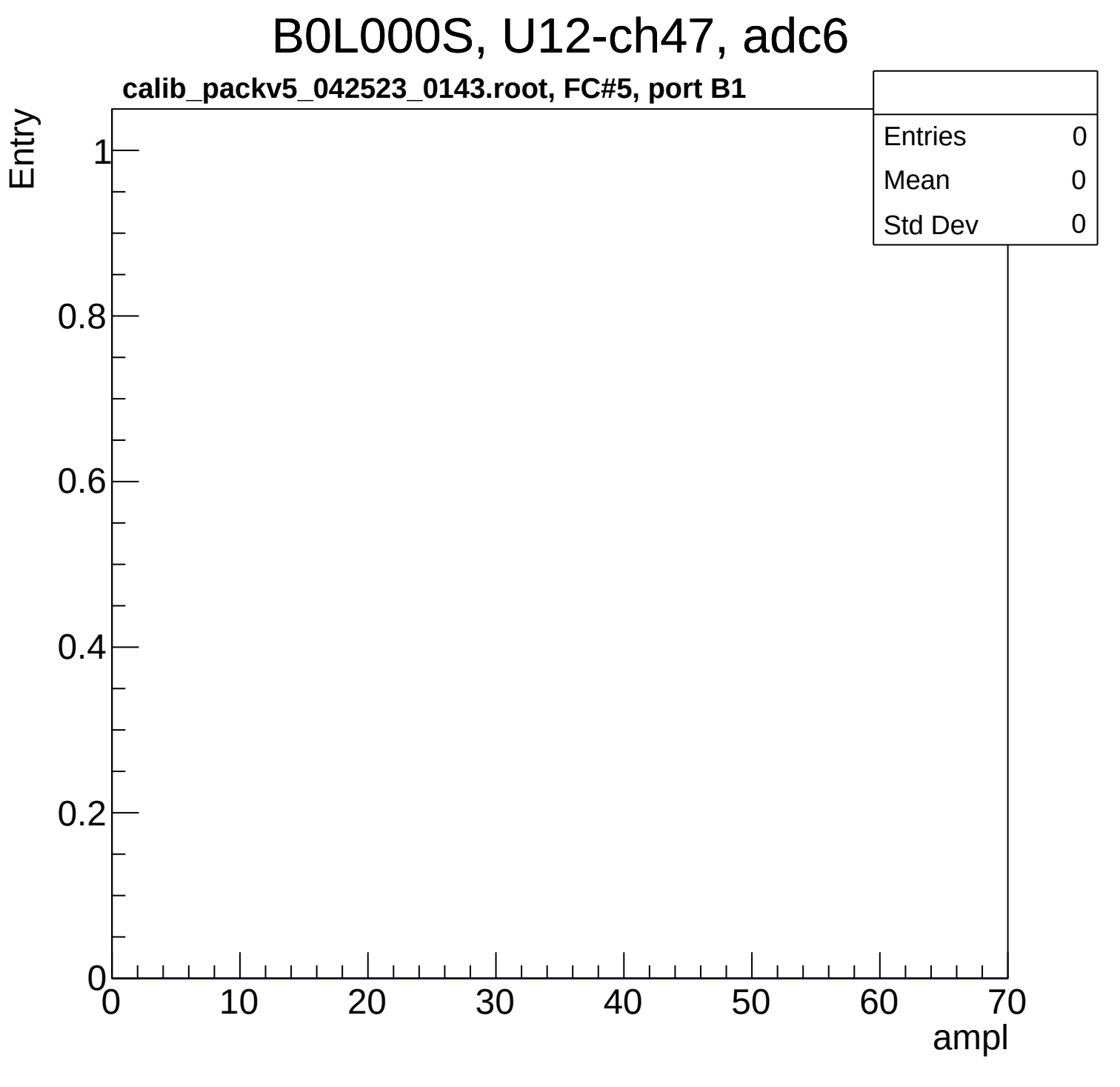
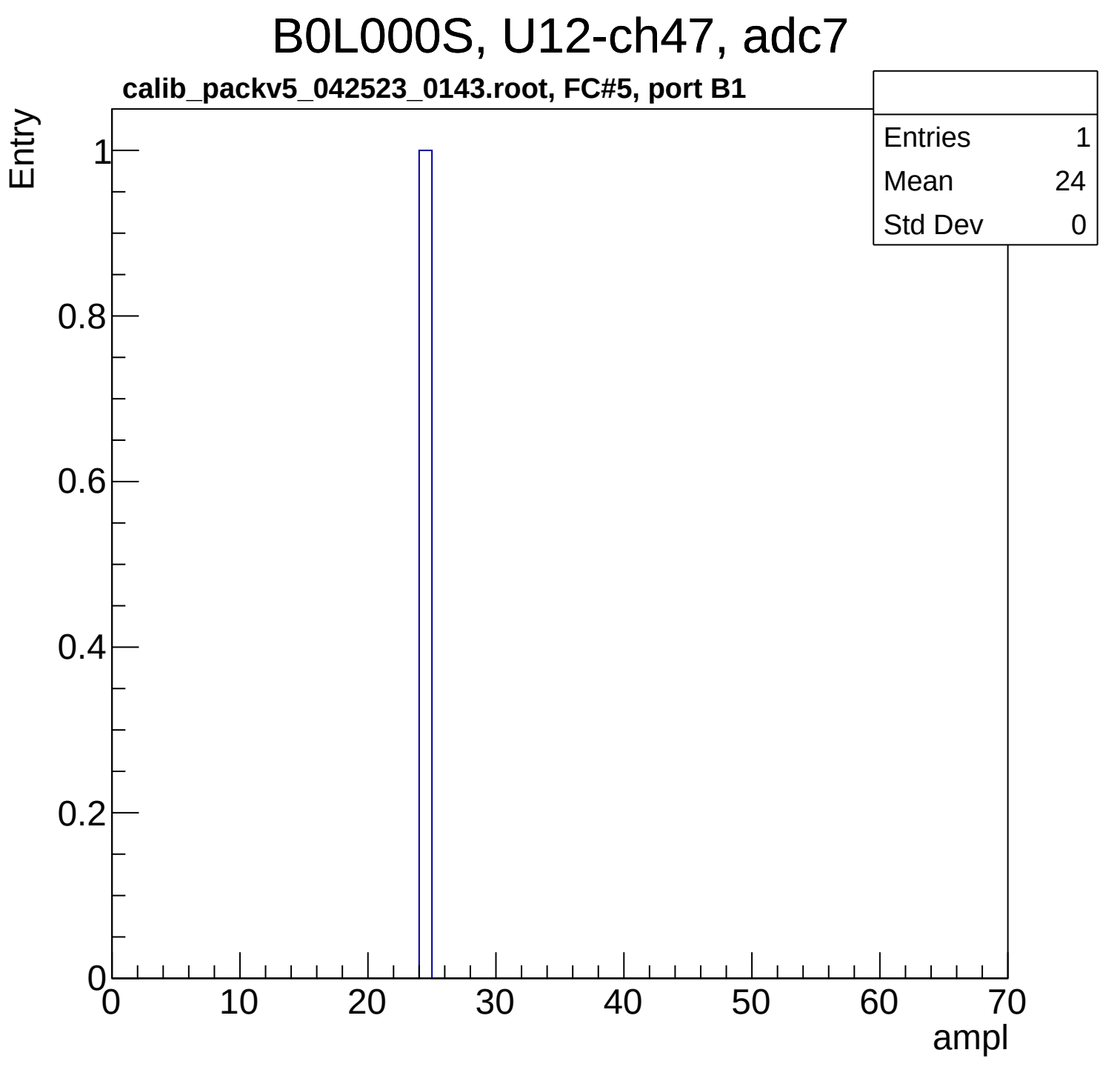

B0L000S, U12-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 52.48
Std Dev 3.11
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.5
Std Dev 8.846
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 61.58
Std Dev 1.552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
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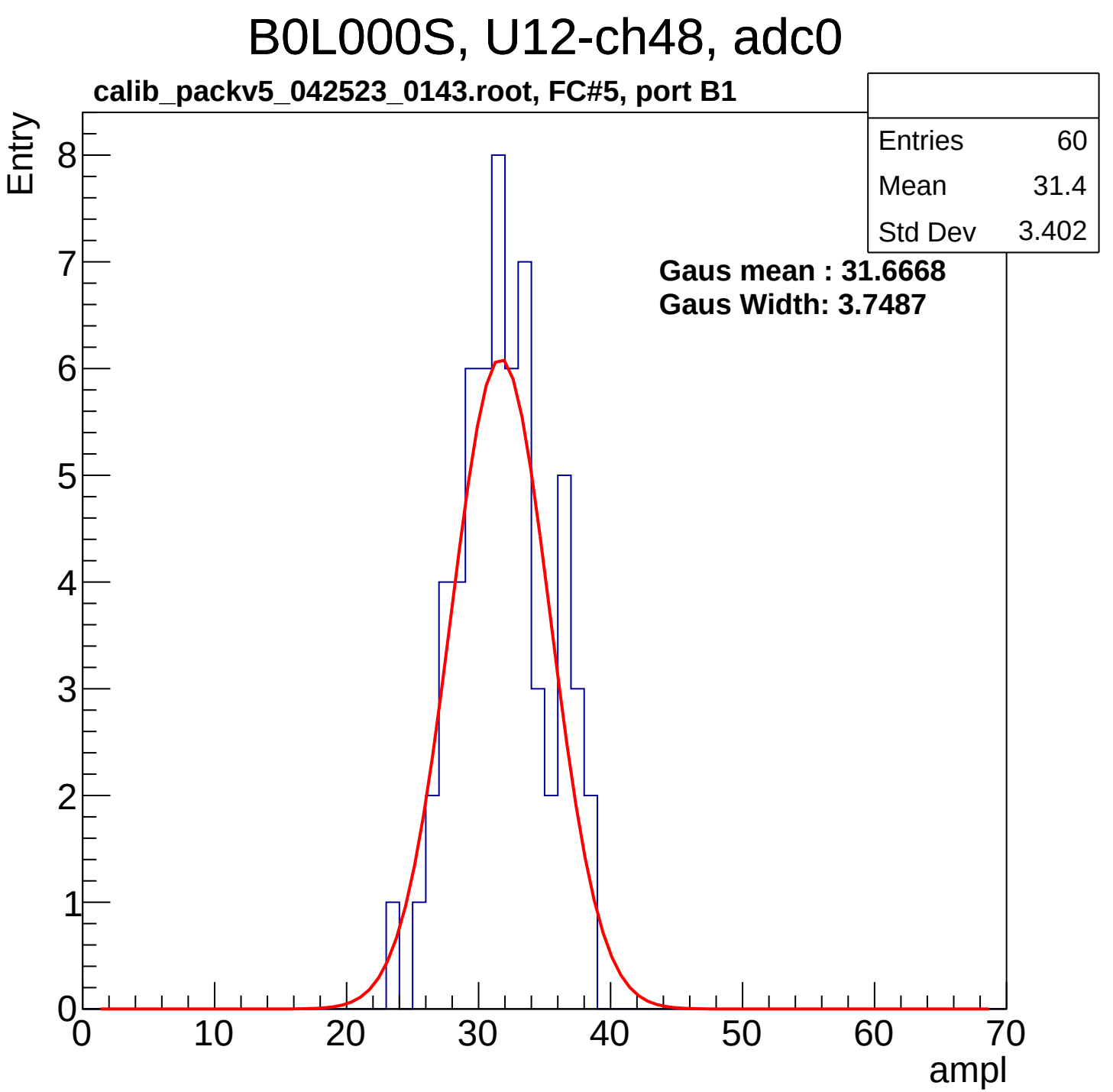
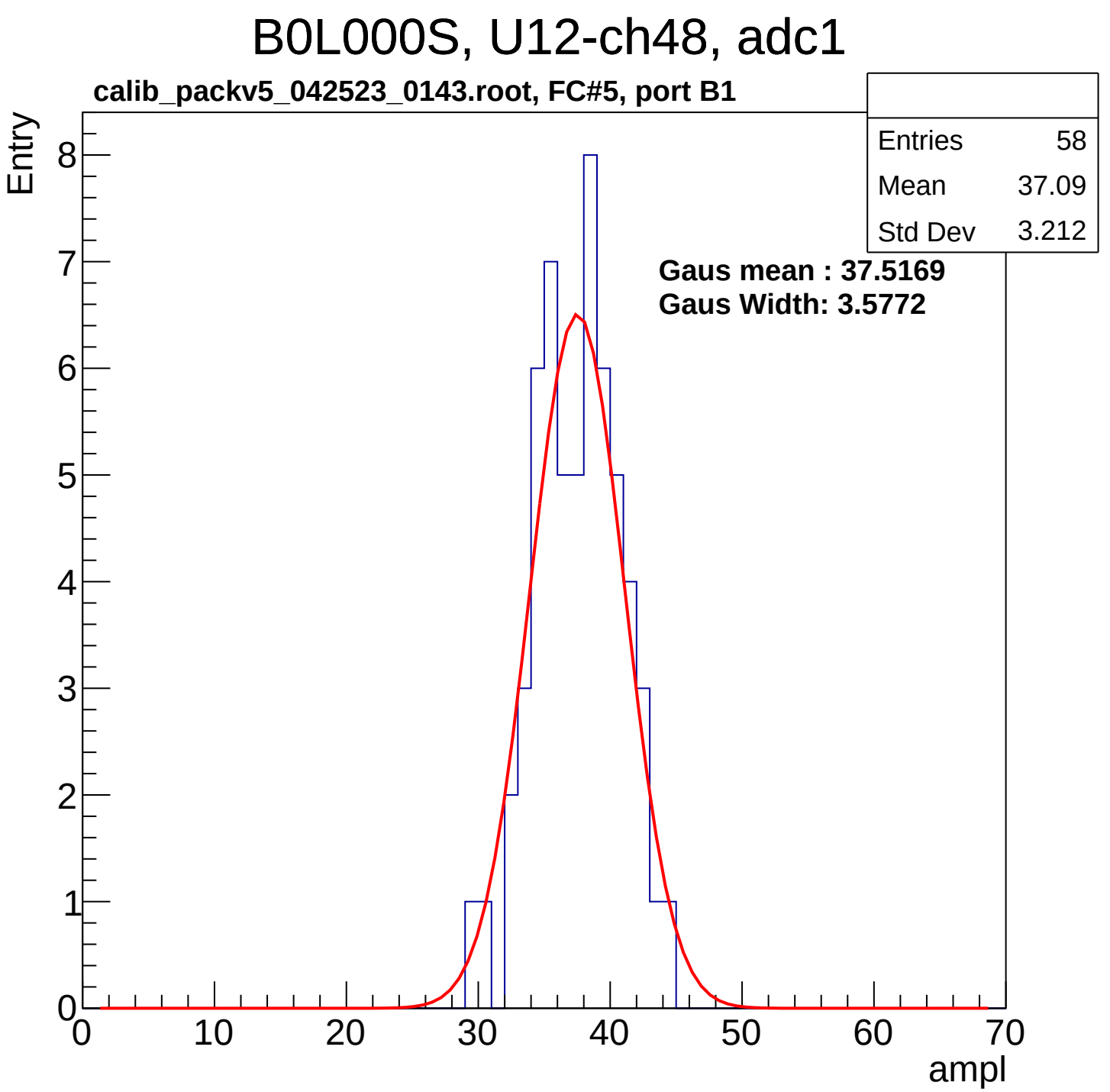
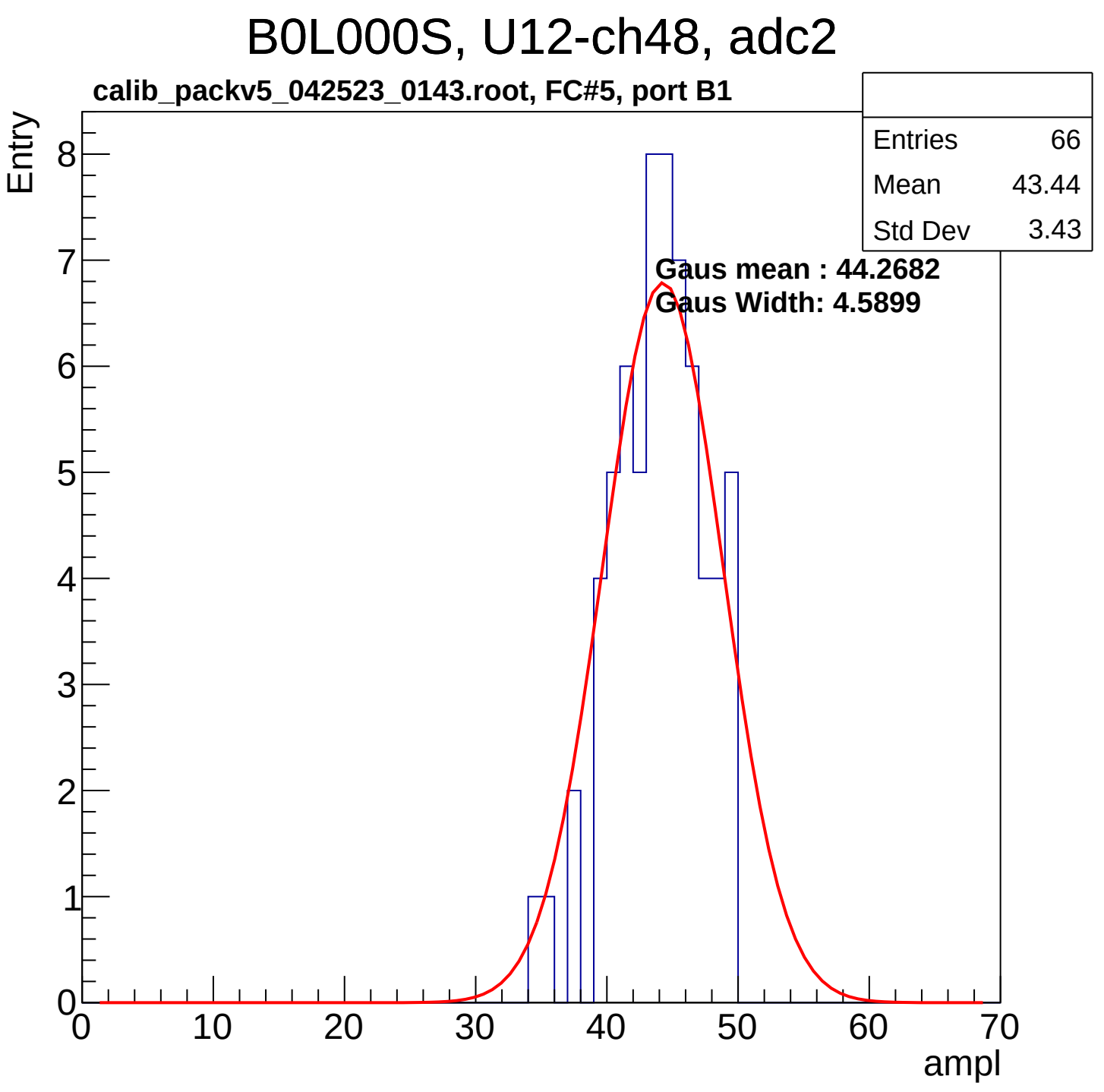
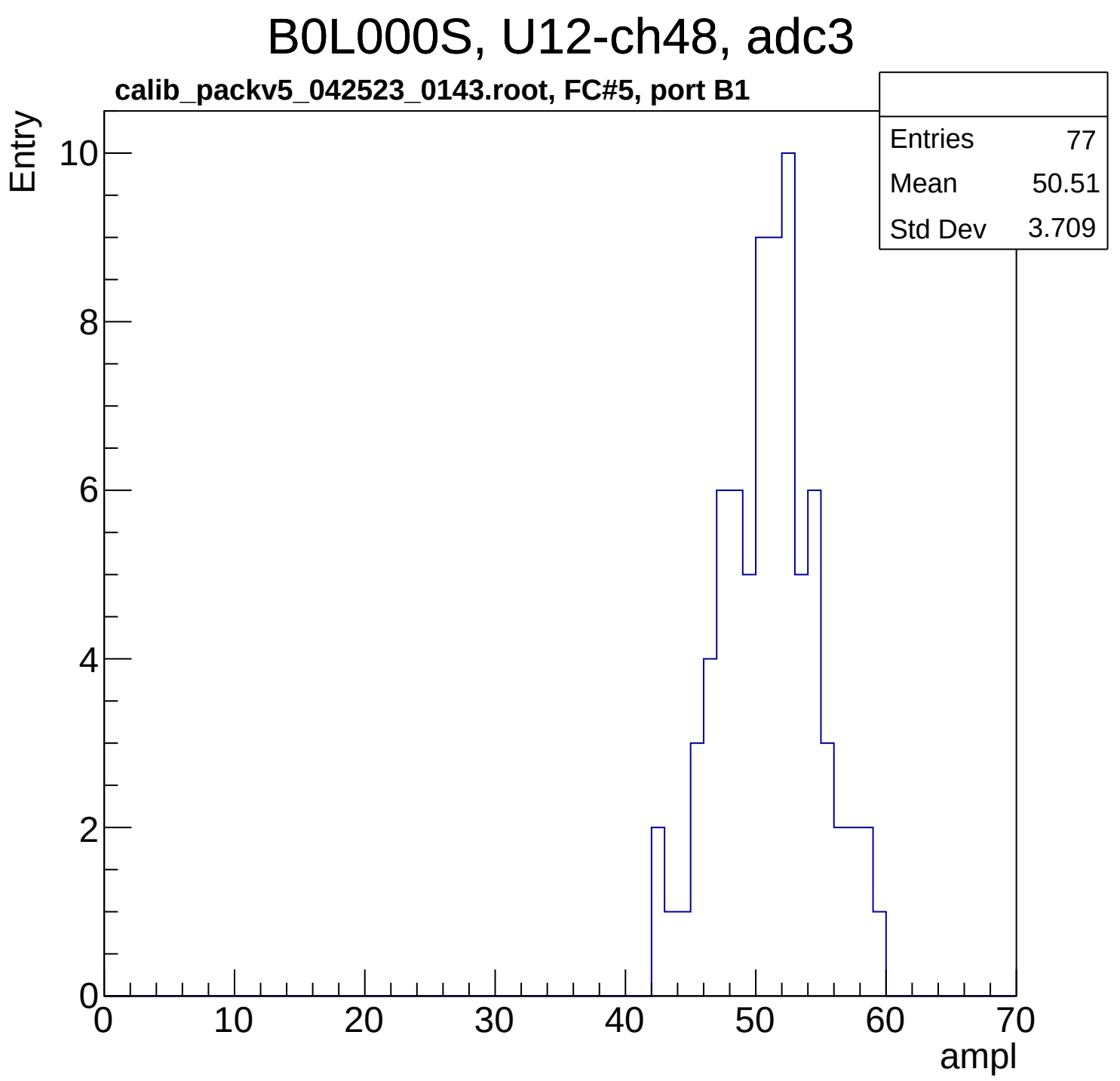
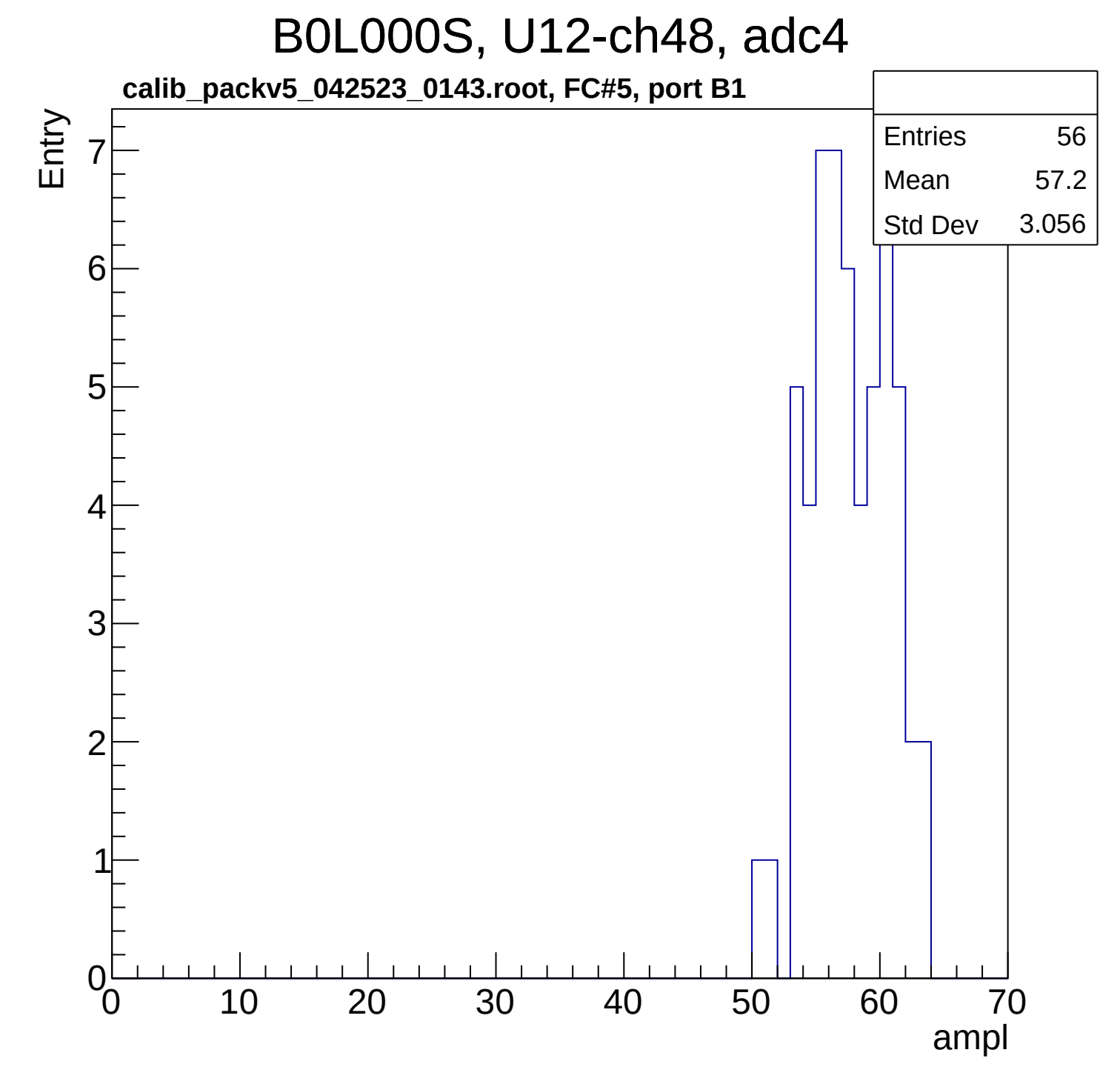
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 31.4
Std Dev 3.402
Gaus mean : 31.6668
Gaus Width: 3.7487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 37.09
Std Dev 3.212
Gaus mean : 37.5169
Gaus Width: 3.5772
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.44
Std Dev 3.43
Gaus mean : 44.2682
Gaus Width: 4.5899
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 50.51
Std Dev 3.709
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.2
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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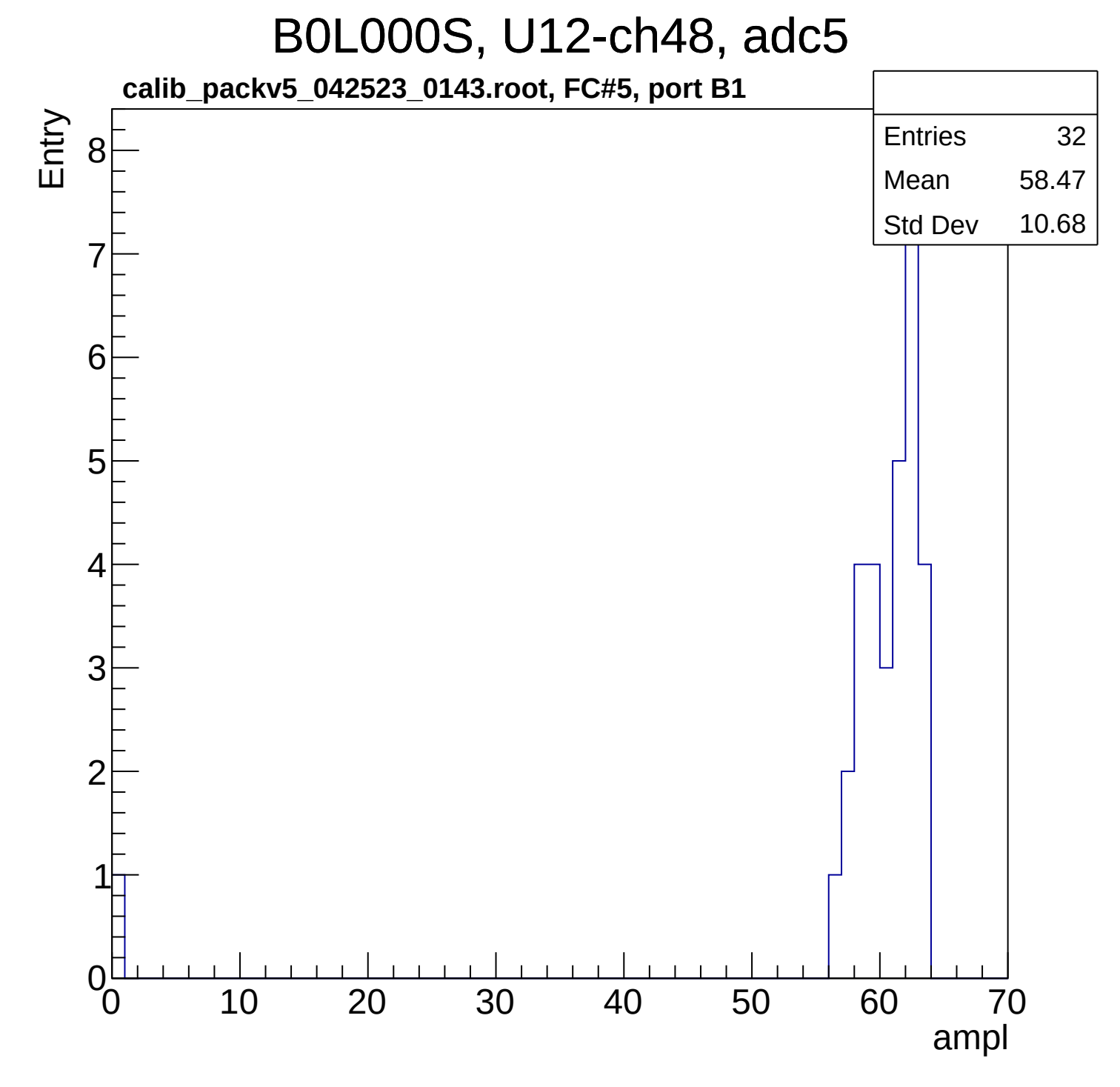
B0L000S, U12-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.47
Std Dev 10.68
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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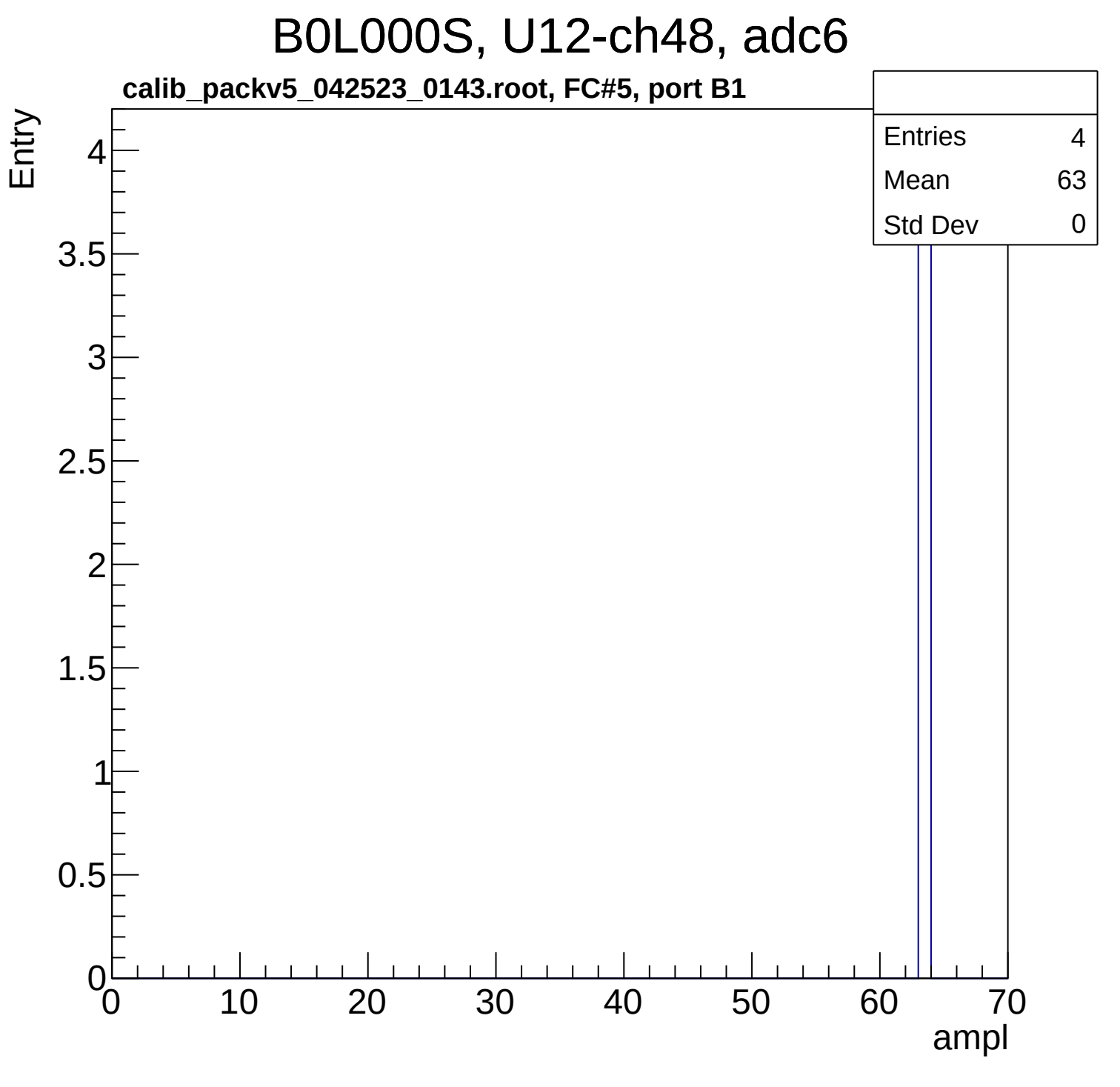
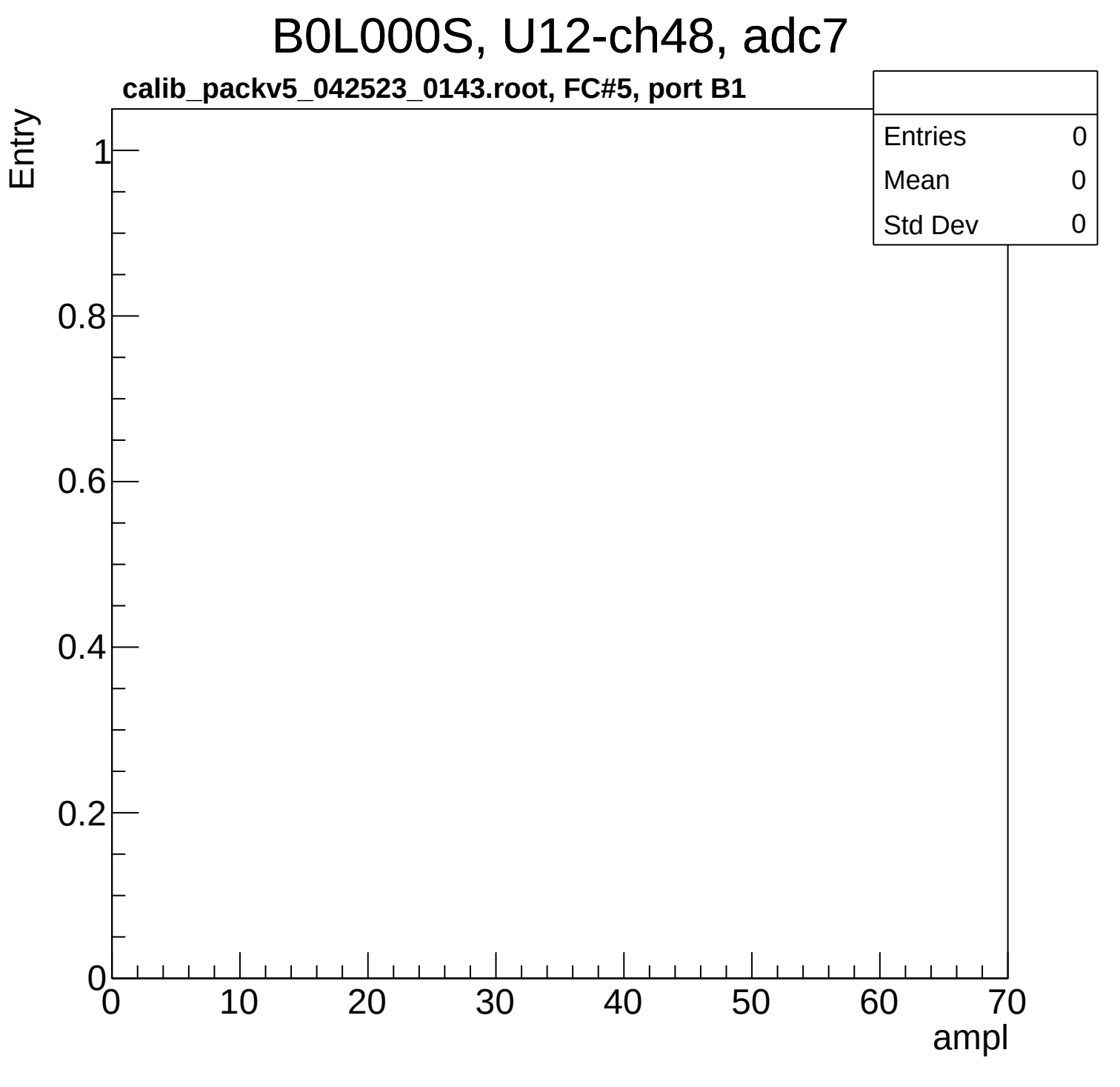

B0L000S, U12-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U12-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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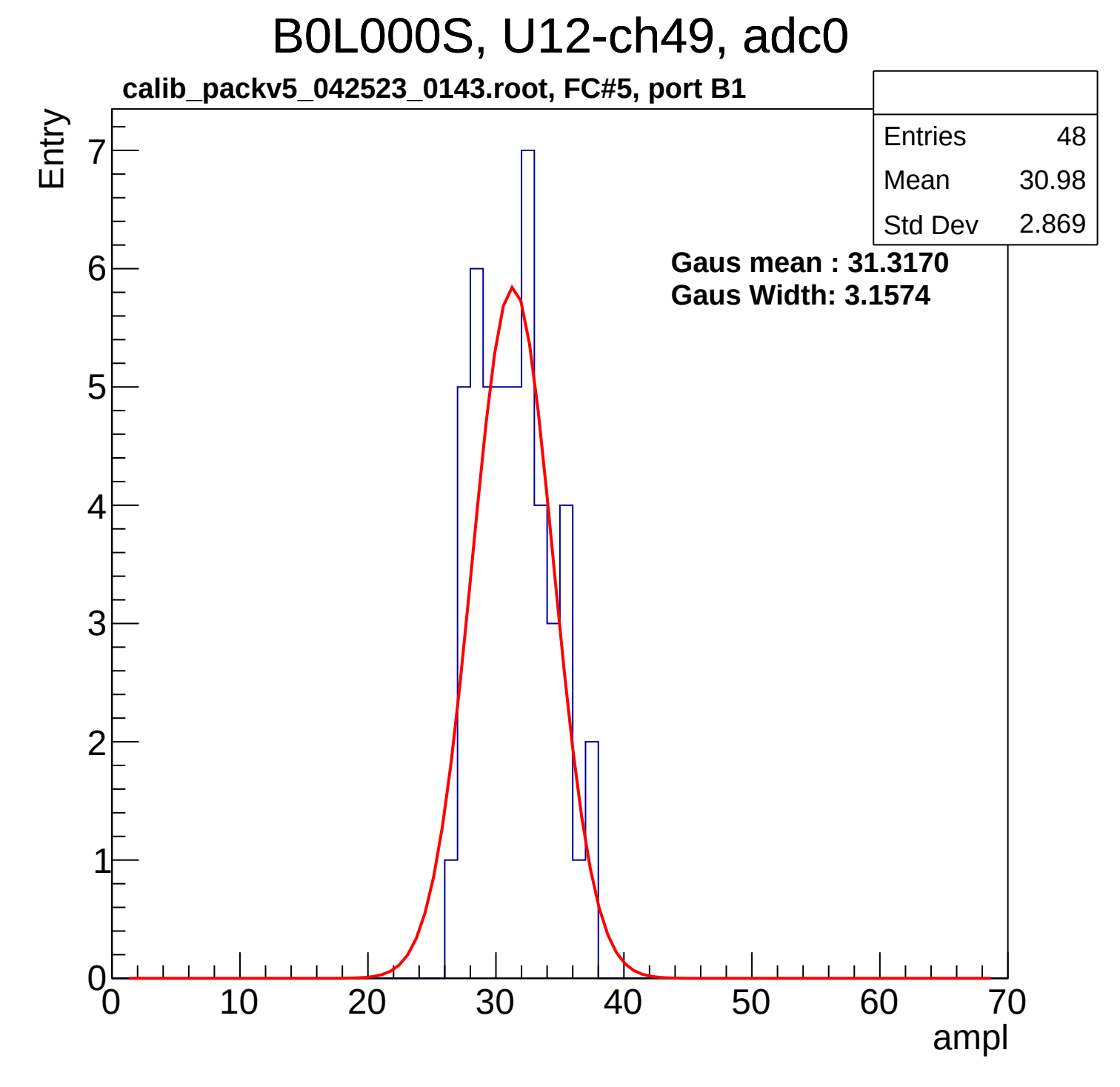
50
60
70
ampl


B0L000S, U12-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 30.98
Std Dev 2.869
Gaus mean : 31.3170
Gaus Width: 3.1574
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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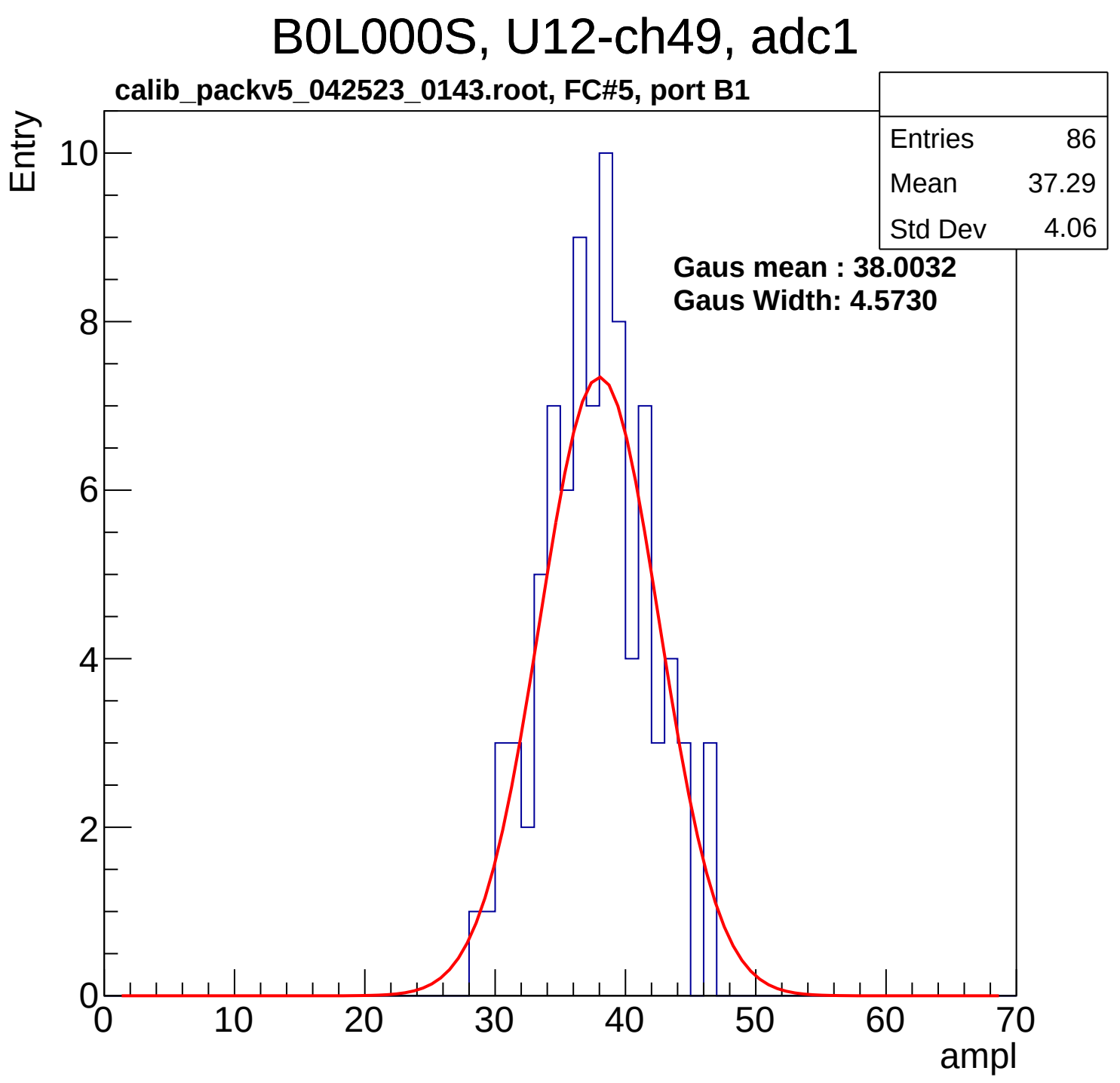
B0L000S, U12-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 37.29
Std Dev 4.06
Gaus mean : 38.0032
Gaus Width: 4.5730
Entry
ampl
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4
6
8
10
0
10
20
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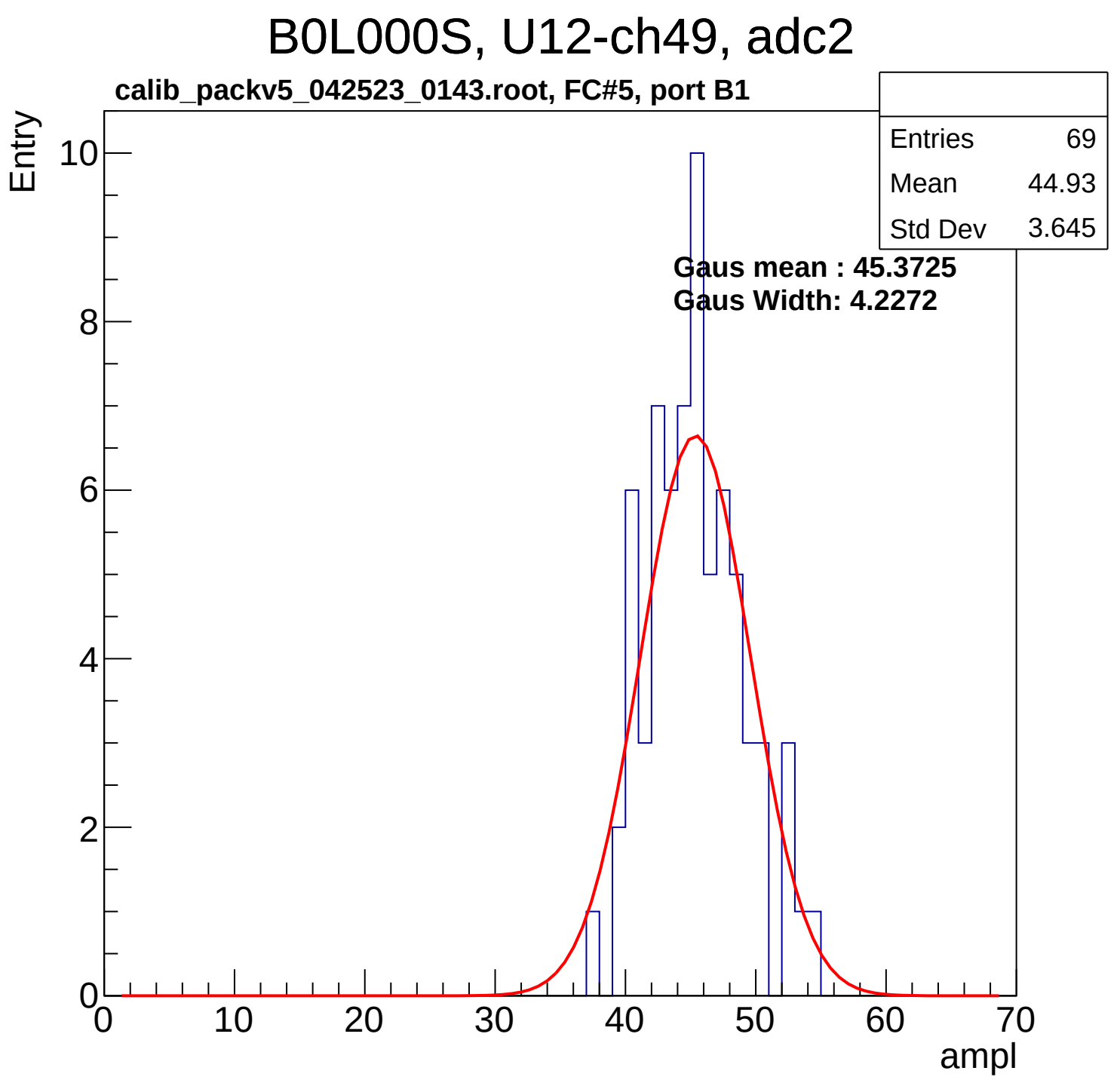
40
50
60
70


B0L000S, U12-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.93
Std Dev 3.645
Gaus mean : 45.3725
Gaus Width: 4.2272
Entry
ampl
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4
6
8
10
0
10
20
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50
60
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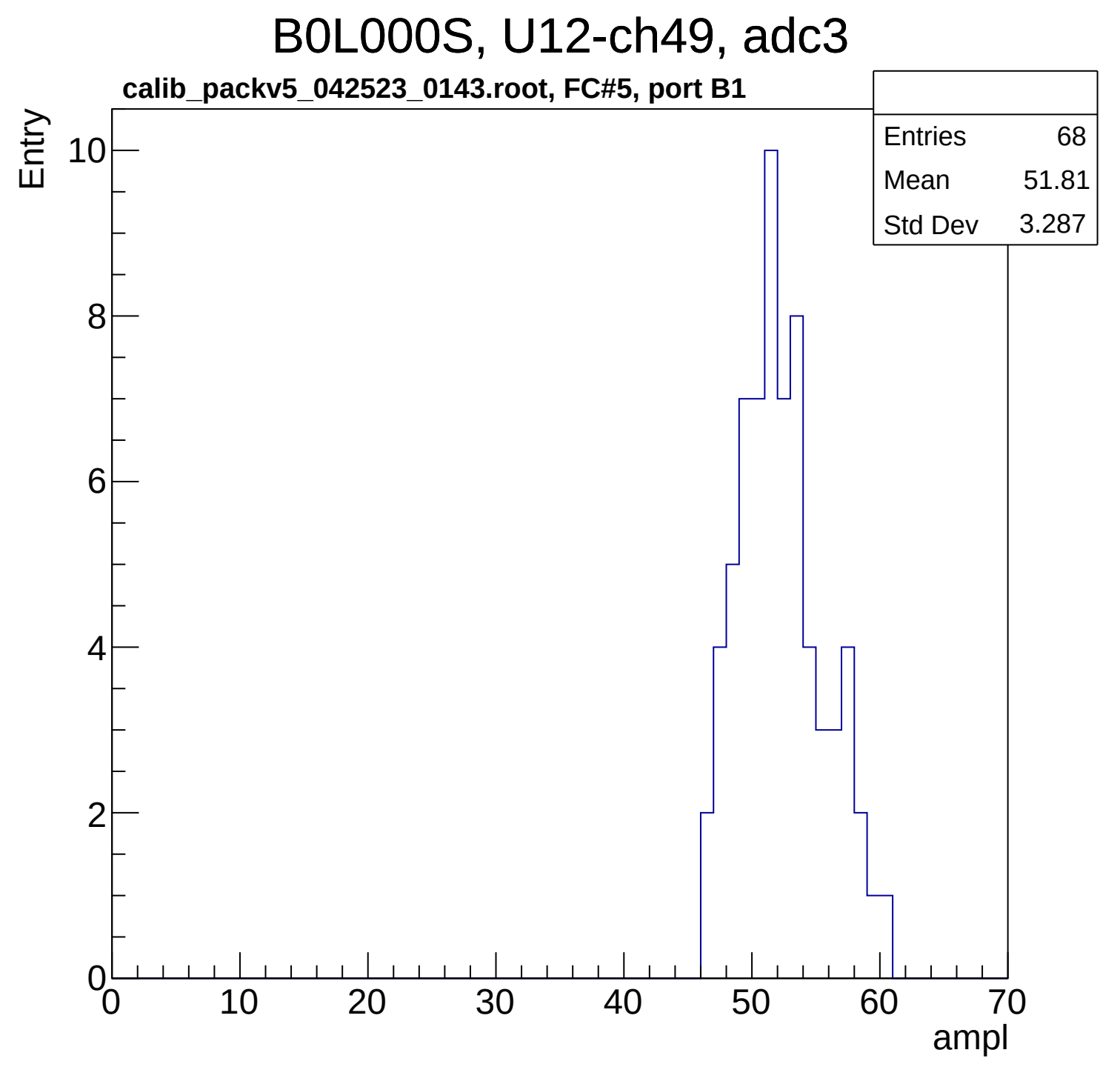
B0L000S, U12-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.81
Std Dev 3.287
Entry
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4
6
8
10
0
10
20
30
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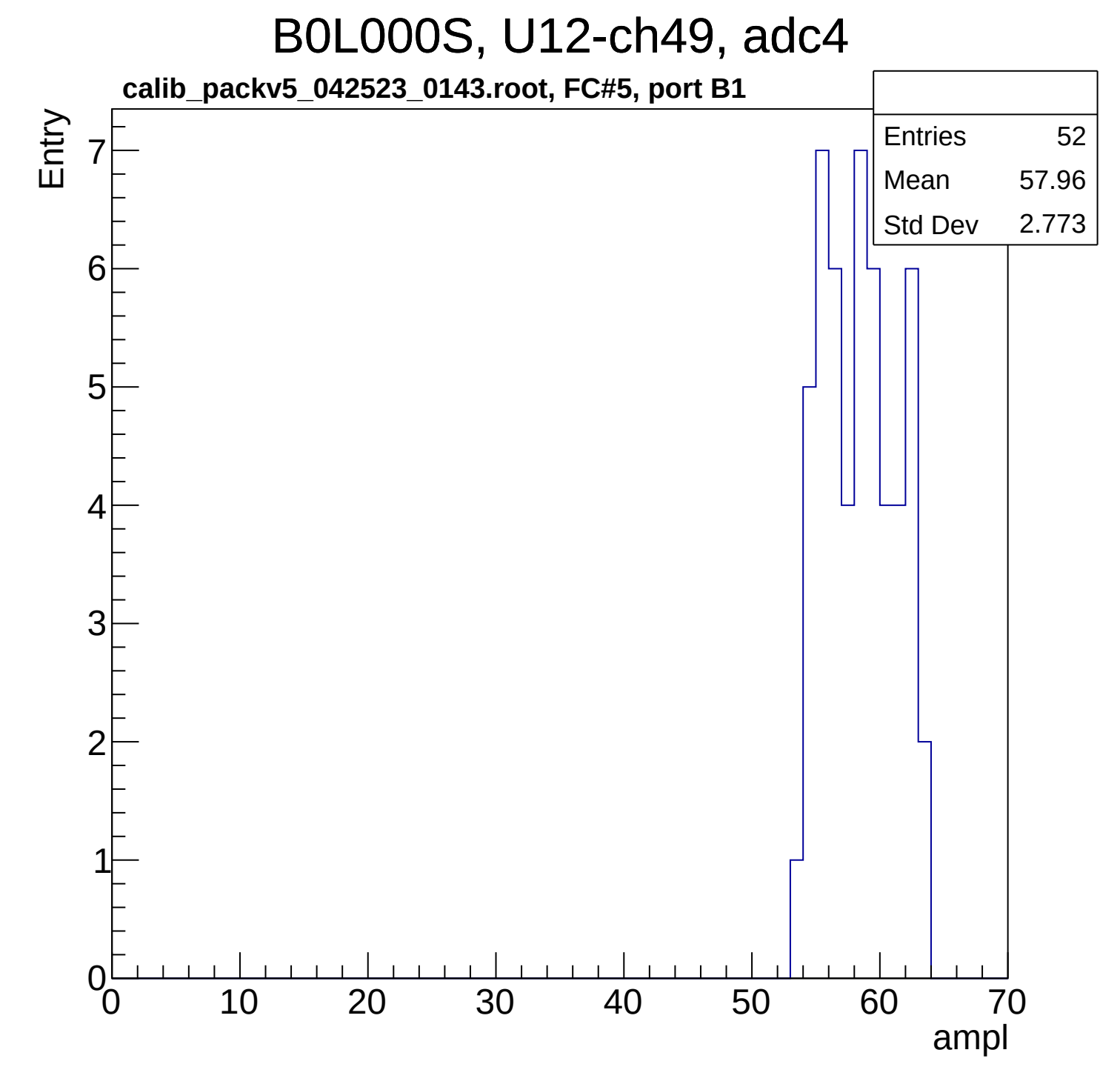
50
60
70
ampl


B0L000S, U12-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.96
Std Dev 2.773
Entry
ampl
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0
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60
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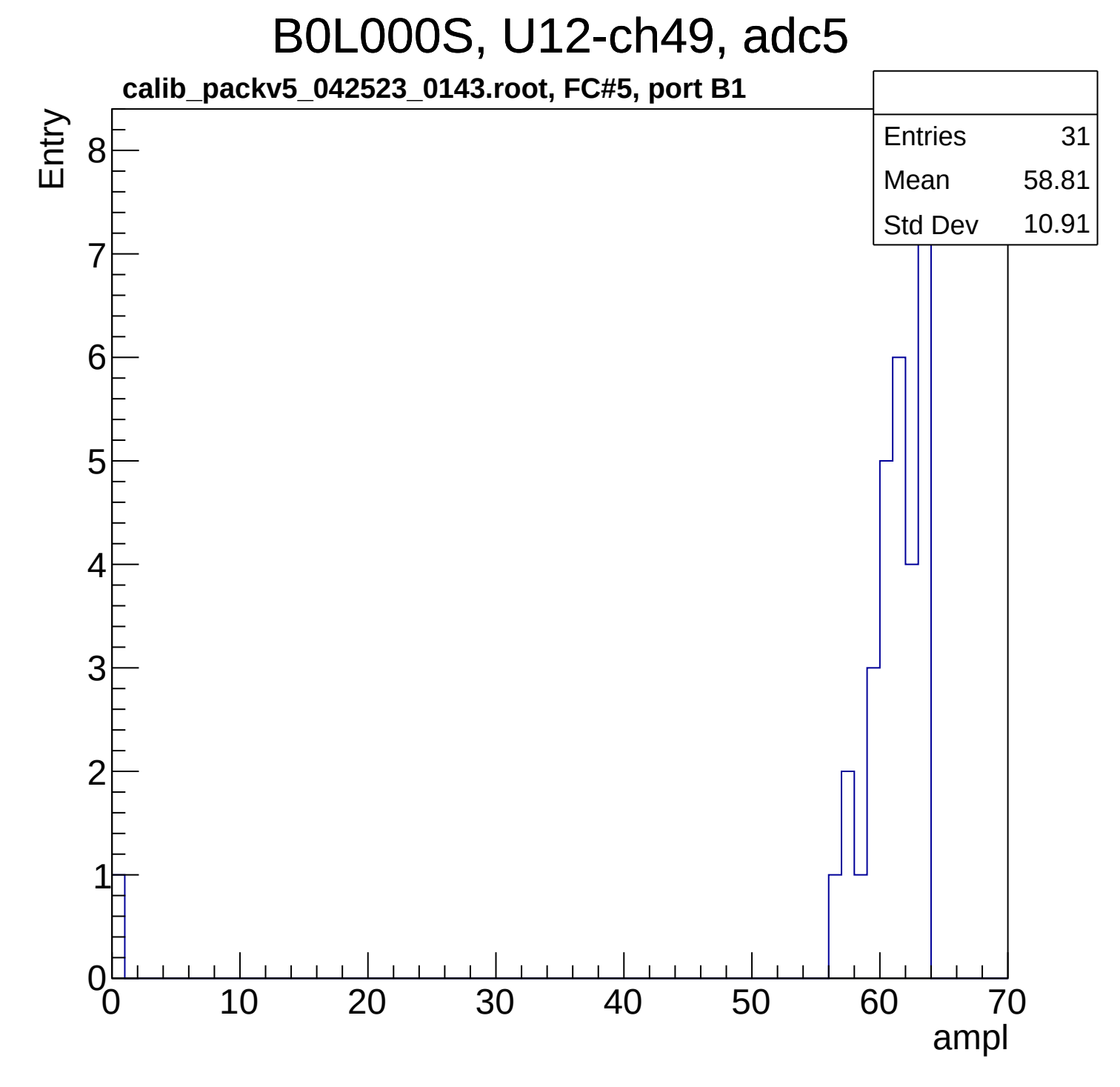
B0L000S, U12-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.81
Std Dev 10.91
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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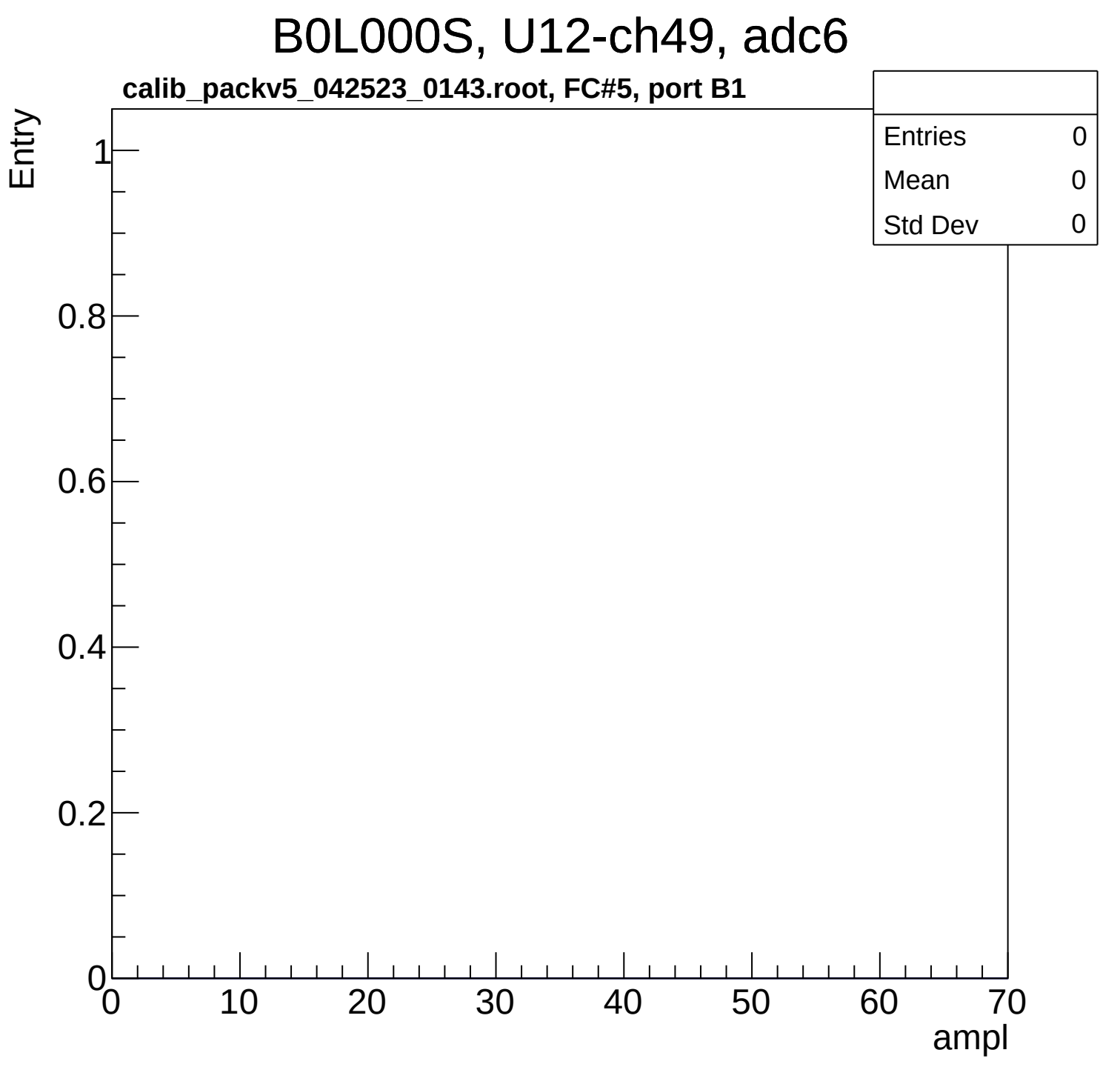
B0L000S, U12-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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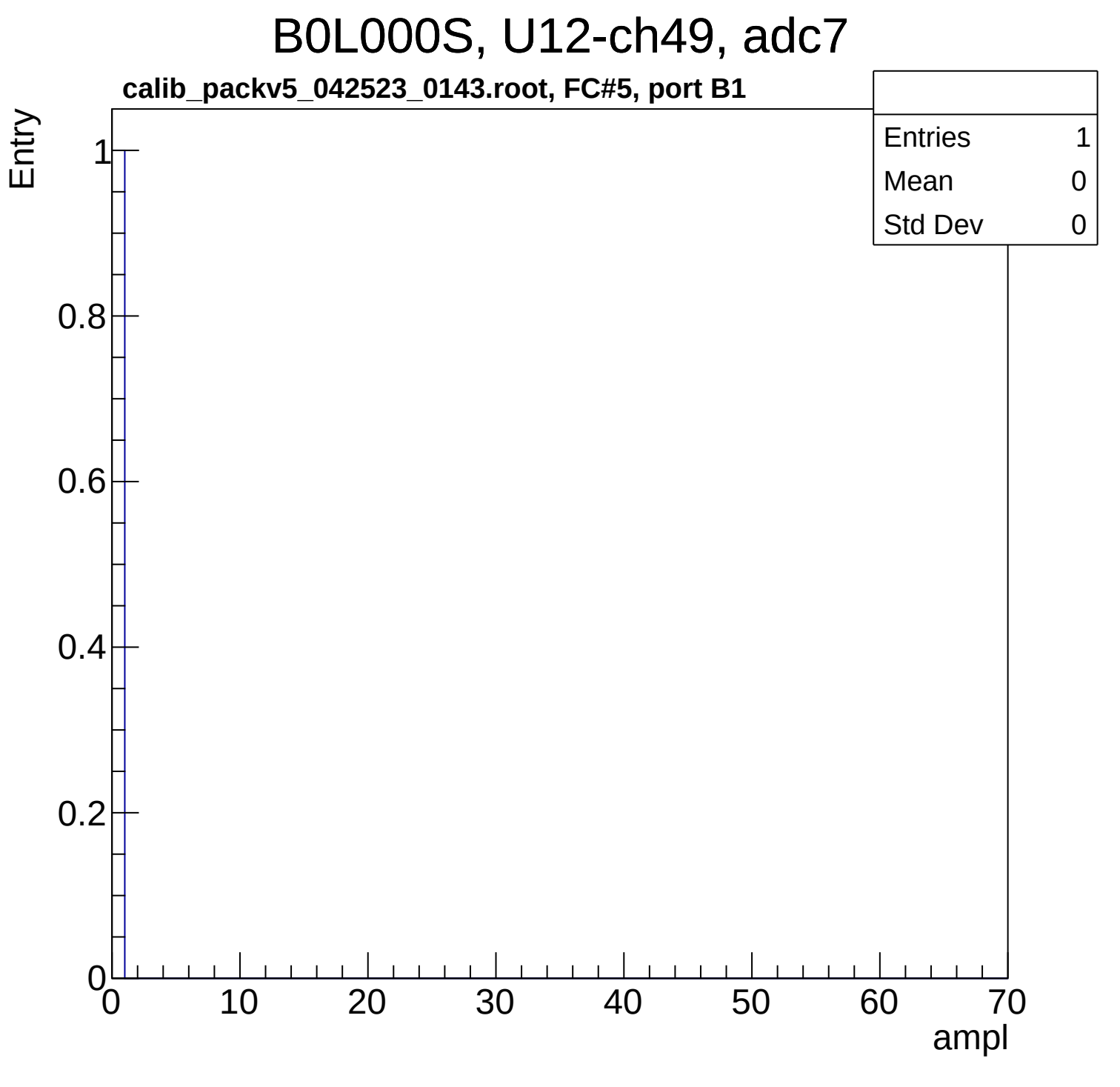
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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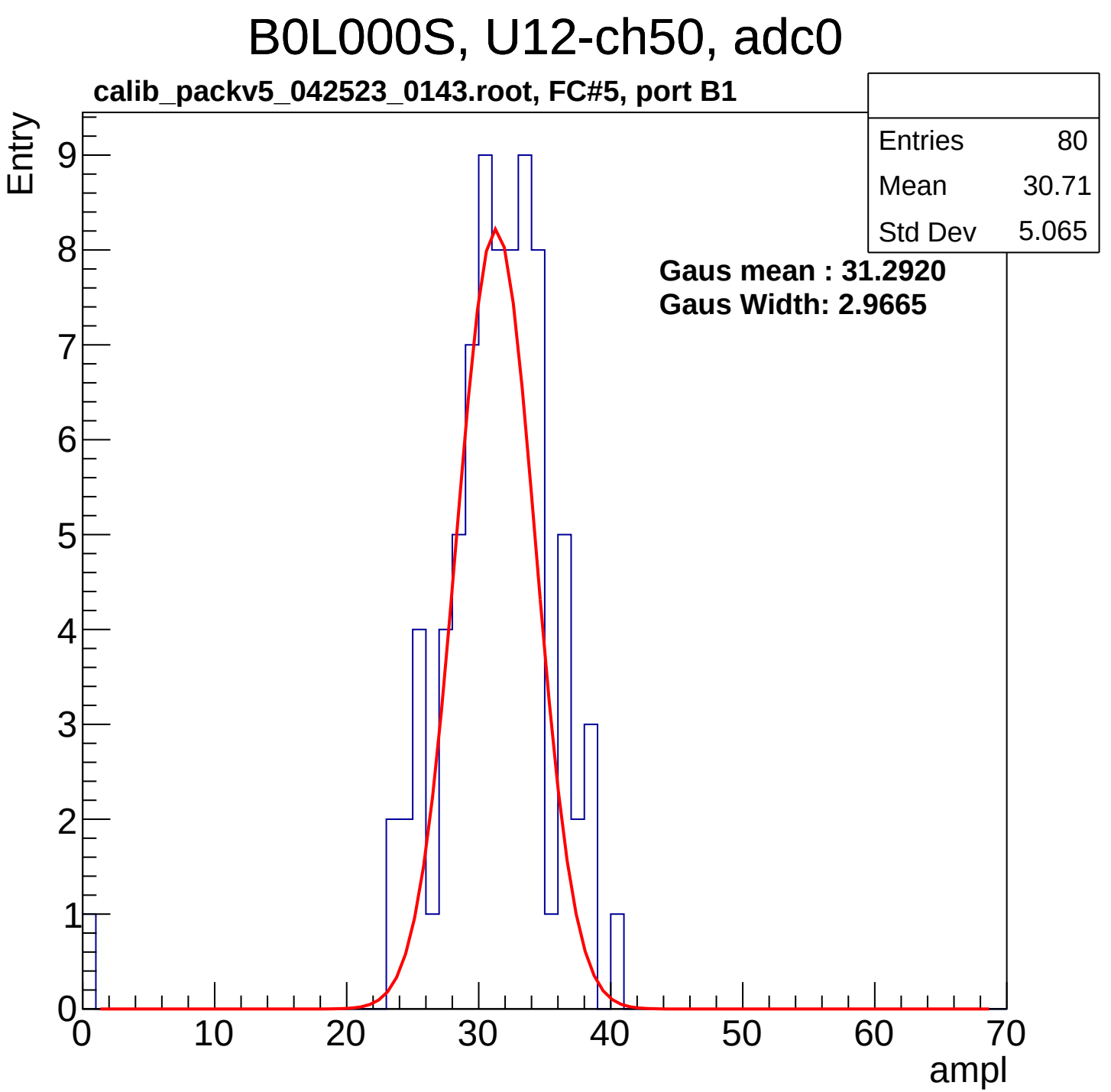
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 30.71
Std Dev 5.065
Gaus mean : 31.2920
Gaus Width: 2.9665
Entry
ampl
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2
3
4
5
6
7
8
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0
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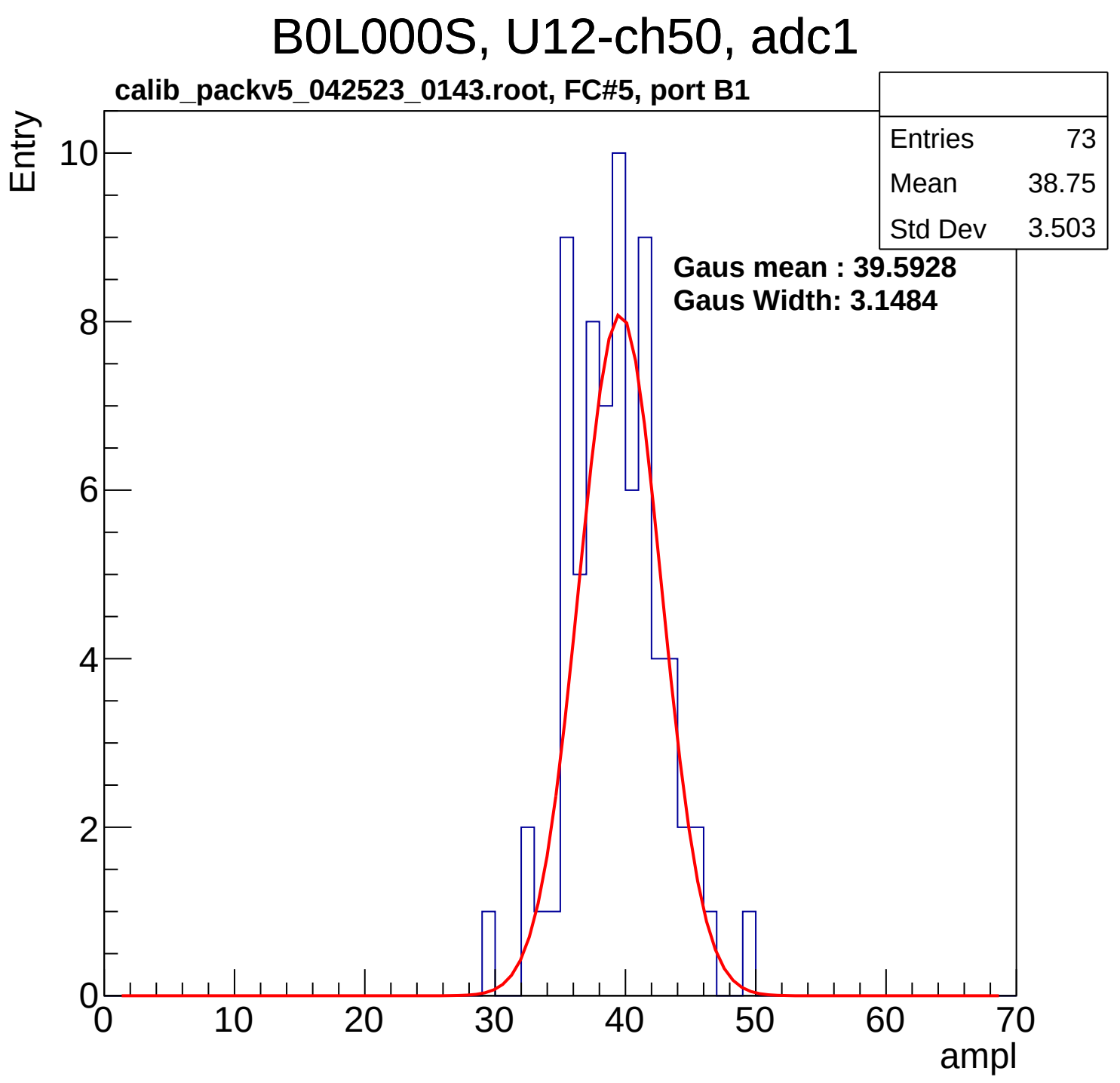
B0L000S, U12-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 38.75
Std Dev 3.503
Gaus mean : 39.5928
Gaus Width: 3.1484
Entry
ampl
0
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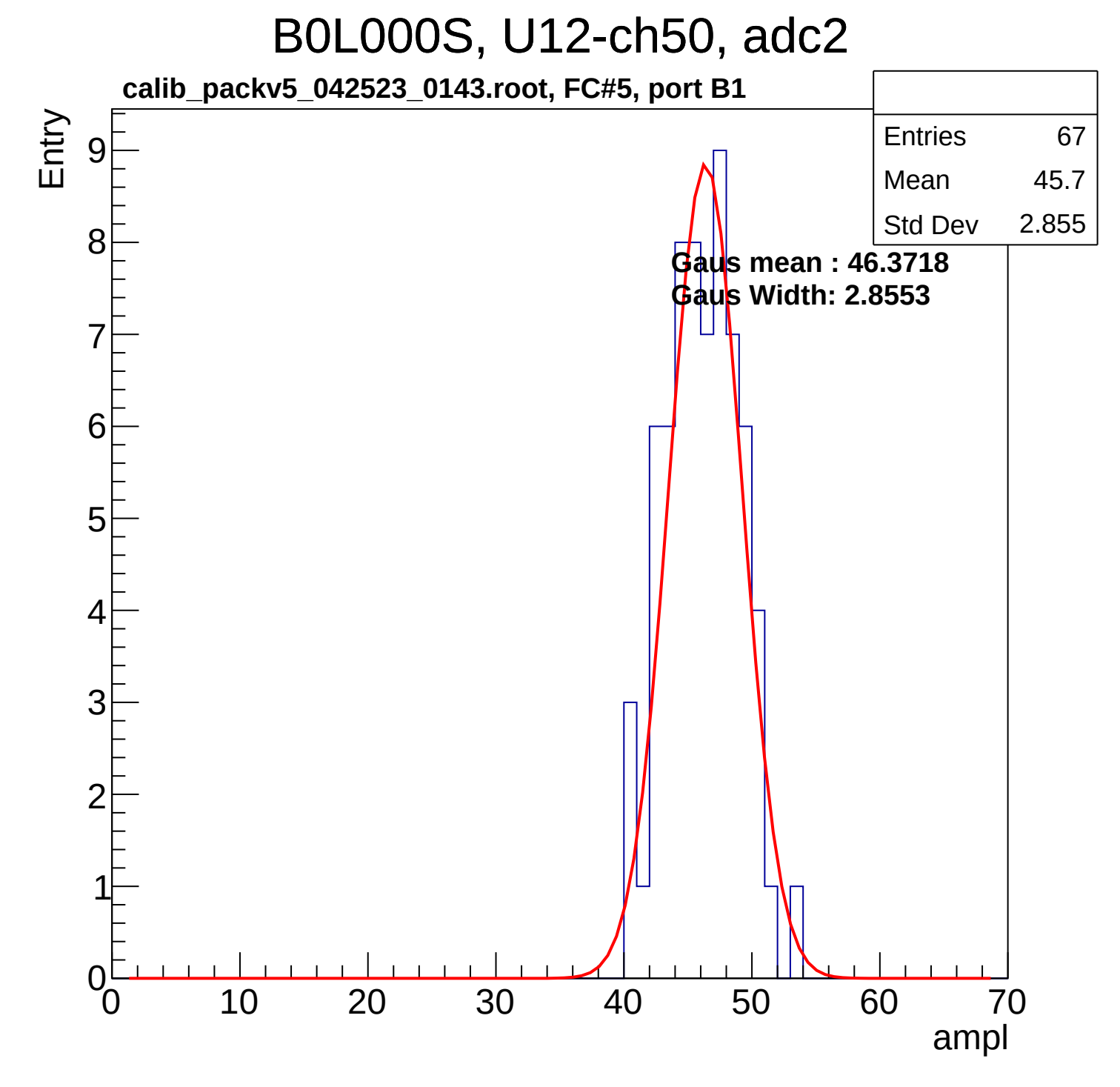
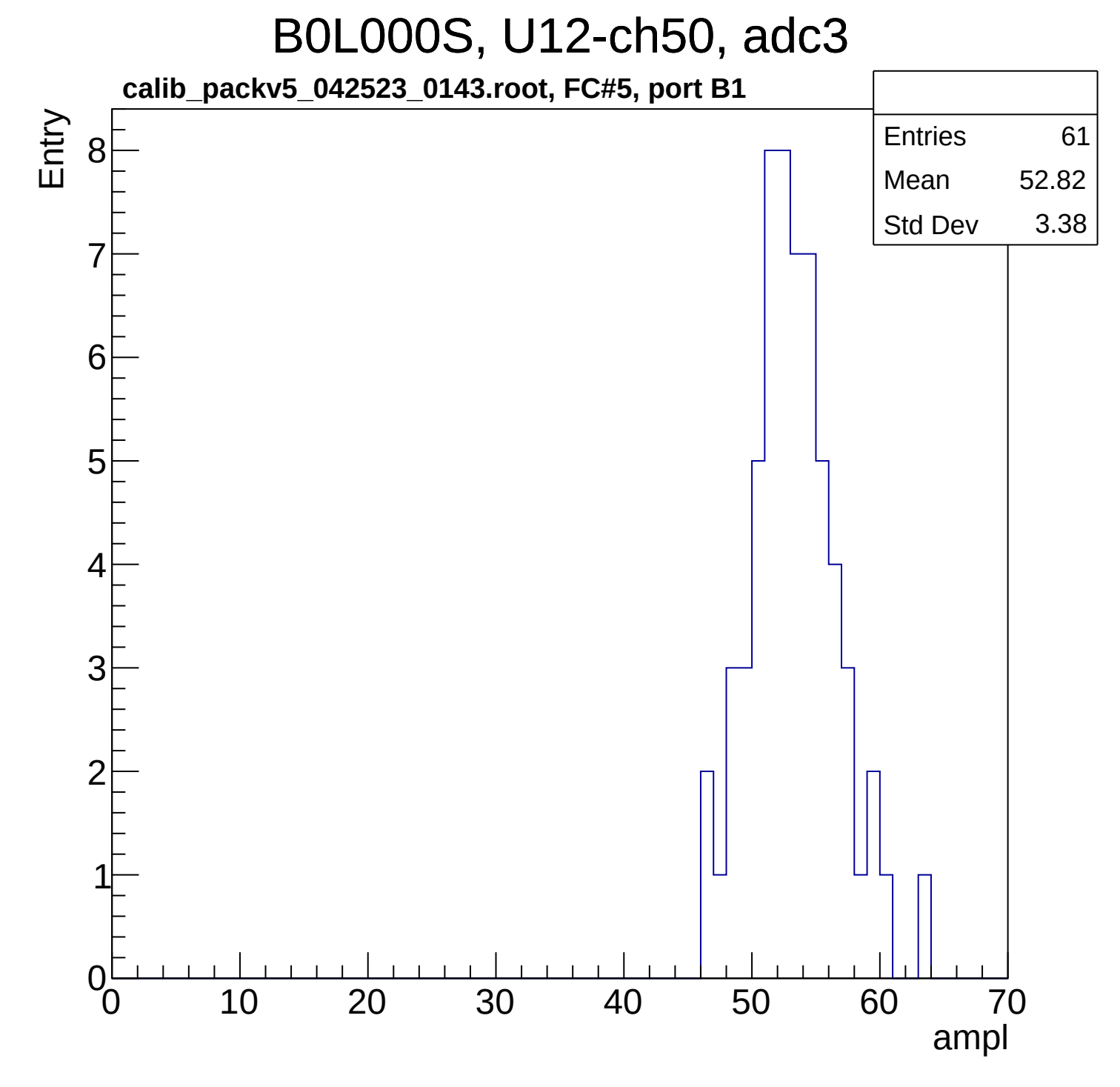
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 45.7
Std Dev 2.855
Gaus mean : 46.3718
Gaus Width: 2.8553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 52.82
Std Dev 3.38
Entry
ampl
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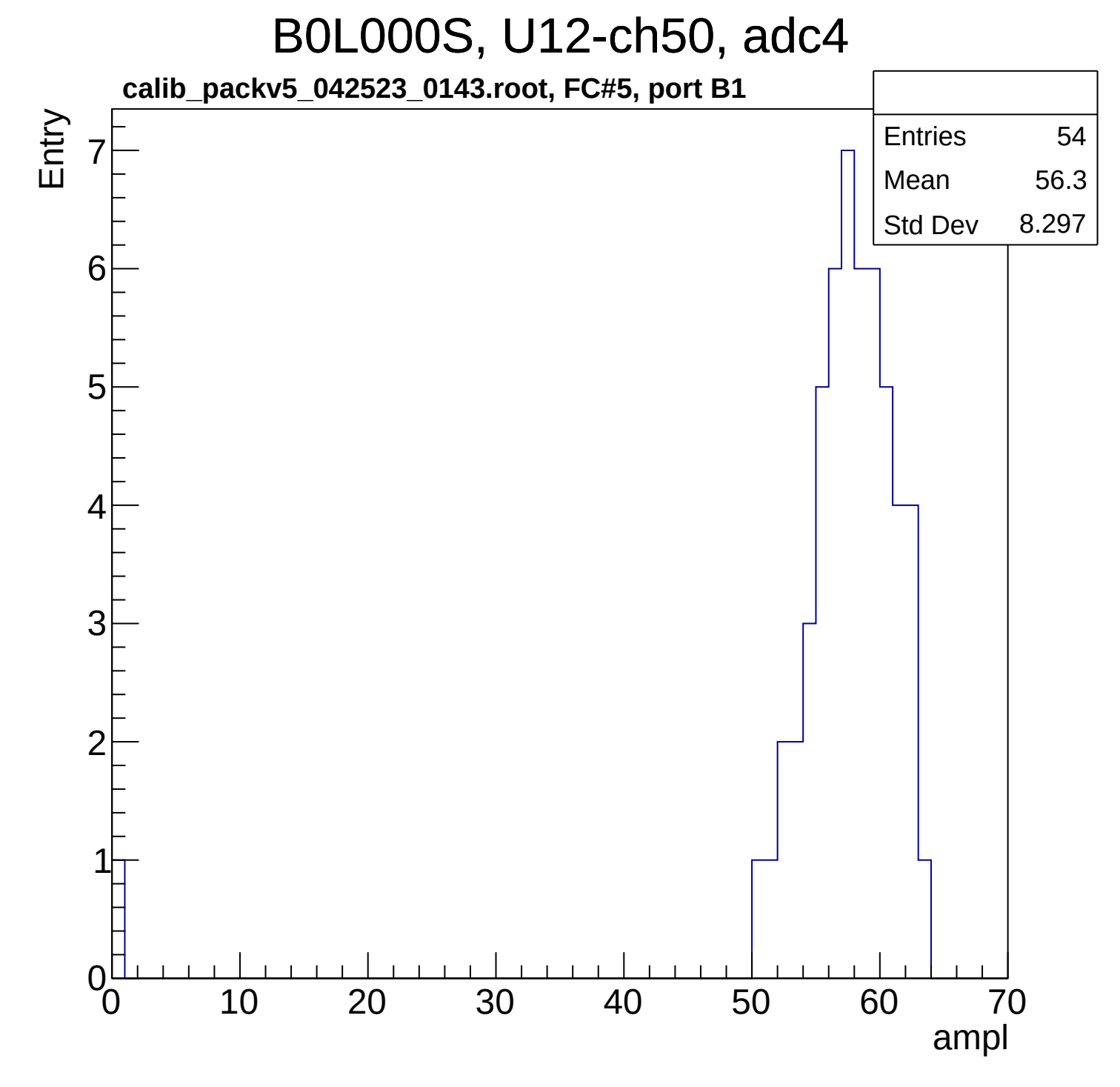
B0L000S, U12-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.3
Std Dev 8.297
Entry
ampl
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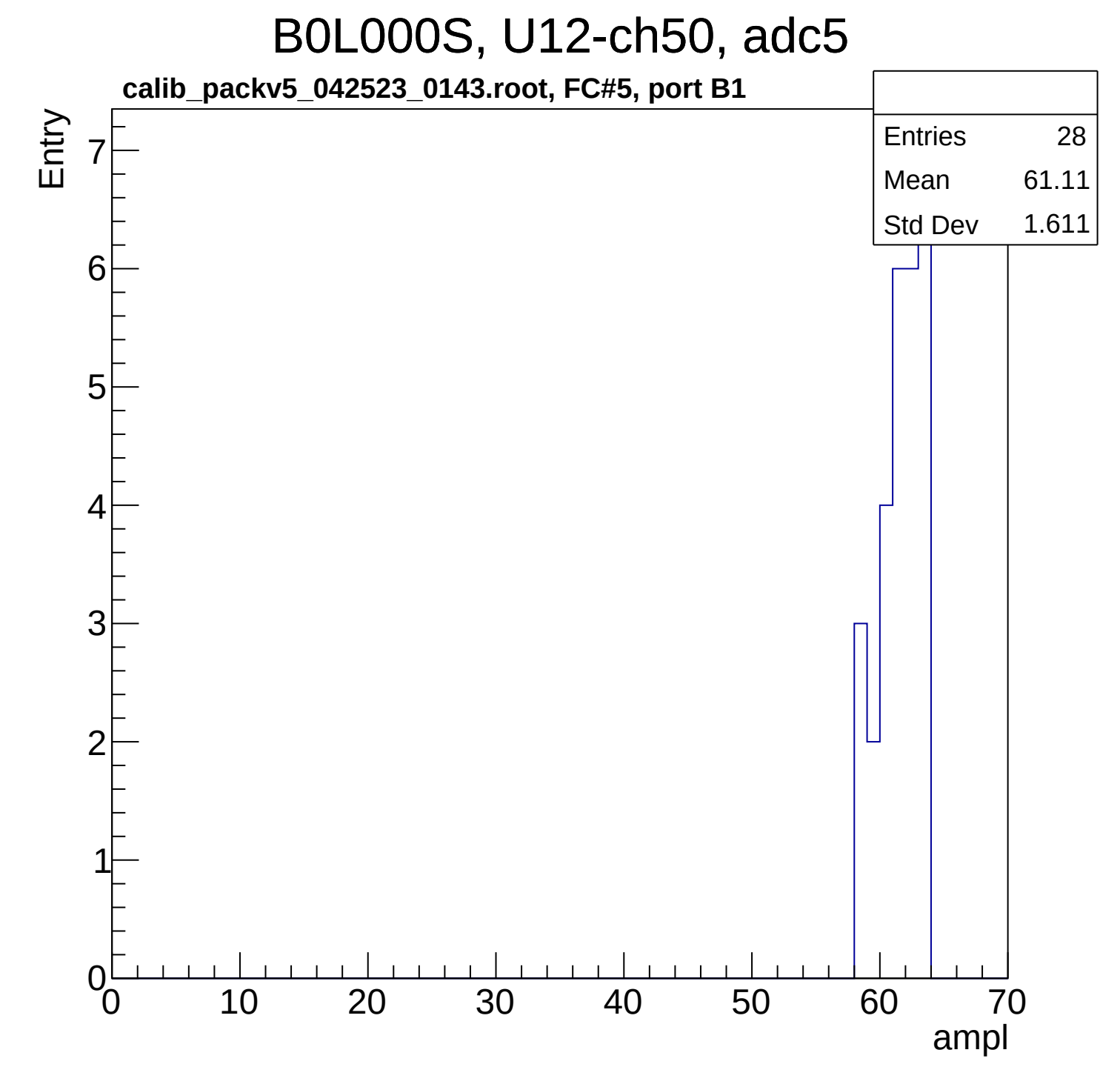
B0L000S, U12-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 61.11
Std Dev 1.611
Entry
ampl
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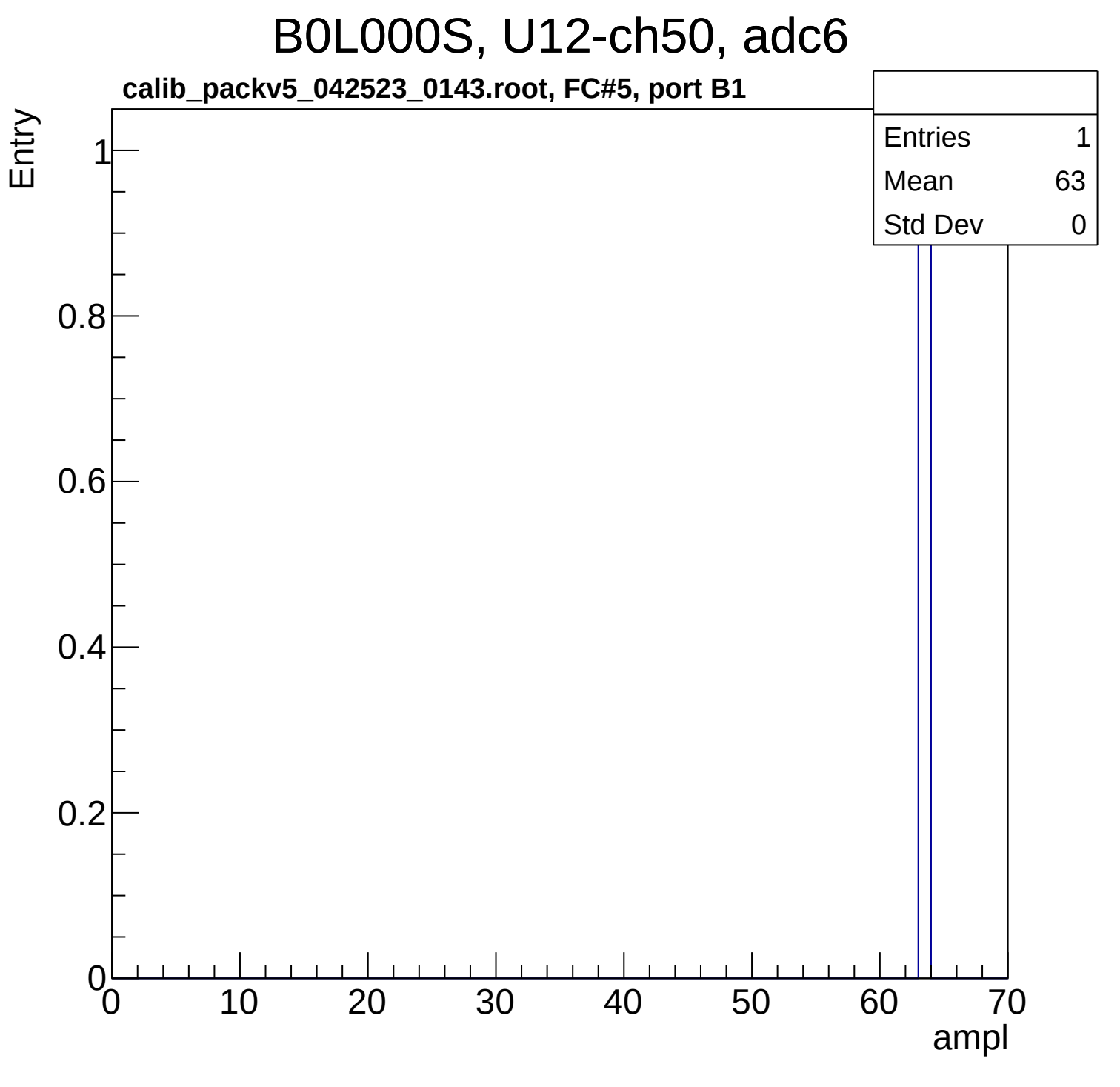
B0L000S, U12-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
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10
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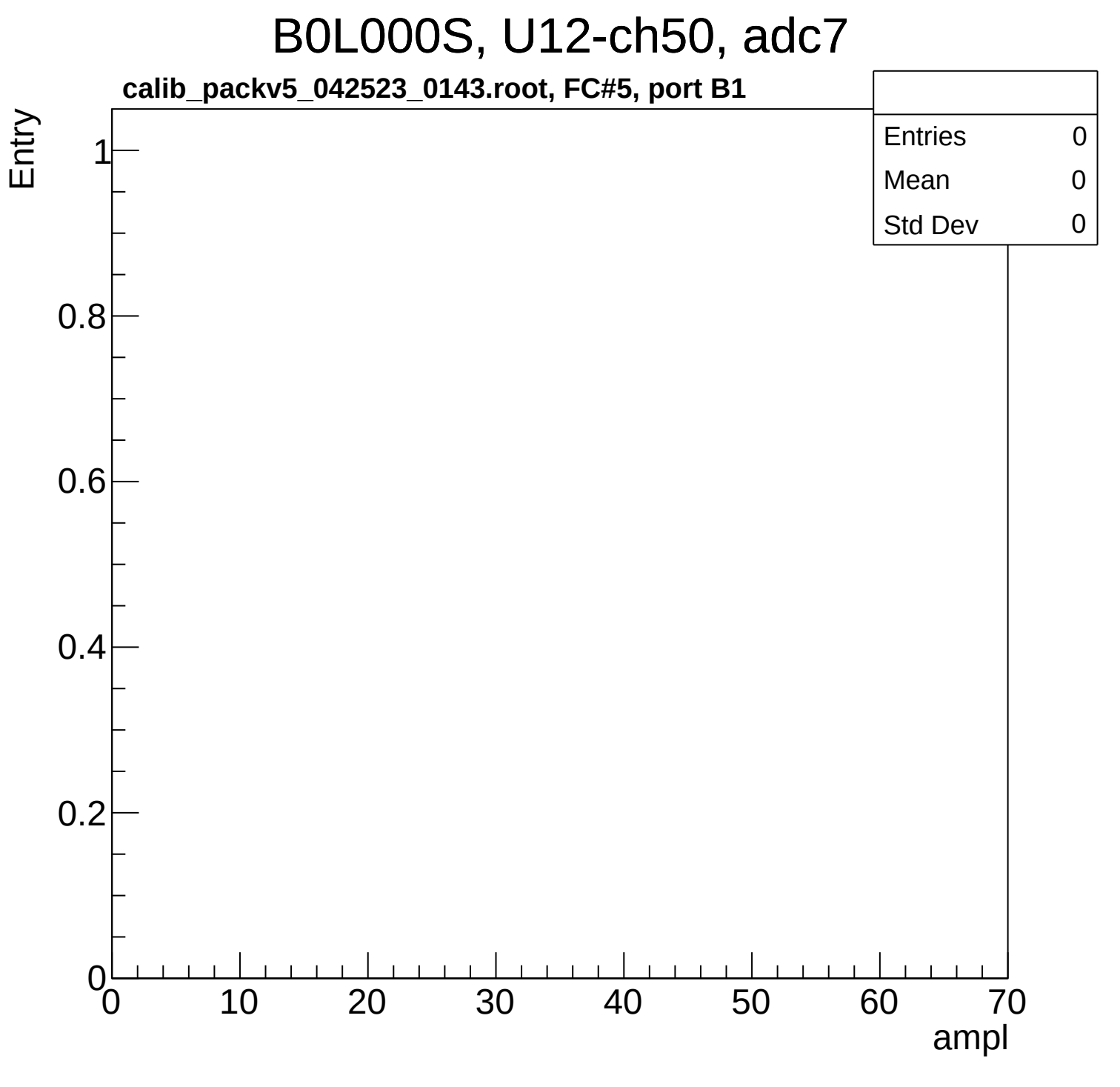
50
60
70
ampl


B0L000S, U12-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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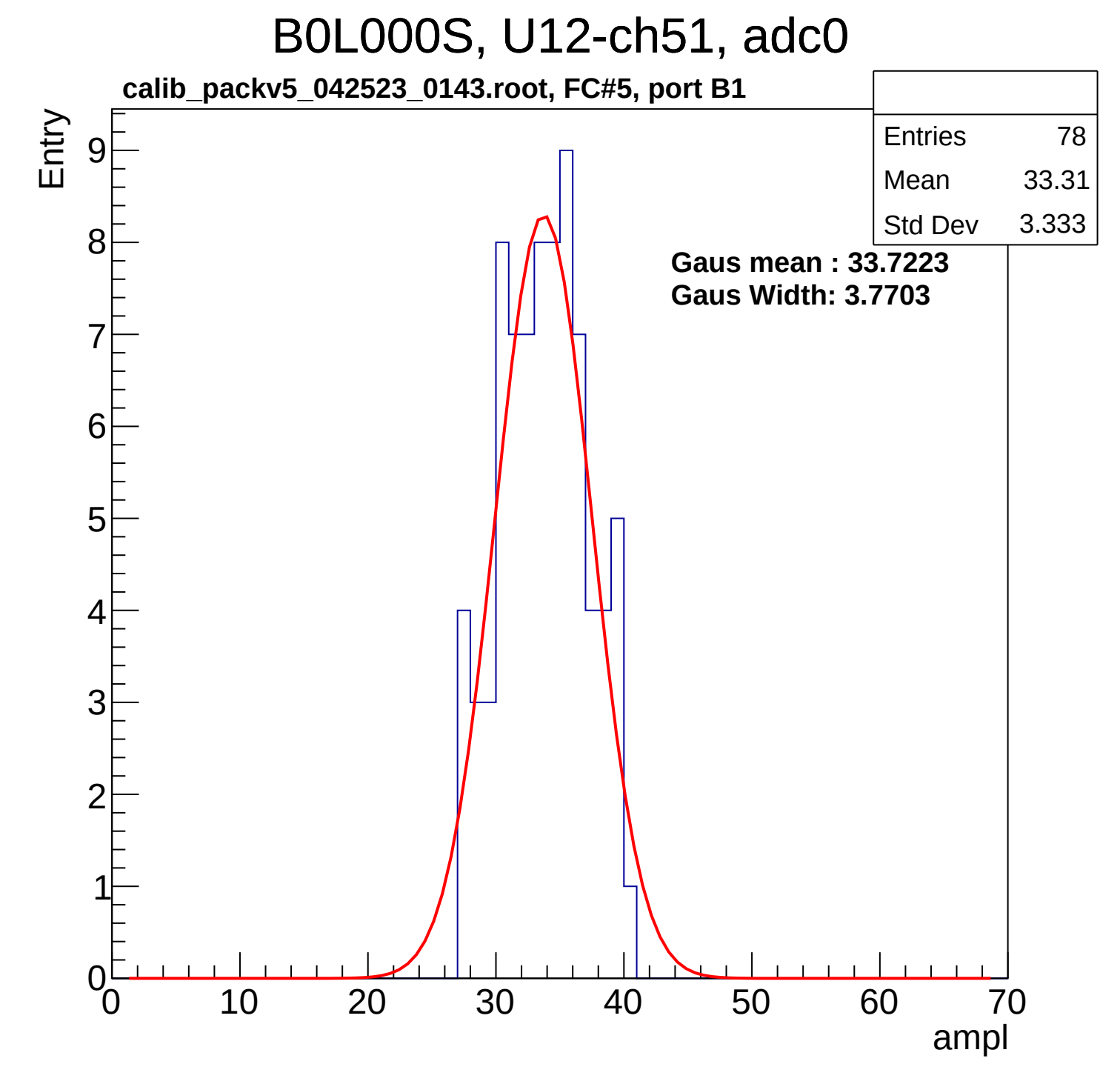
50
60
70
ampl


B0L000S, U12-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 33.31
Std Dev 3.333
Gaus mean : 33.7223
Gaus Width: 3.7703
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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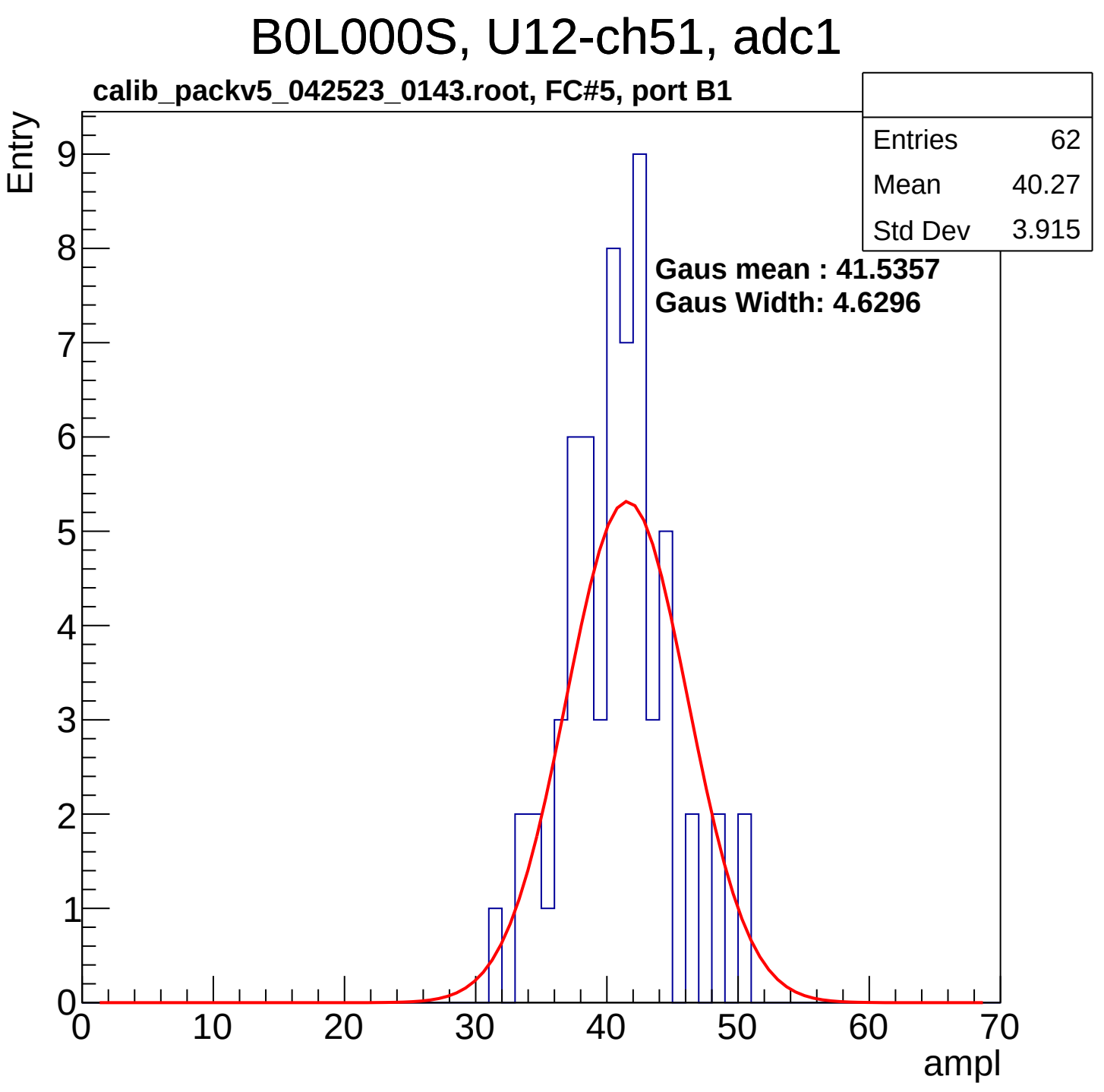
B0L000S, U12-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 40.27
Std Dev 3.915
Gaus mean : 41.5357
Gaus Width: 4.6296
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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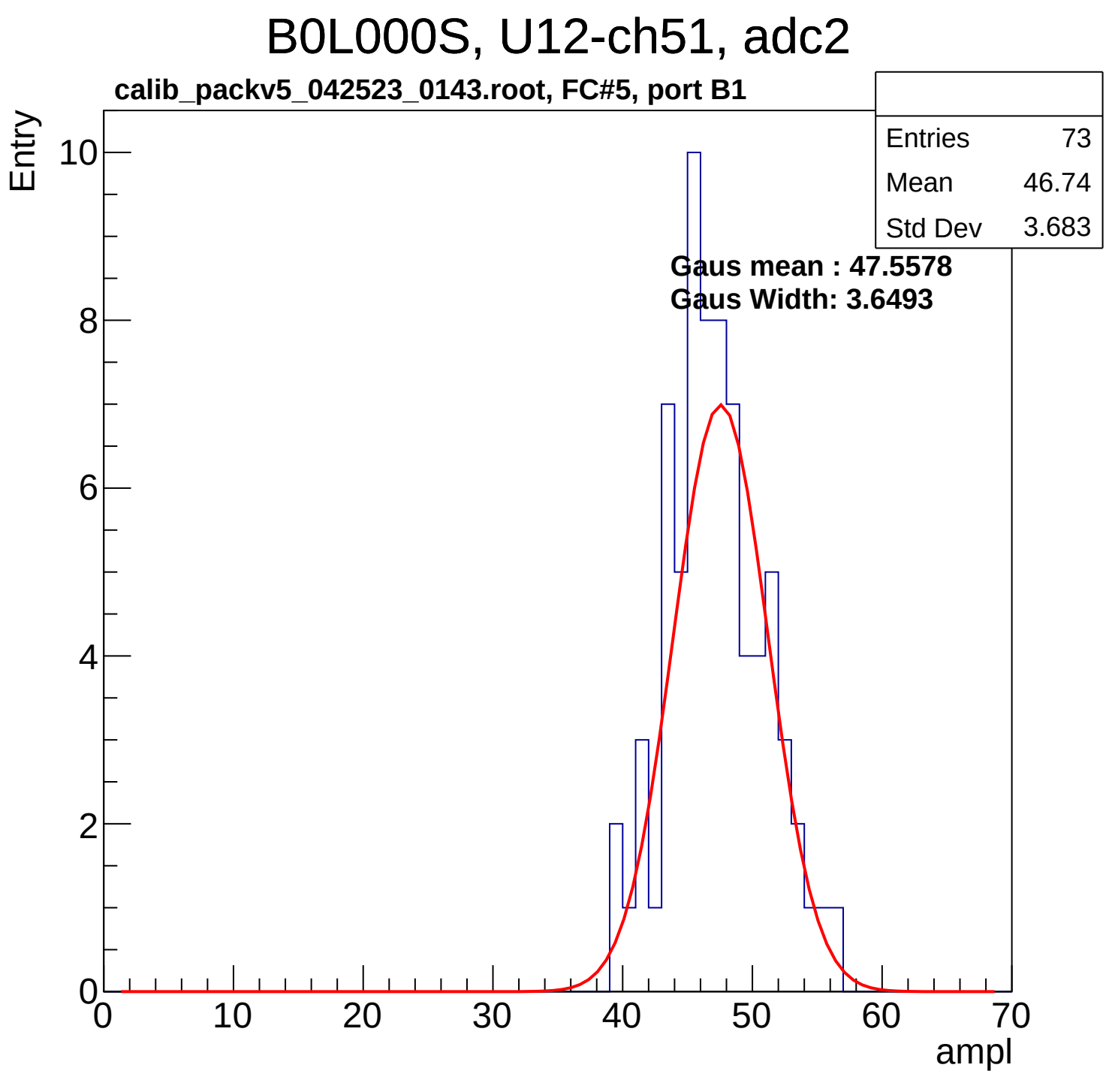
B0L000S, U12-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 46.74
Std Dev 3.683
Gaus mean : 47.5578
Gaus Width: 3.6493
Entry
ampl
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2
4
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0
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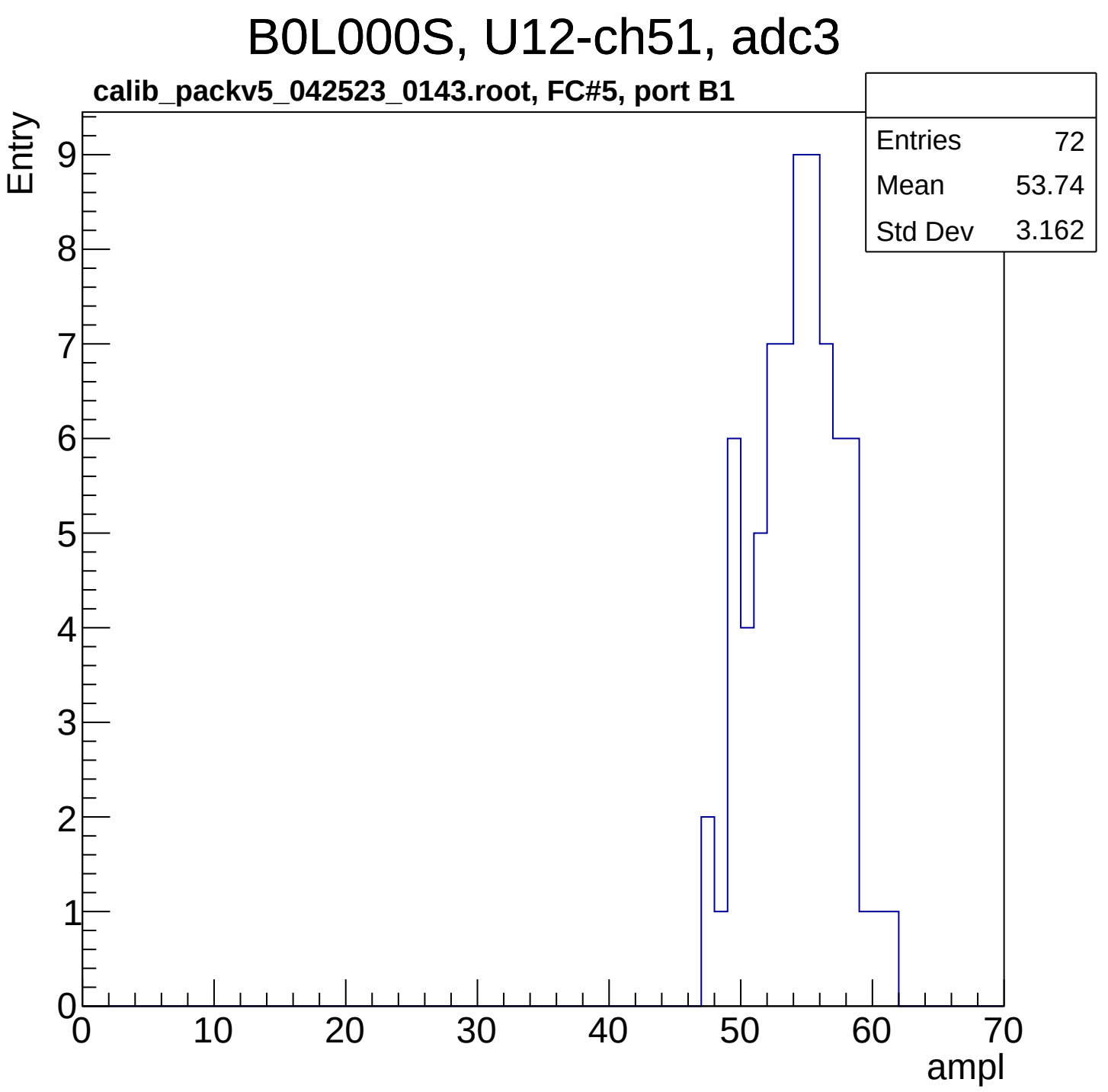
B0L000S, U12-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 53.74
Std Dev 3.162
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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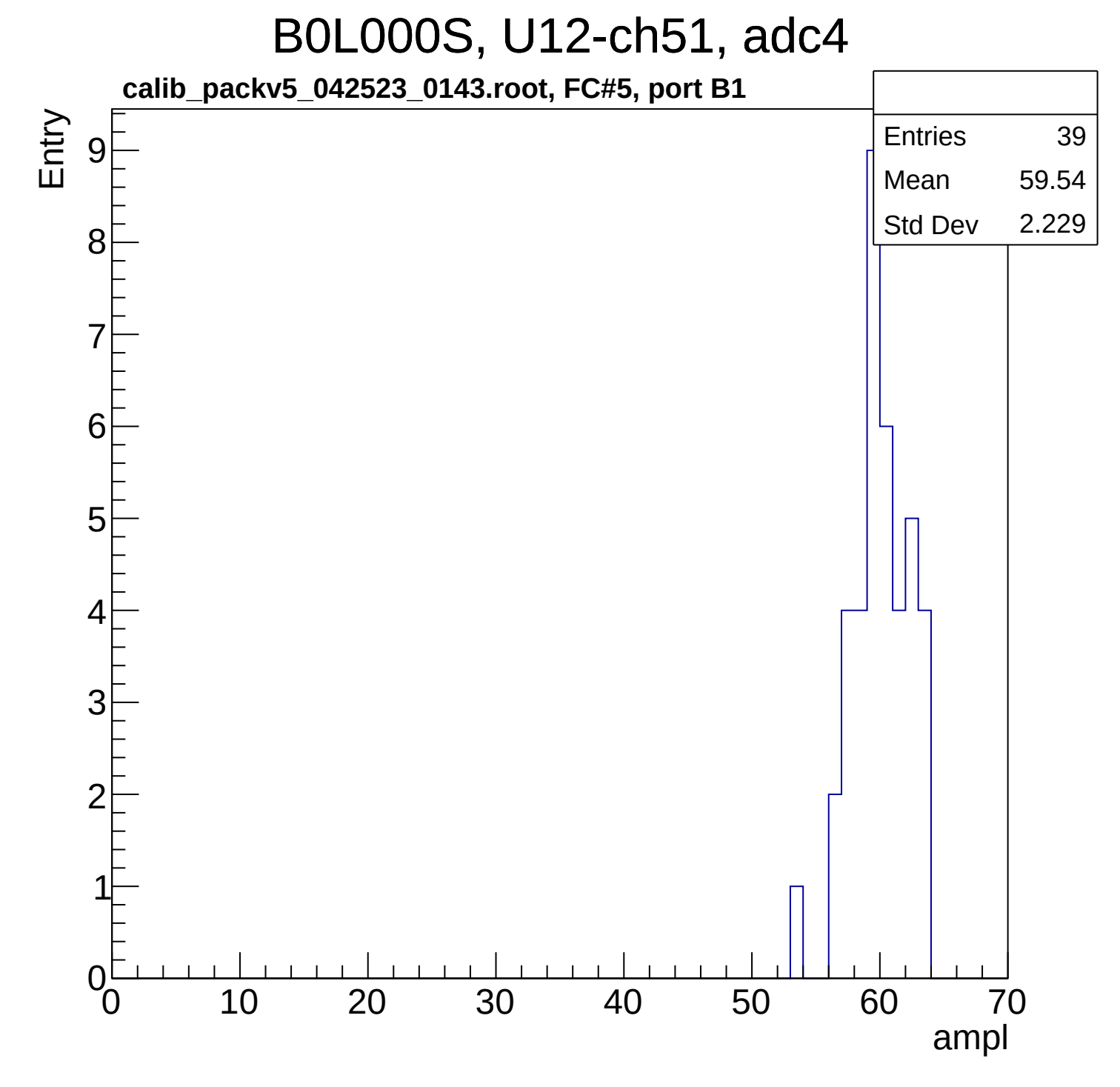
B0L000S, U12-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59.54
Std Dev 2.229
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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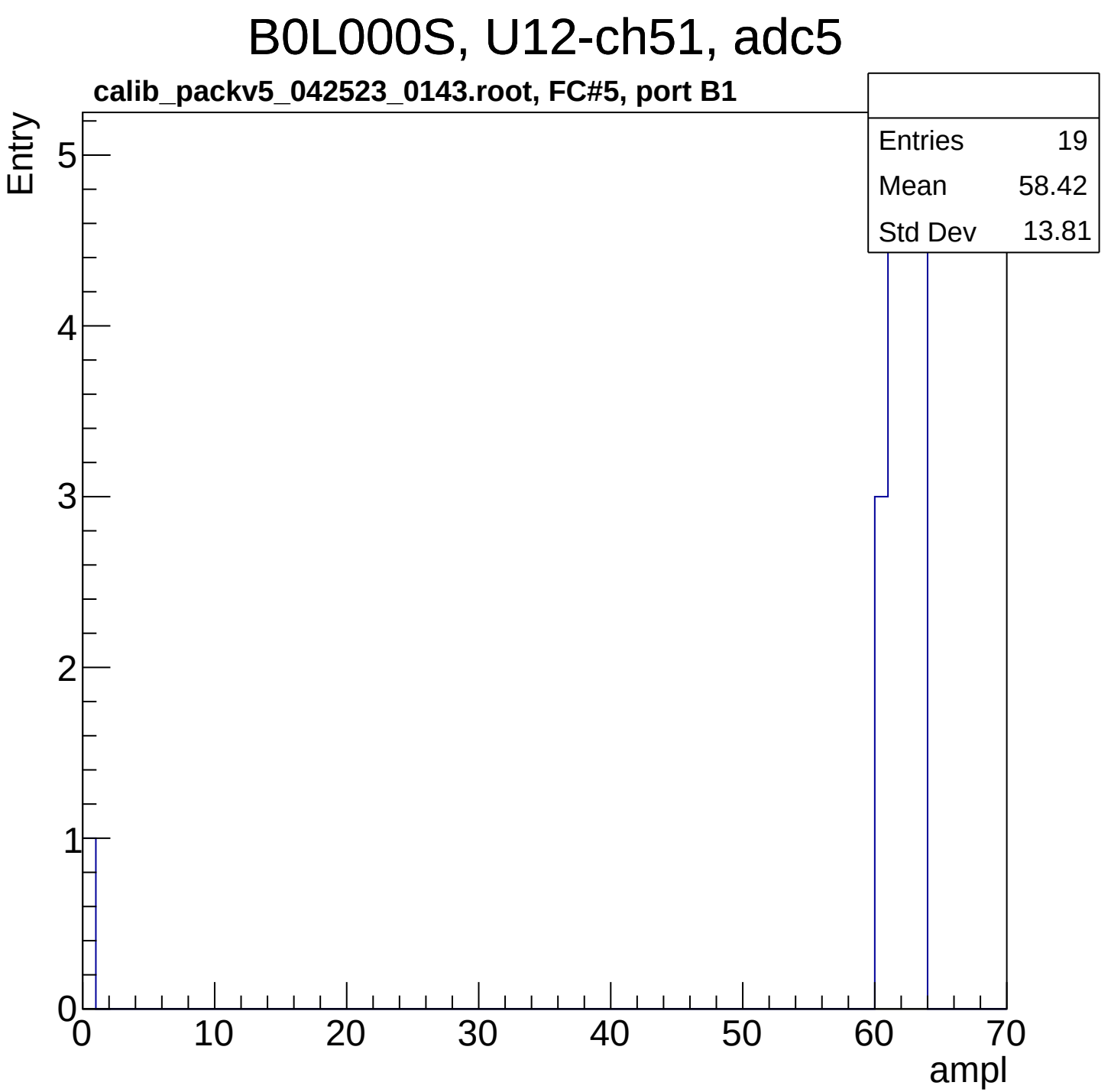
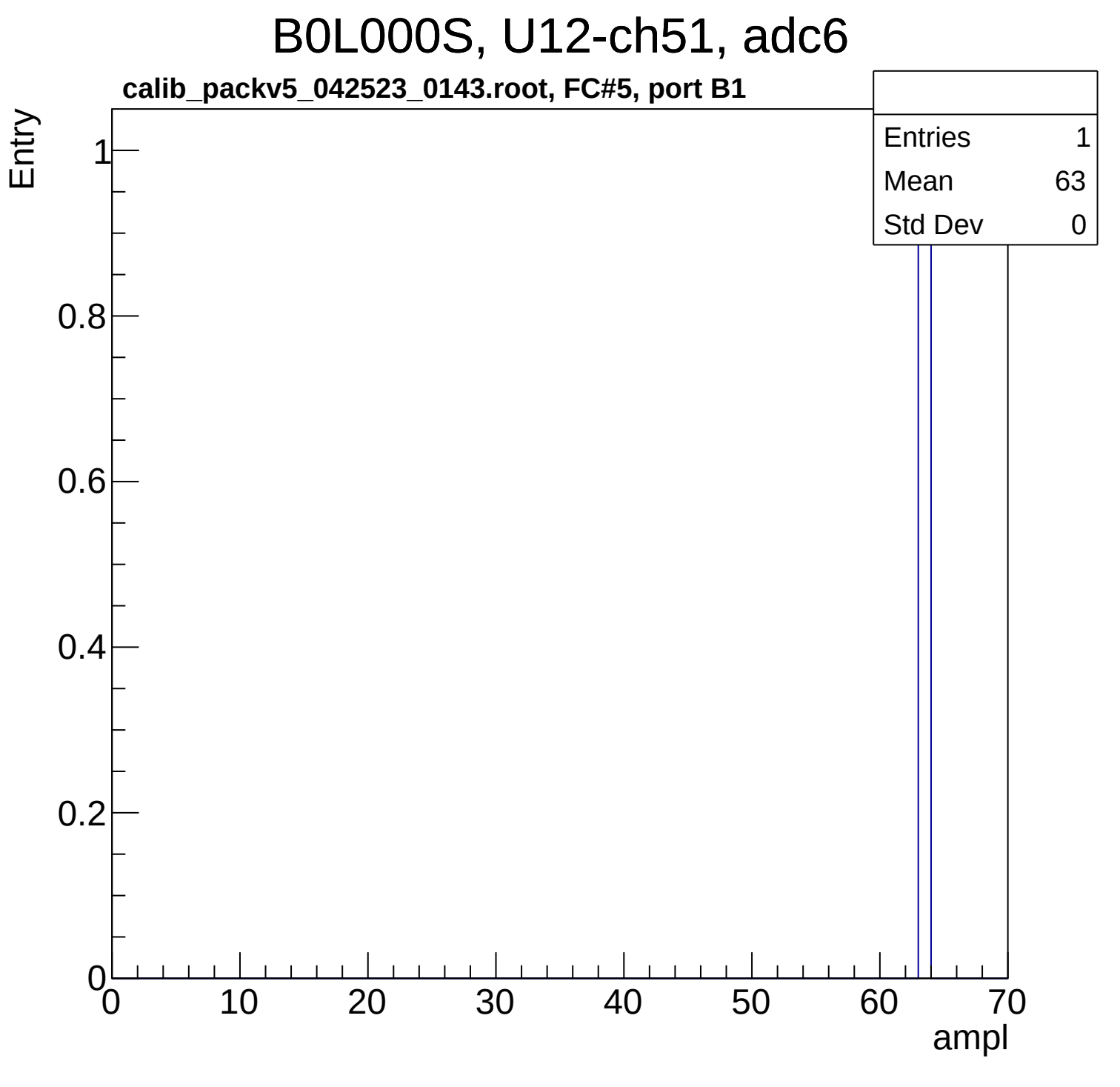
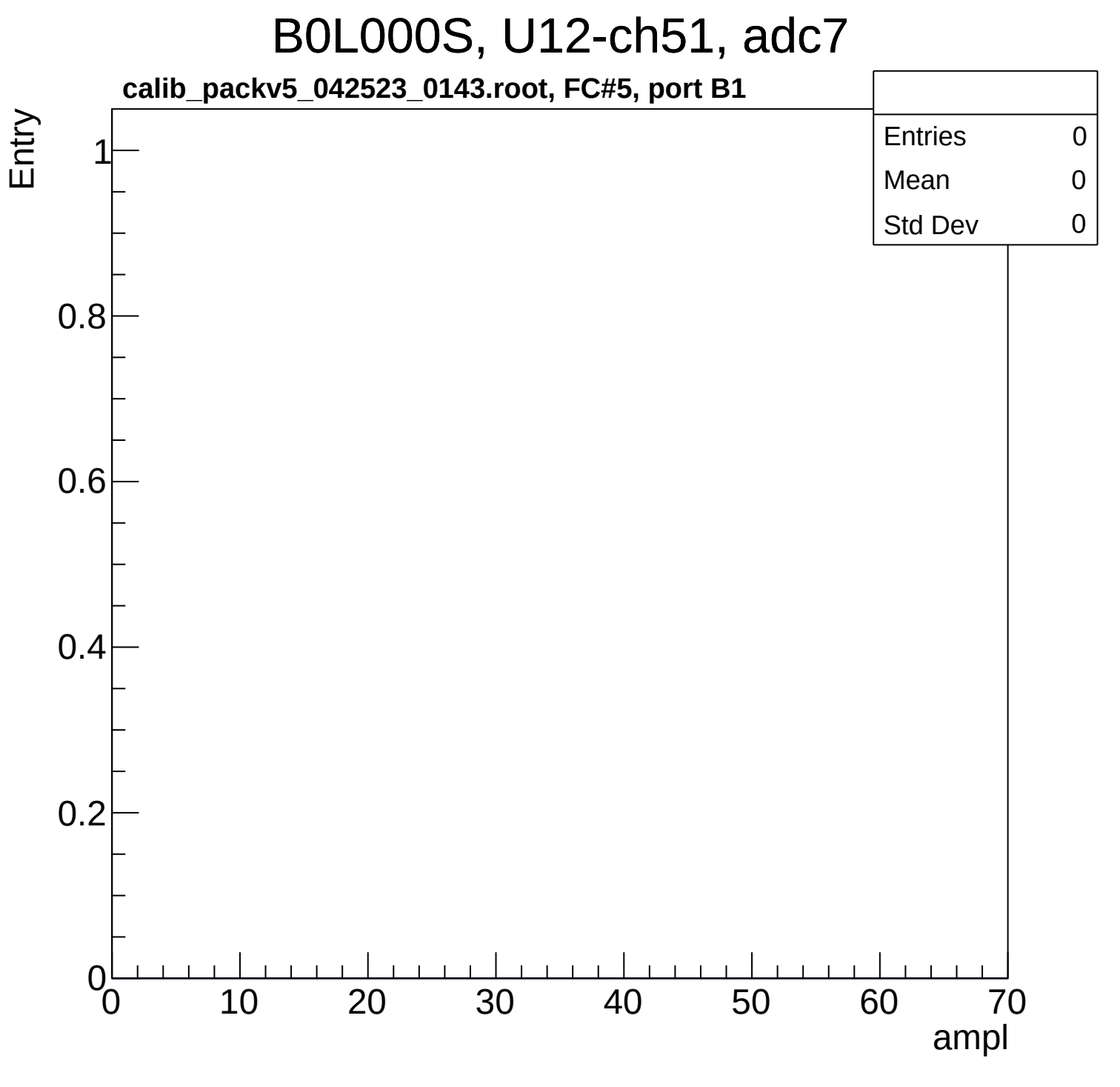

B0L000S, U12-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.42
Std Dev 13.81
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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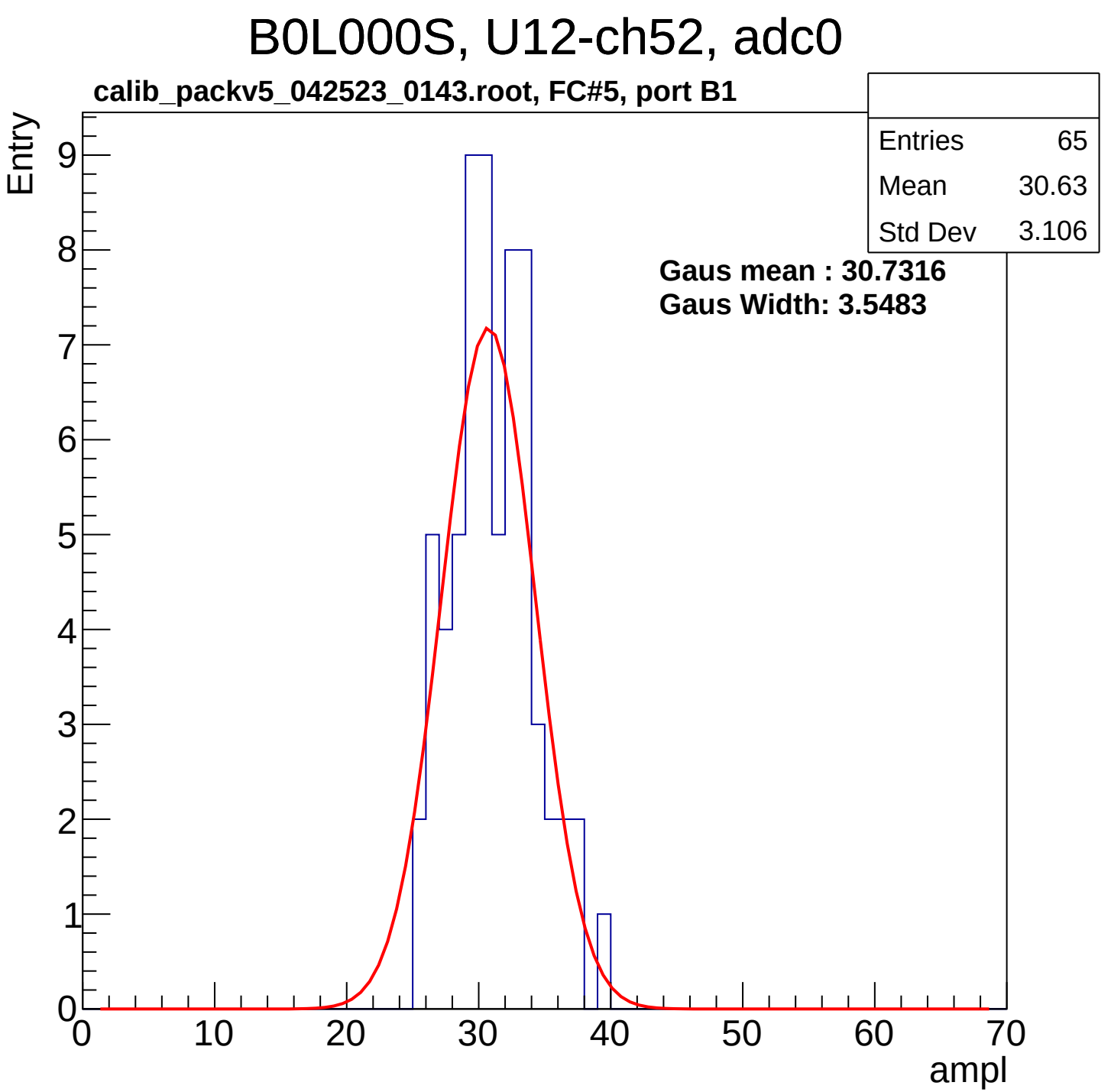
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.63
Std Dev 3.106
Gaus mean : 30.7316
Gaus Width: 3.5483
Entry
ampl
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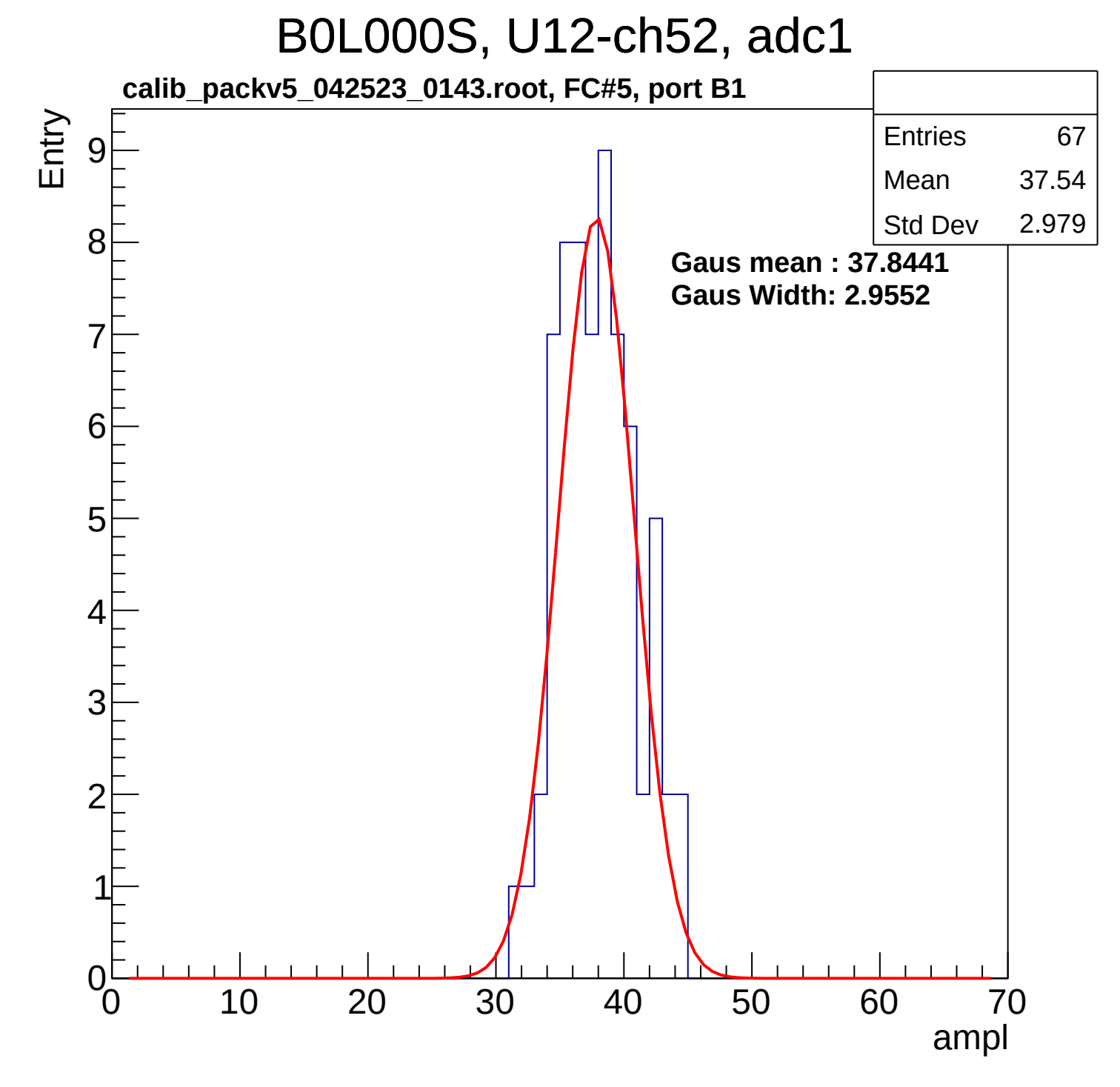
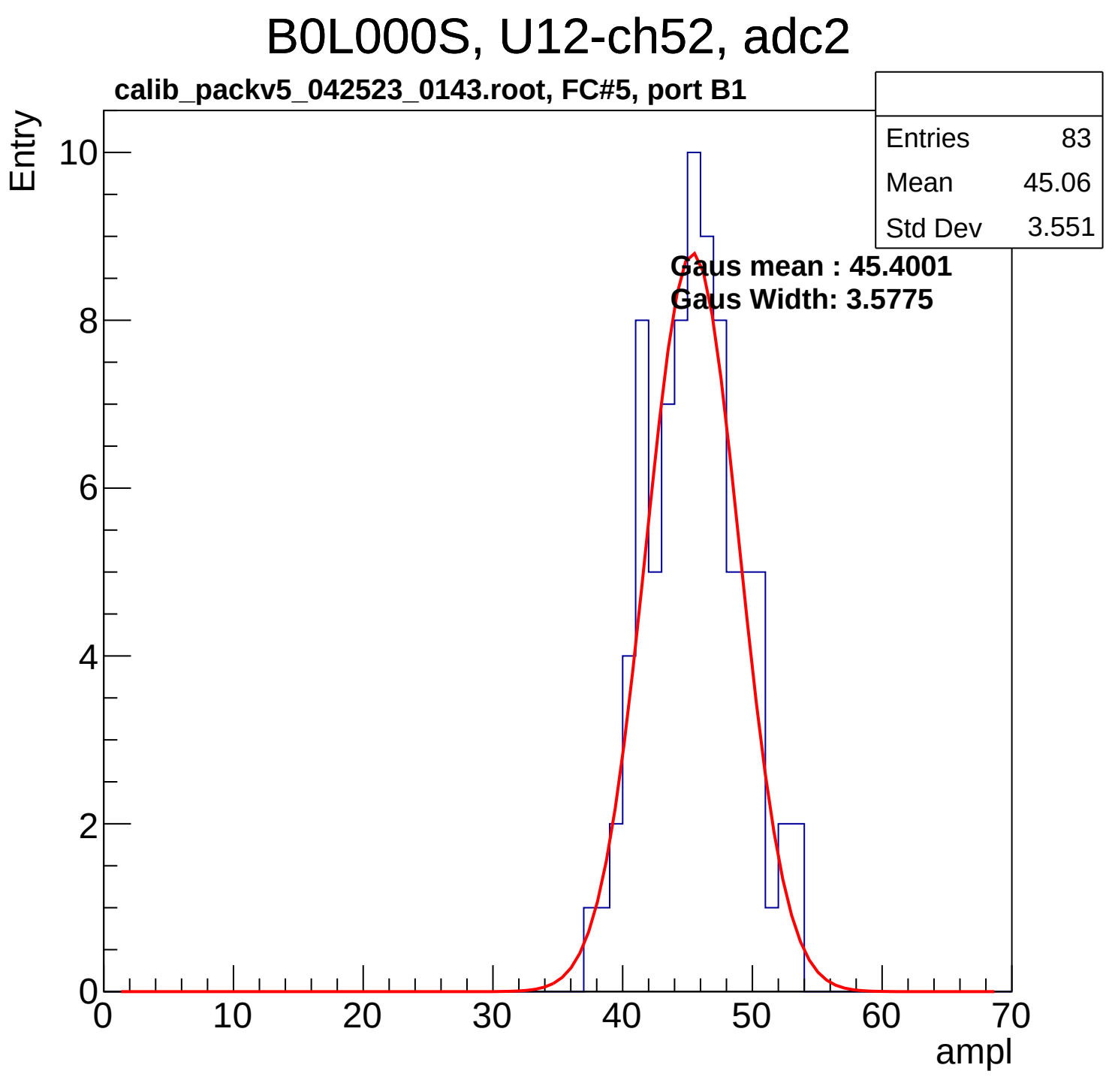
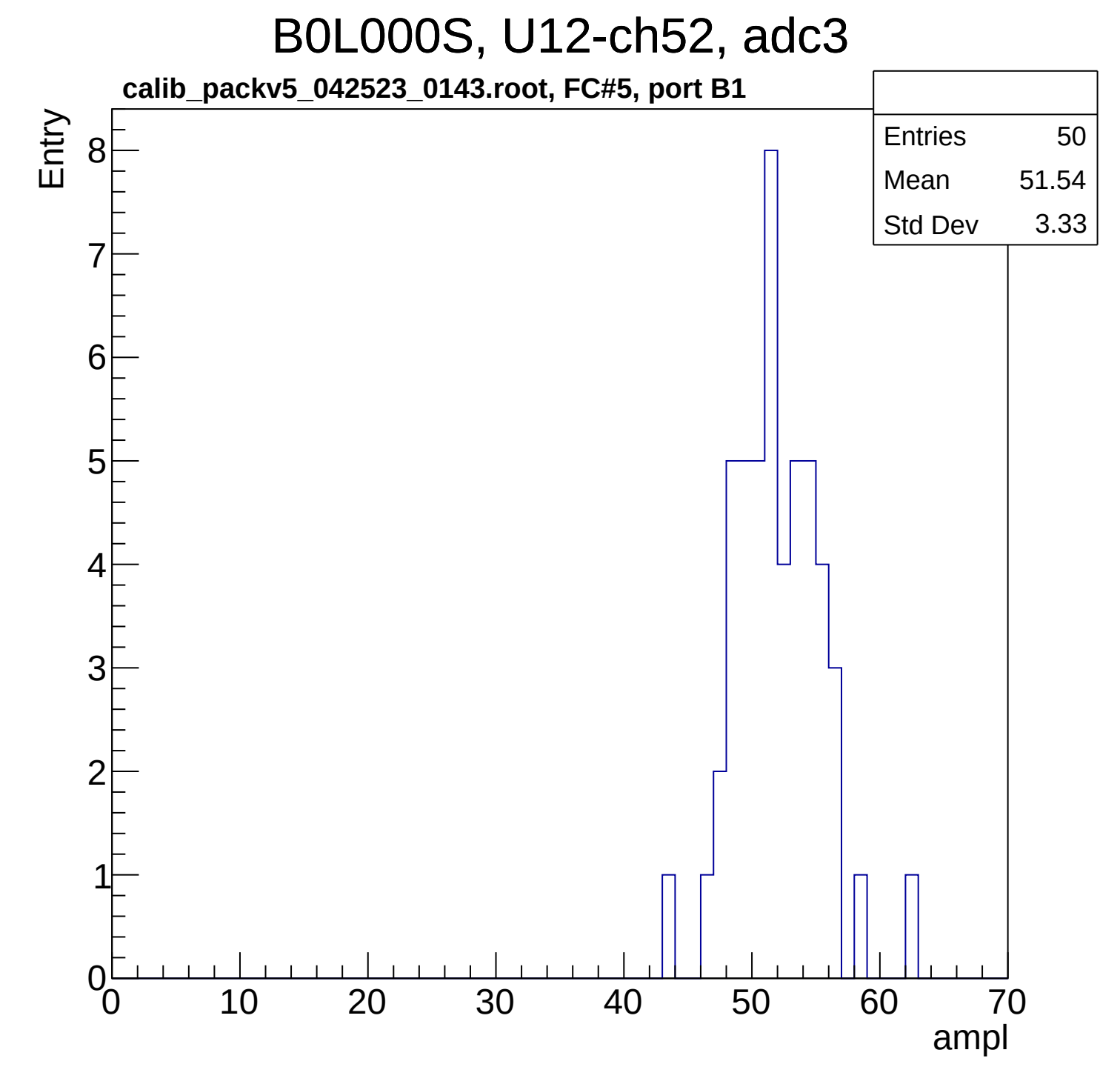
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.54
Std Dev 2.979
Gaus mean : 37.8441
Gaus Width: 2.9552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 45.06
Std Dev 3.551
Gaus mean : 45.4001
Gaus Width: 3.5775
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 51.54
Std Dev 3.33
Entry
ampl
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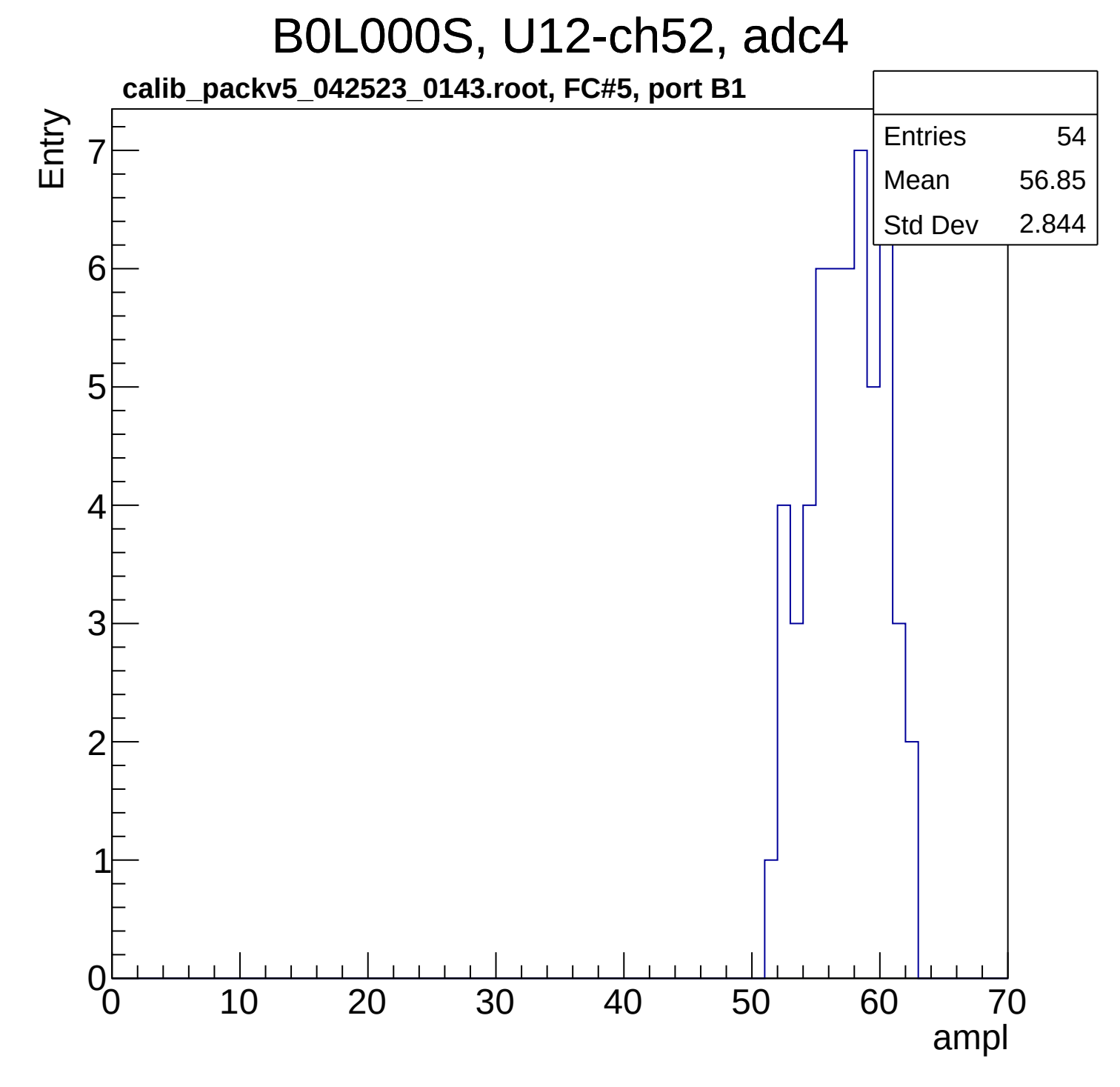
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.85
Std Dev 2.844
Entry
ampl
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1
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3
4
5
6
7
0
10
20
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40
50
60
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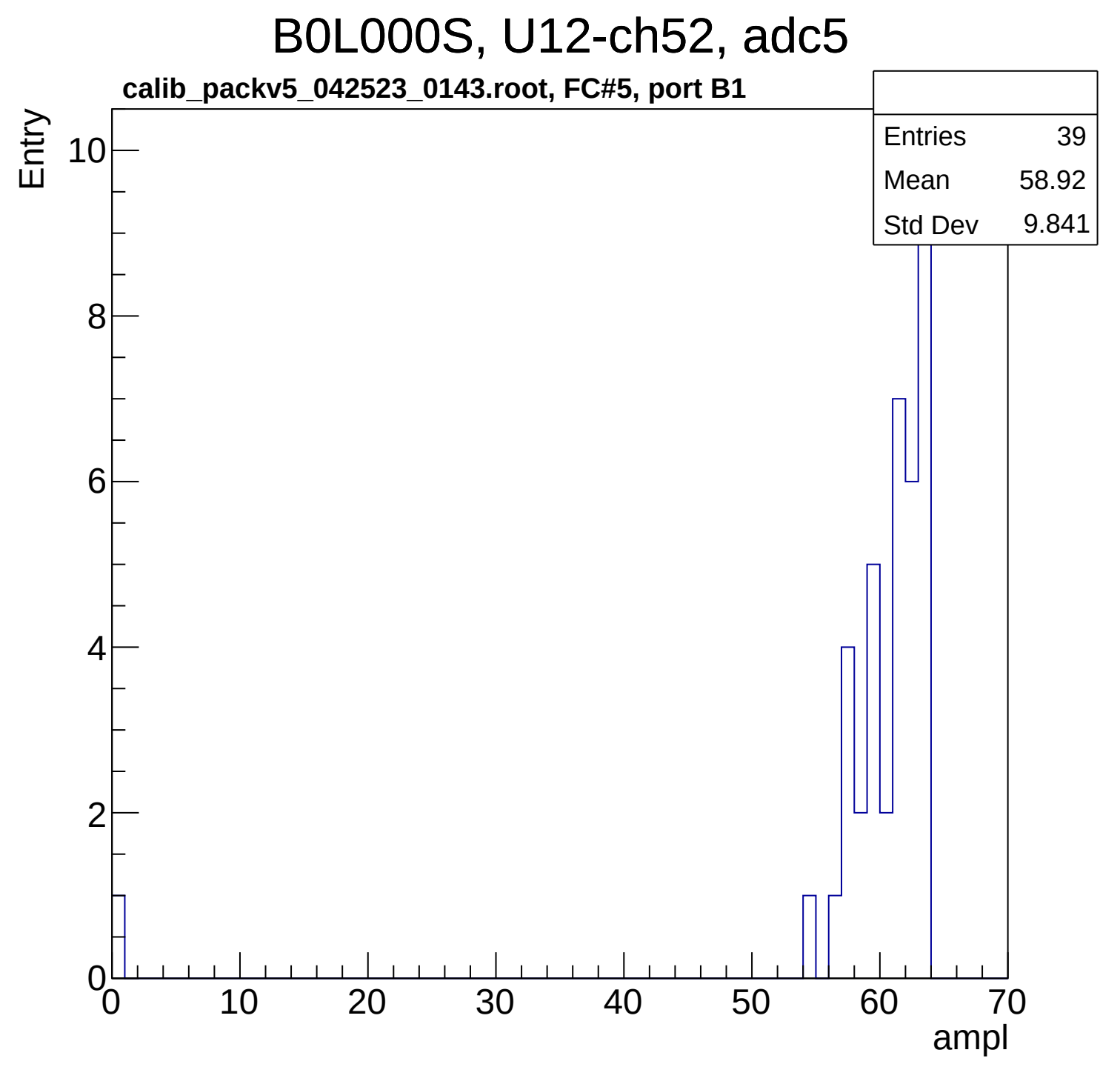
B0L000S, U12-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.92
Std Dev 9.841
Entry
ampl
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2
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0
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20
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40
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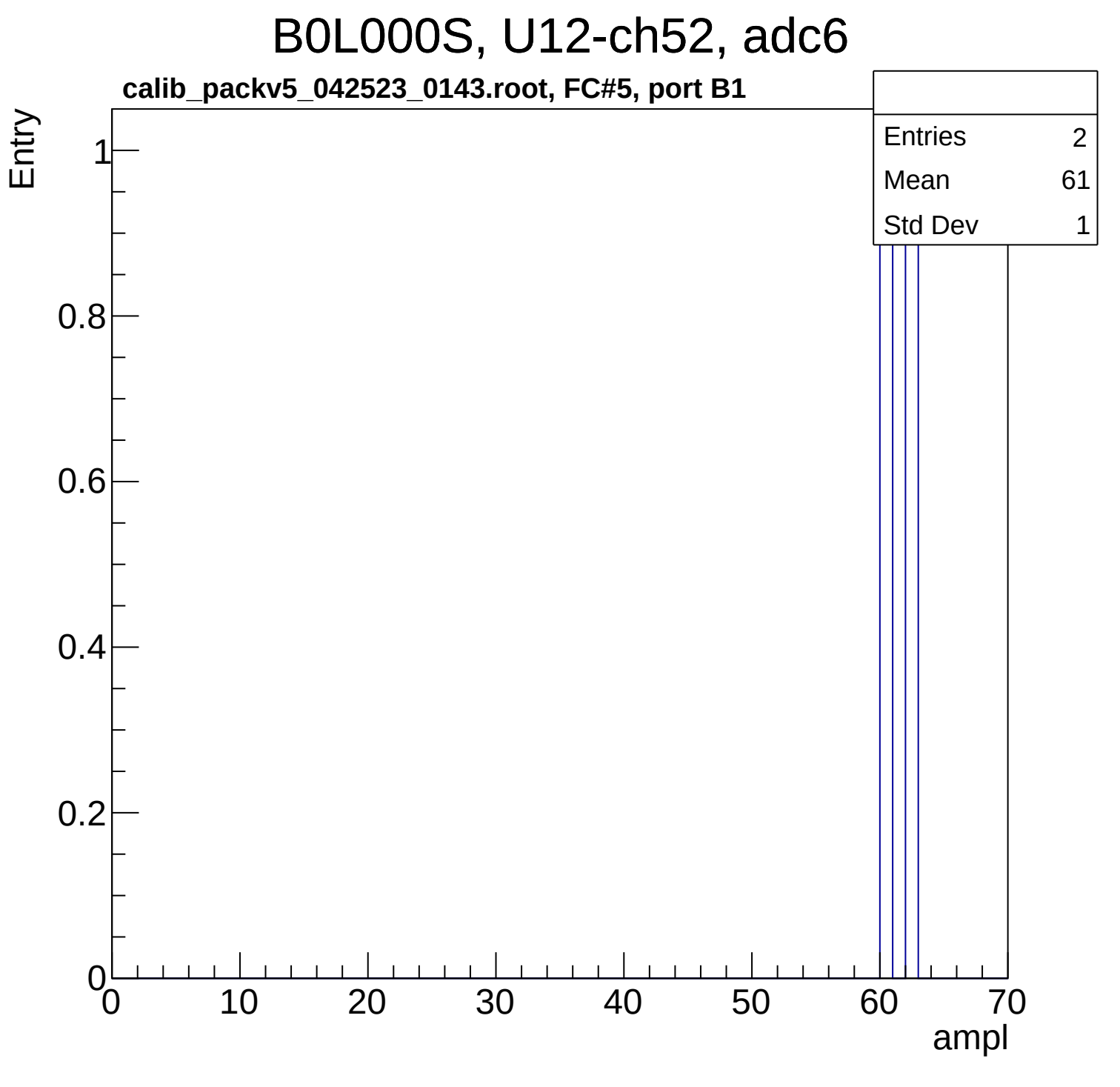
B0L000S, U12-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61
Std Dev 1
Entry
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0.6
0.4
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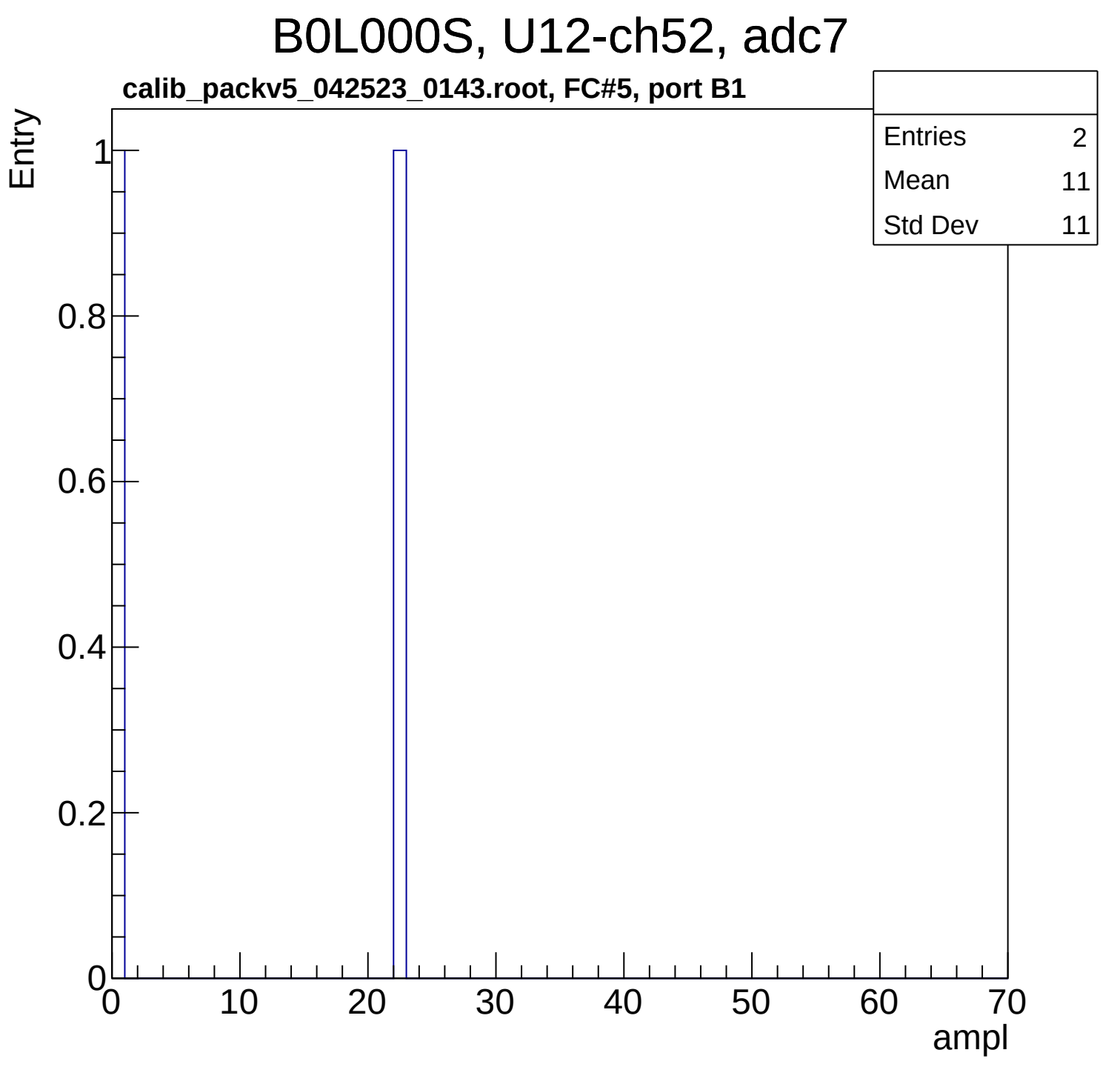
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
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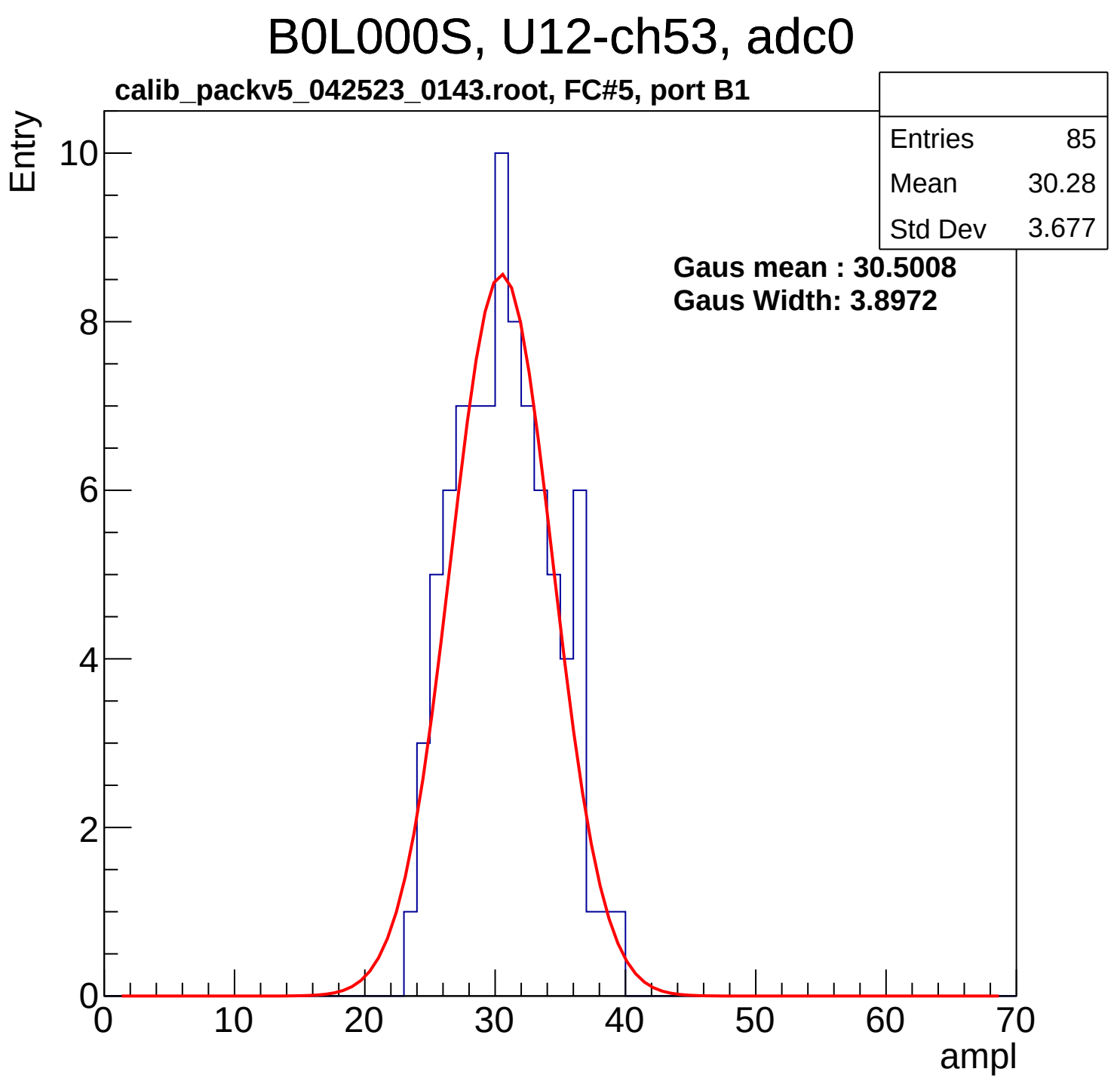
70
ampl


B0L000S, U12-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.28
Std Dev 3.677
Gaus mean : 30.5008
Gaus Width: 3.8972
Entry
ampl
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4
6
8
10
0
10
20
30
40
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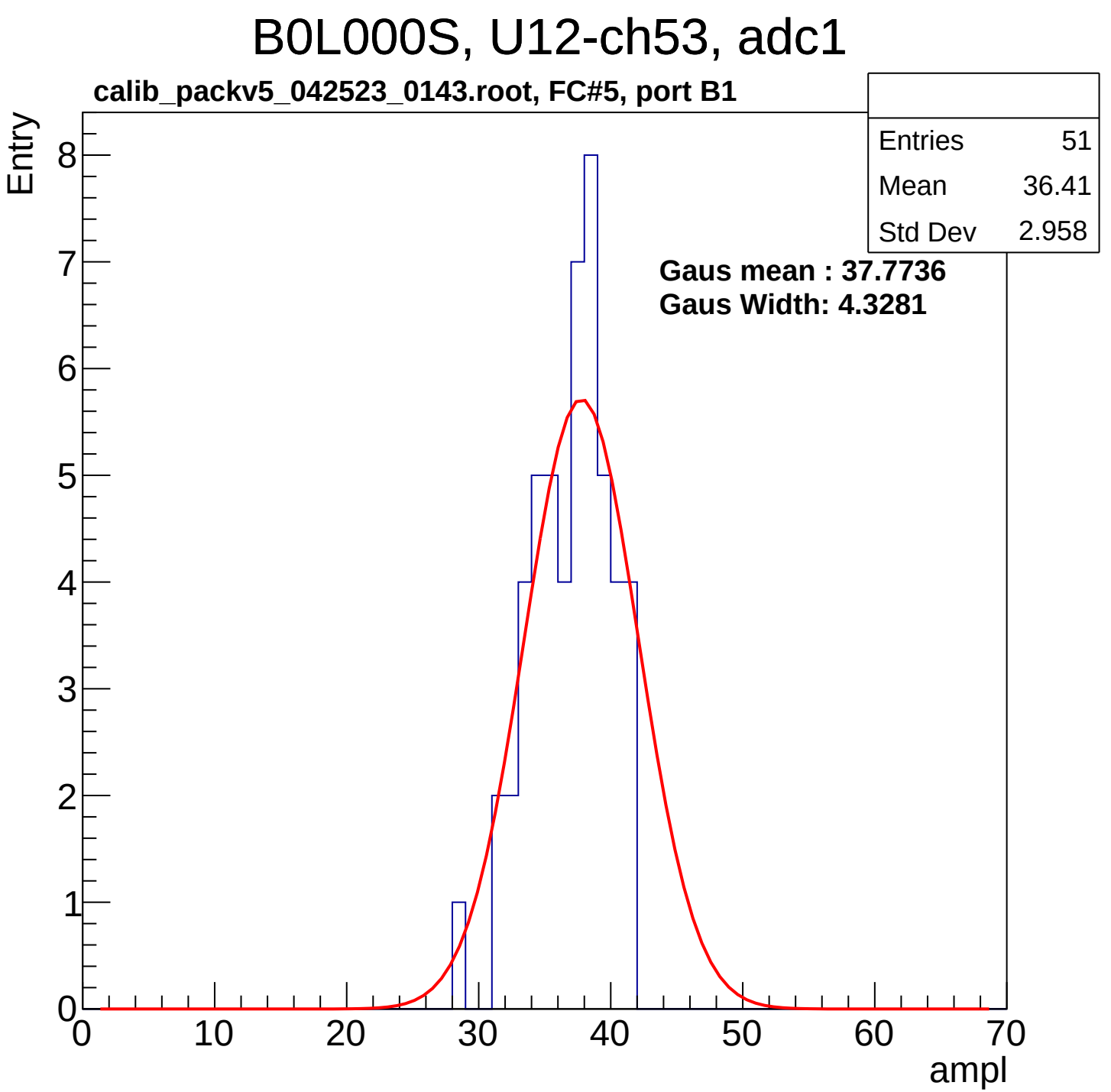
60
70


B0L000S, U12-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 36.41
Std Dev 2.958
Gaus mean : 37.7736
Gaus Width: 4.3281
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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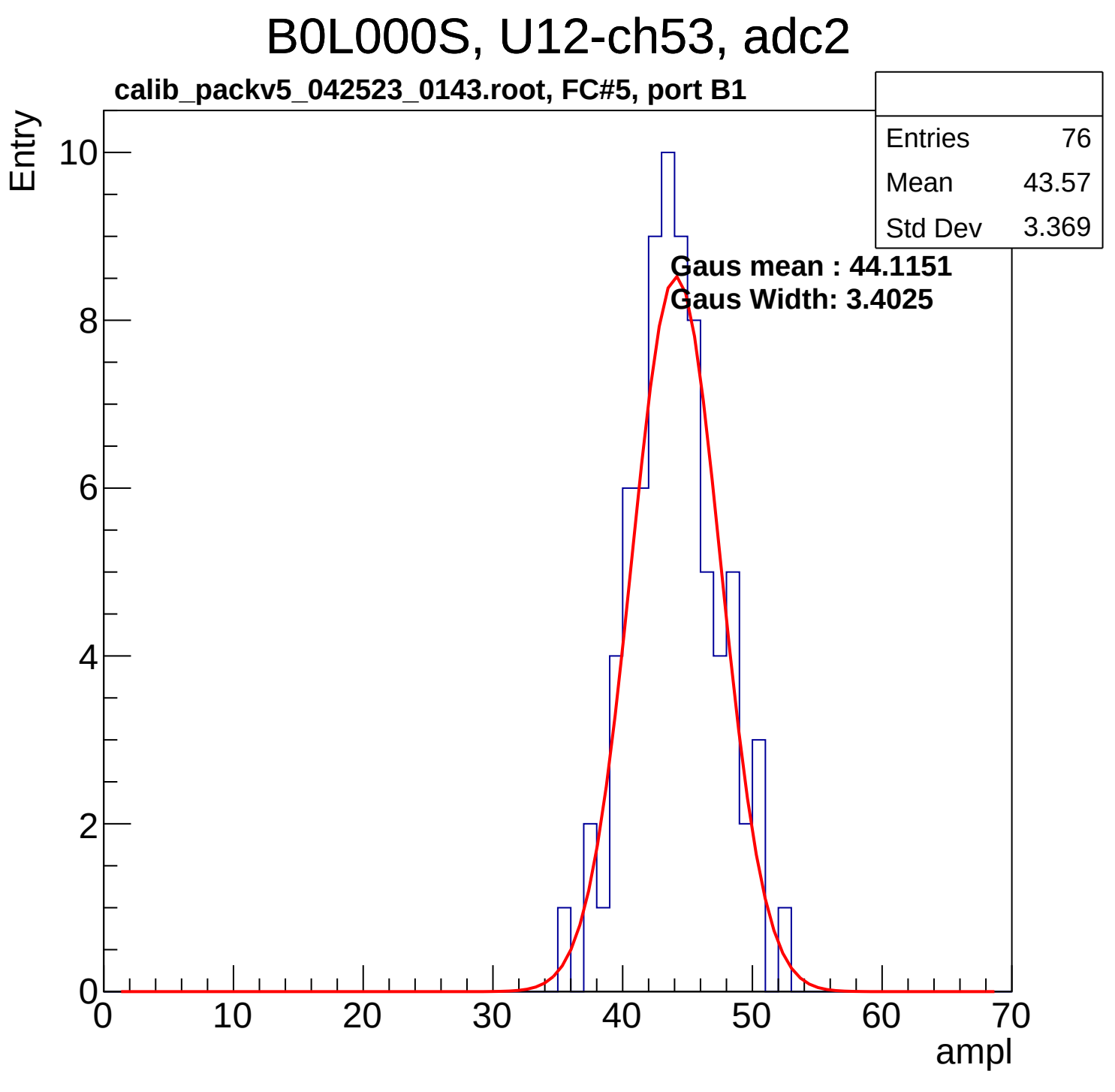
40
50
60
70


B0L000S, U12-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 43.57
Std Dev 3.369
Gaus mean : 44.1151
Gaus Width: 3.4025
Entry
ampl
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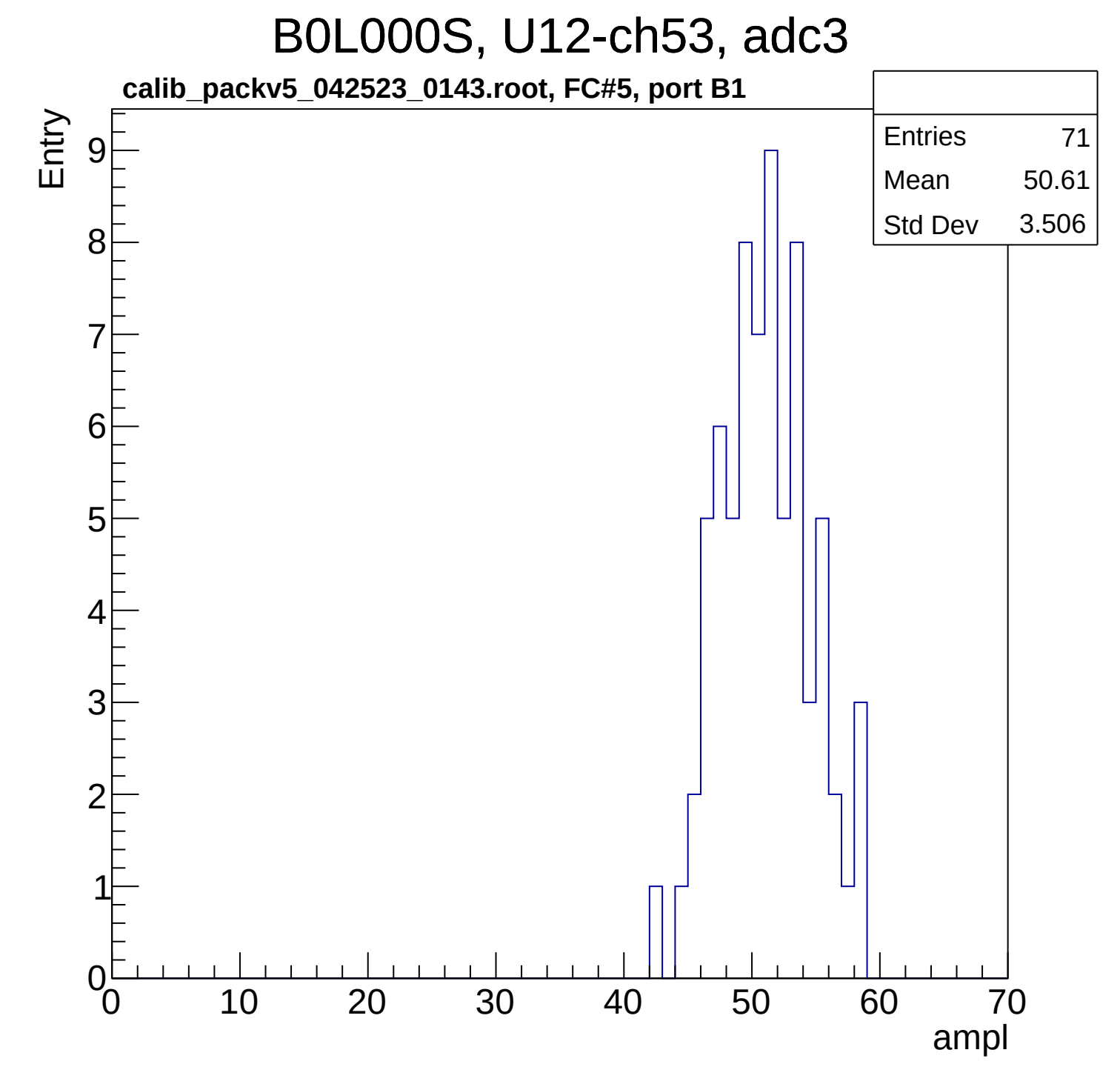
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.61
Std Dev 3.506
Entry
ampl
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4
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6
7
8
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0
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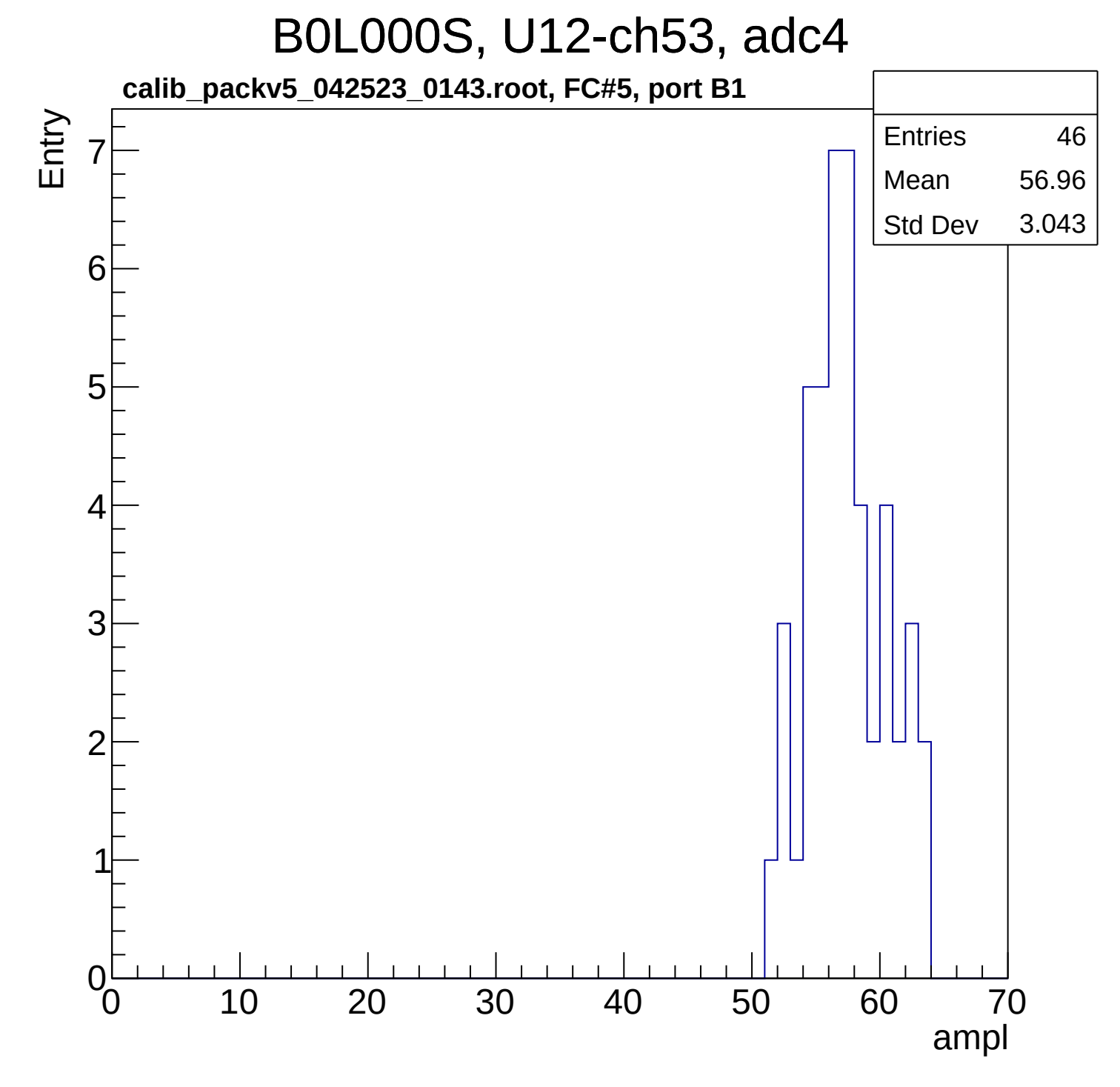
B0L000S, U12-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.96
Std Dev 3.043
Entry
ampl
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0
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60
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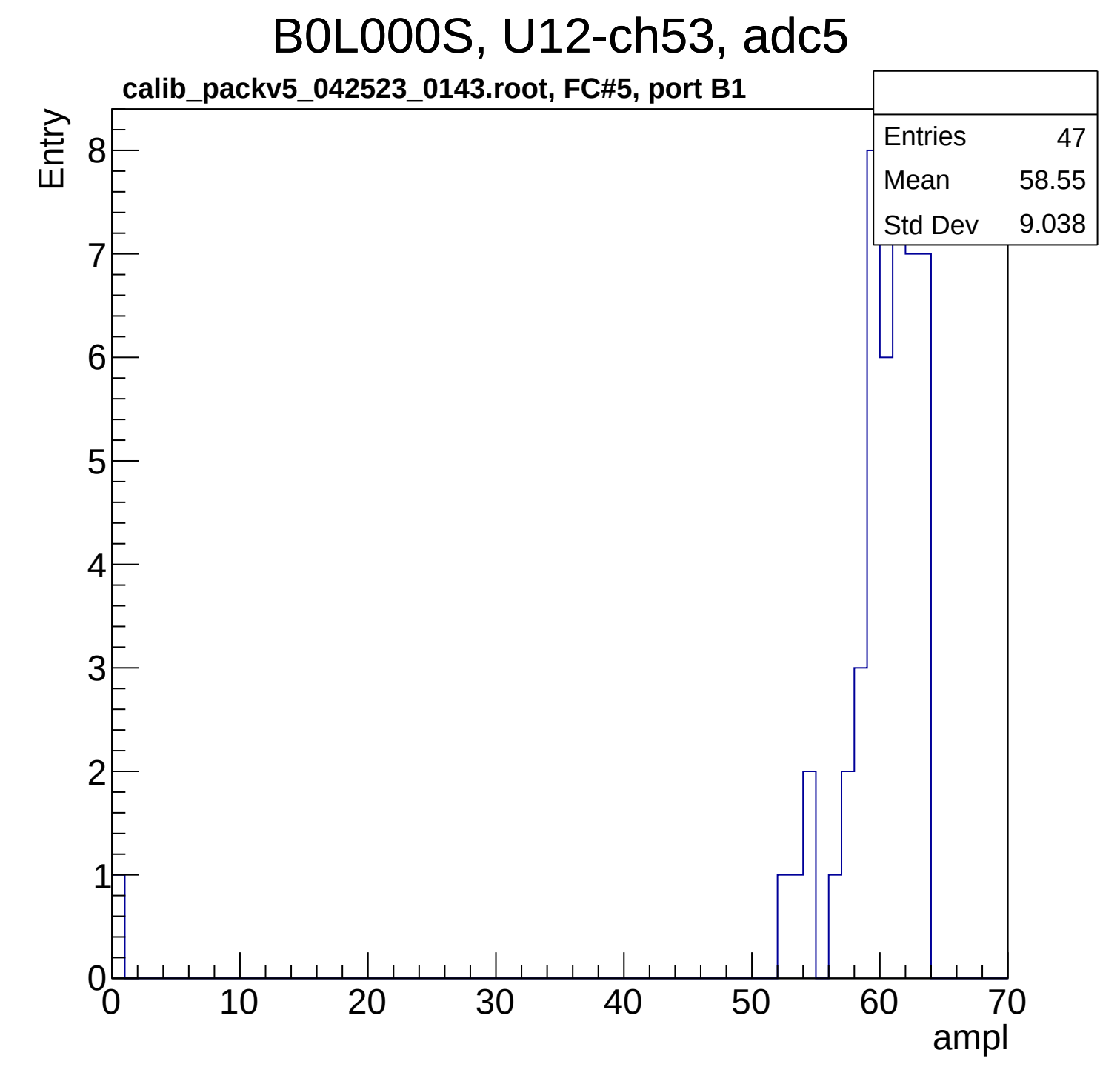
B0L000S, U12-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.55
Std Dev 9.038
Entry
ampl
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2
3
4
5
6
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8
0
10
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60
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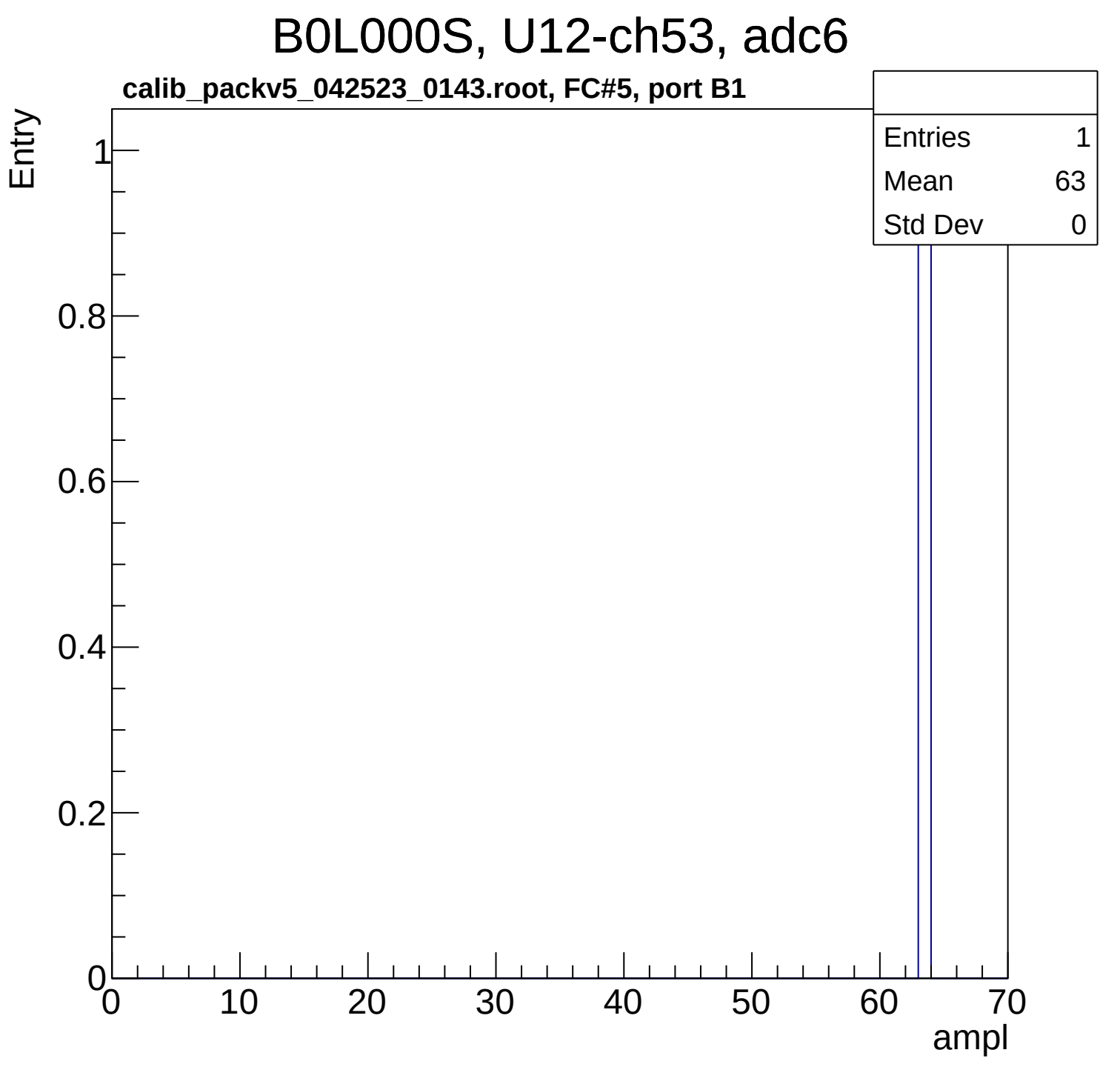
B0L000S, U12-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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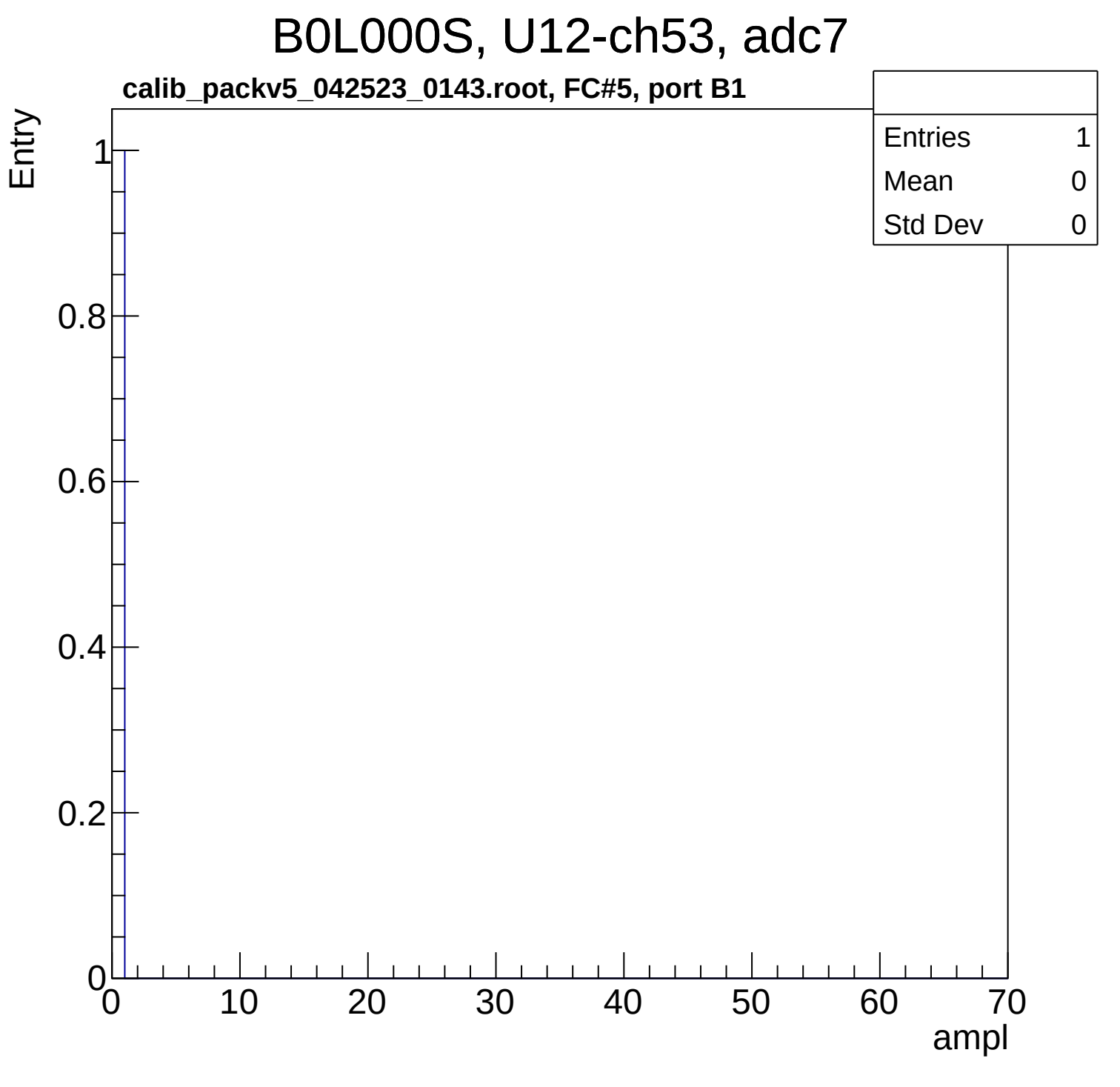
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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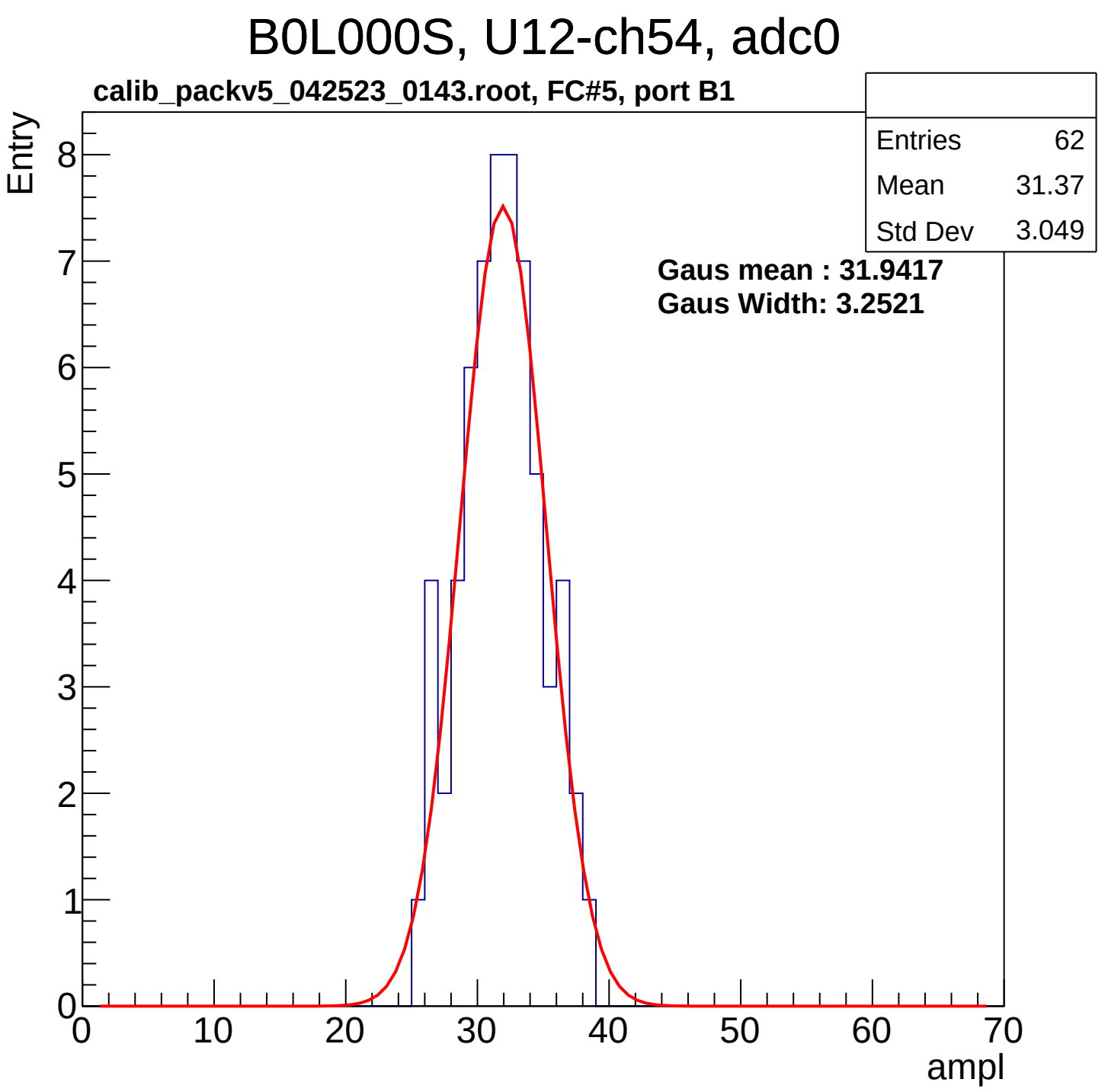
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 31.37
Std Dev 3.049
Gaus mean : 31.9417
Gaus Width: 3.2521
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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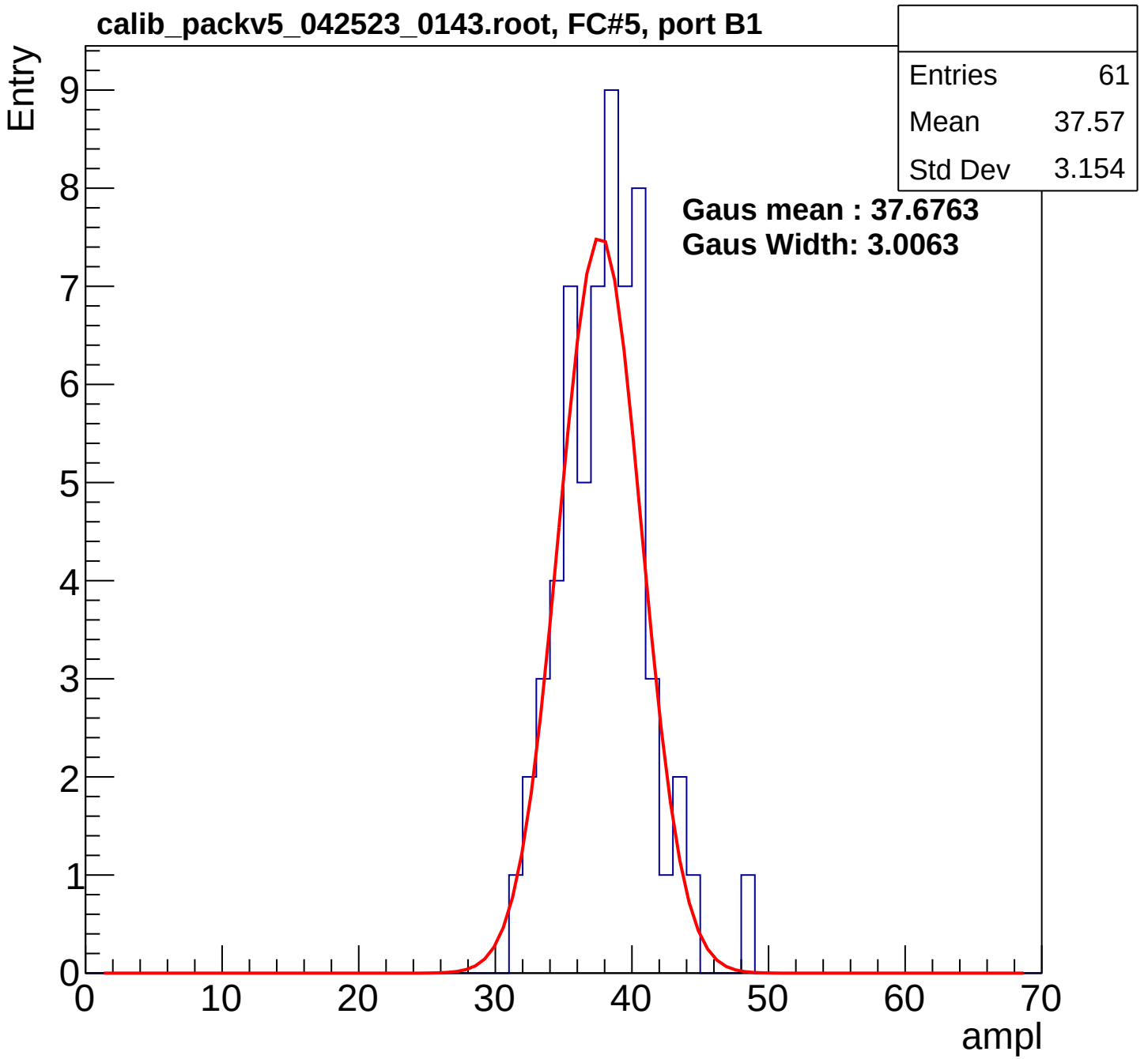
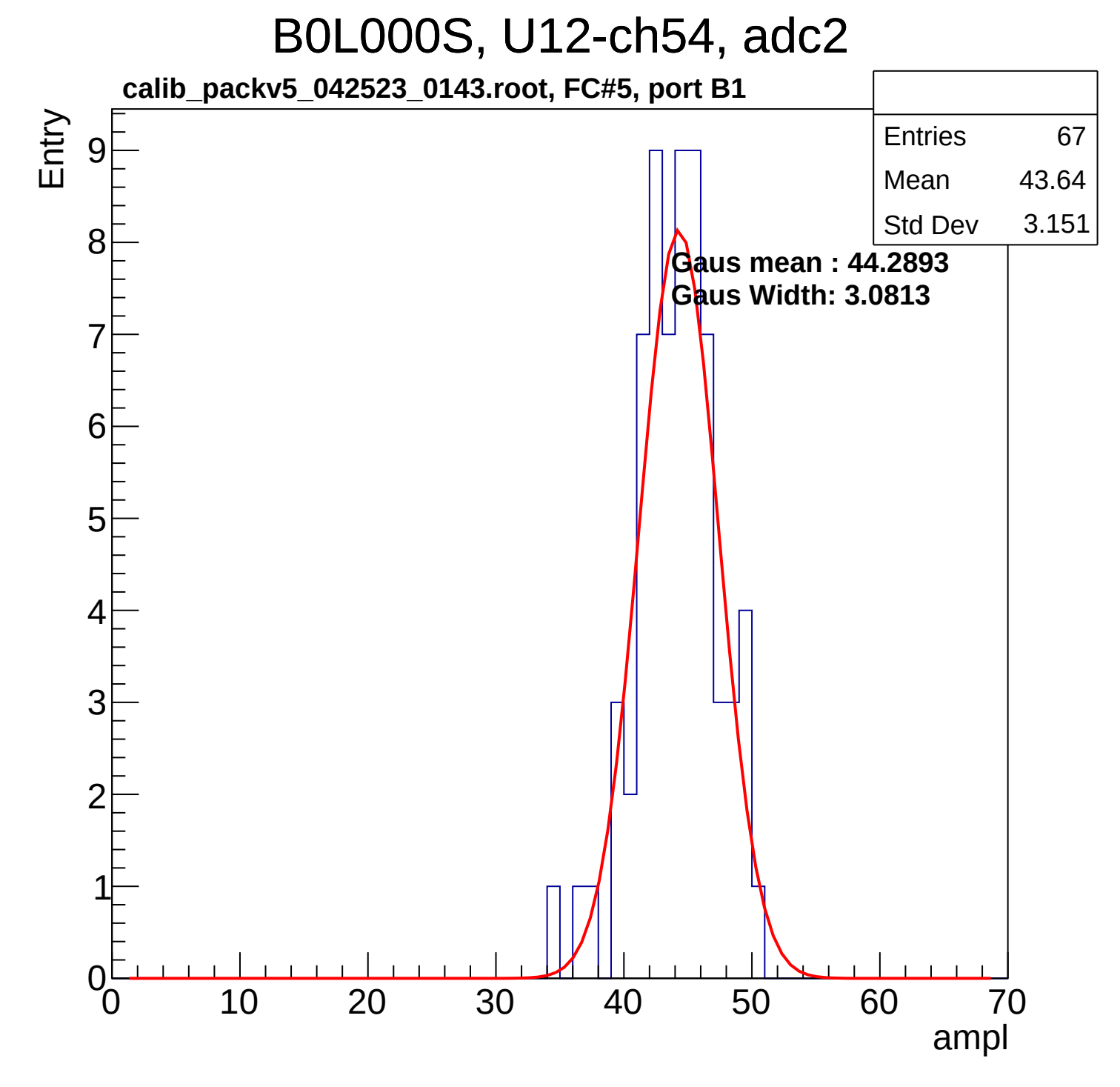

B0L000S, U12-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 37.57
Std Dev 3.154
Gaus mean : 37.6763
Gaus Width: 3.0063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.64
Std Dev 3.151
Gaus mean : 44.2893
Gaus Width: 3.0813
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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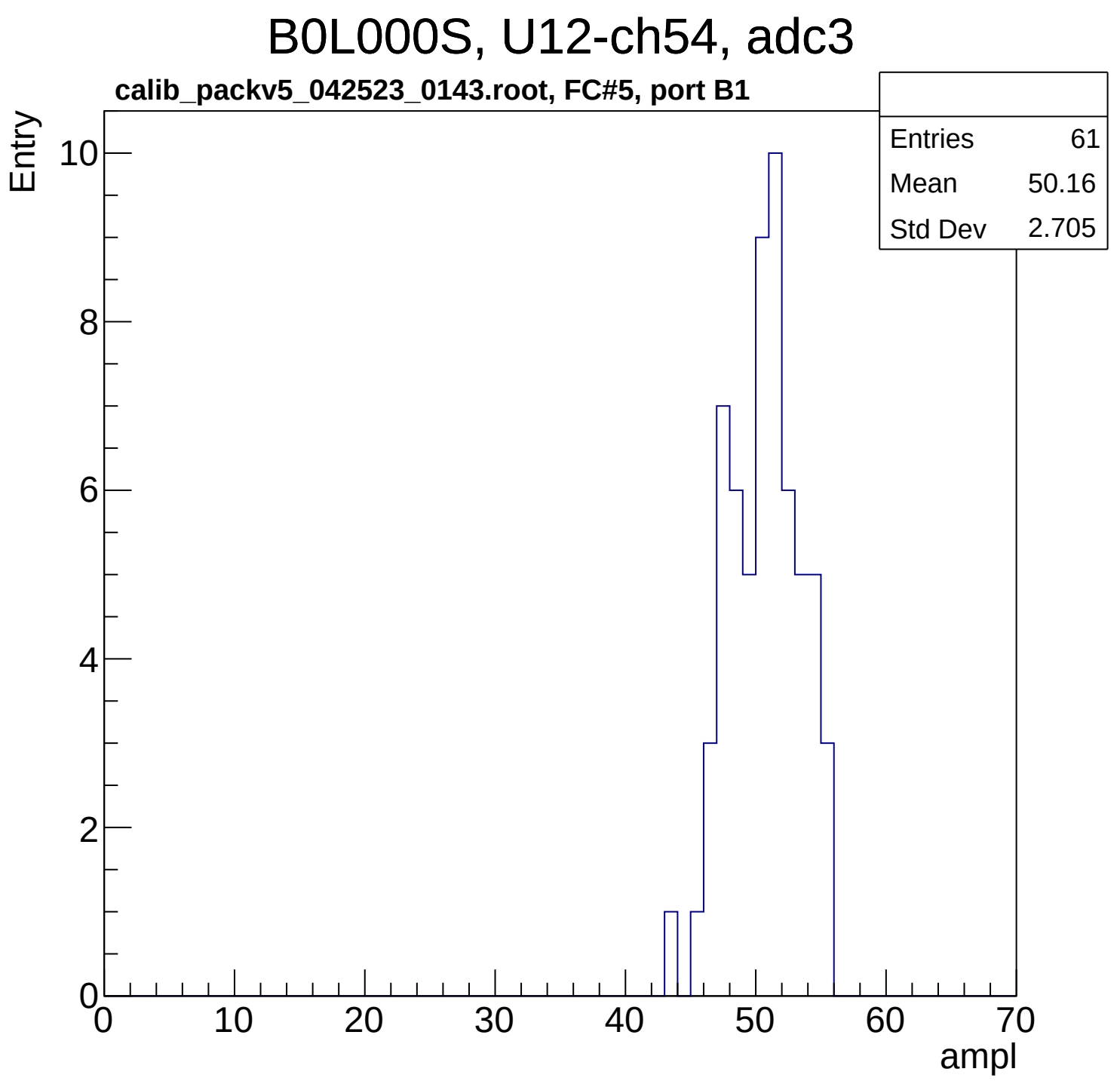
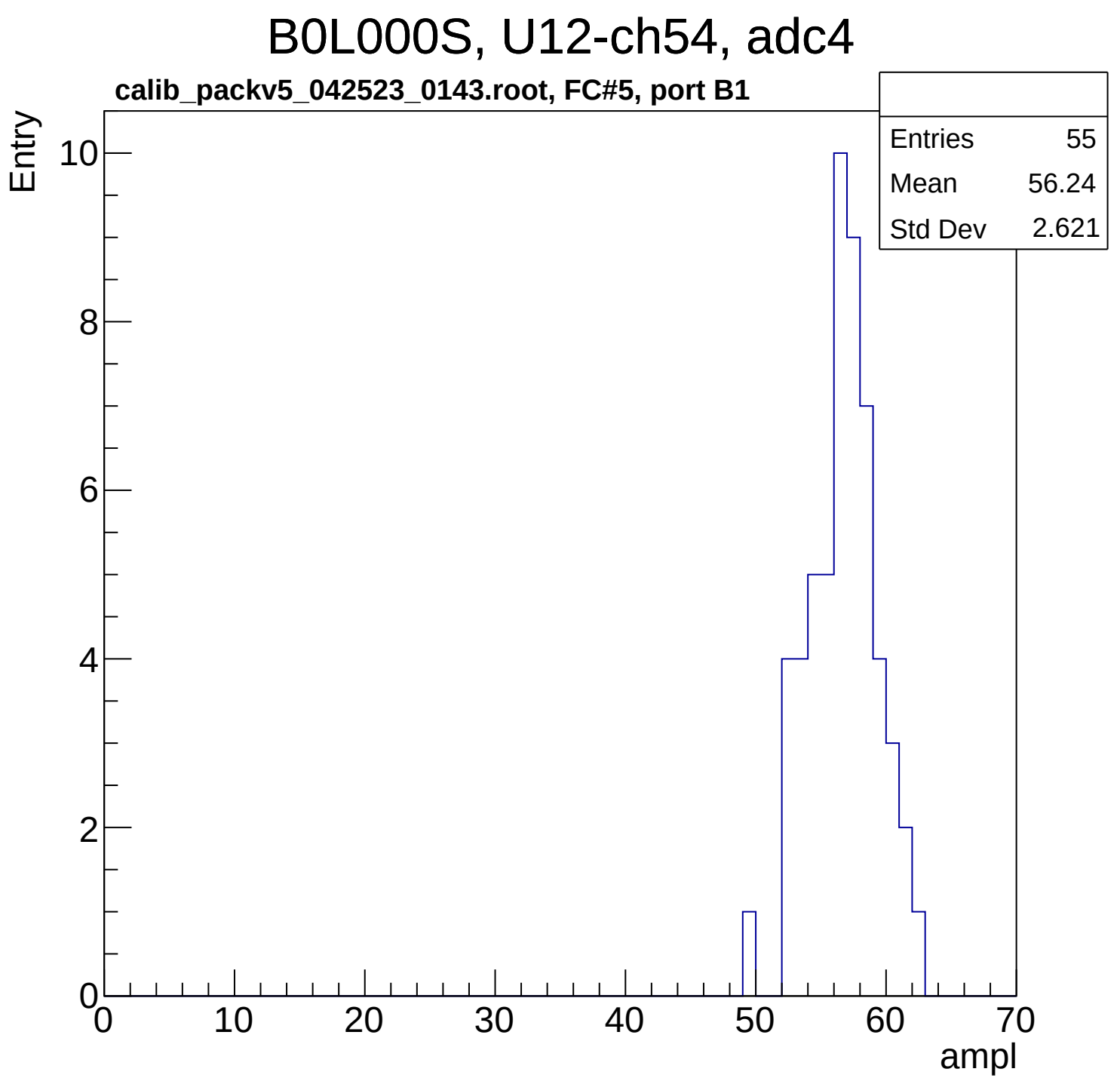
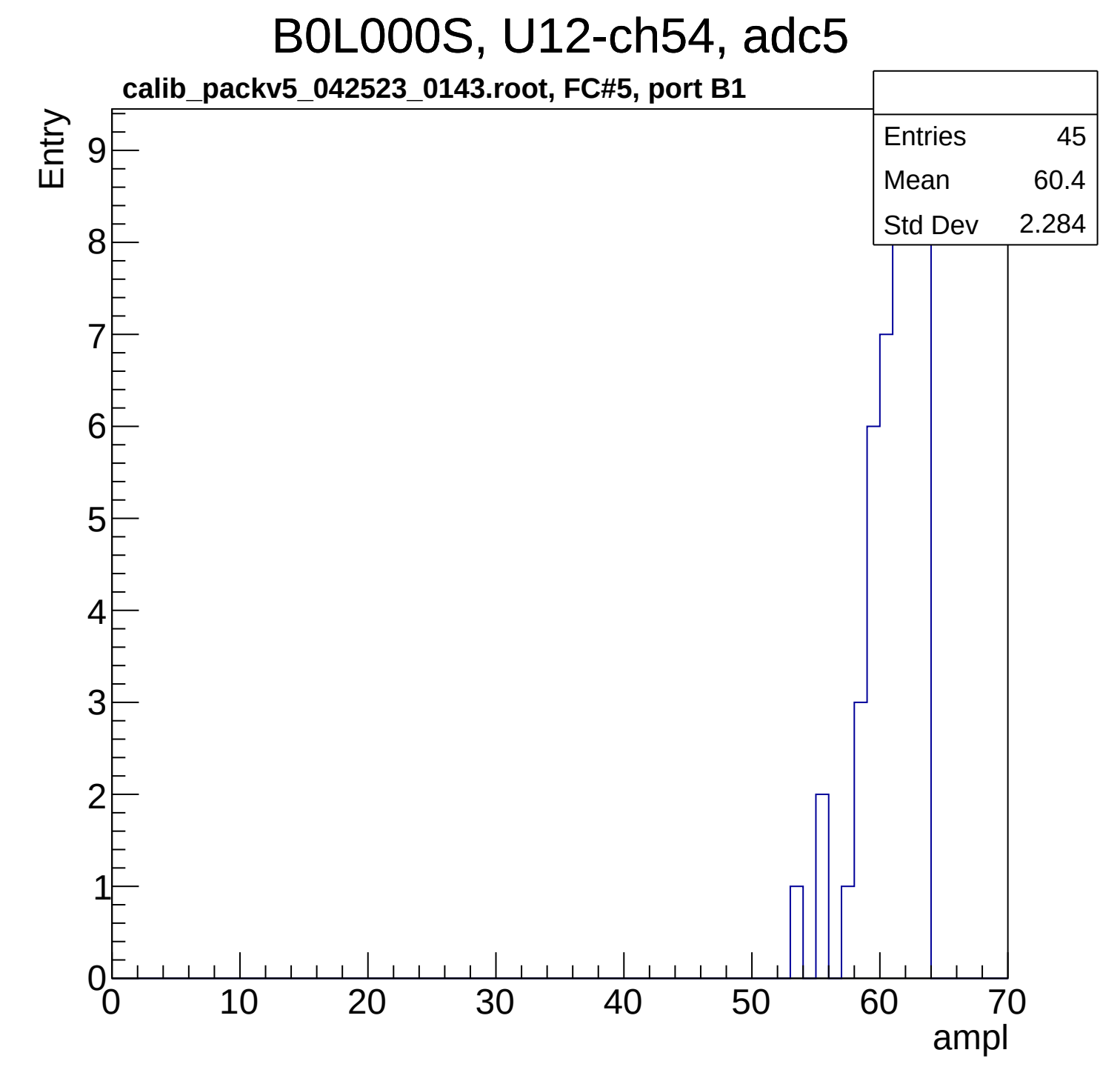

B0L000S, U12-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.16
Std Dev 2.705
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.24
Std Dev 2.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.4
Std Dev 2.284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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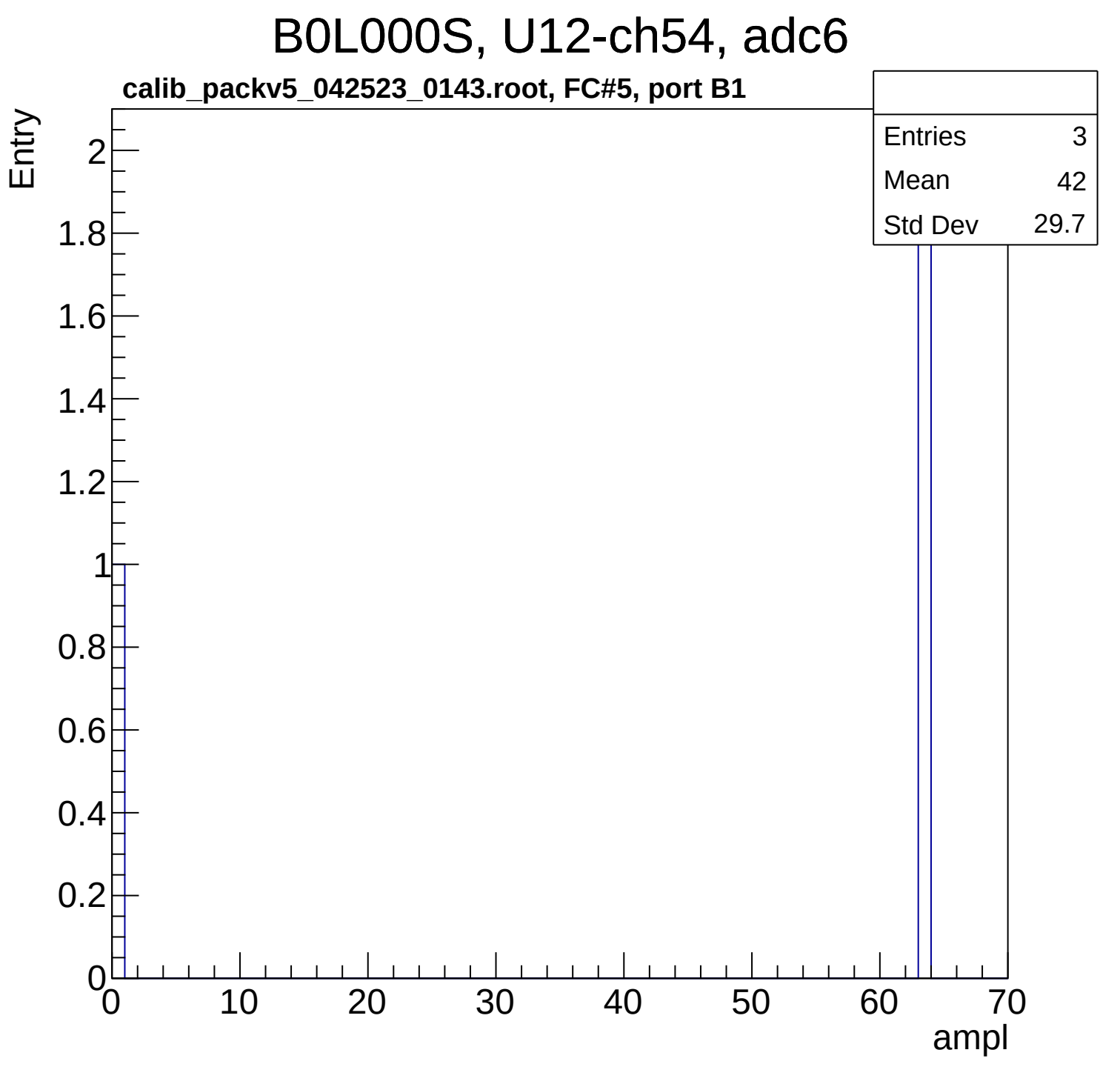
B0L000S, U12-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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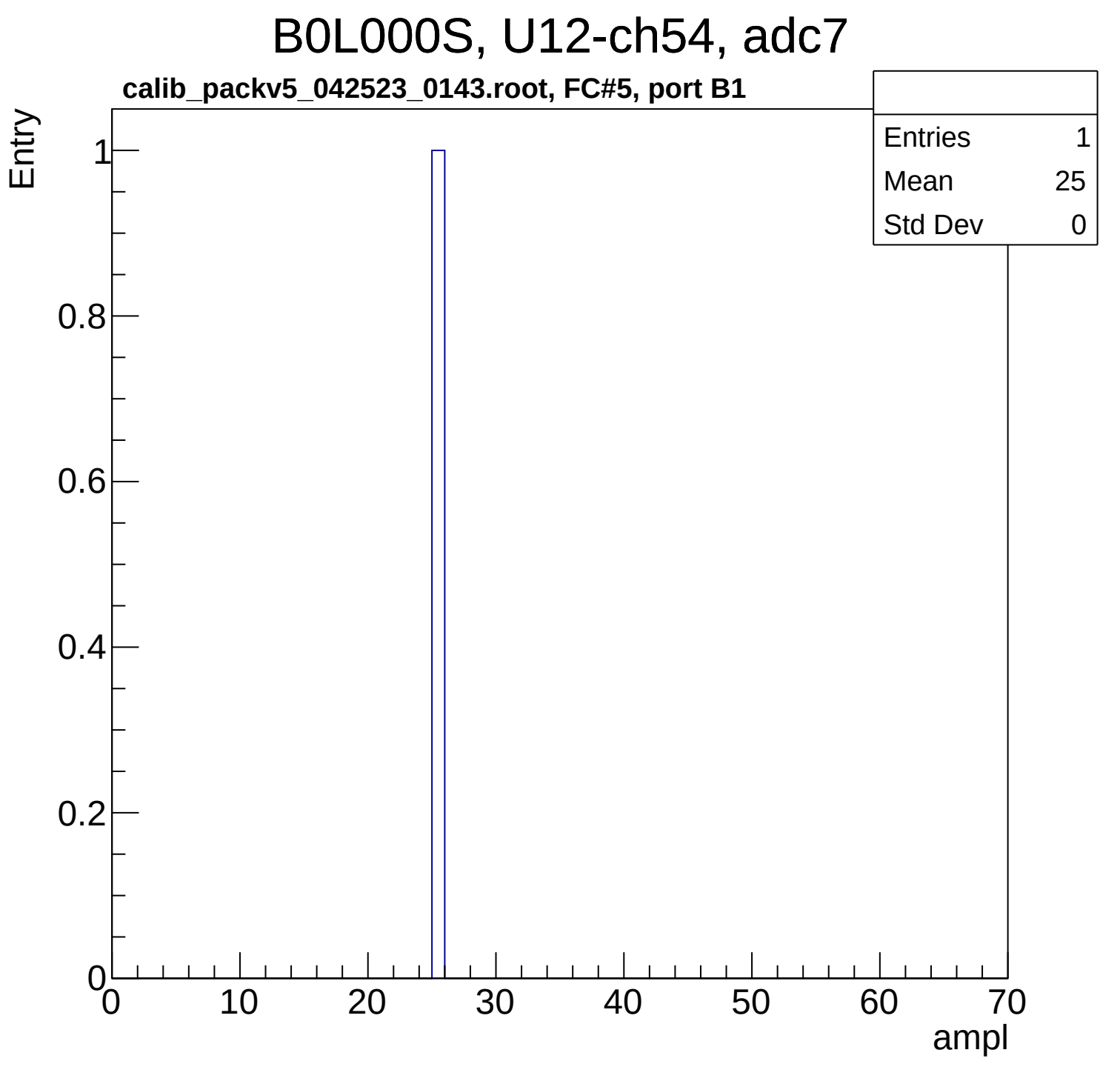
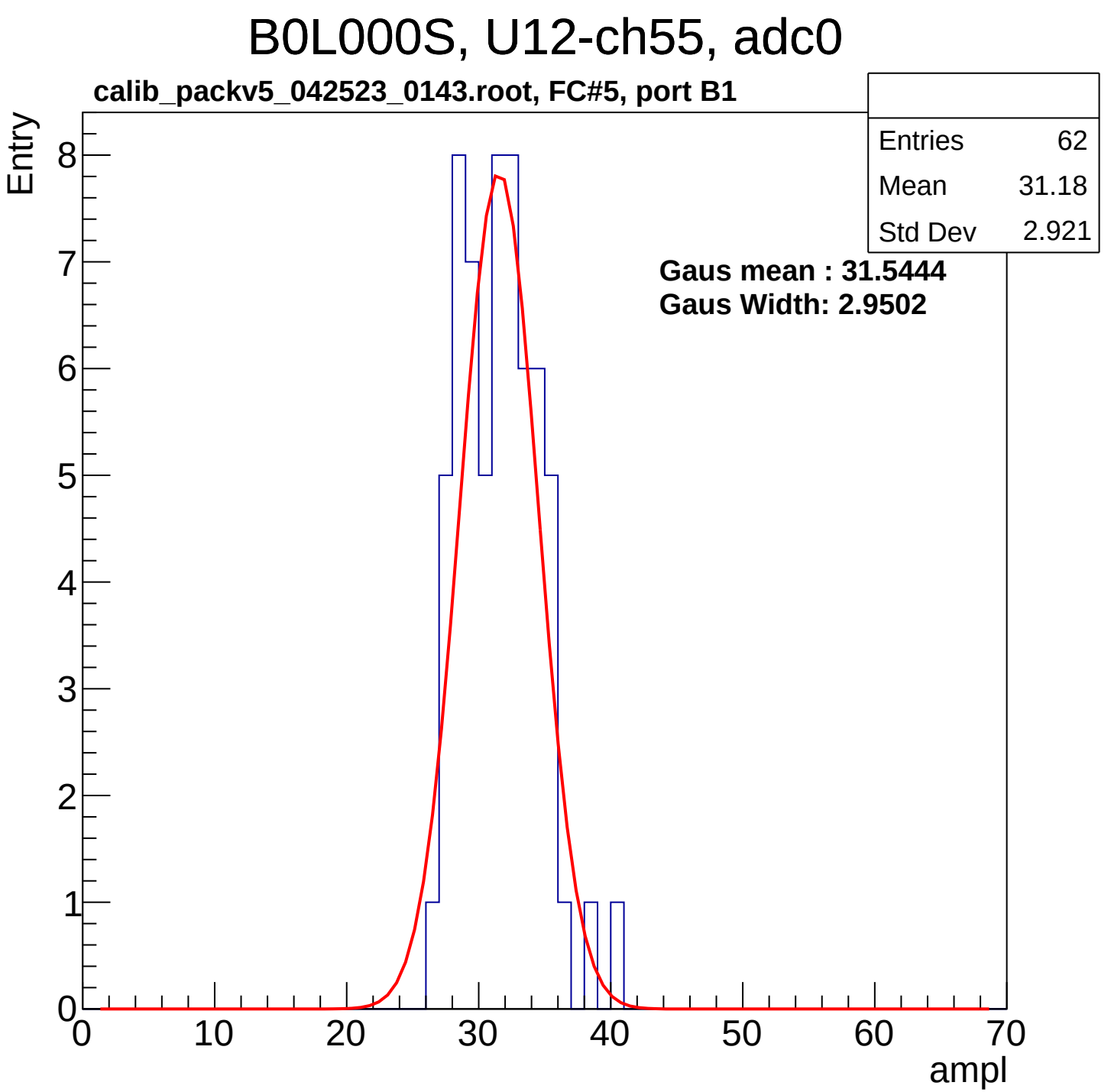
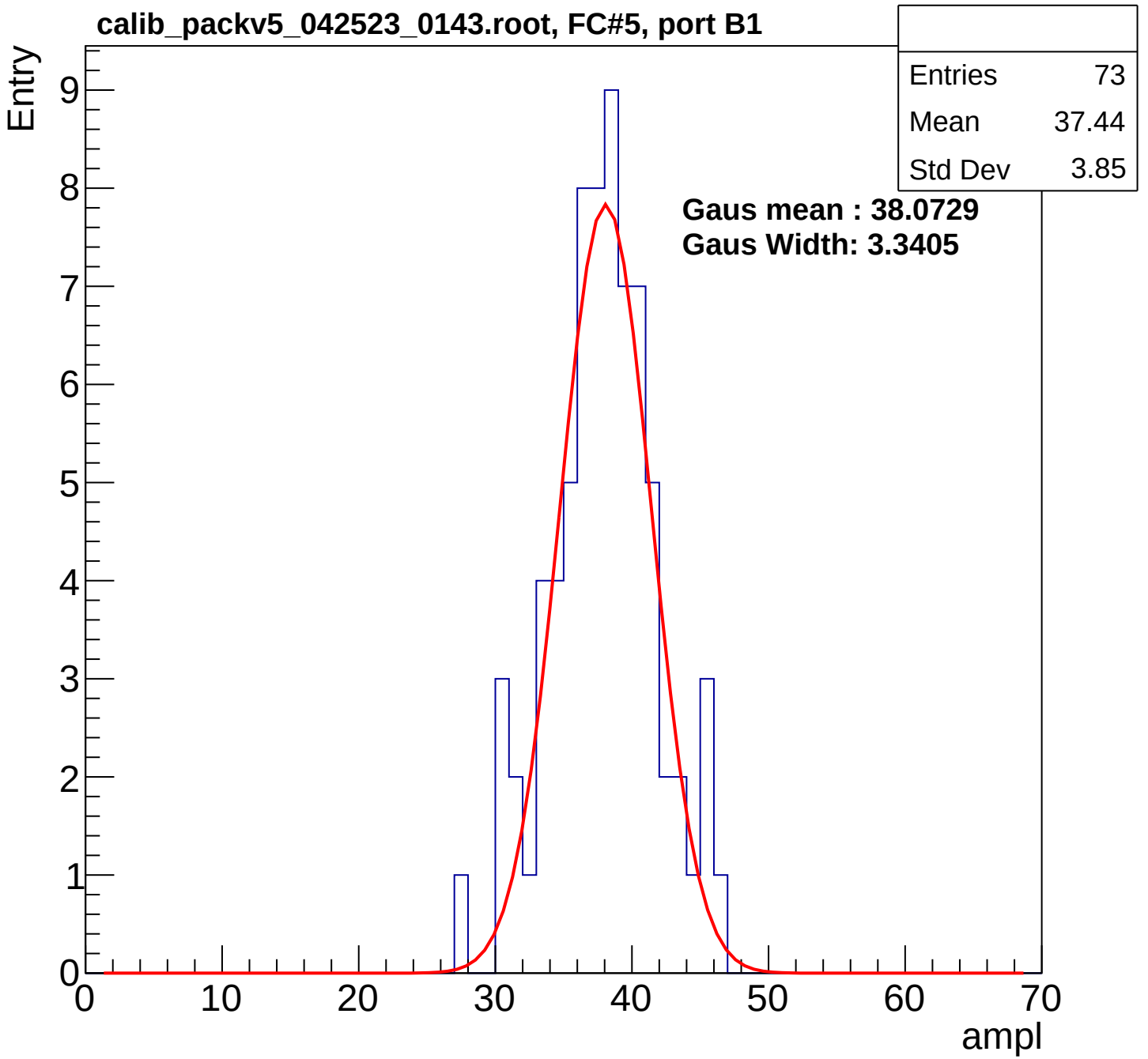

B0L000S, U12-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 31.18
Std Dev 2.921
Gaus mean : 31.5444
Gaus Width: 2.9502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 37.44
Std Dev 3.85
Gaus mean : 38.0729
Gaus Width: 3.3405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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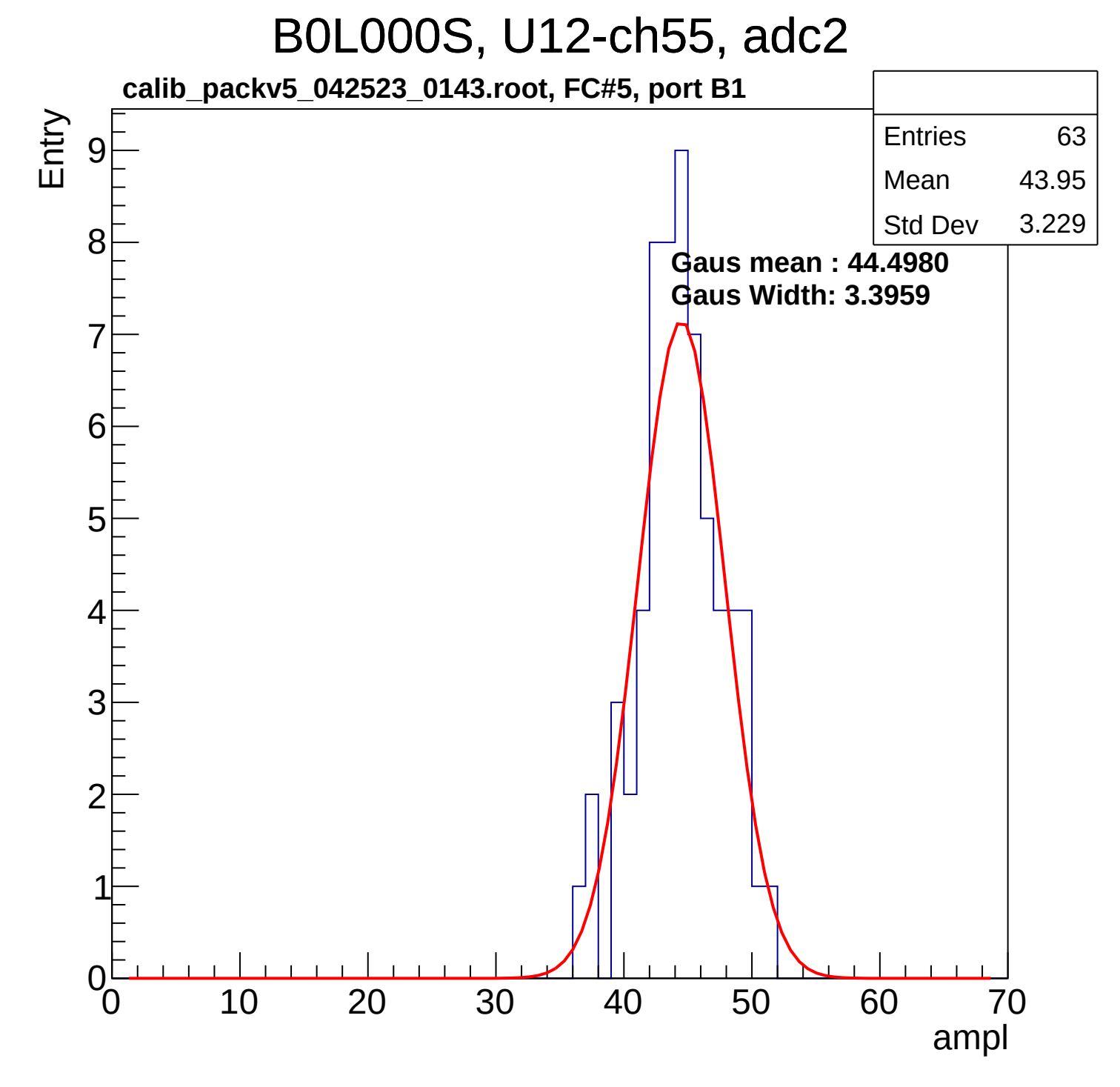
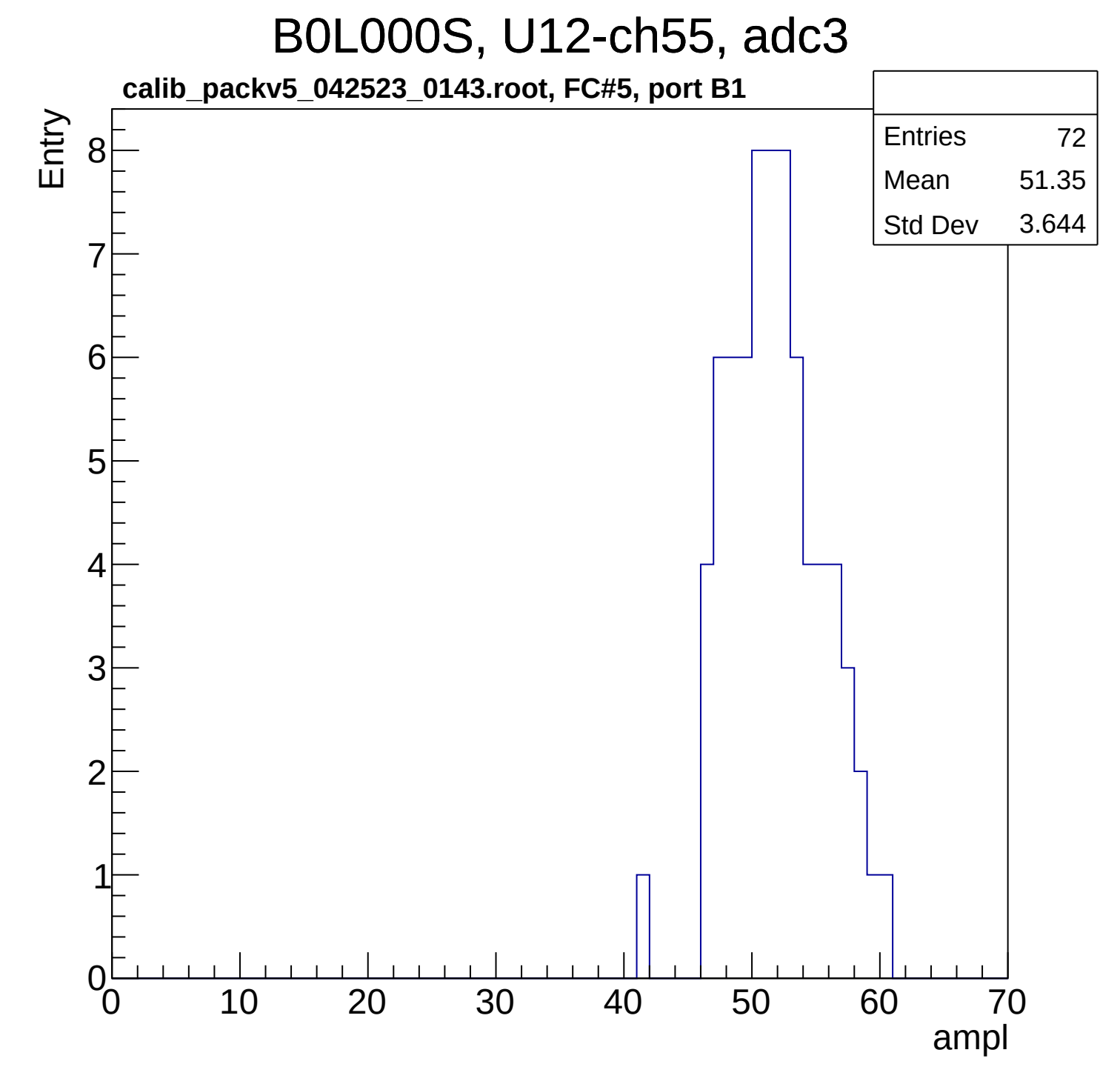
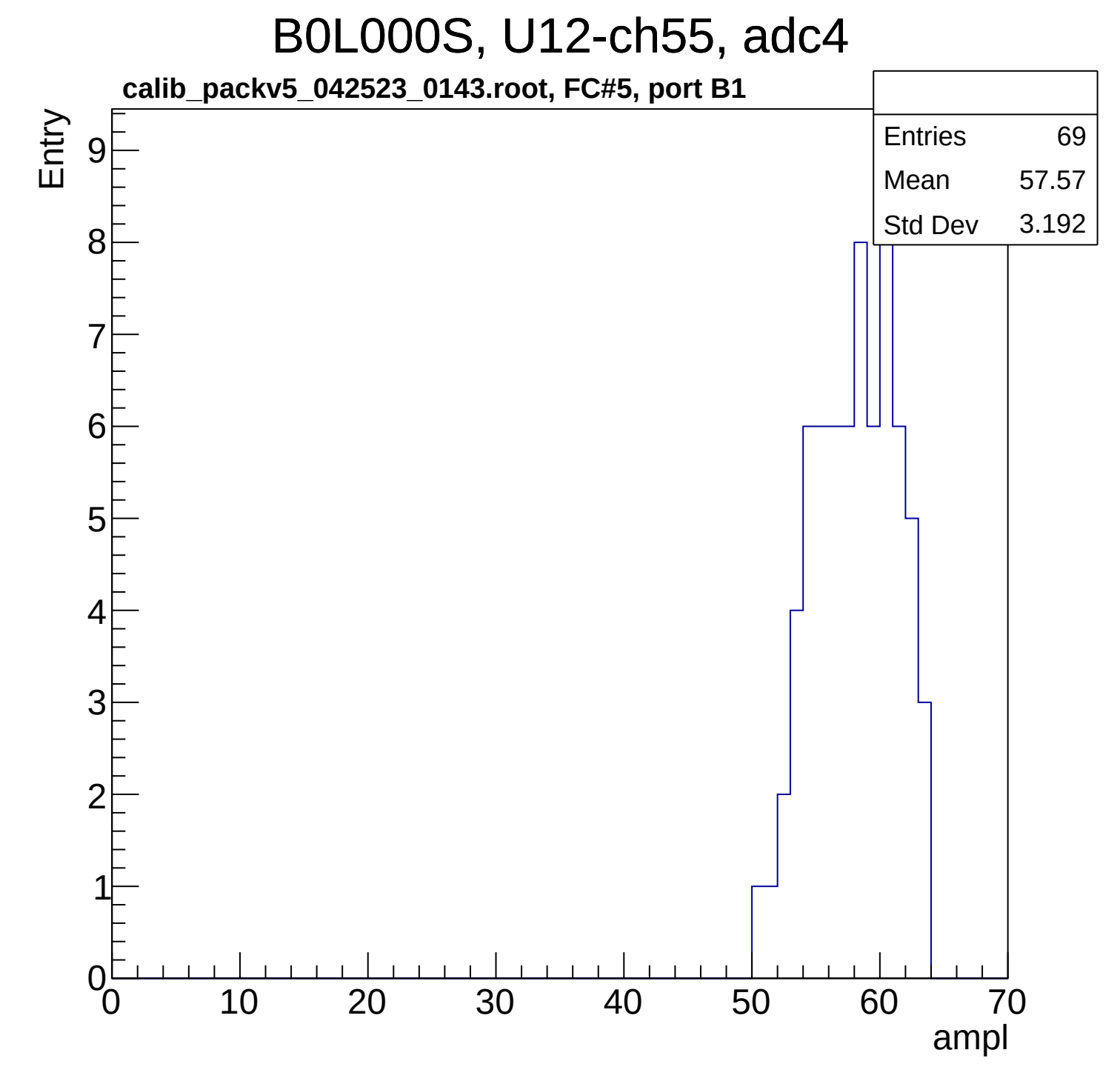
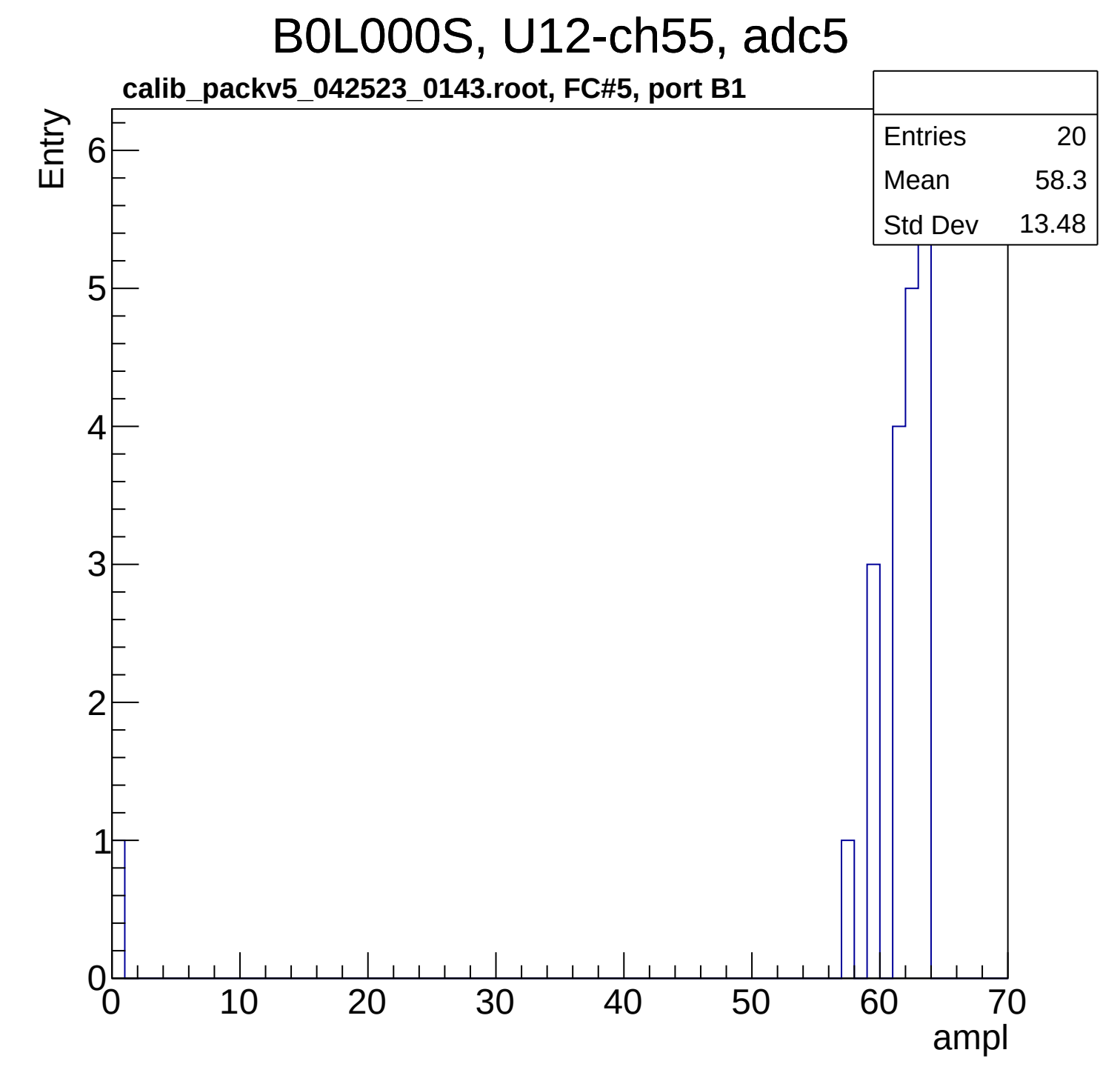
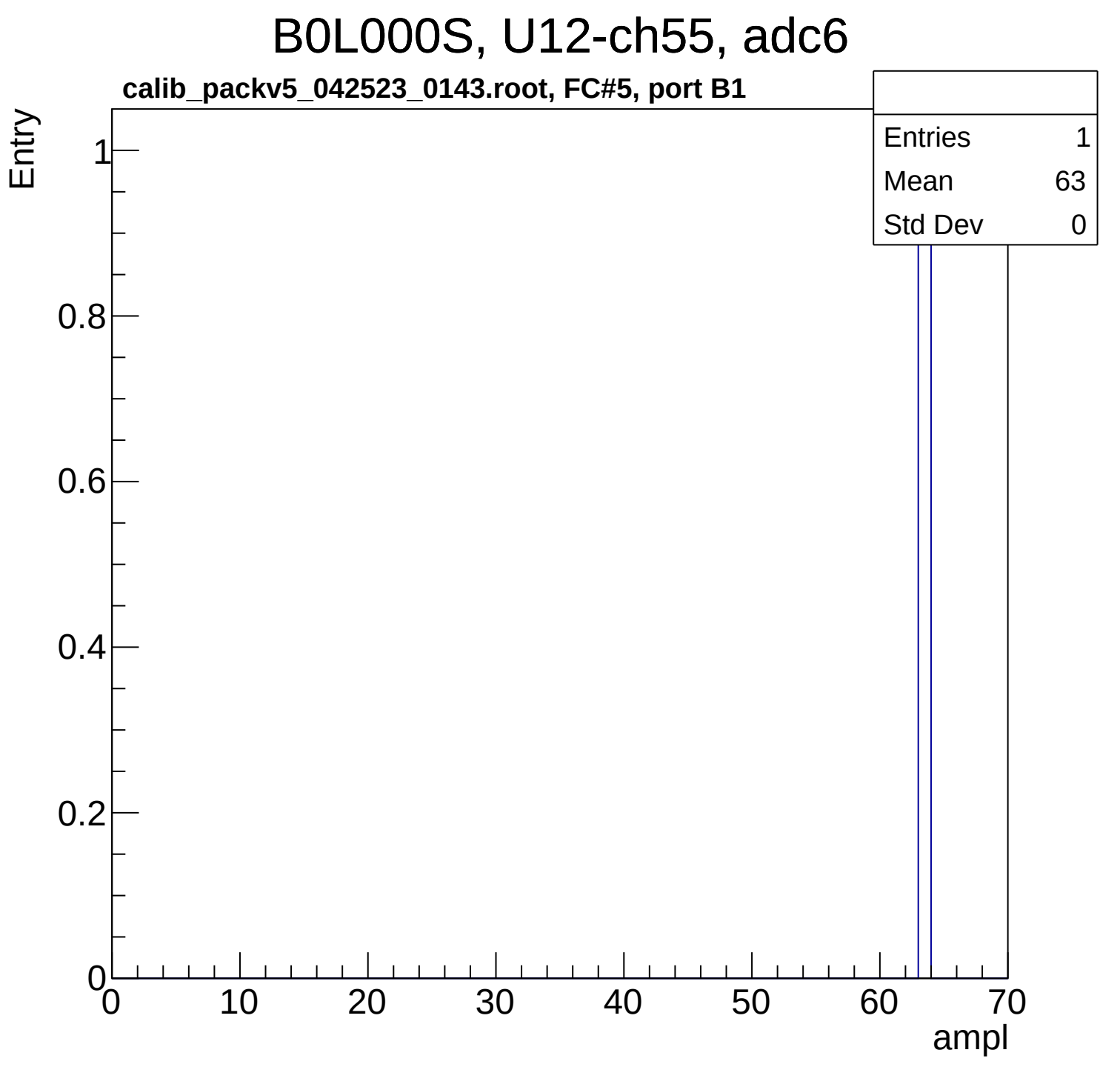
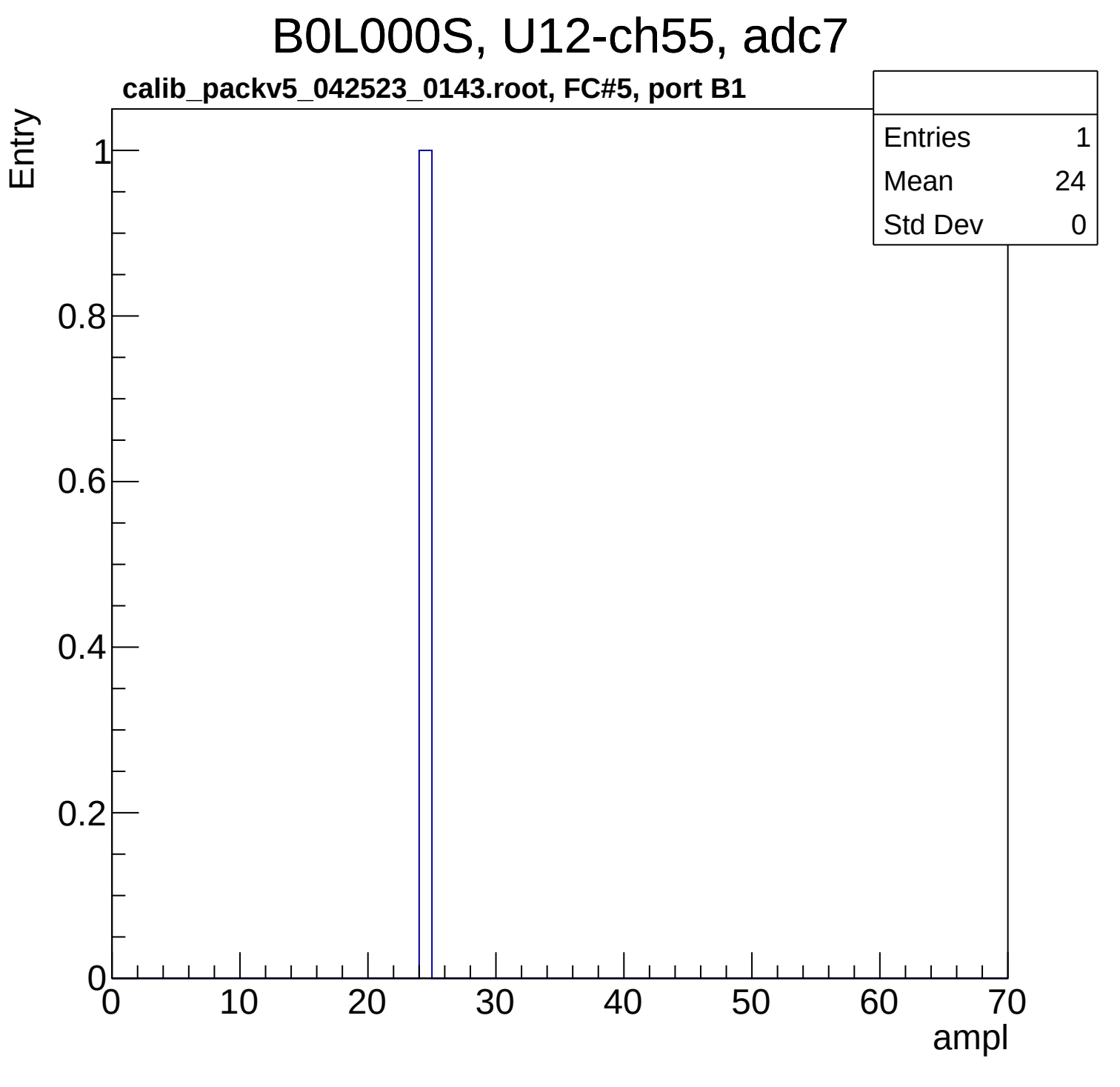

B0L000S, U12-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.95
Std Dev 3.229
Gaus mean : 44.4980
Gaus Width: 3.3959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

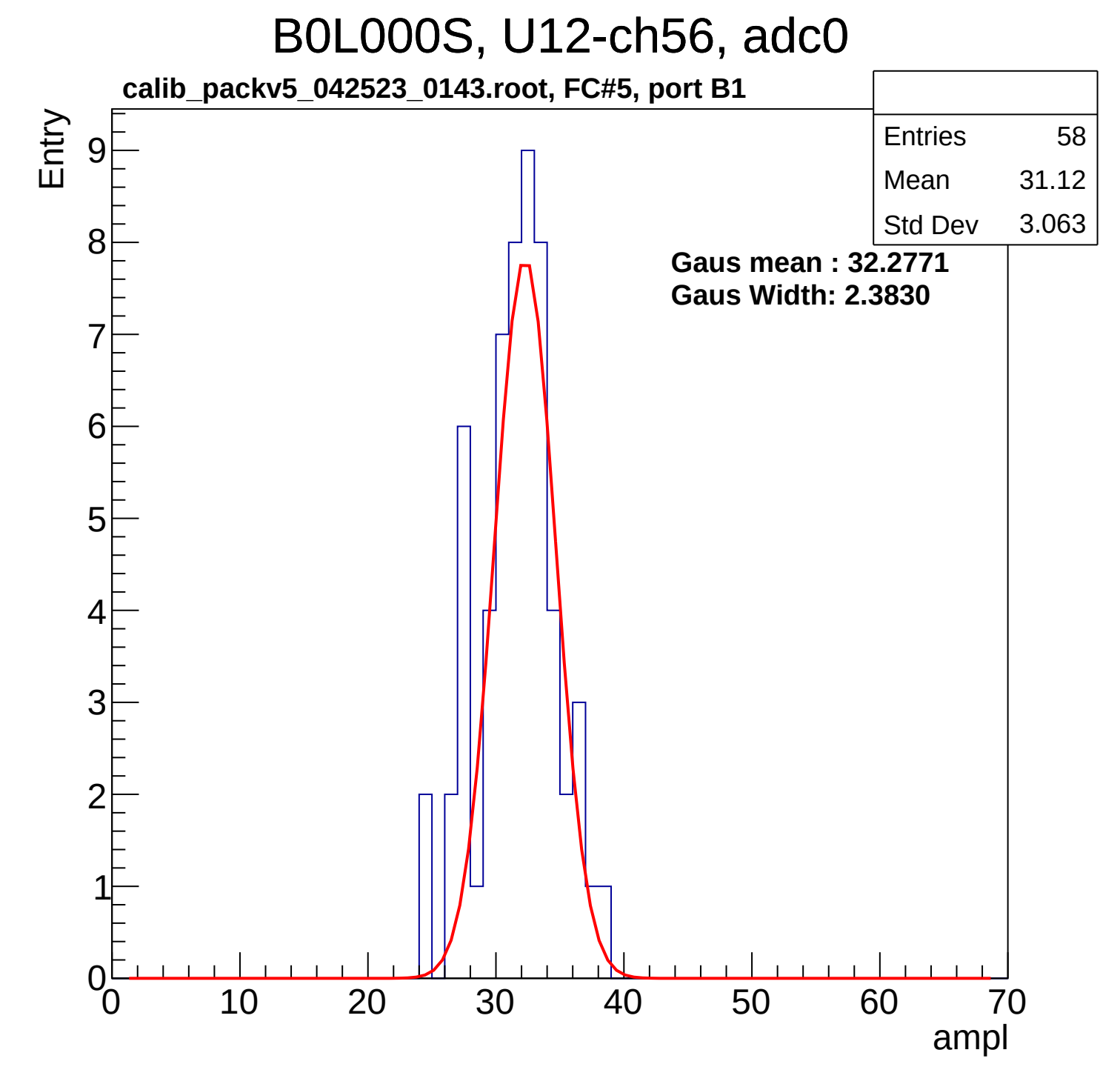

B0L000S, U12-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 51.35
Std Dev 3.644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

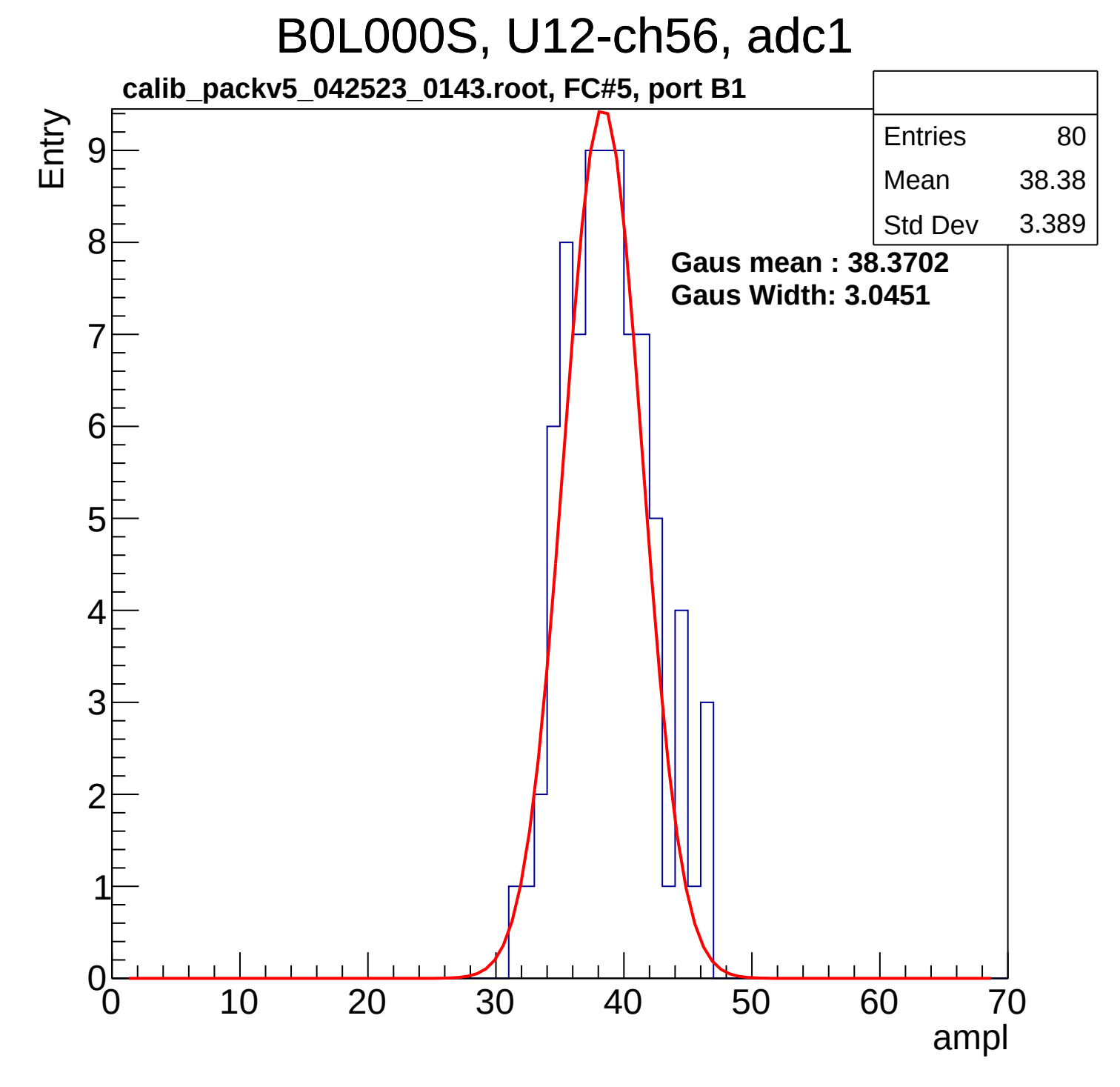

B0L000S, U12-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 57.57
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.3
Std Dev 13.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 31.12
Std Dev 3.063
Gaus mean : 32.2771
Gaus Width: 2.3830
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 38.38
Std Dev 3.389
Gaus mean : 38.3702
Gaus Width: 3.0451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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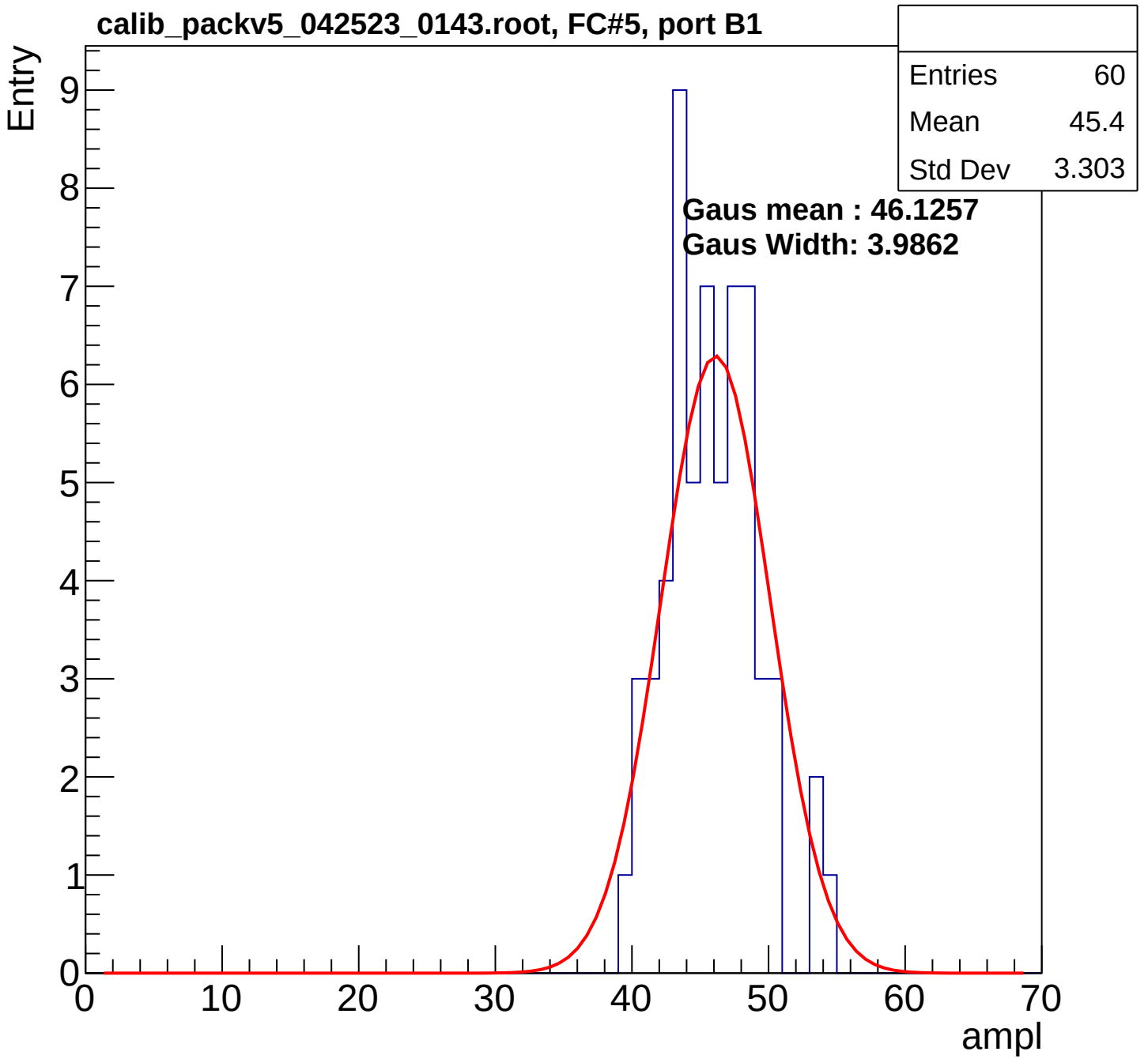
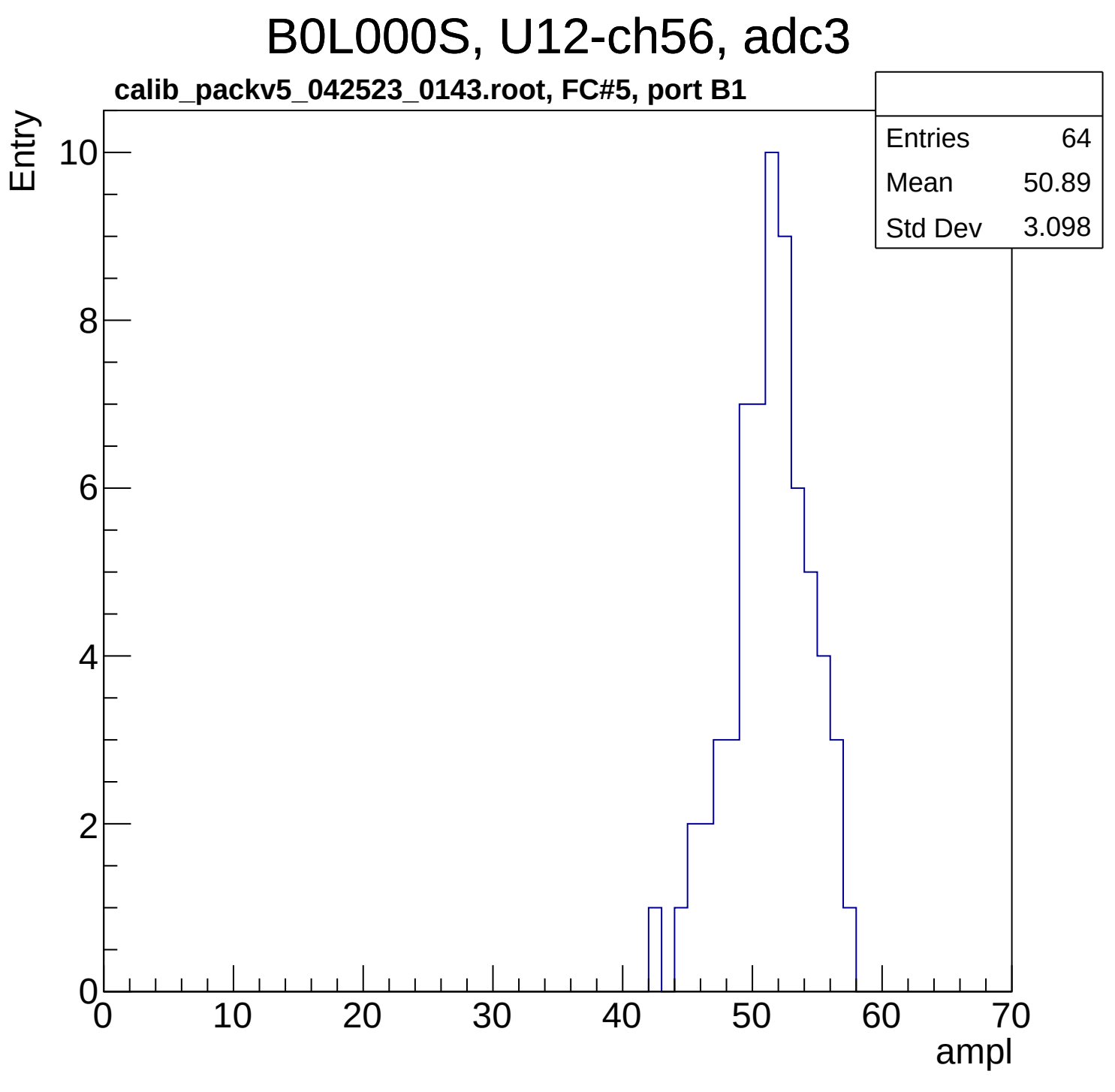
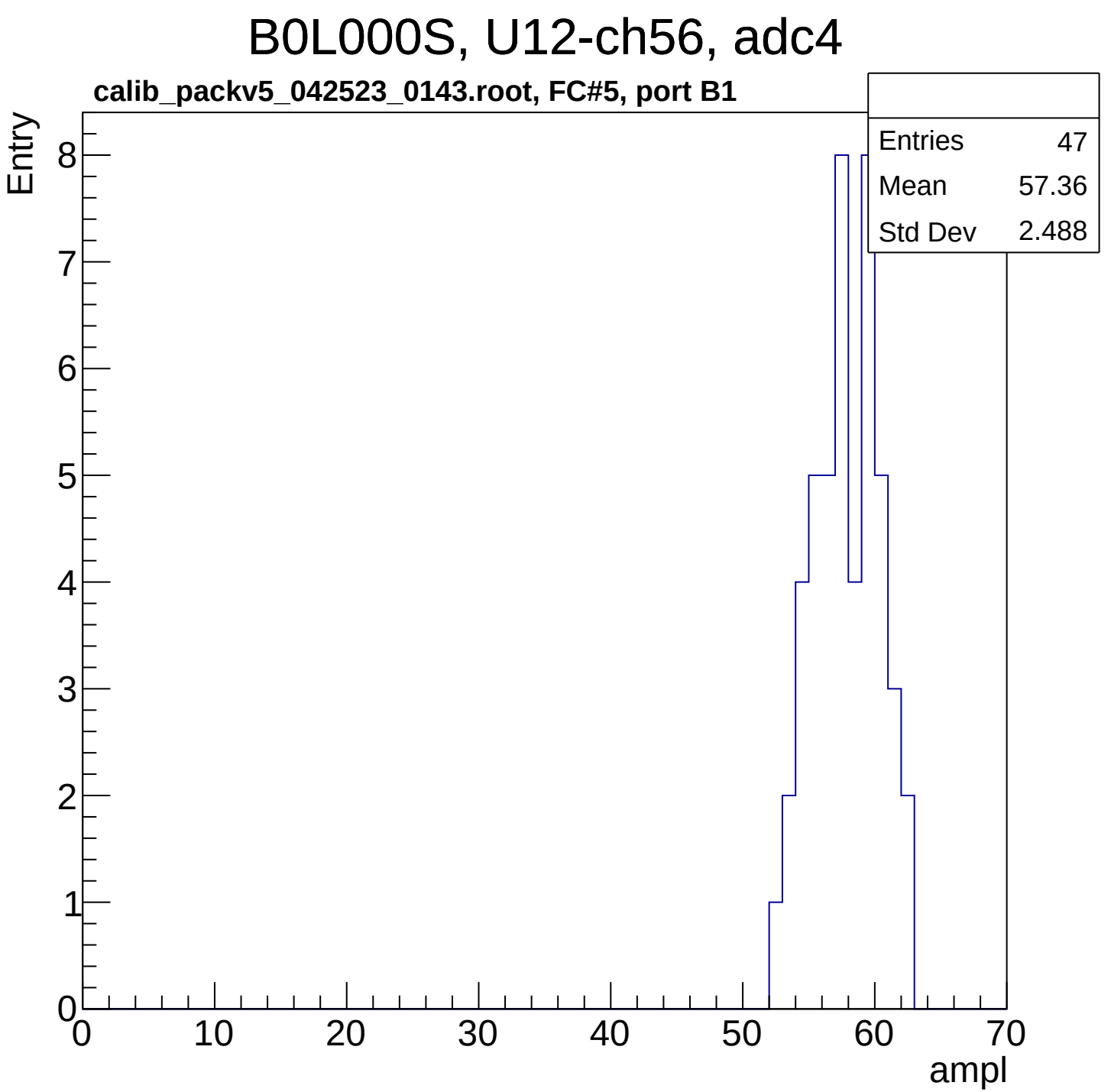

B0L000S, U12-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 45.4
Std Dev 3.303
Gaus mean : 46.1257
Gaus Width: 3.9862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50.89
Std Dev 3.098
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.36
Std Dev 2.488
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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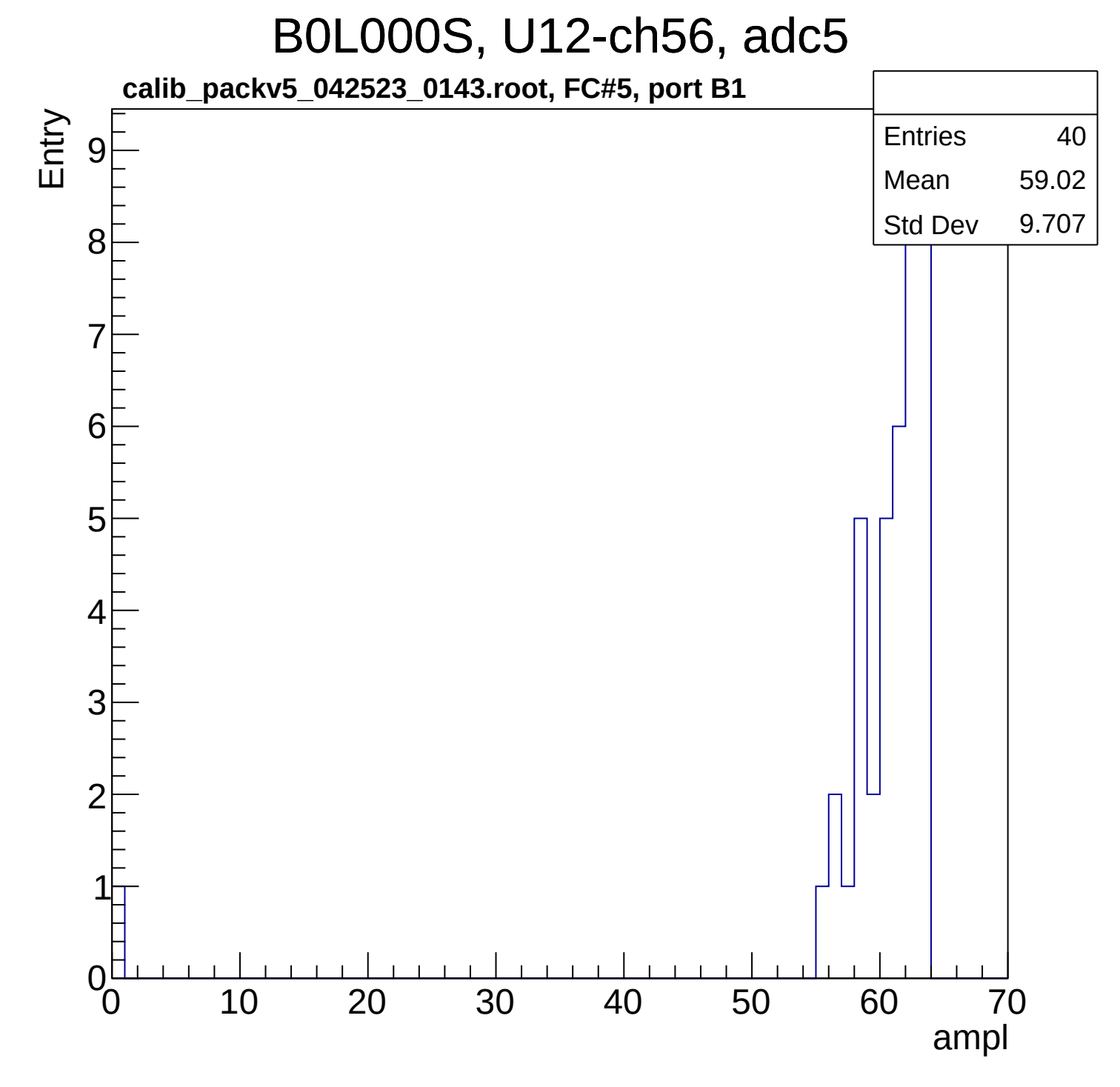
B0L000S, U12-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.02
Std Dev 9.707
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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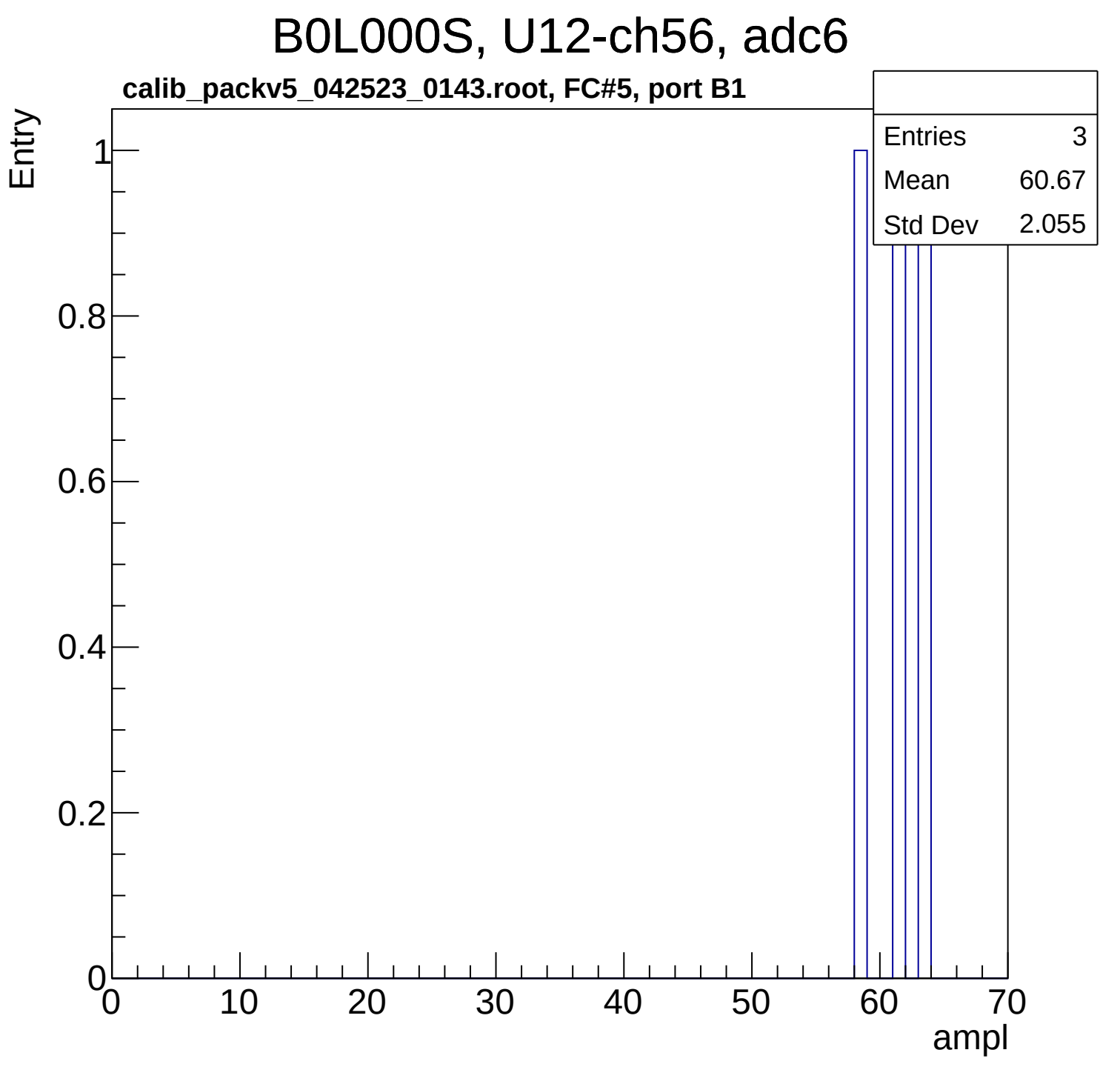
60
70


B0L000S, U12-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.67
Std Dev 2.055
Entry
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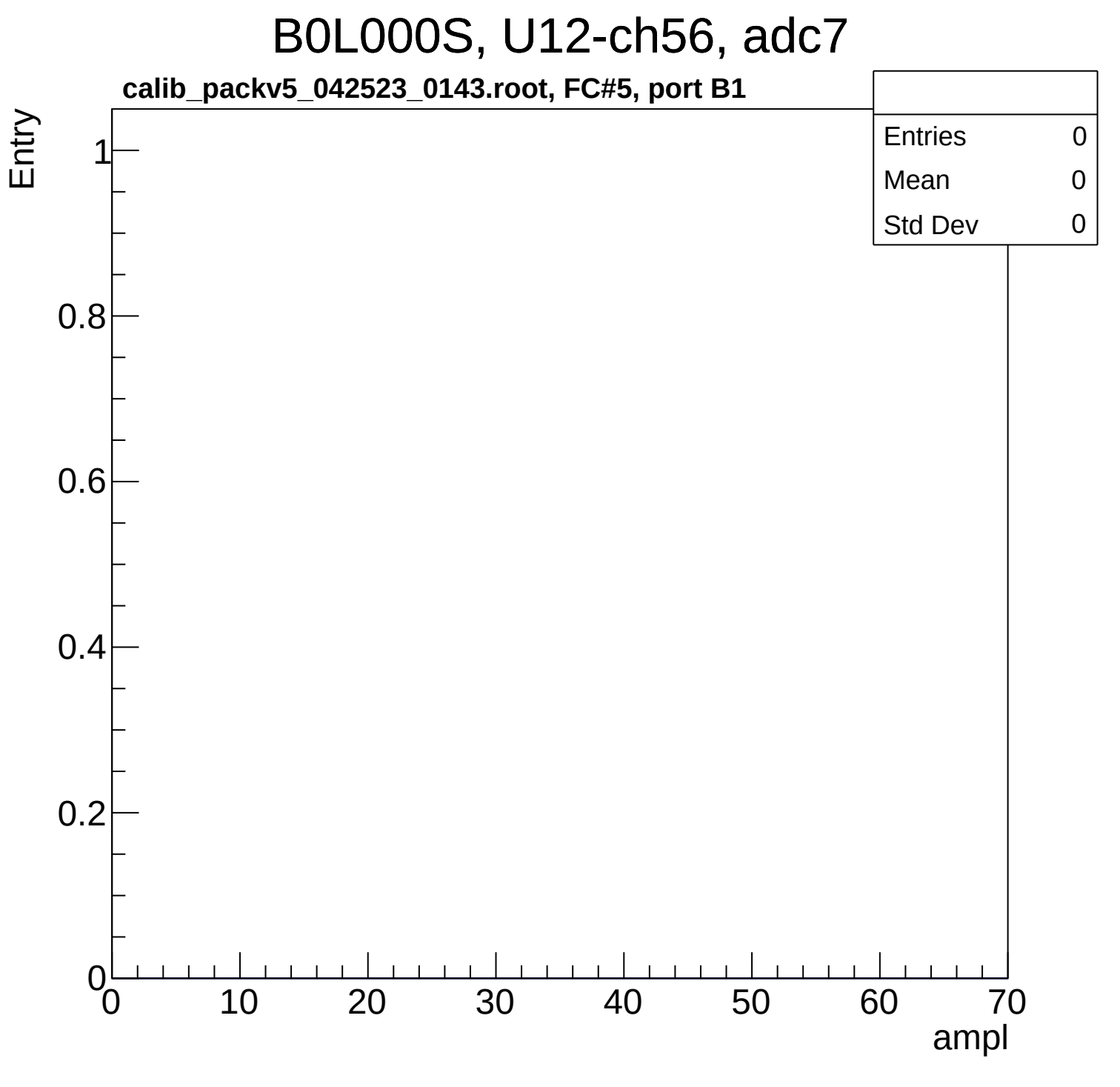
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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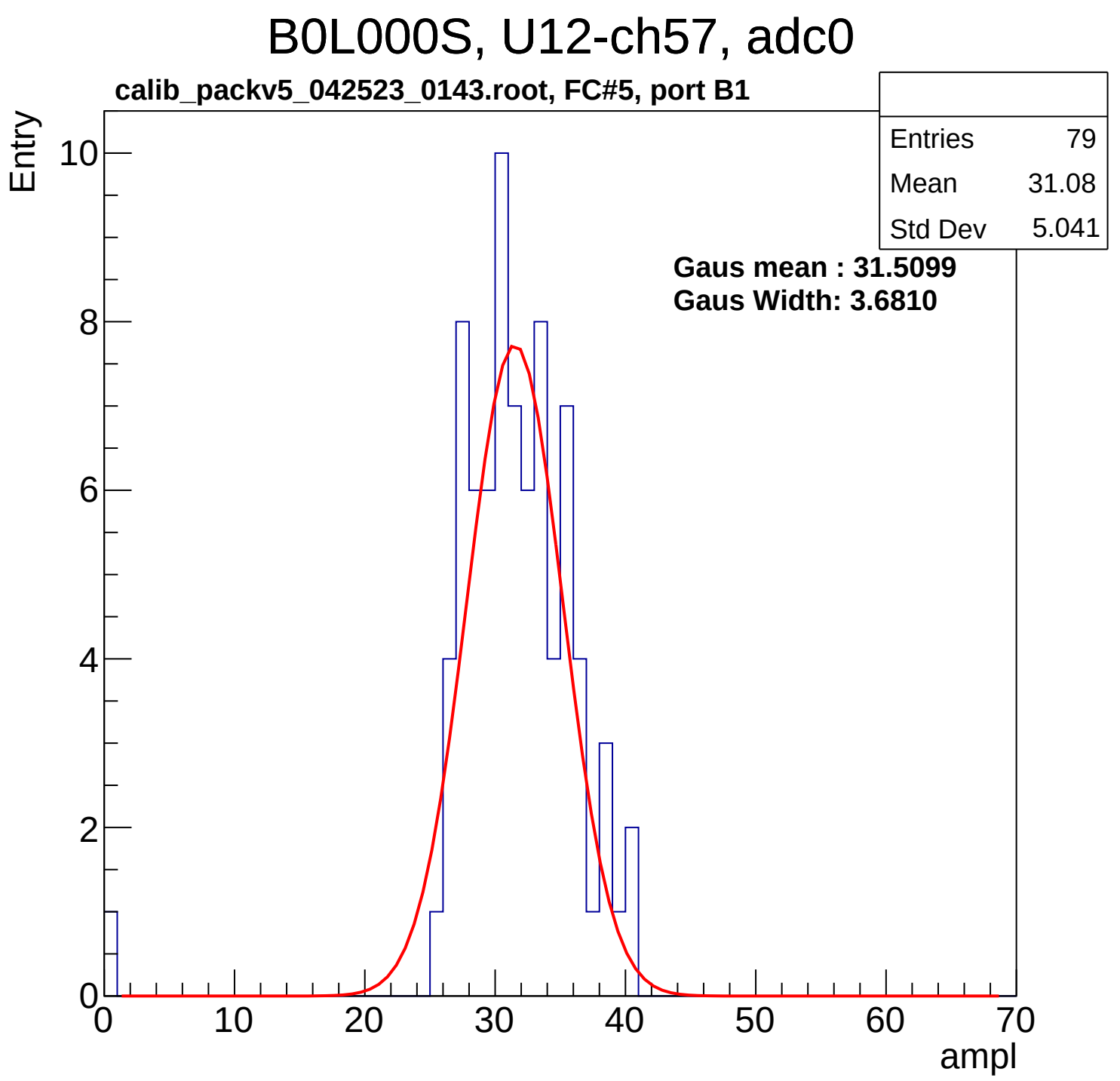
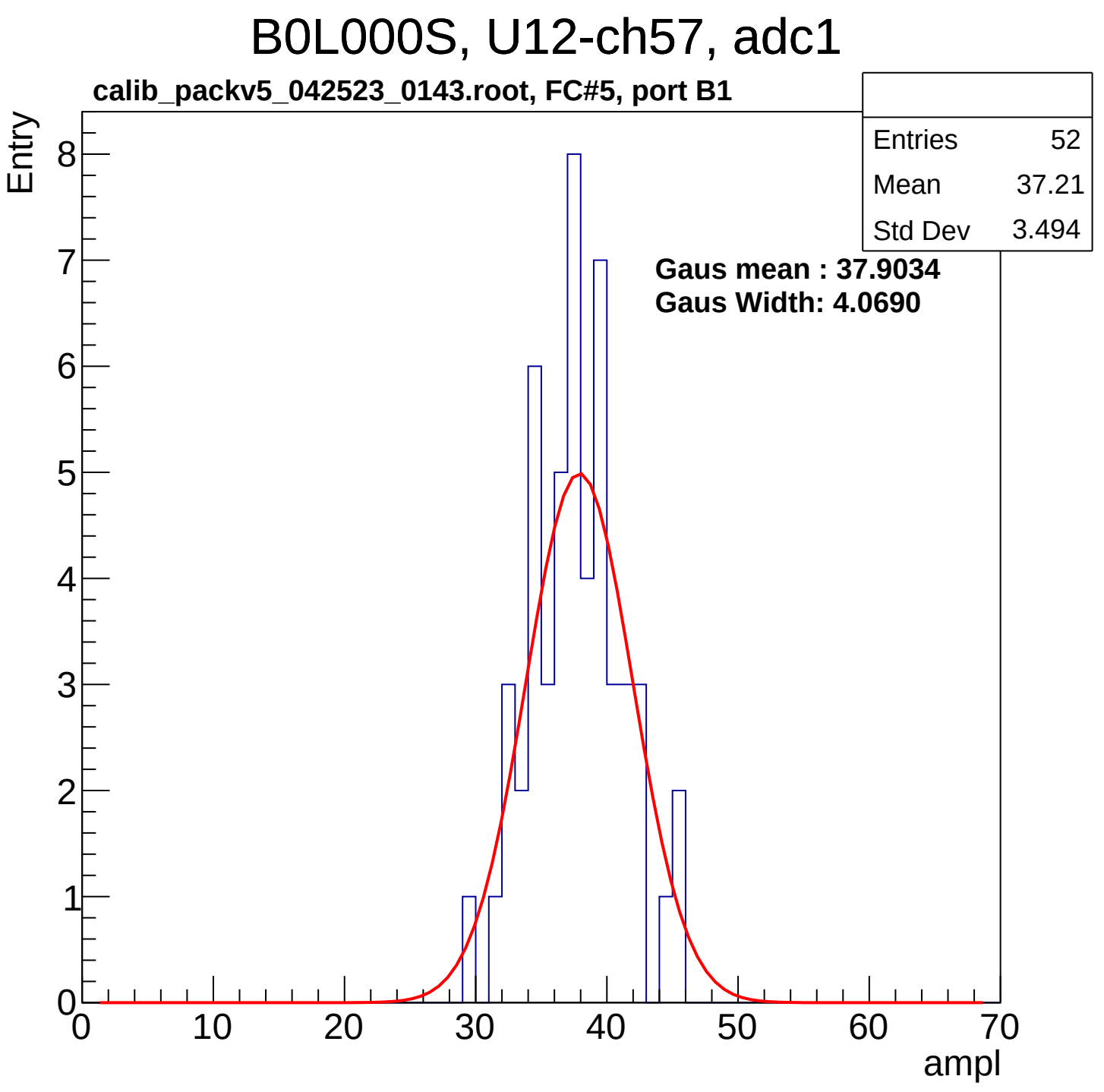
50
60
70
ampl


B0L000S, U12-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 31.08
Std Dev 5.041
Gaus mean : 31.5099
Gaus Width: 3.6810
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 37.21
Std Dev 3.494
Gaus mean : 37.9034
Gaus Width: 4.0690
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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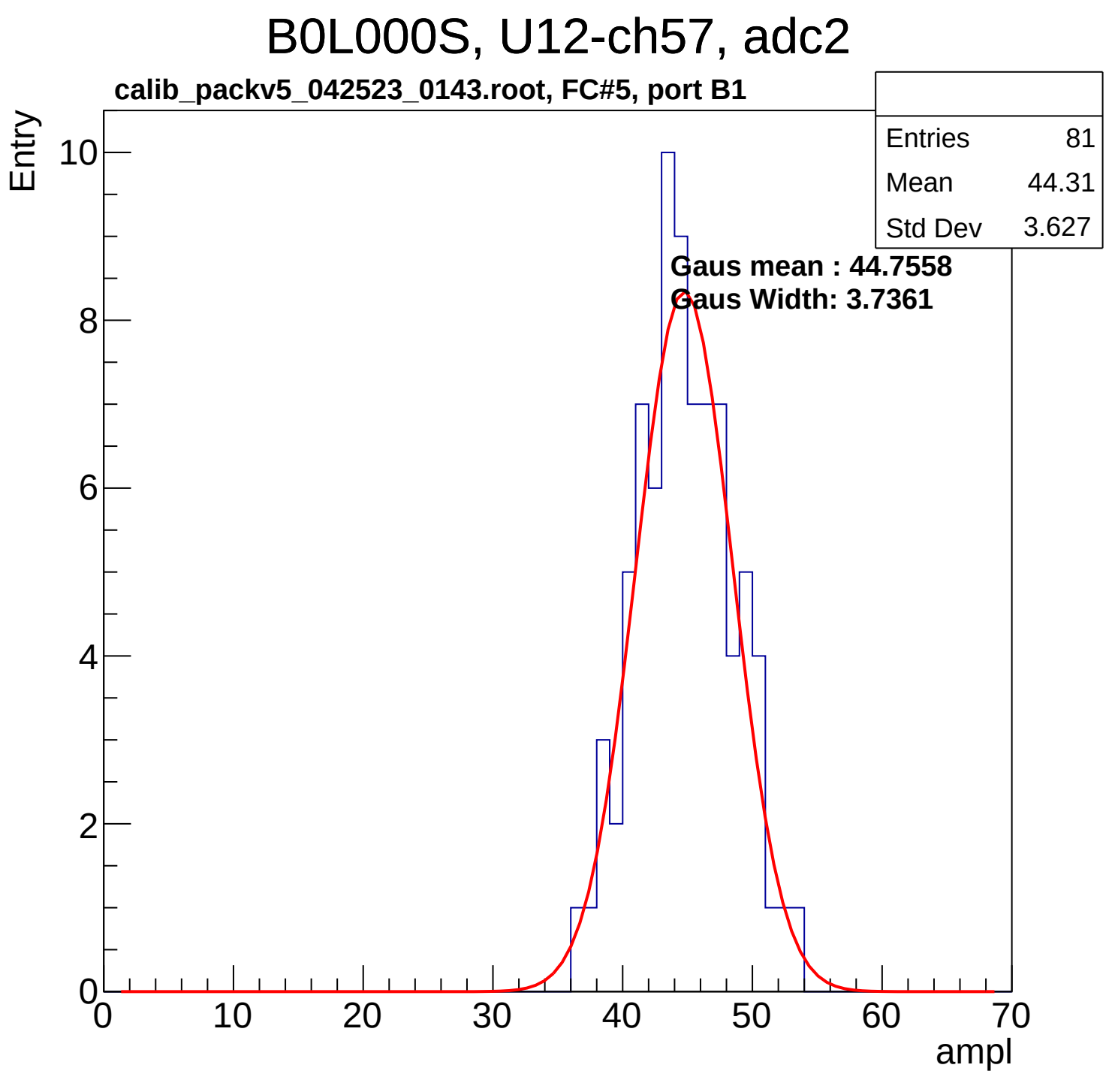
B0L000S, U12-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 44.31
Std Dev 3.627
Gaus mean : 44.7558
Gaus Width: 3.7361
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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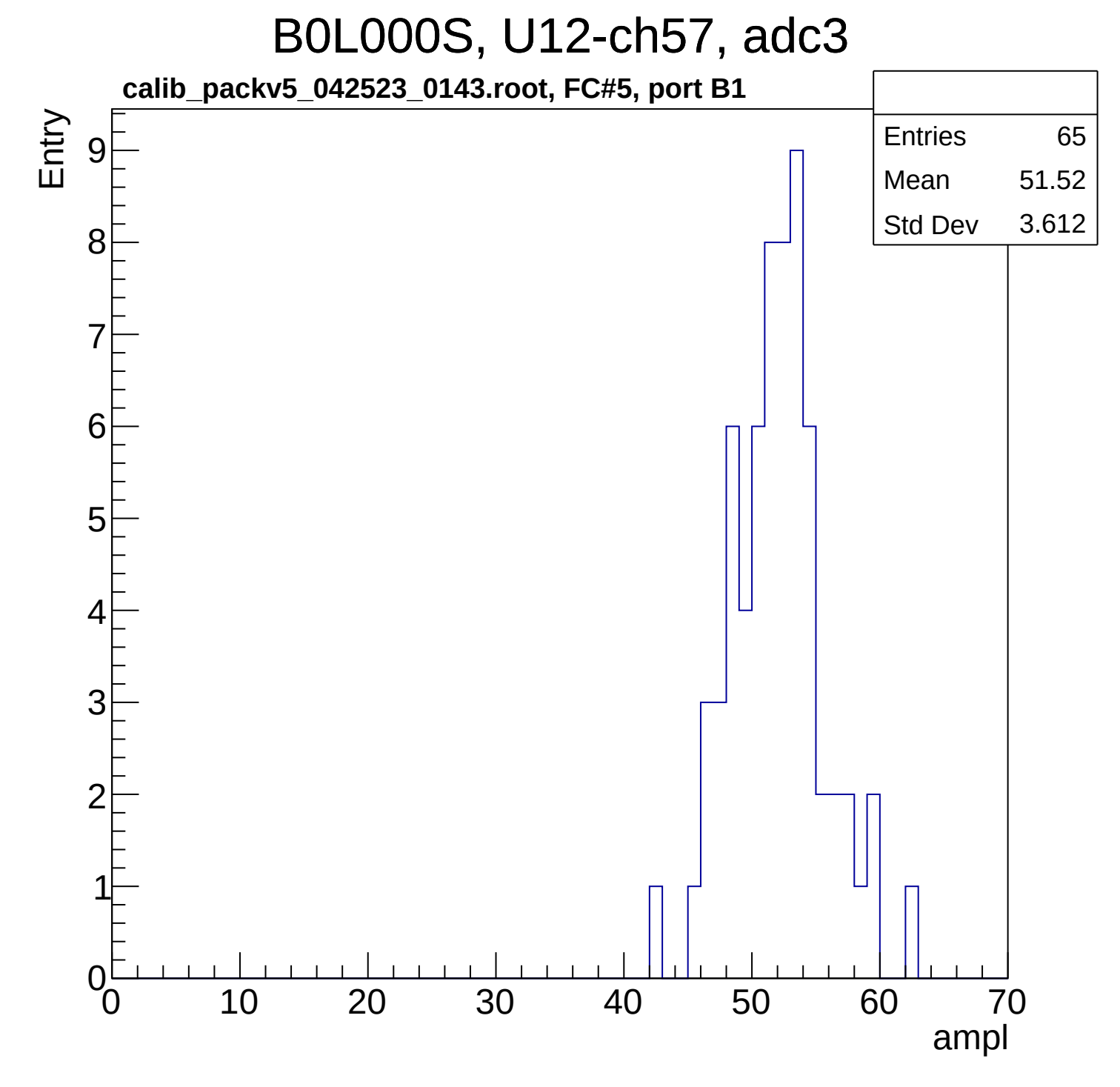
B0L000S, U12-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 51.52
Std Dev 3.612
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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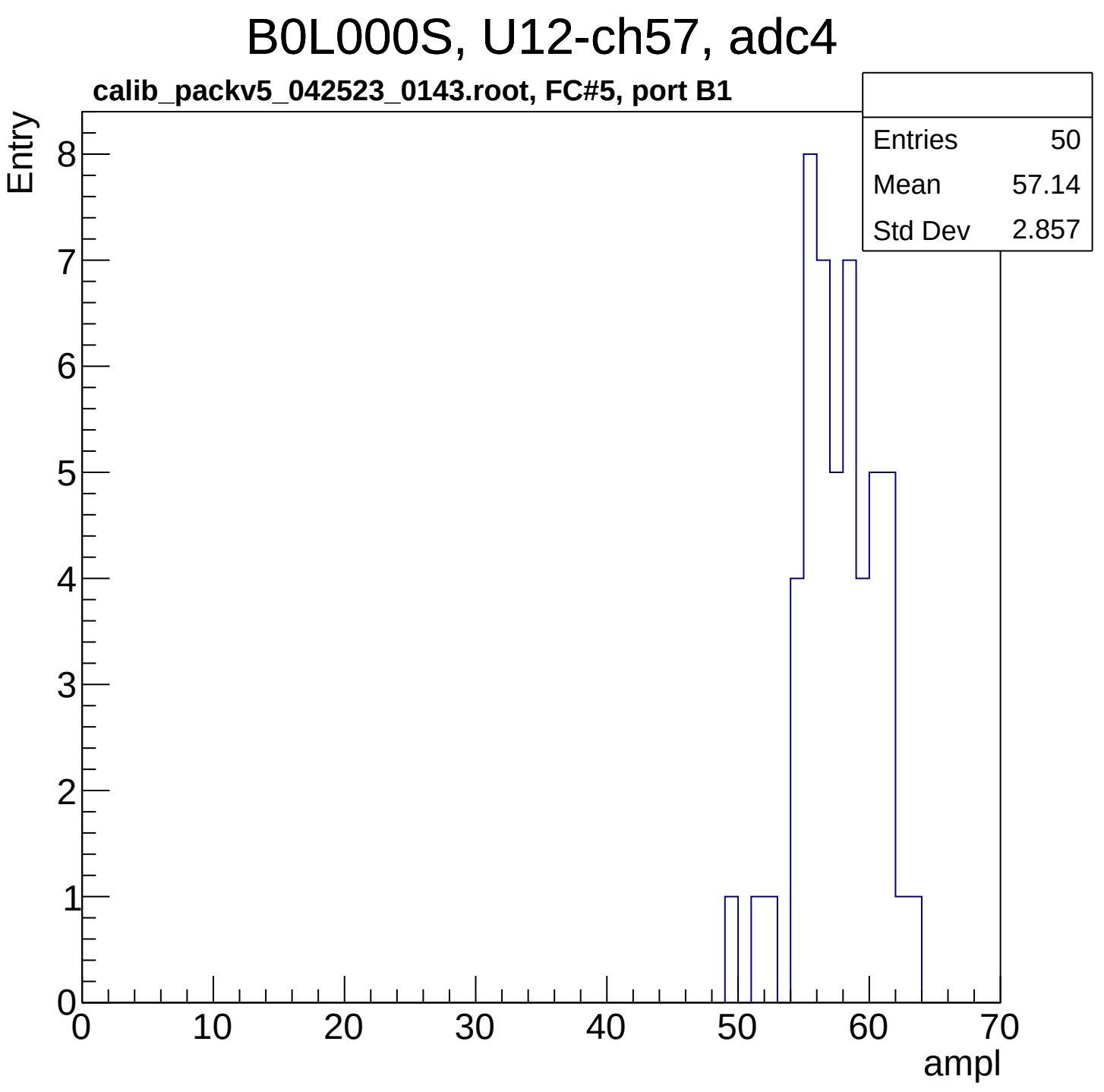
B0L000S, U12-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.14
Std Dev 2.857
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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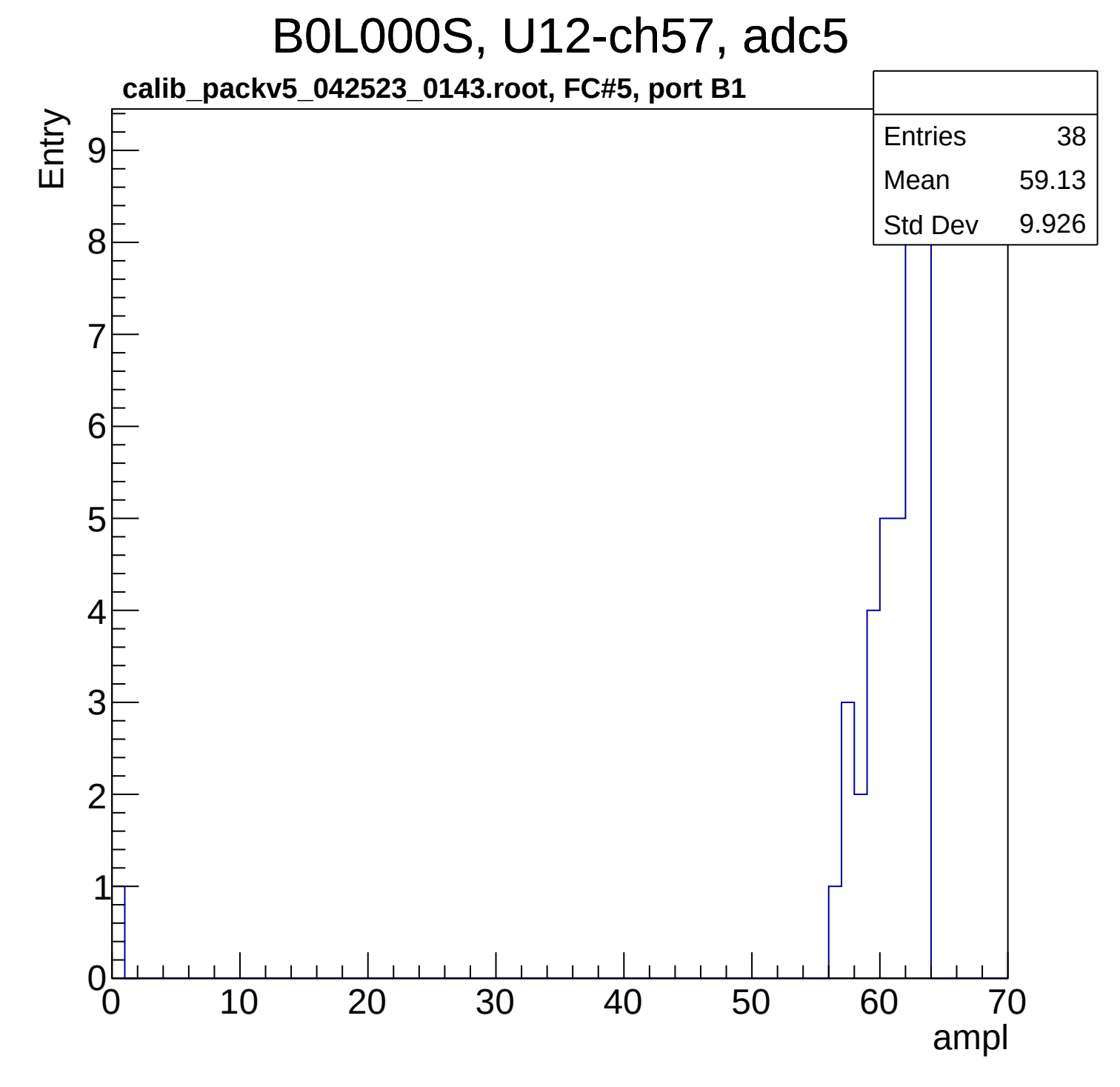
B0L000S, U12-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.13
Std Dev 9.926
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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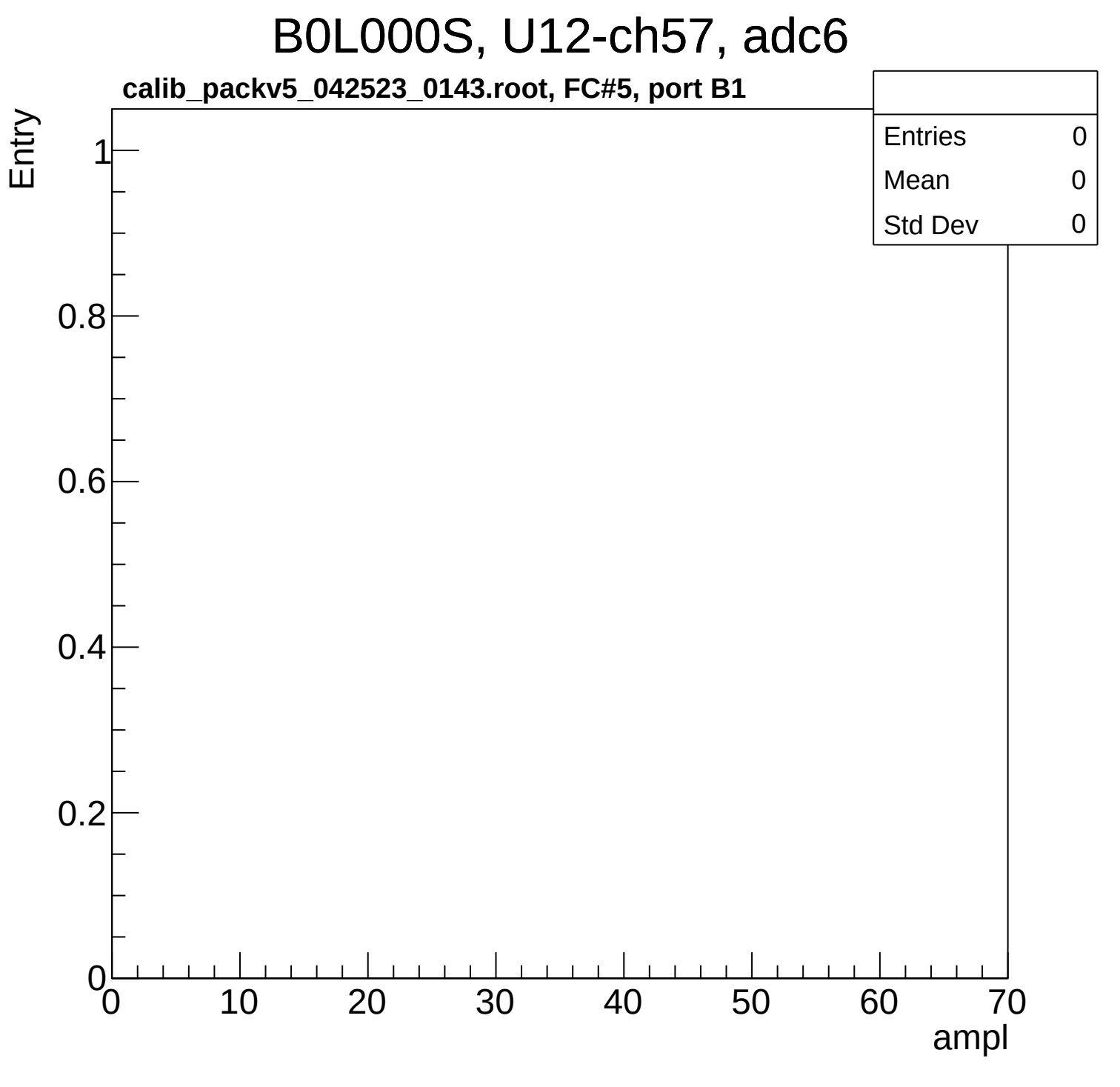
B0L000S, U12-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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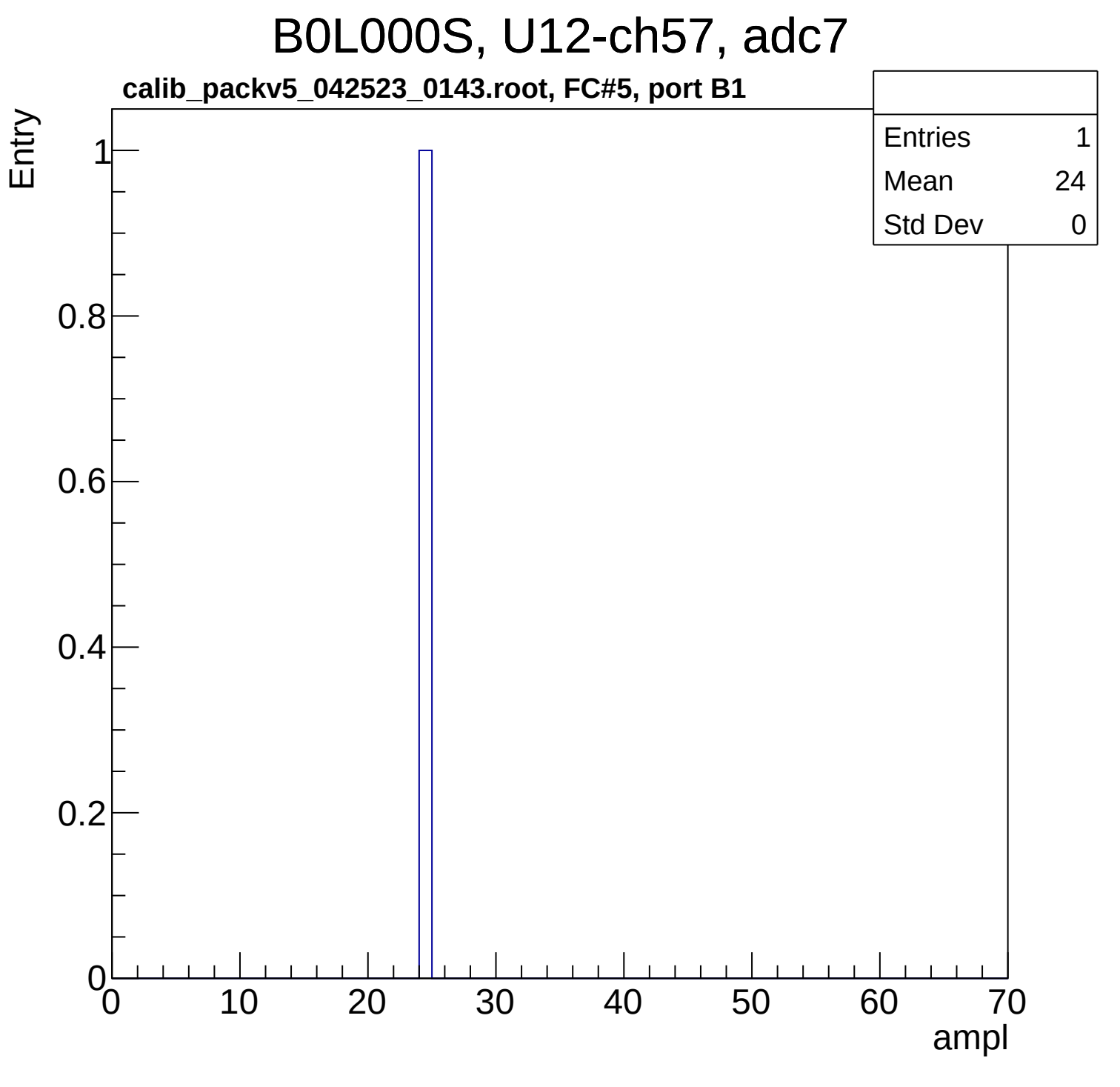
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
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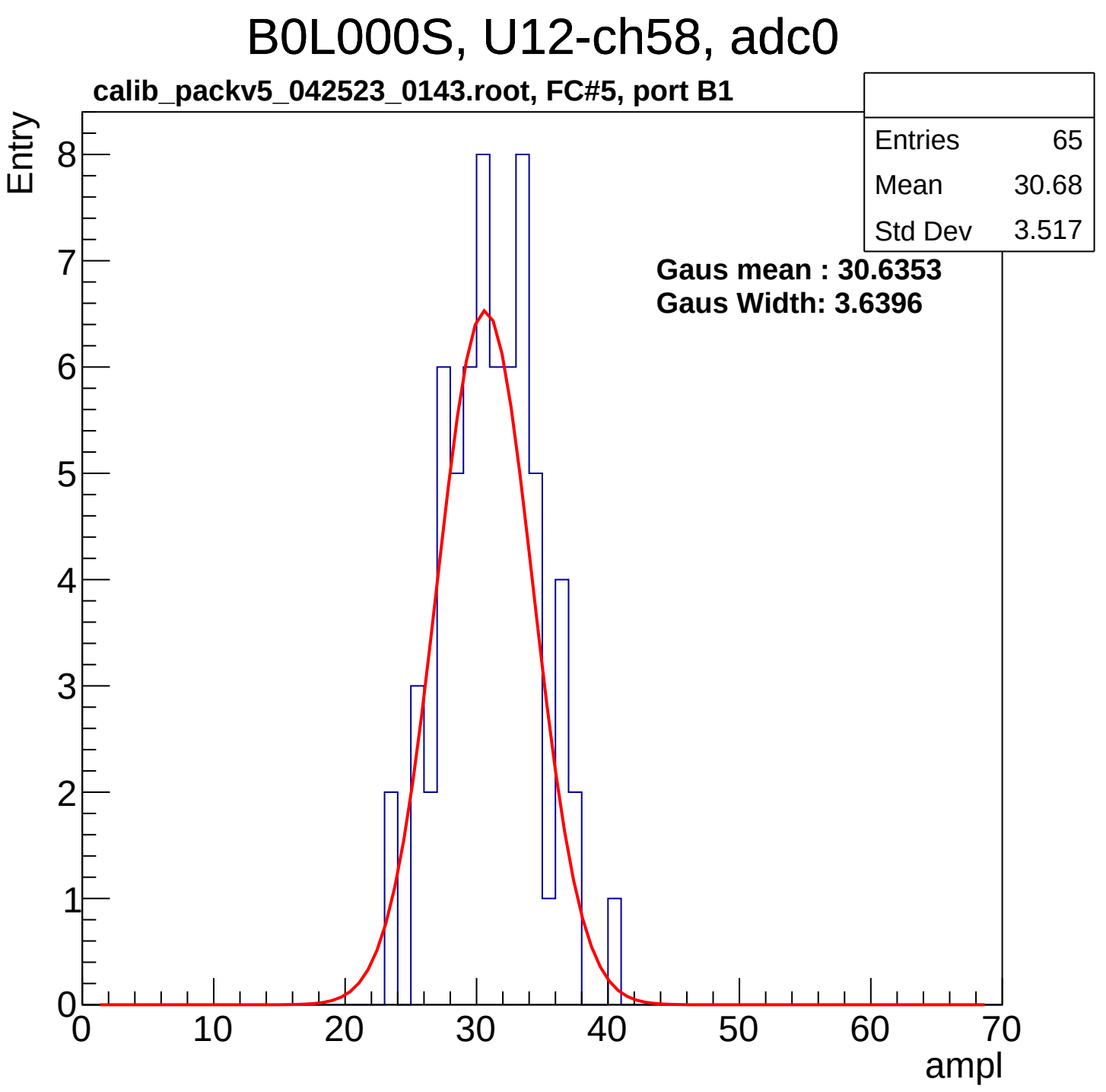
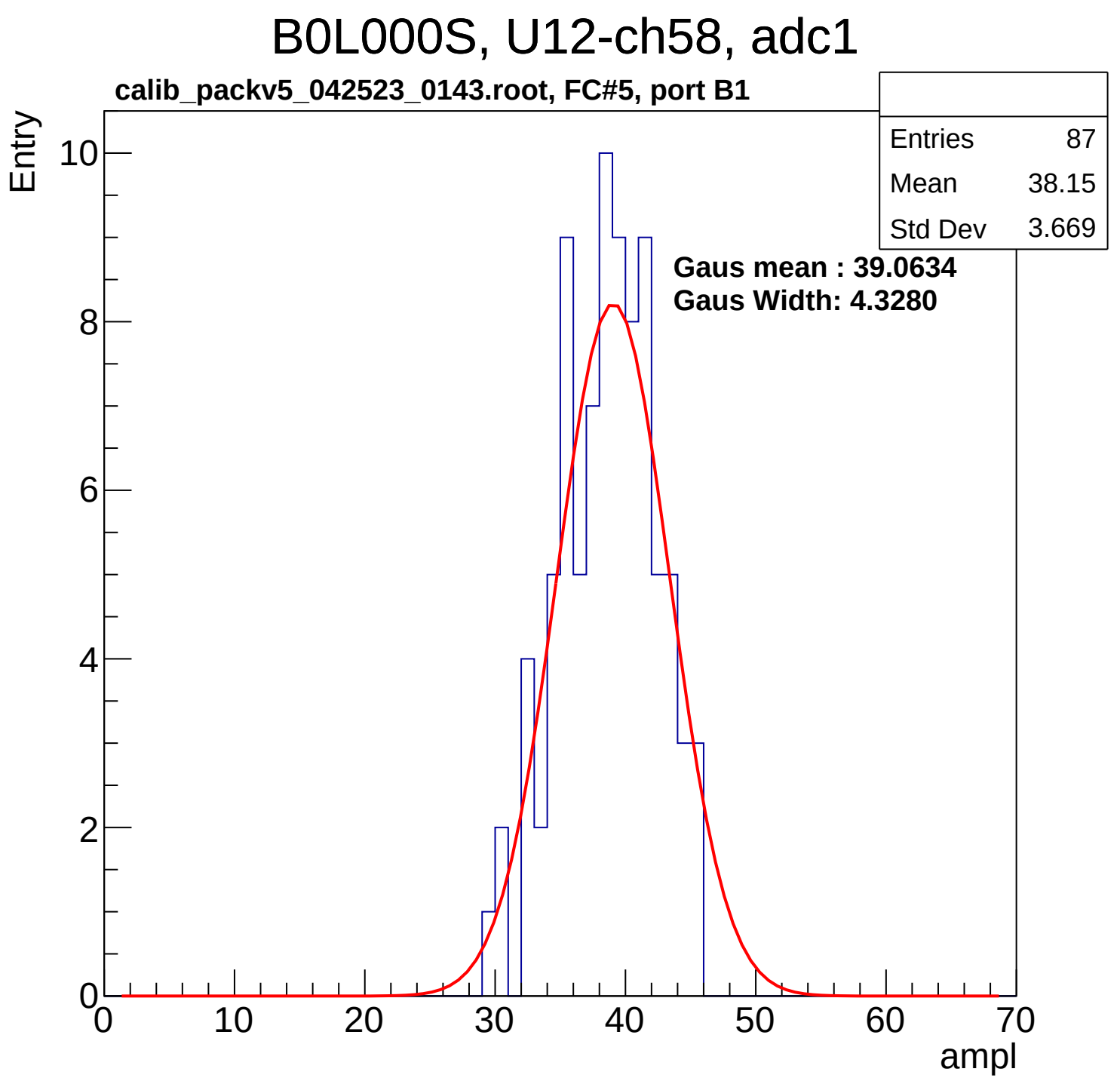
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.68
Std Dev 3.517
Gaus mean : 30.6353
Gaus Width: 3.6396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 38.15
Std Dev 3.669
Gaus mean : 39.0634
Gaus Width: 4.3280
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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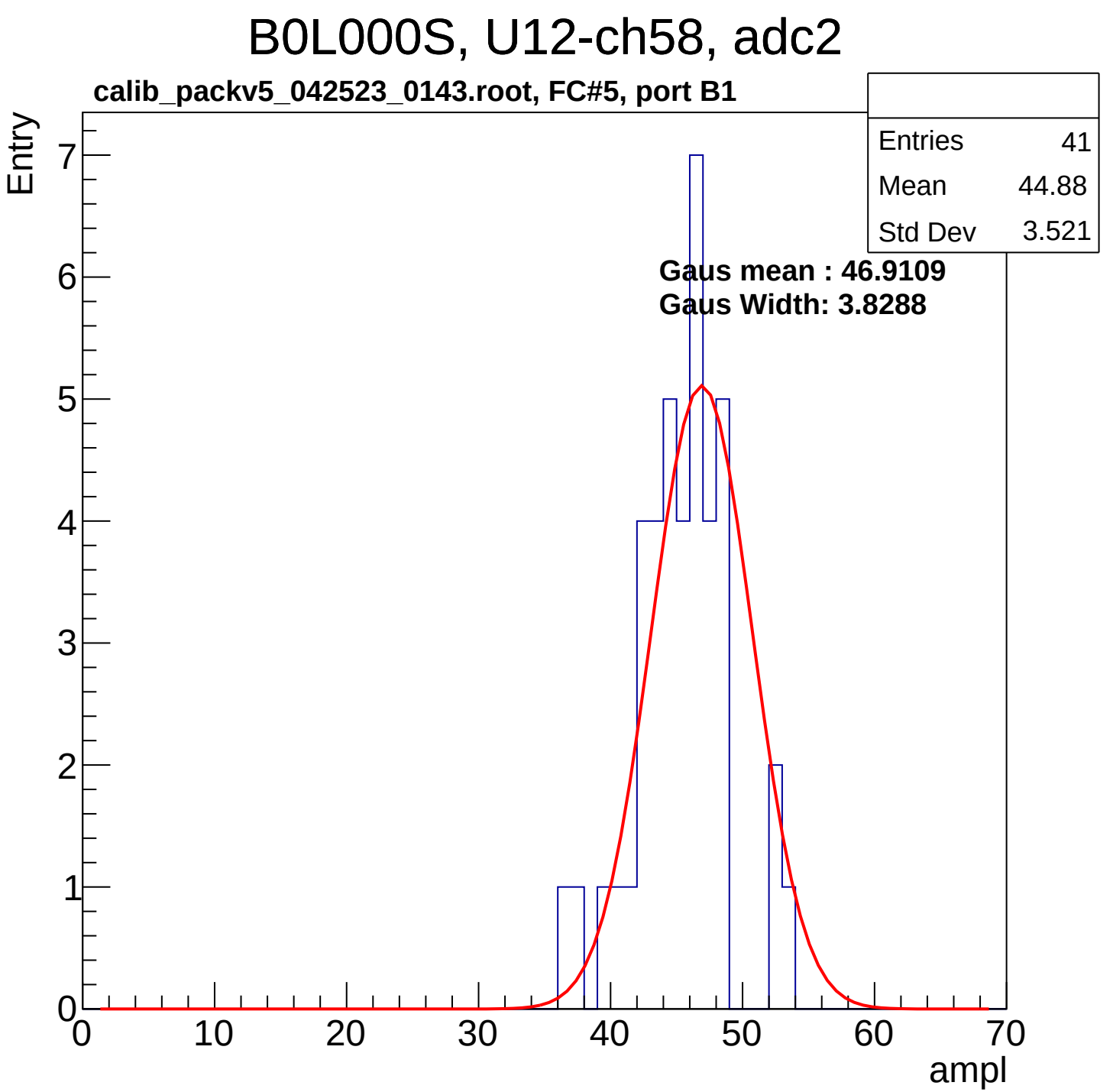
B0L000S, U12-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 44.88
Std Dev 3.521
Gaus mean : 46.9109
Gaus Width: 3.8288
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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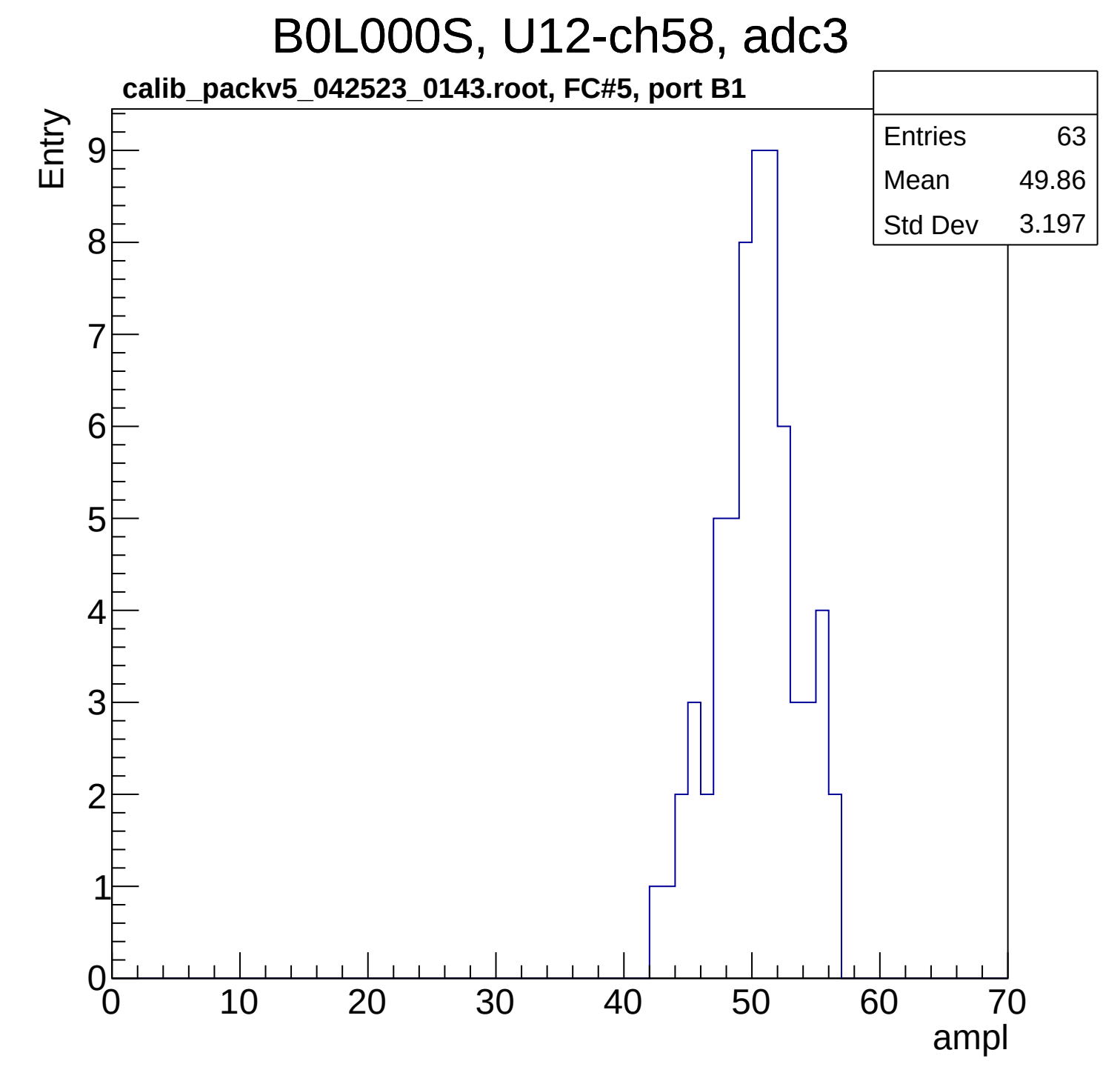
B0L000S, U12-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.86
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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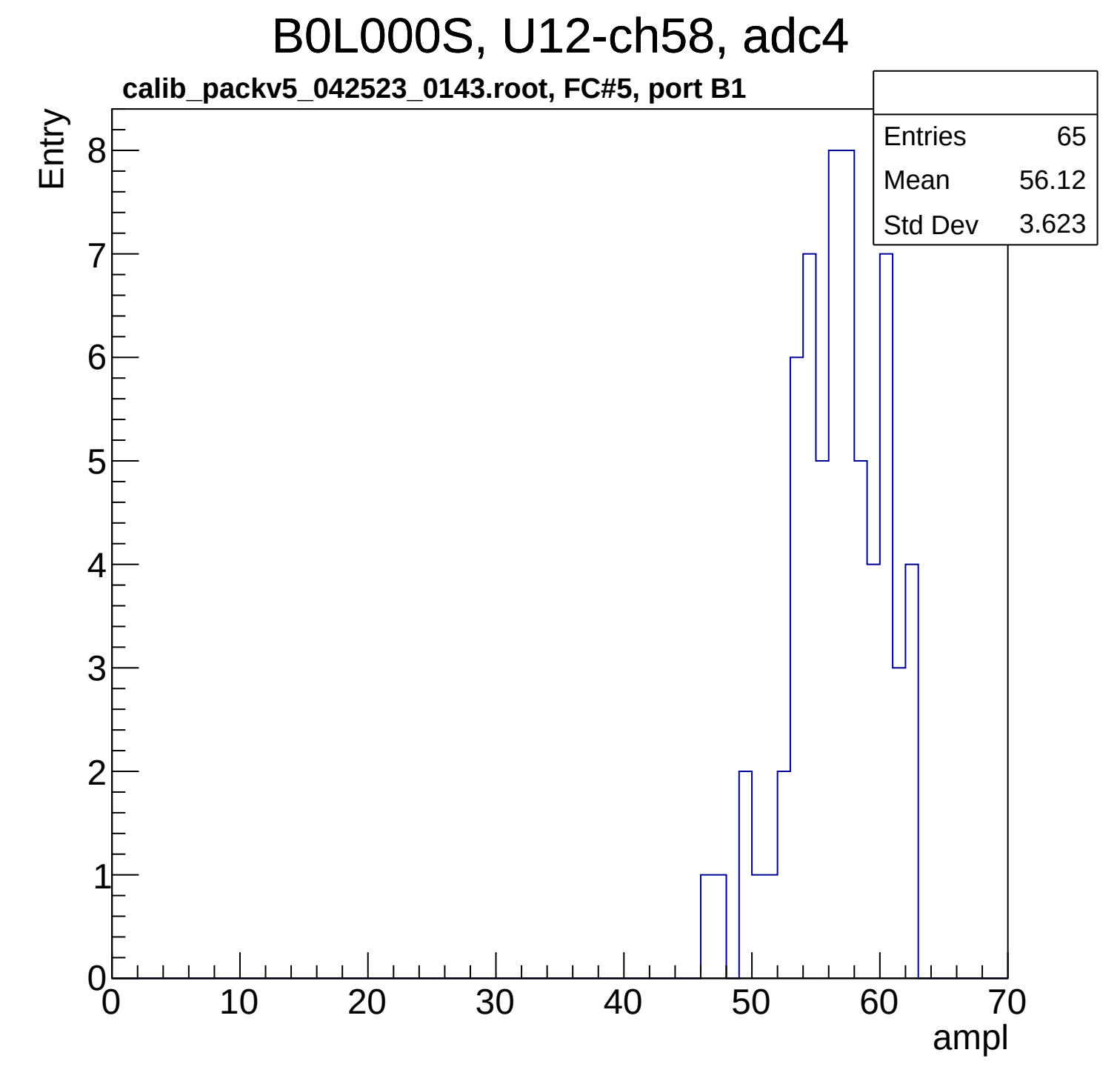
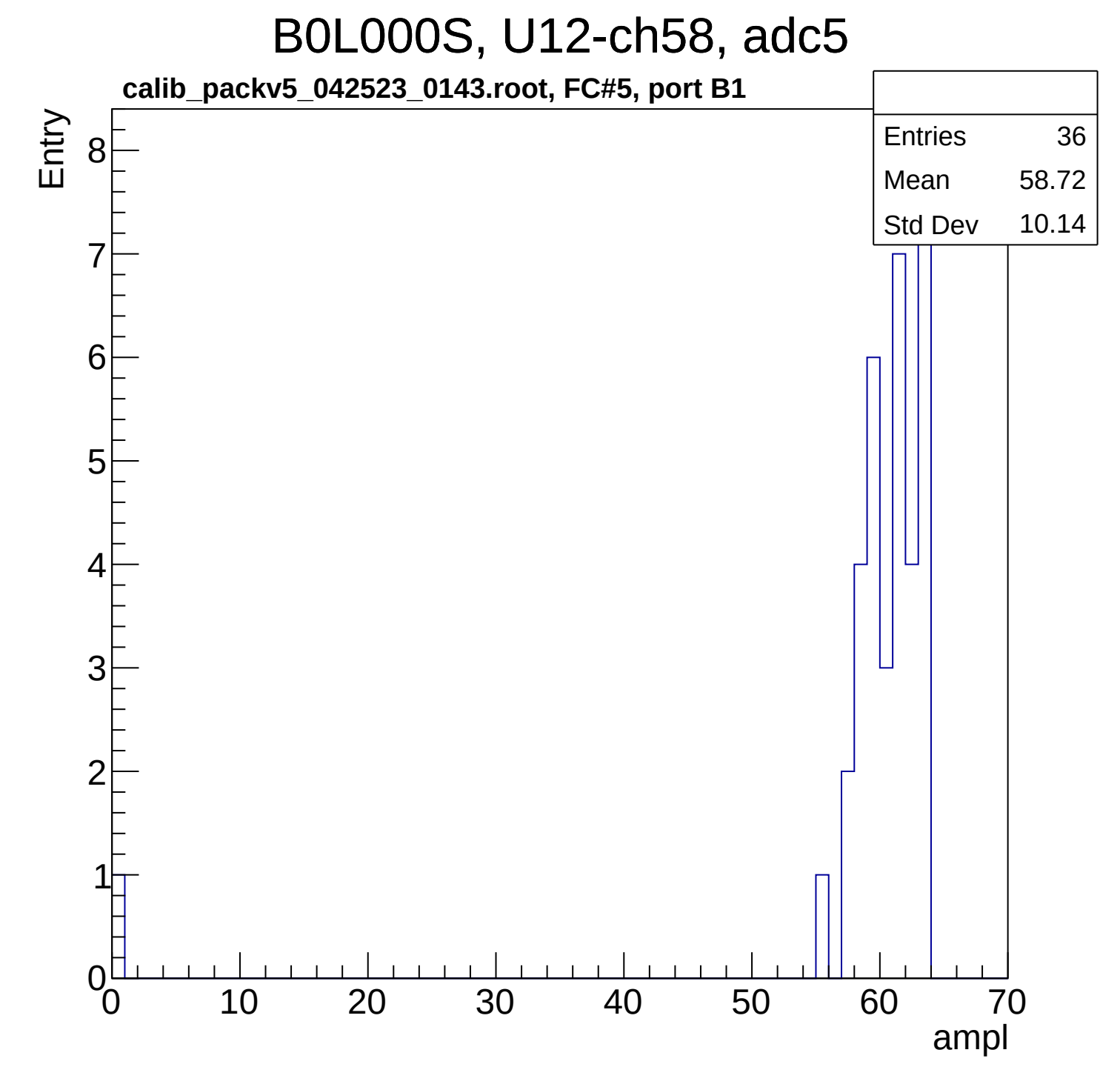
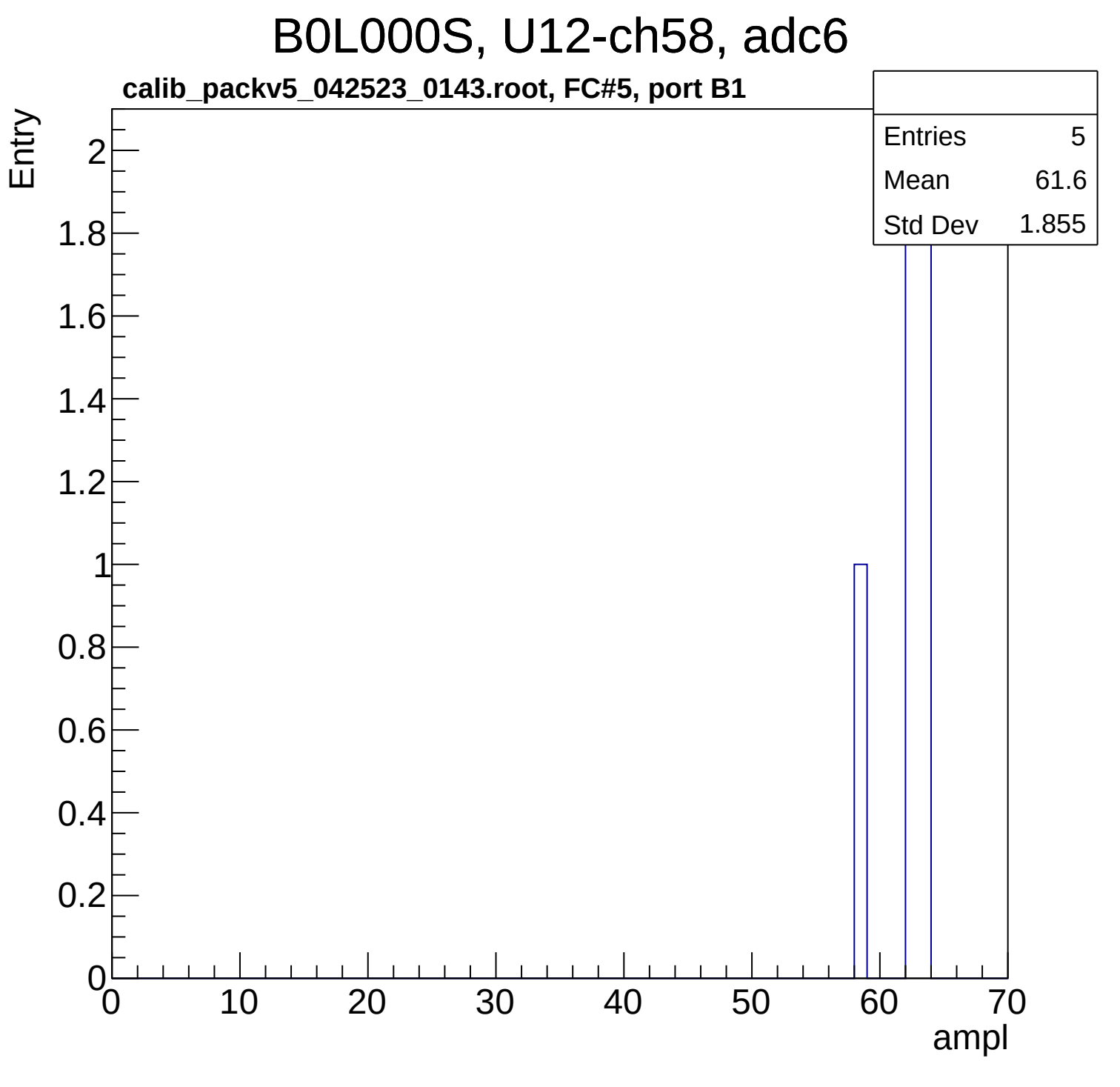

B0L000S, U12-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 56.12
Std Dev 3.623
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.72
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.6
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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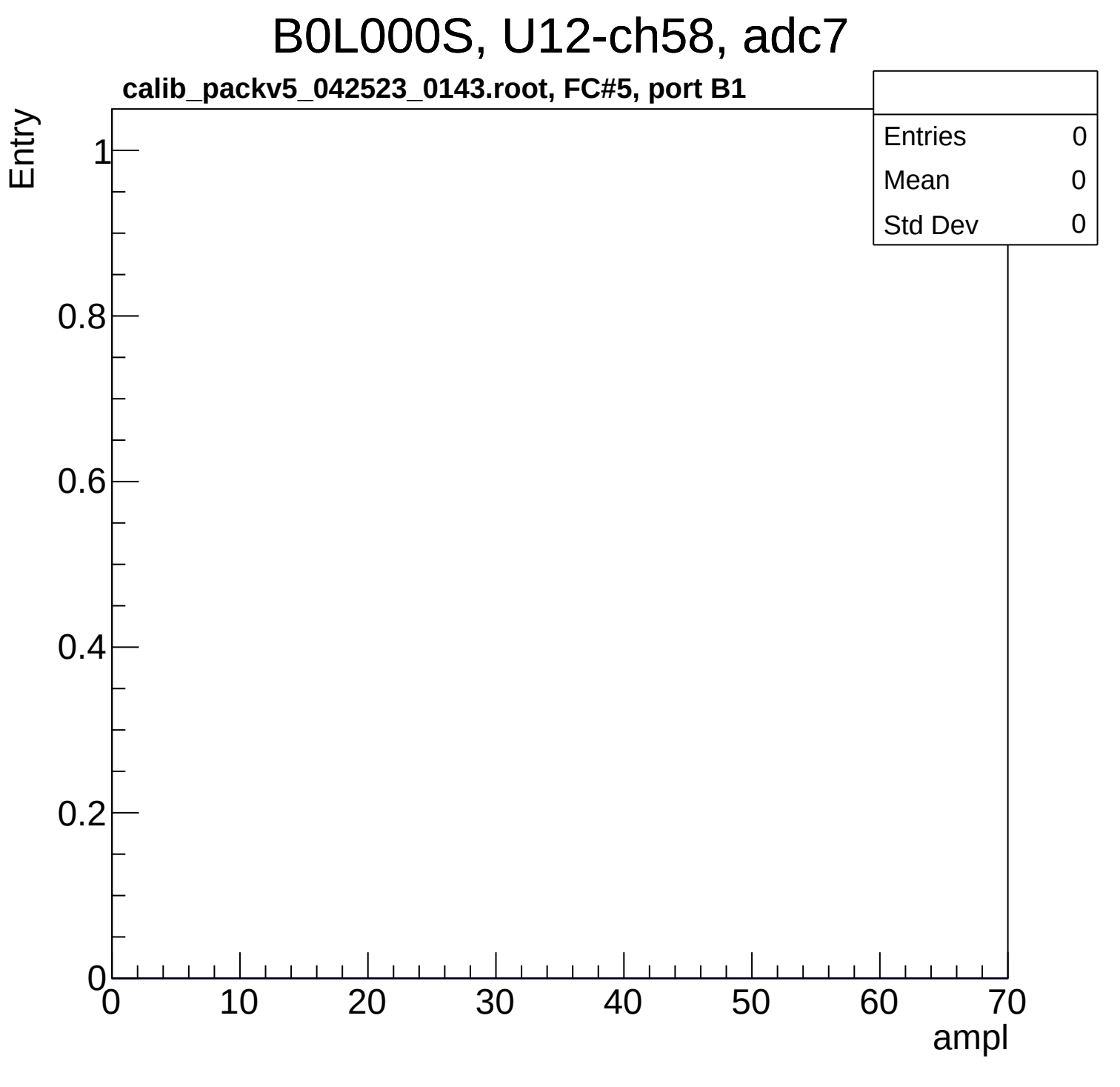
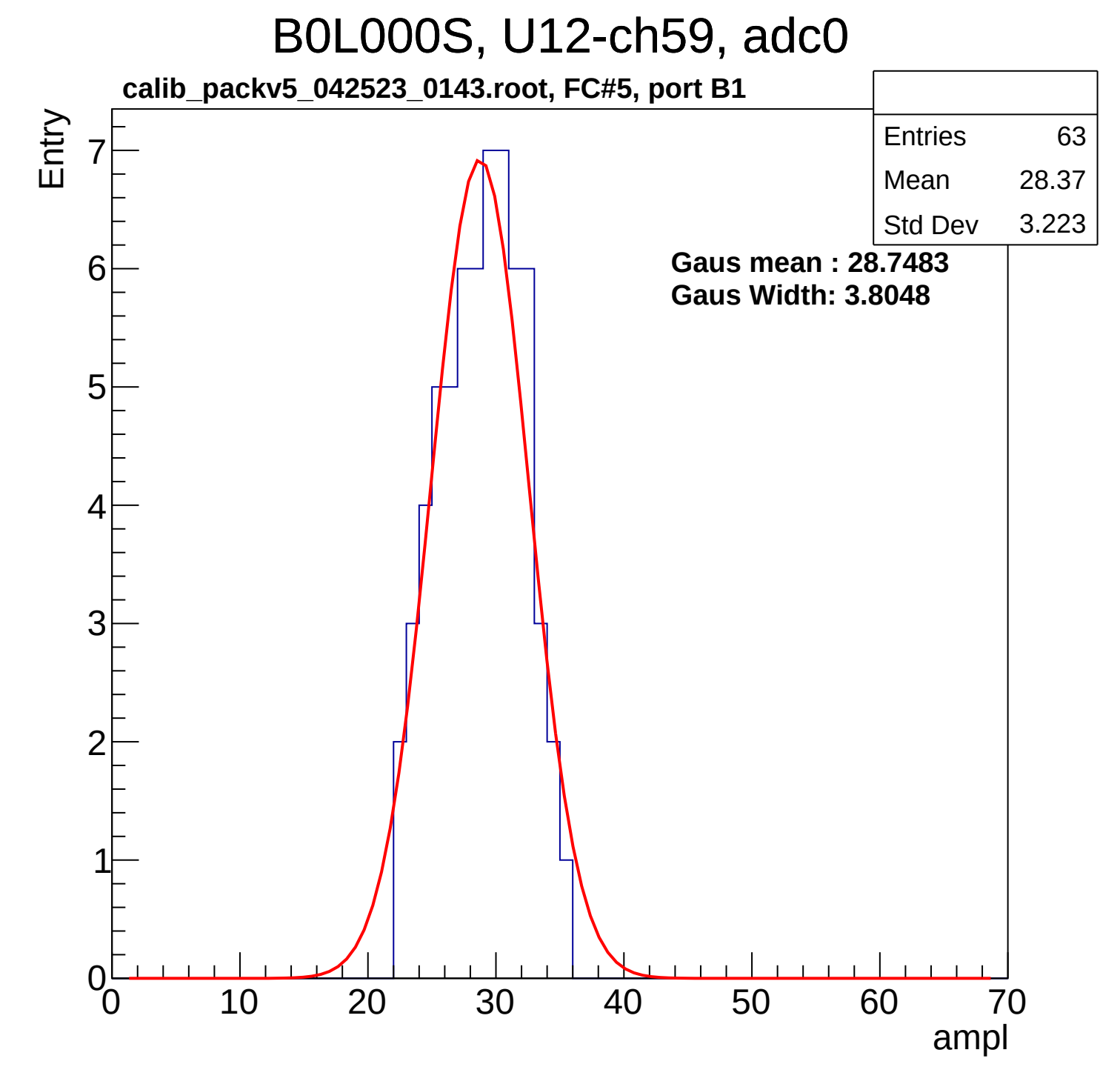
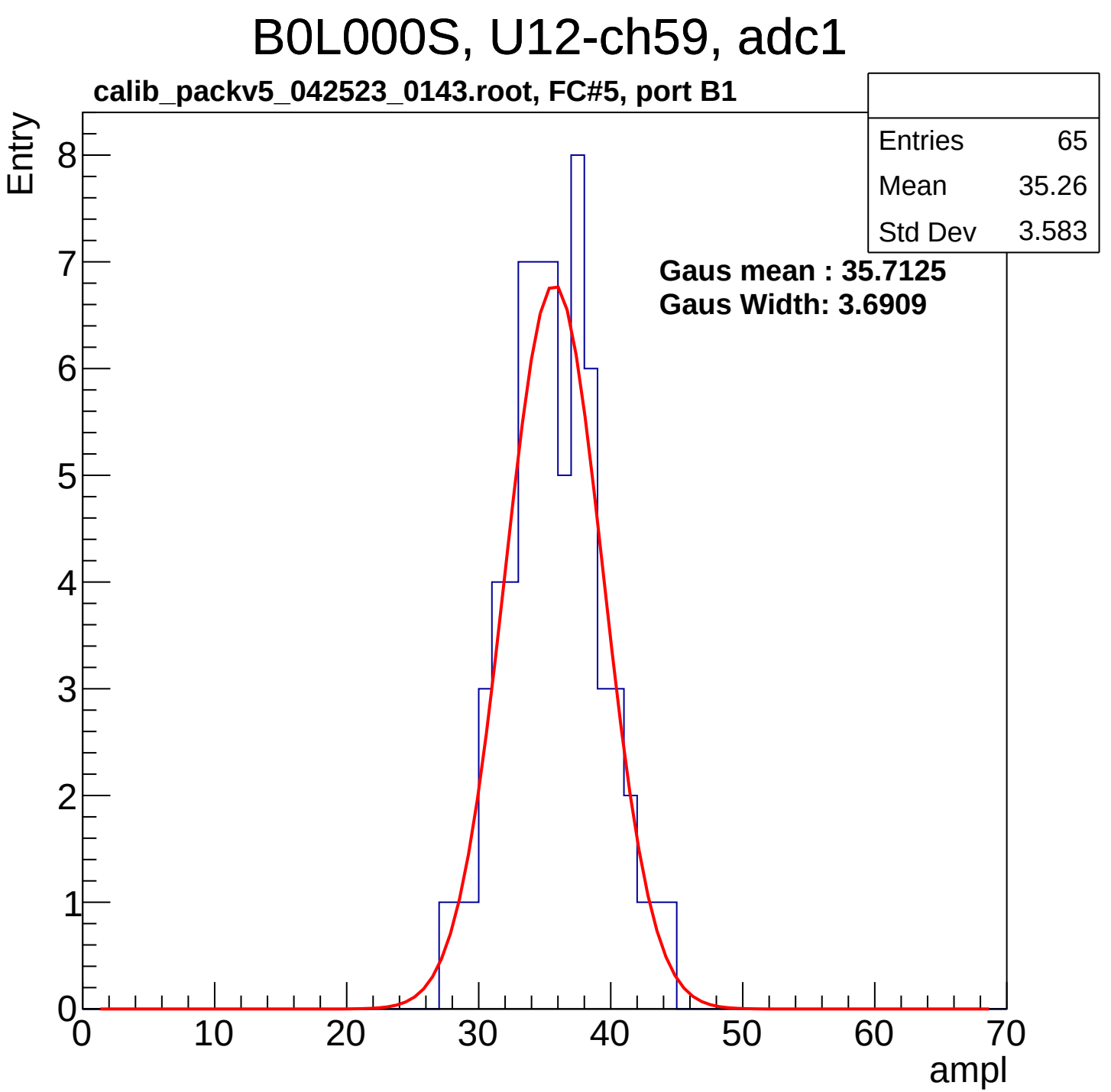
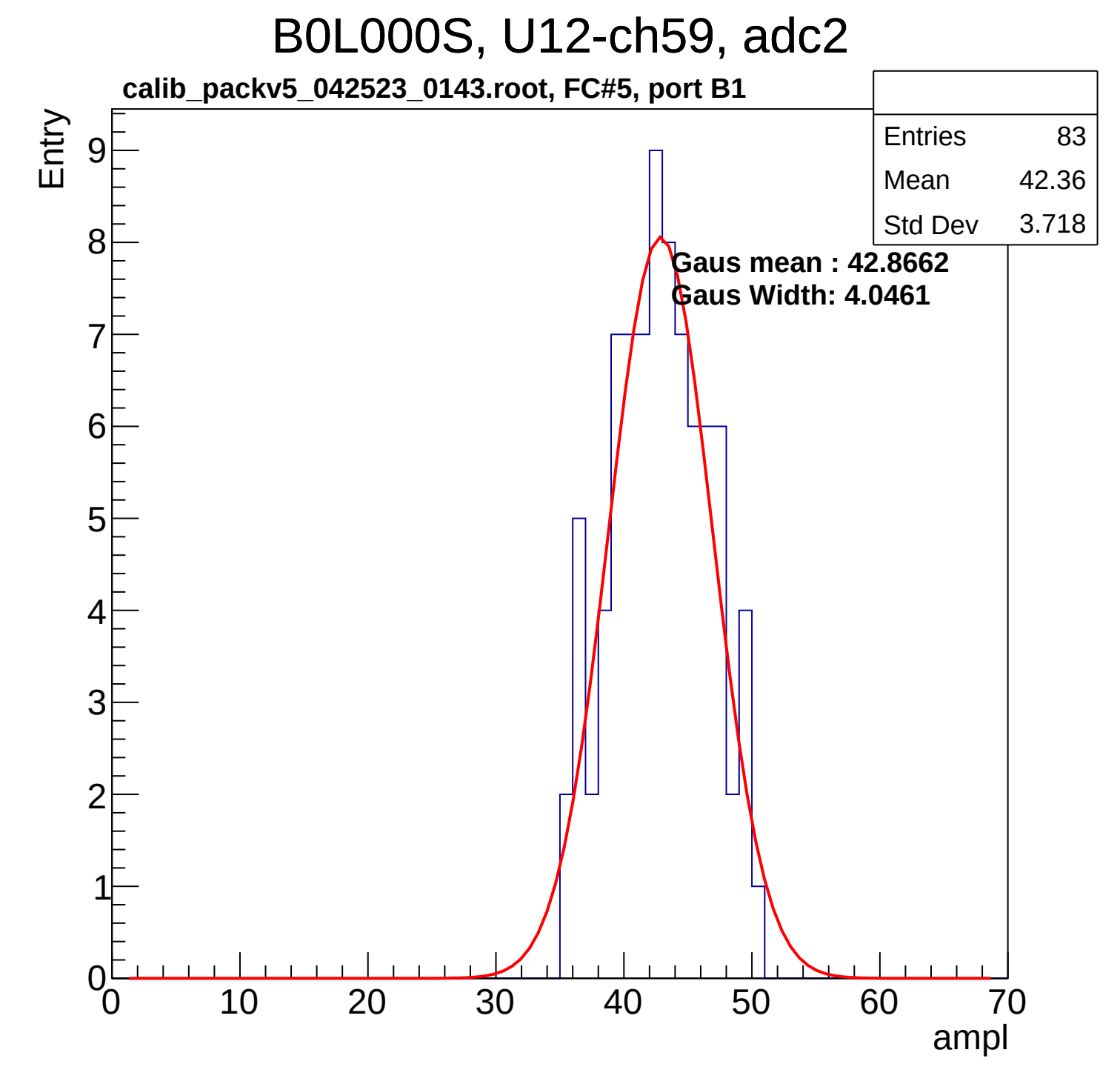
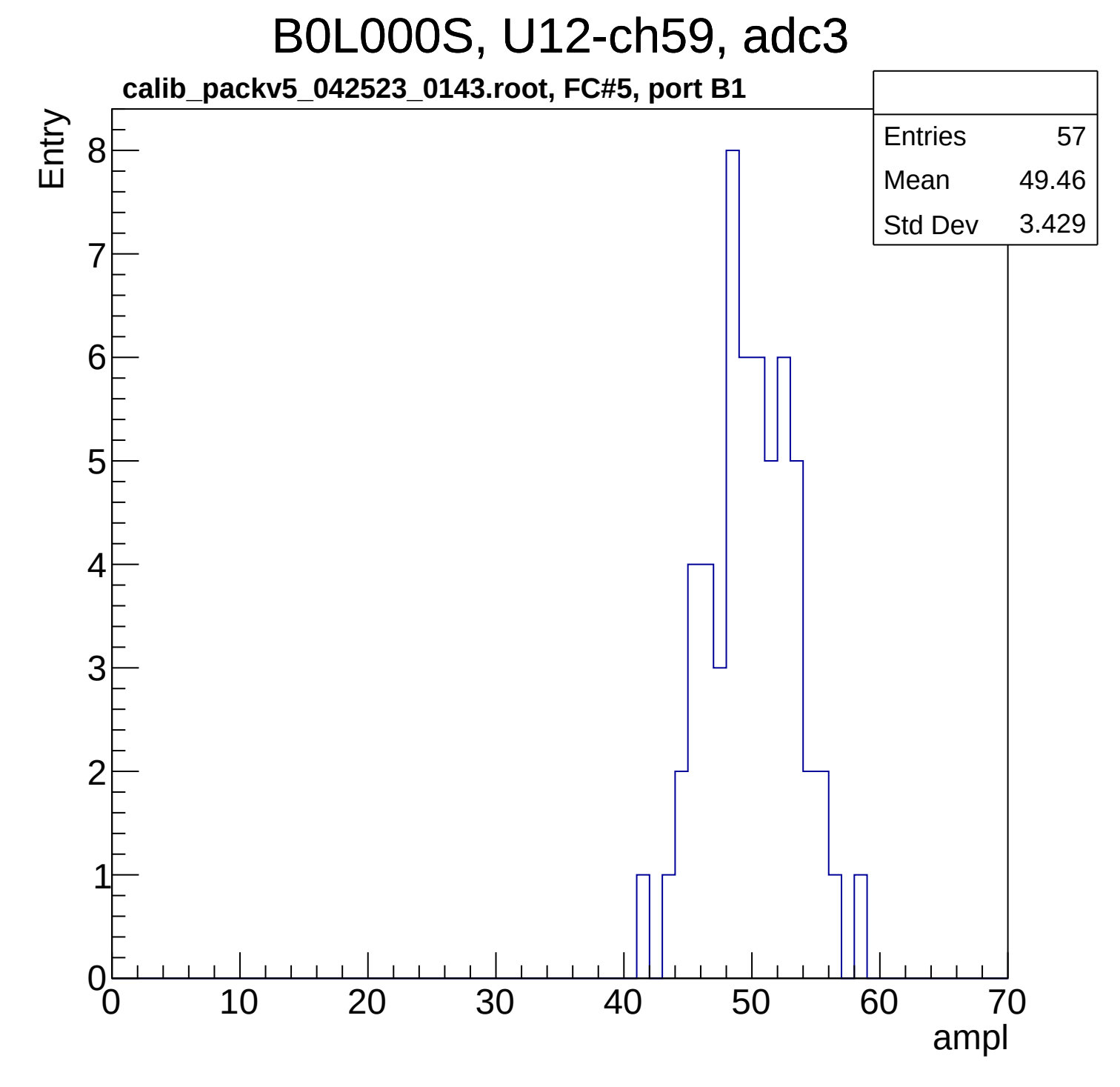

B0L000S, U12-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.37
Std Dev 3.223
Gaus mean : 28.7483
Gaus Width: 3.8048
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 35.26
Std Dev 3.583
Gaus mean : 35.7125
Gaus Width: 3.6909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 42.36
Std Dev 3.718
Gaus mean : 42.8662
Gaus Width: 4.0461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.46
Std Dev 3.429
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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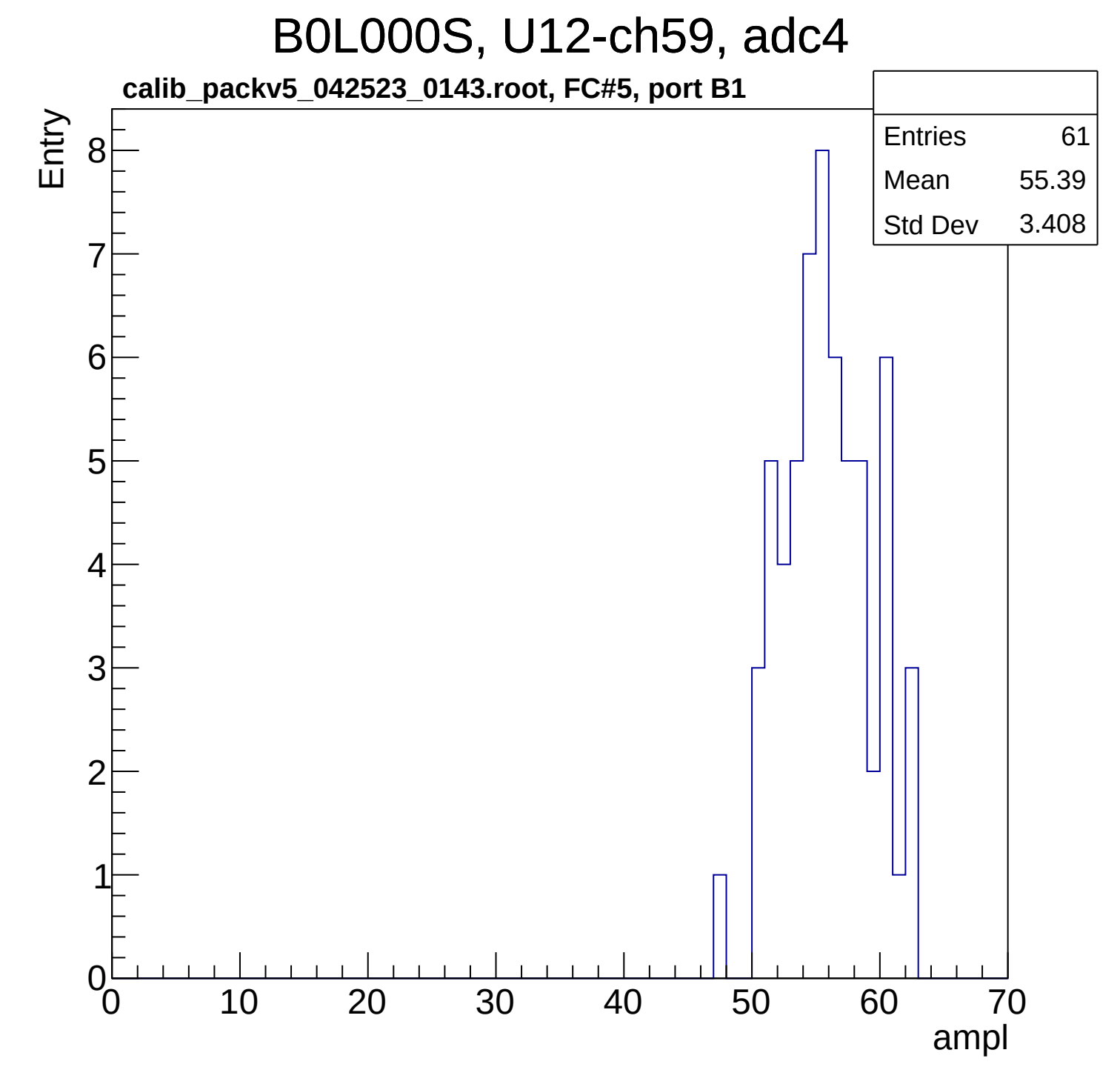
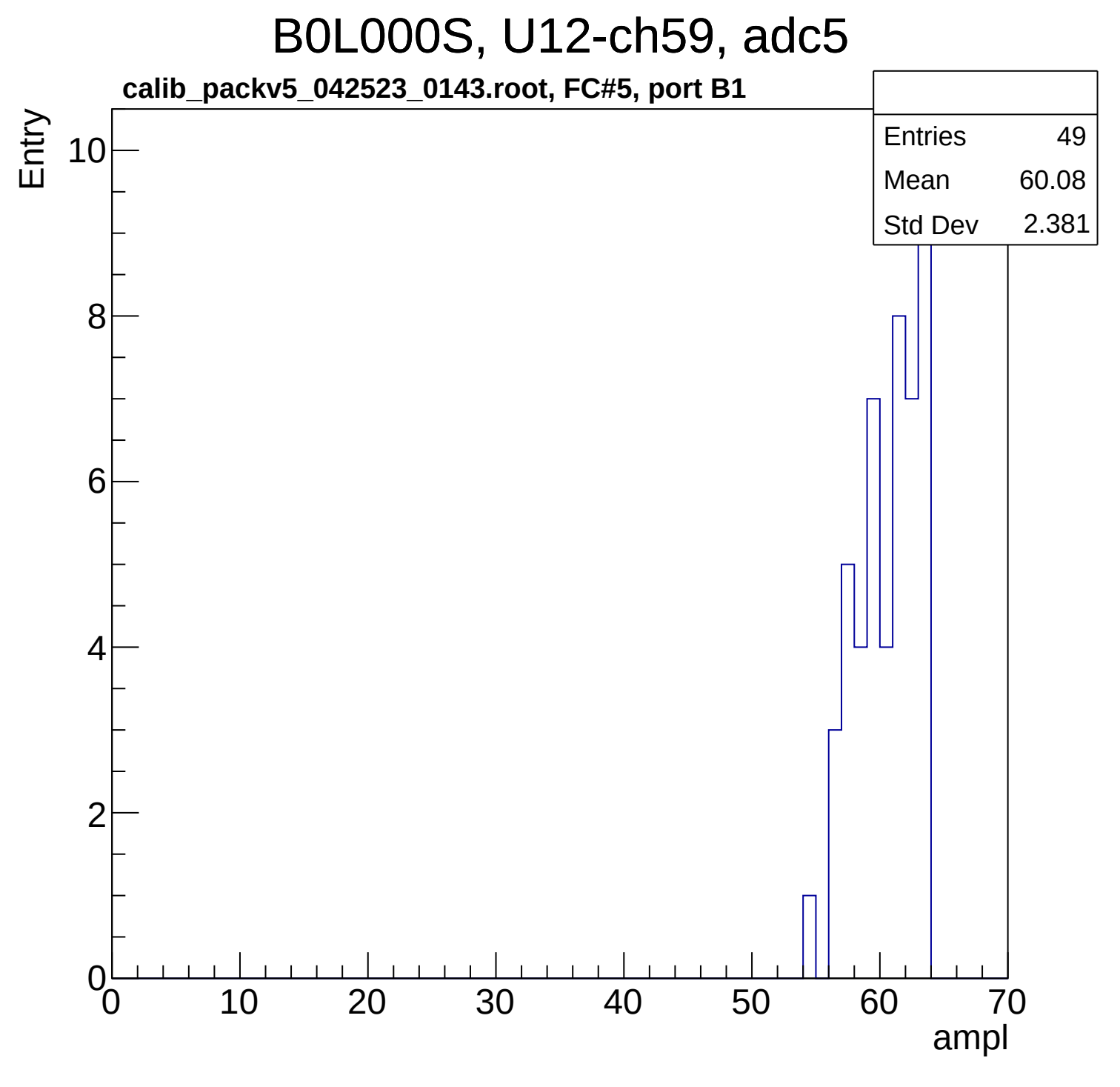
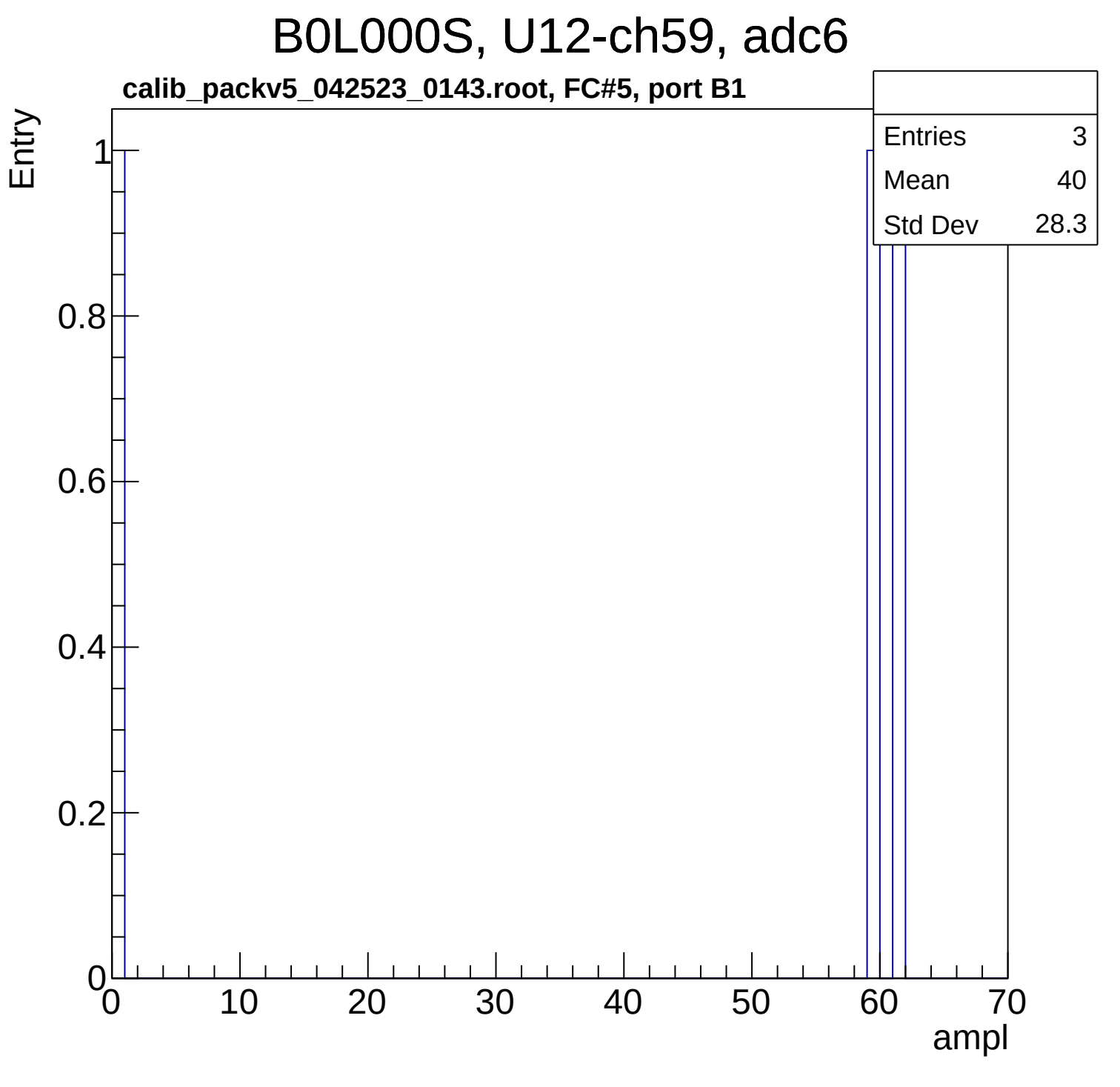
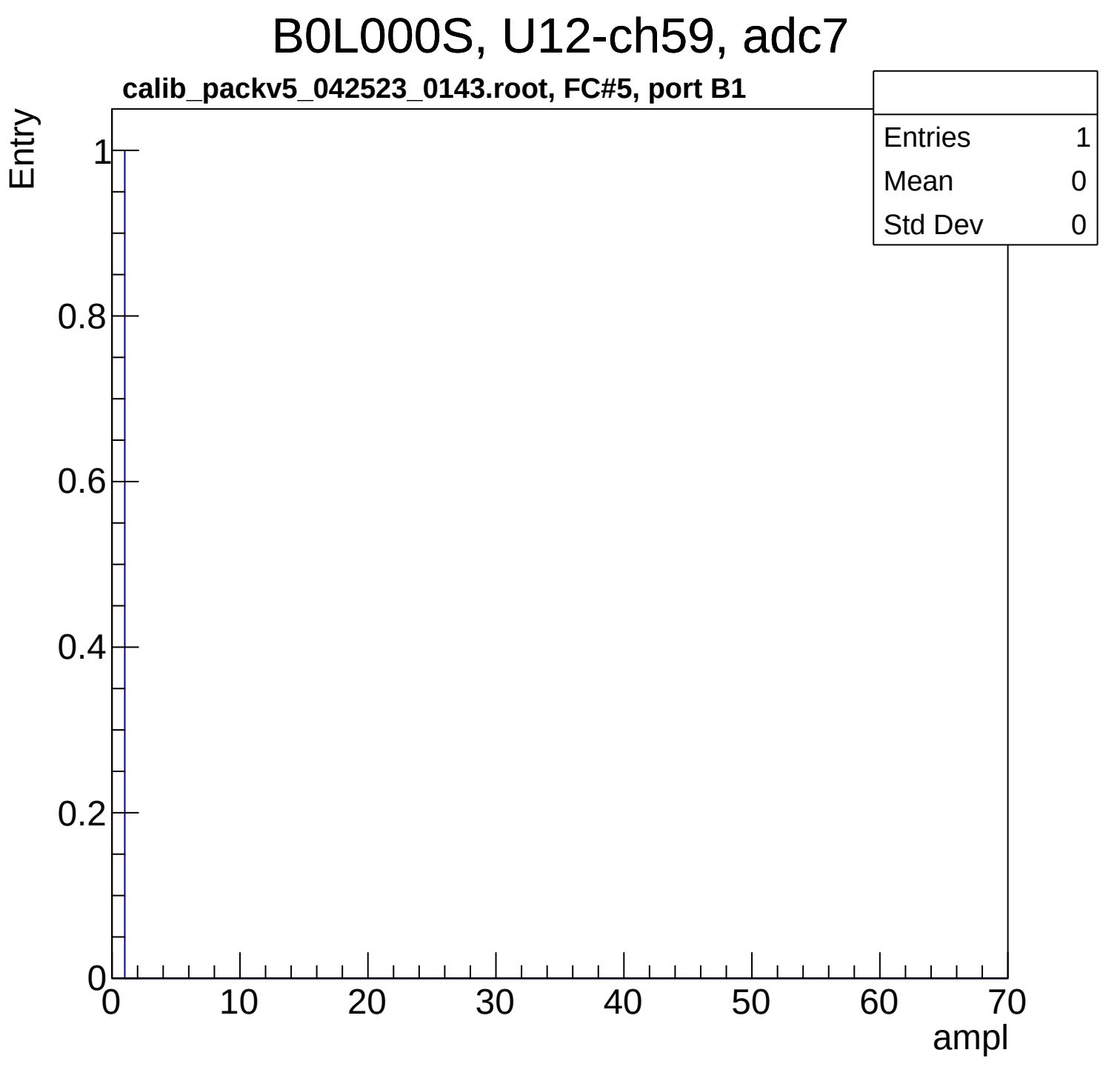
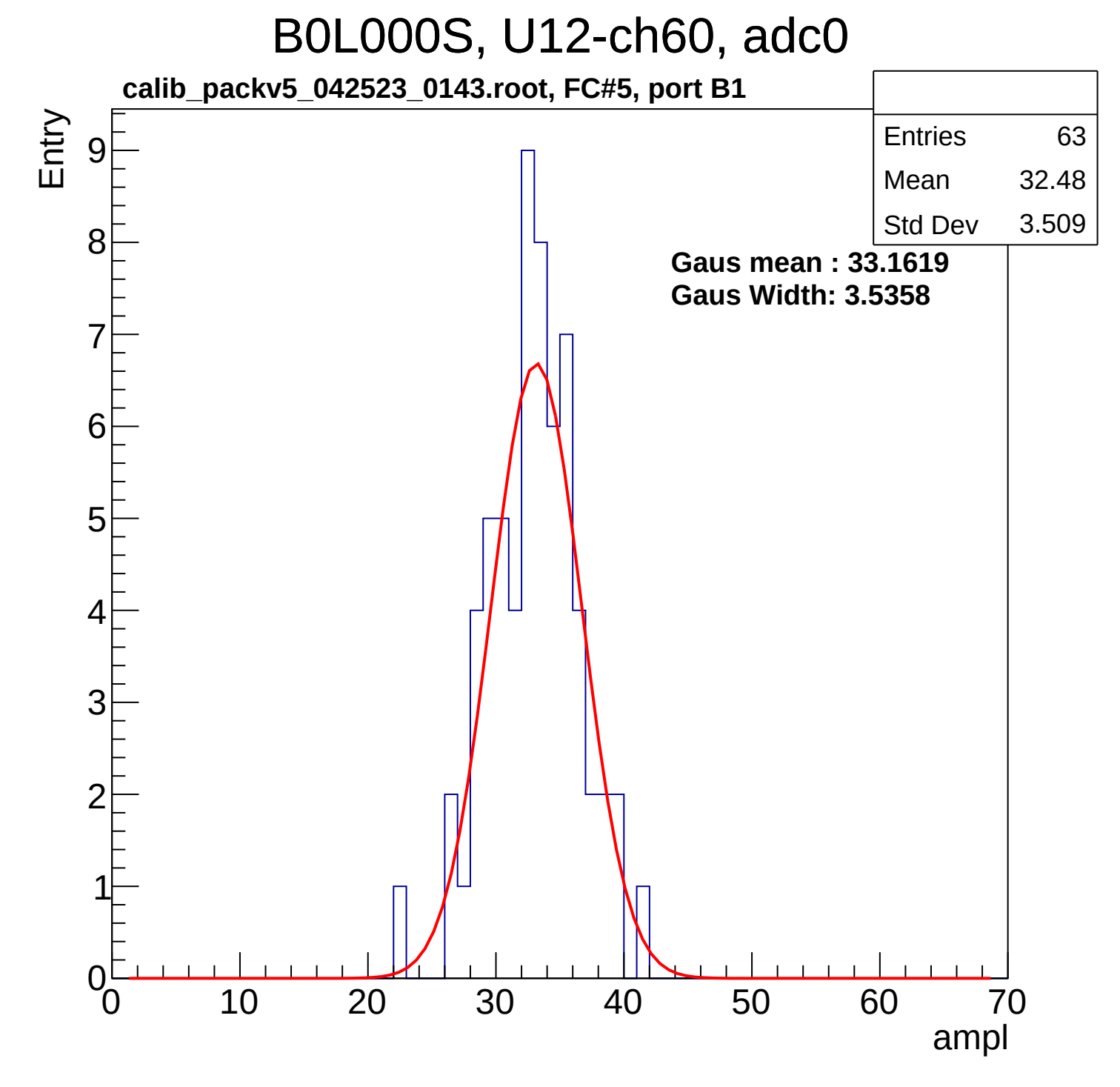
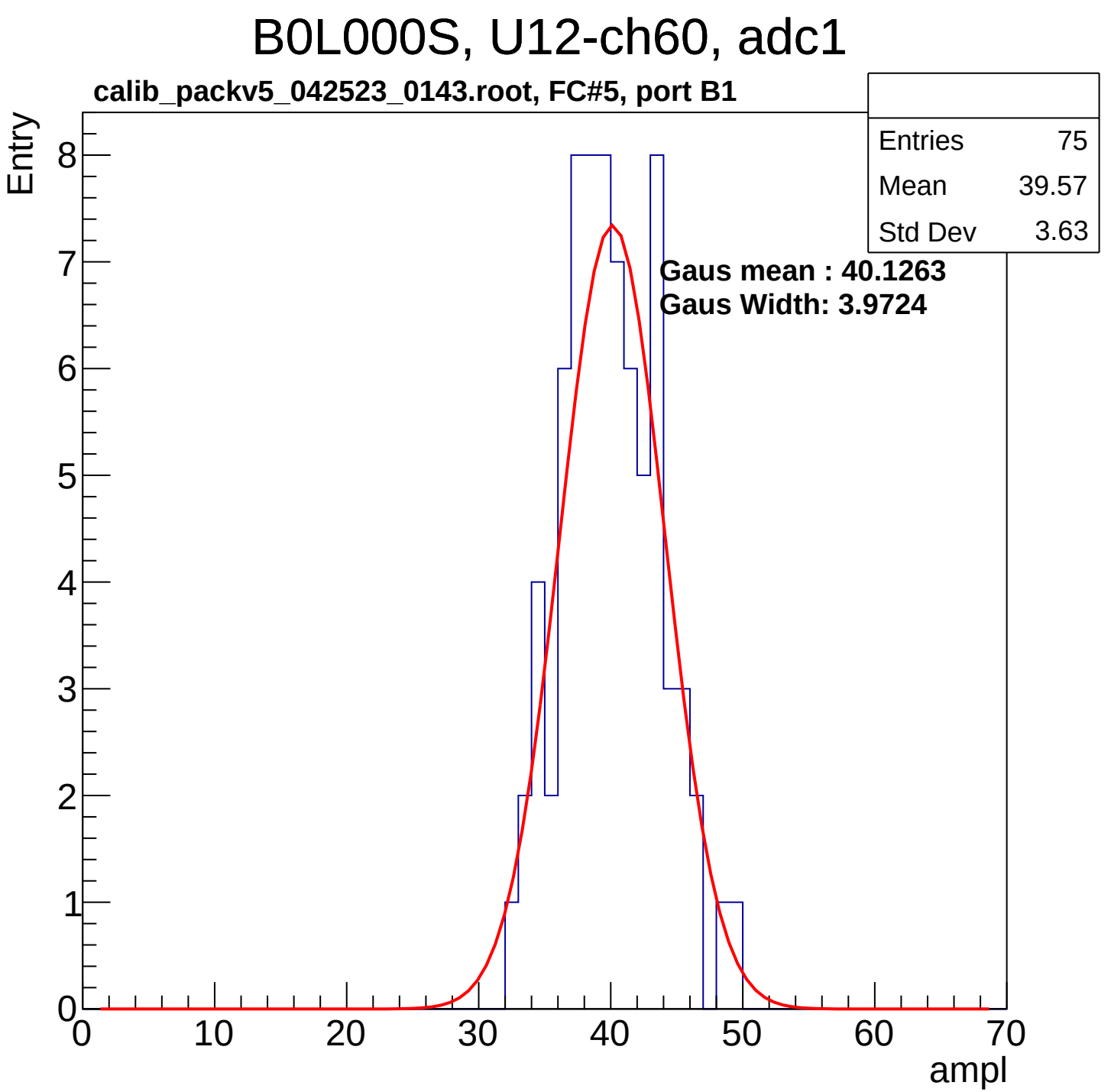

B0L000S, U12-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.39
Std Dev 3.408
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

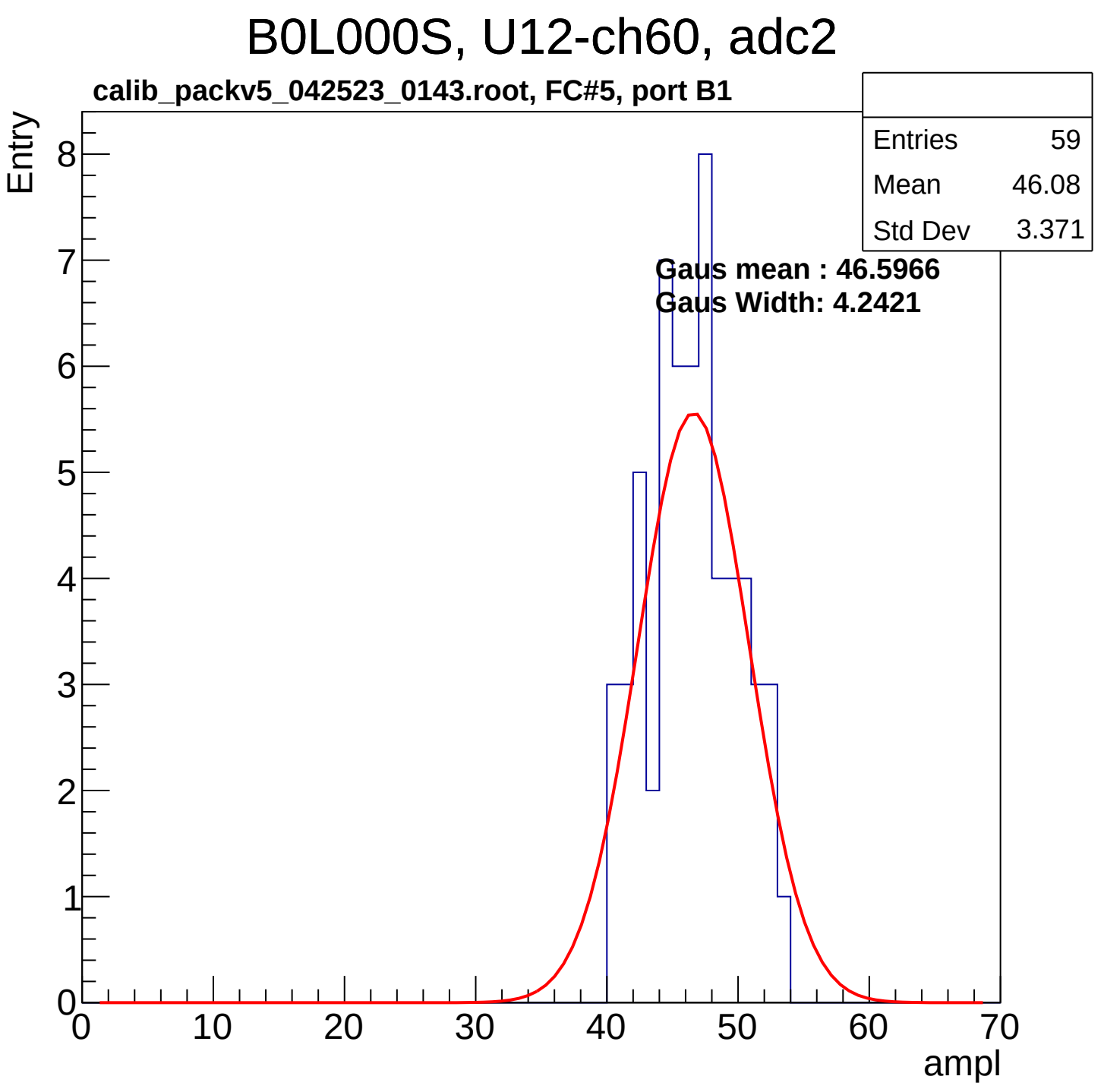

B0L000S, U12-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 60.08
Std Dev 2.381
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 40
Std Dev 28.3
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U12-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 32.48
Std Dev 3.509
Gaus mean : 33.1619
Gaus Width: 3.5358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 39.57
Std Dev 3.63
Gaus mean : 40.1263
Gaus Width: 3.9724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 46.08
Std Dev 3.371
Gaus mean : 46.5966
Gaus Width: 4.2421
Entry
ampl
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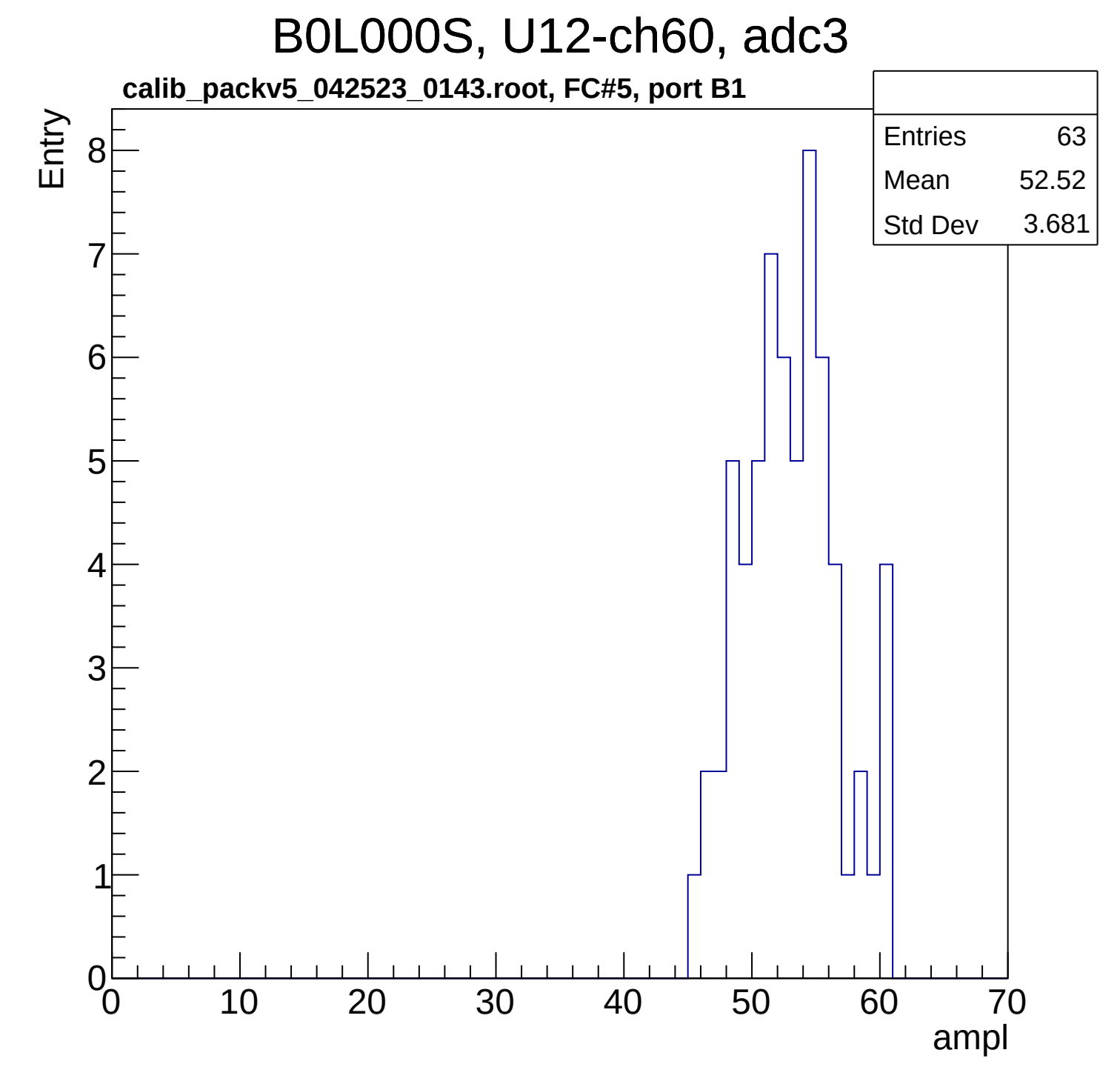
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 52.52
Std Dev 3.681
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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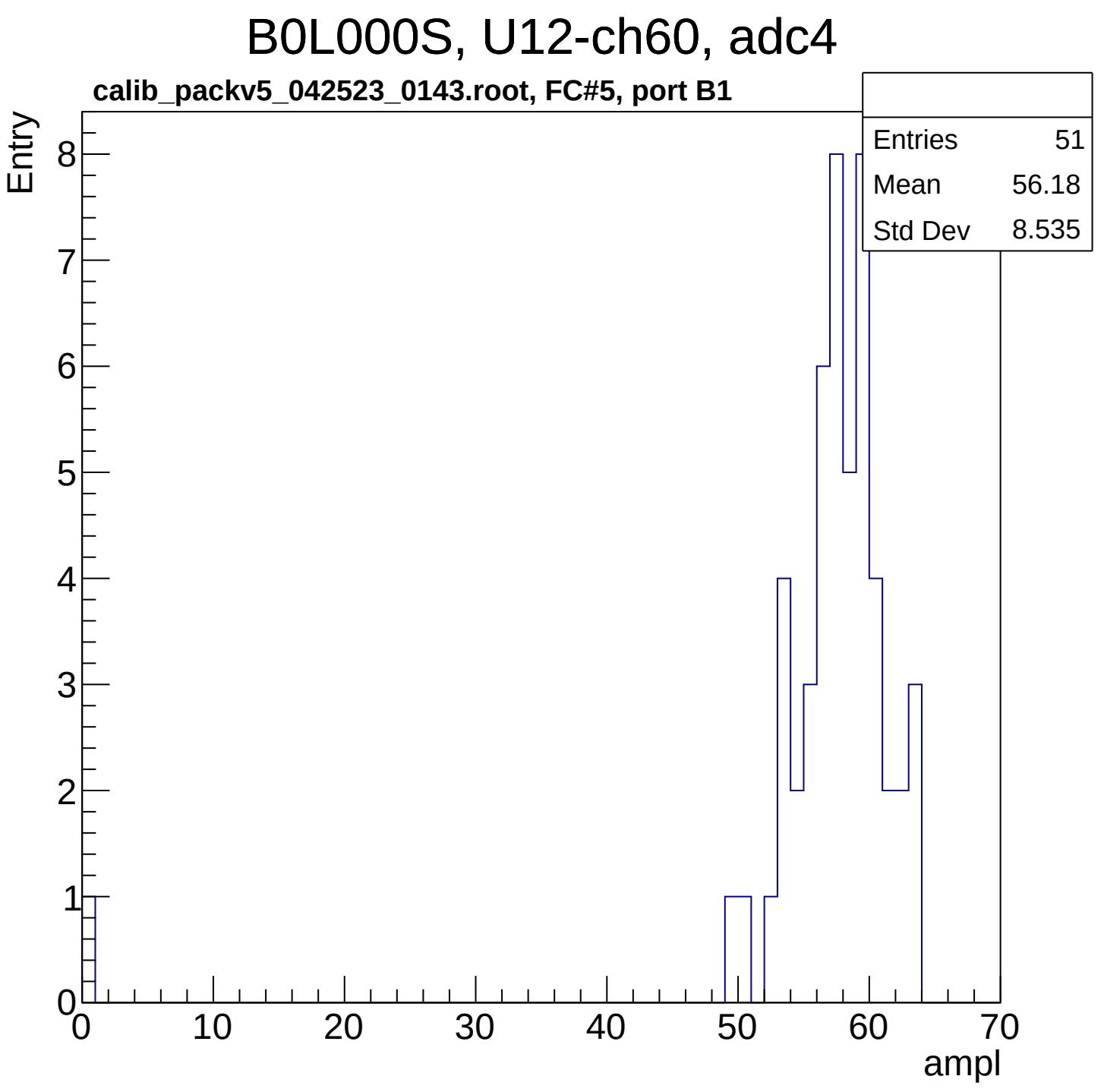
B0L000S, U12-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.18
Std Dev 8.535
Entry
ampl
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1
2
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0
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60
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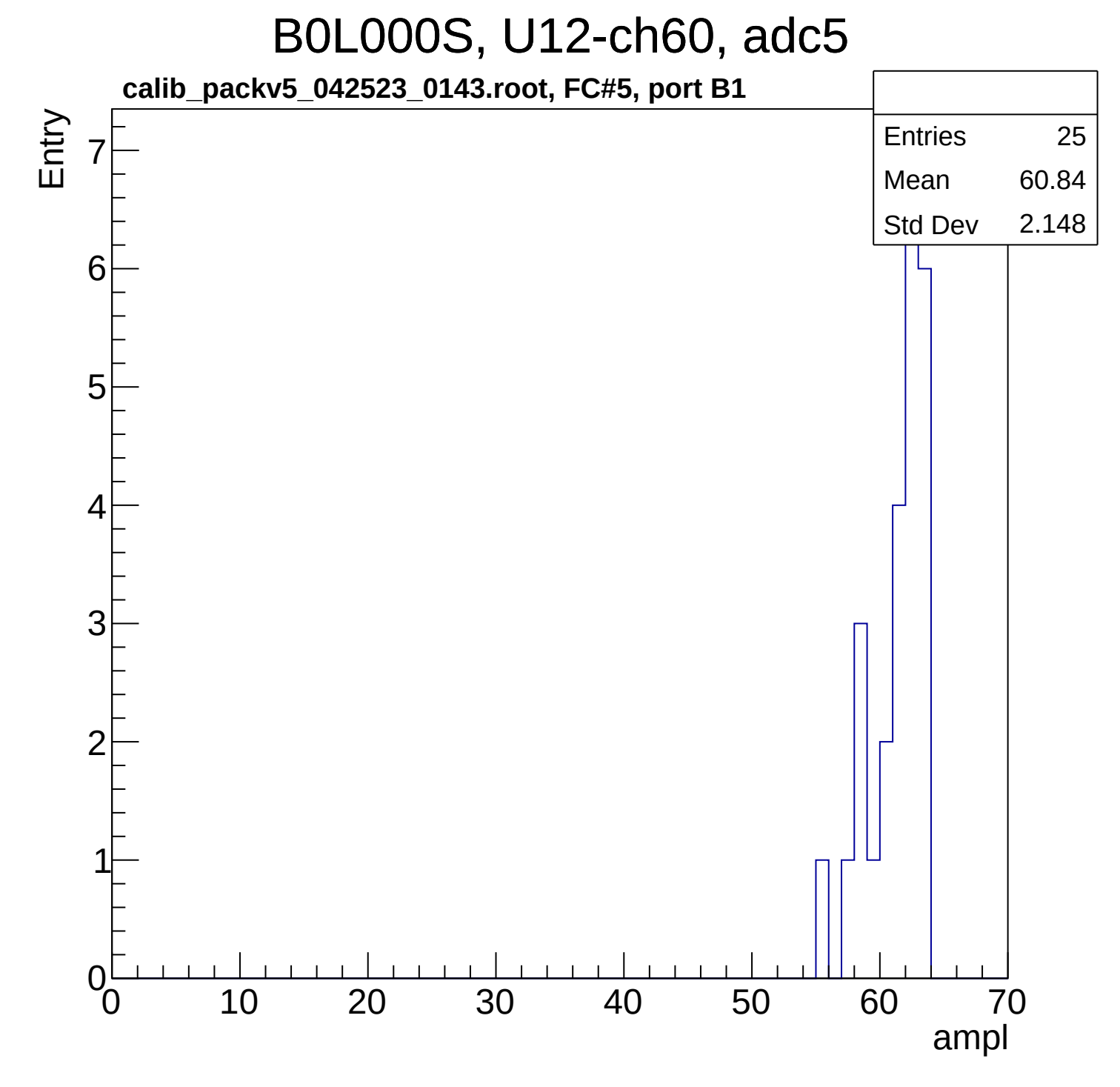
B0L000S, U12-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 60.84
Std Dev 2.148
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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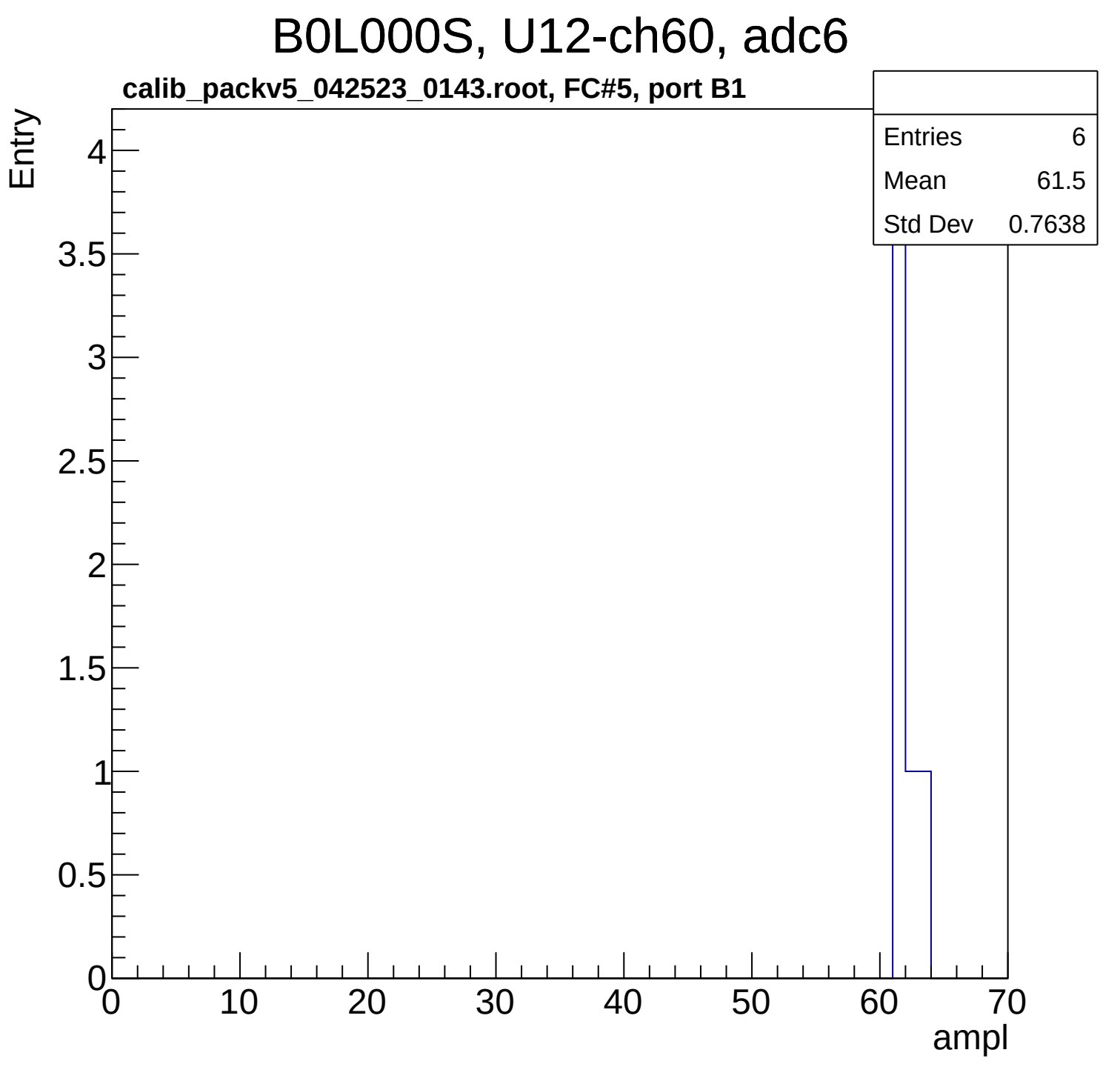
B0L000S, U12-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.5
Std Dev 0.7638
Entry
ampl
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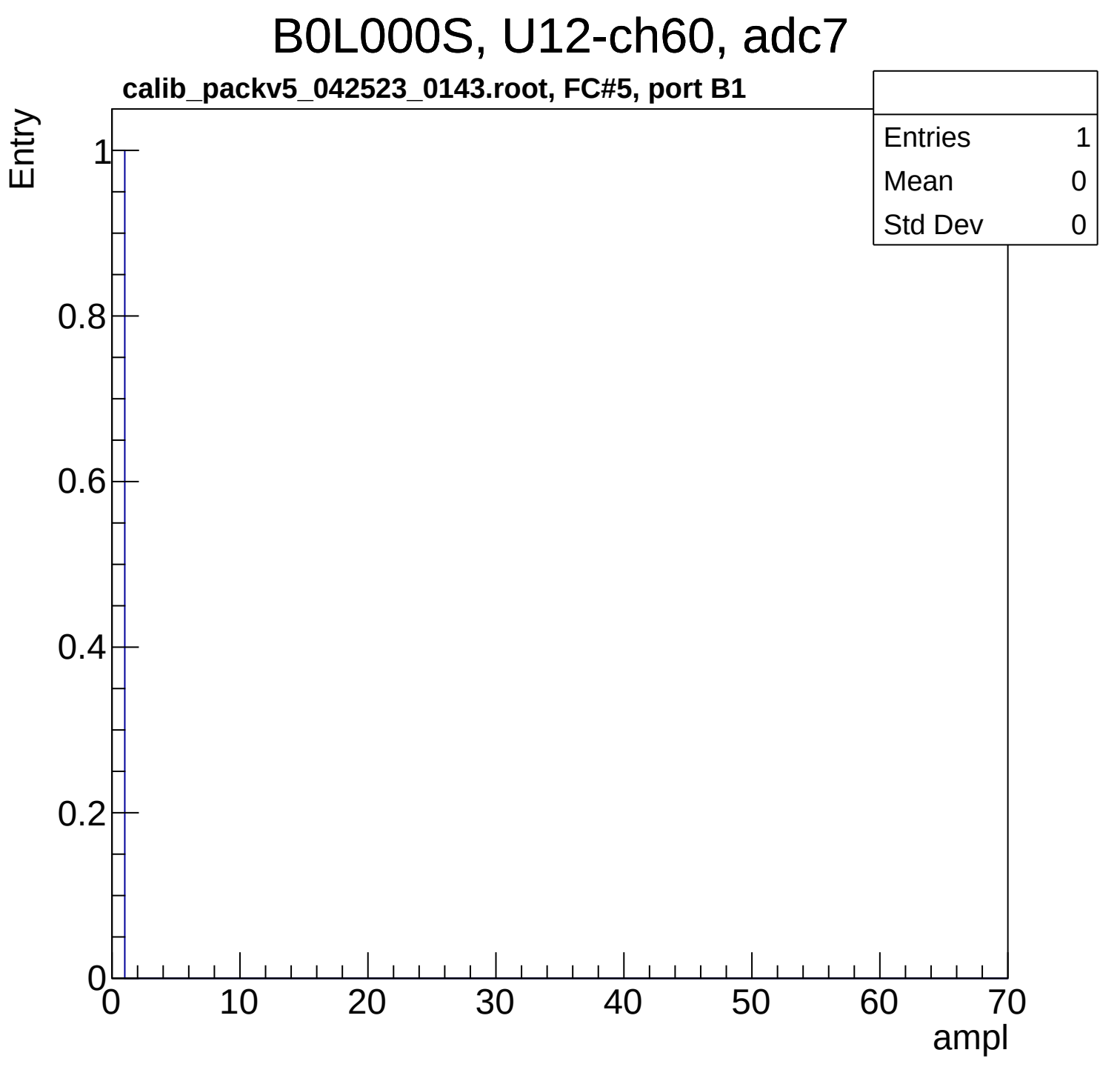
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U12-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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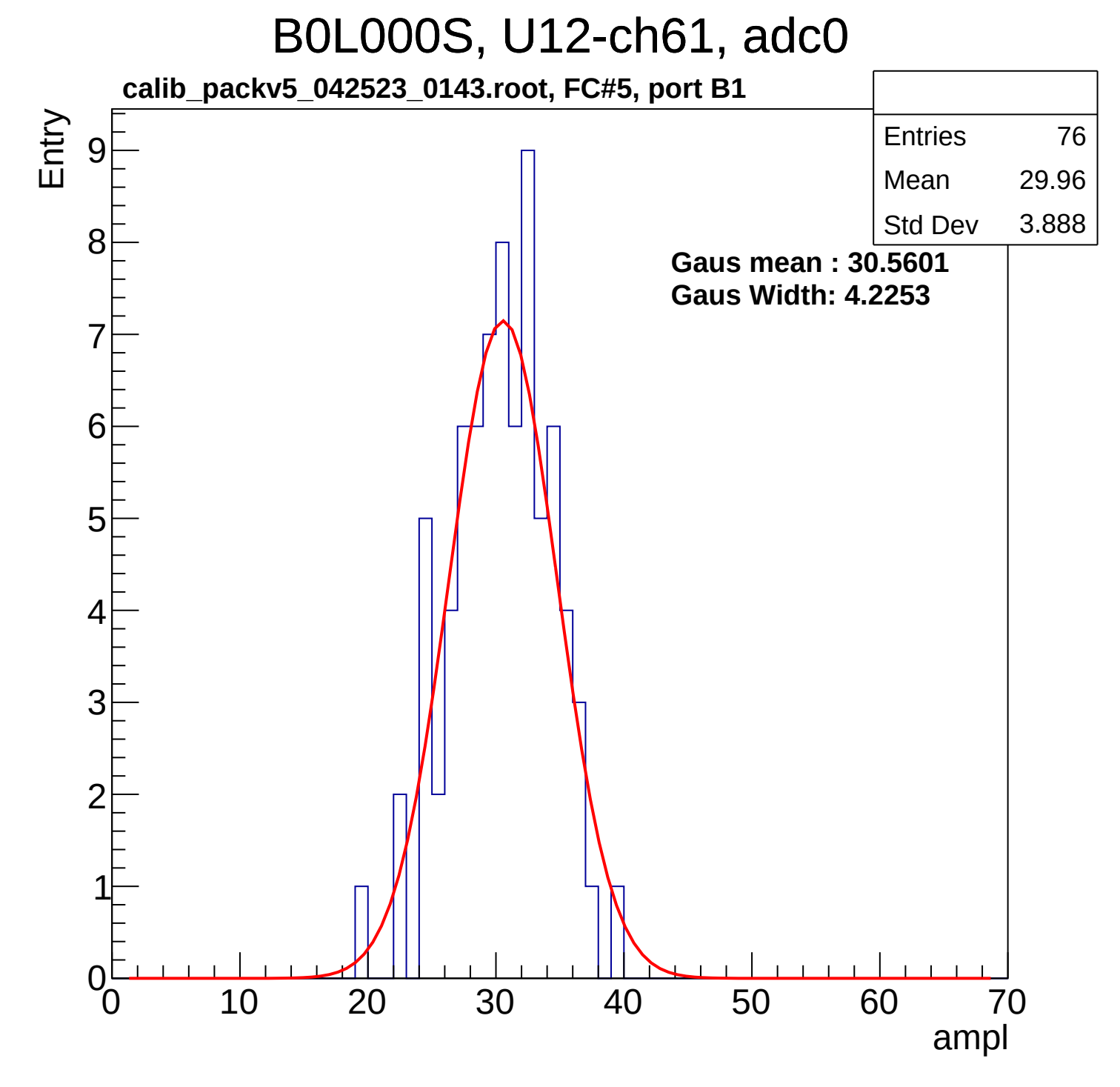
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.96
Std Dev 3.888
Gaus mean : 30.5601
Gaus Width: 4.2253
Entry
ampl
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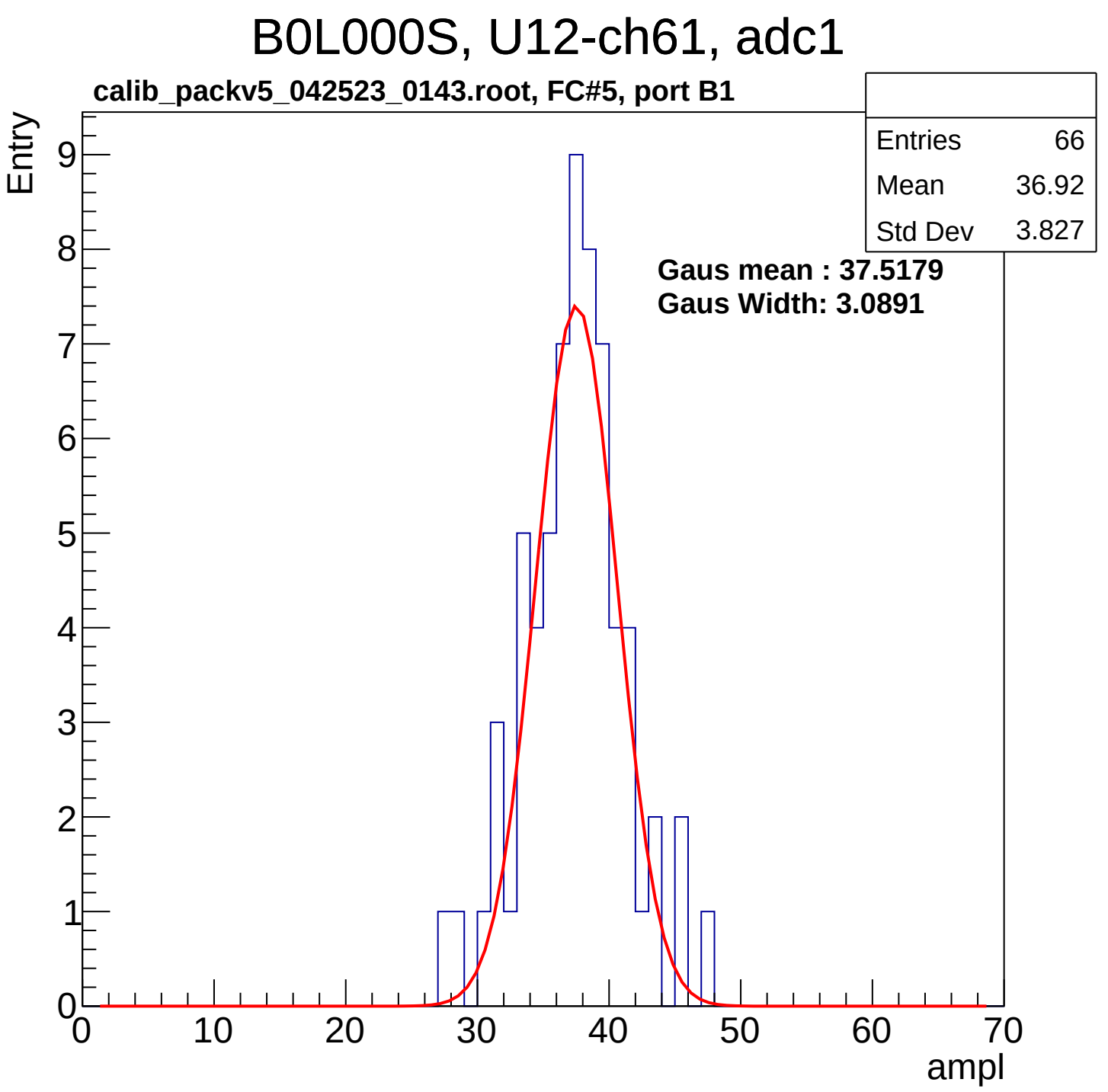
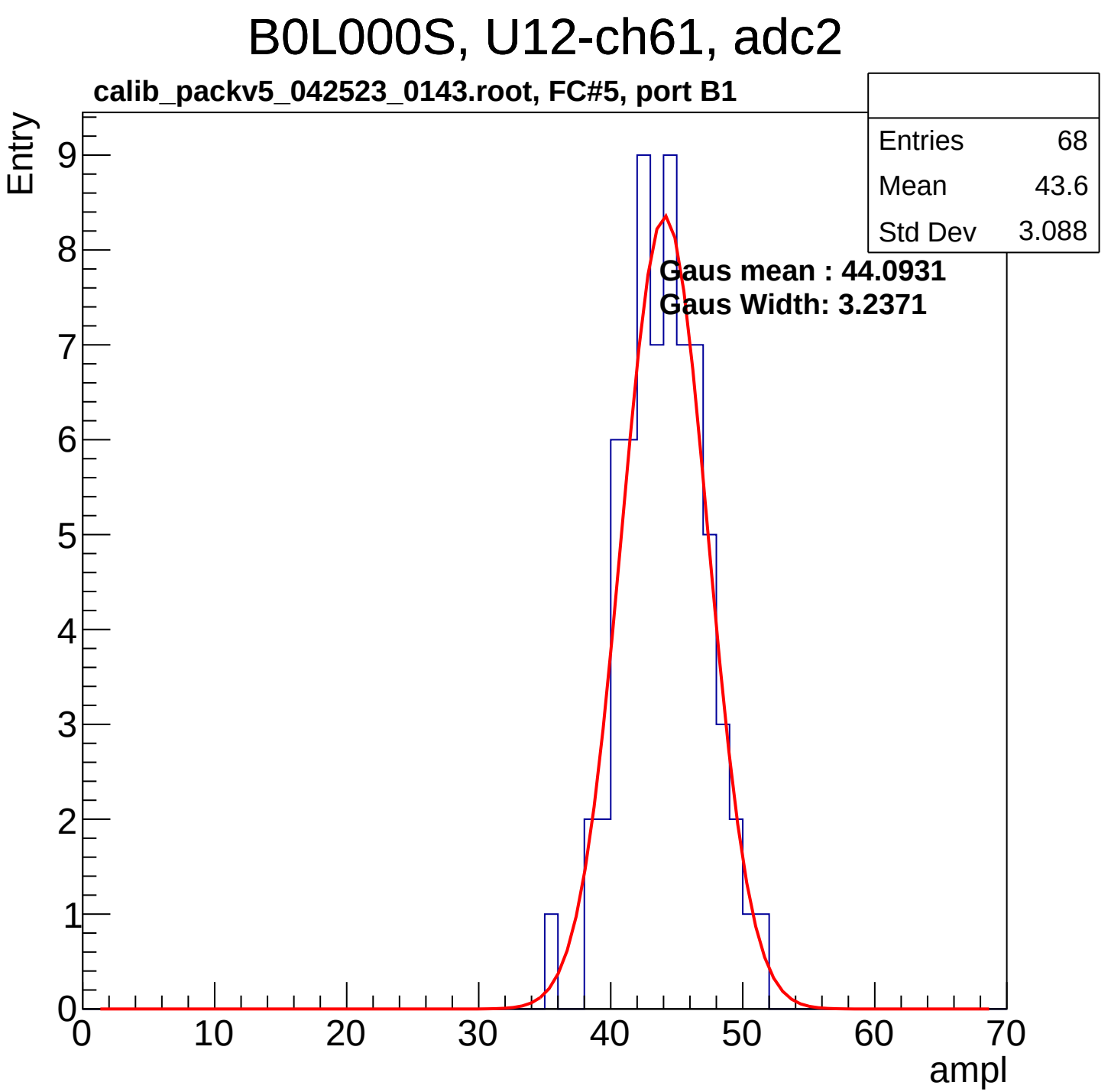
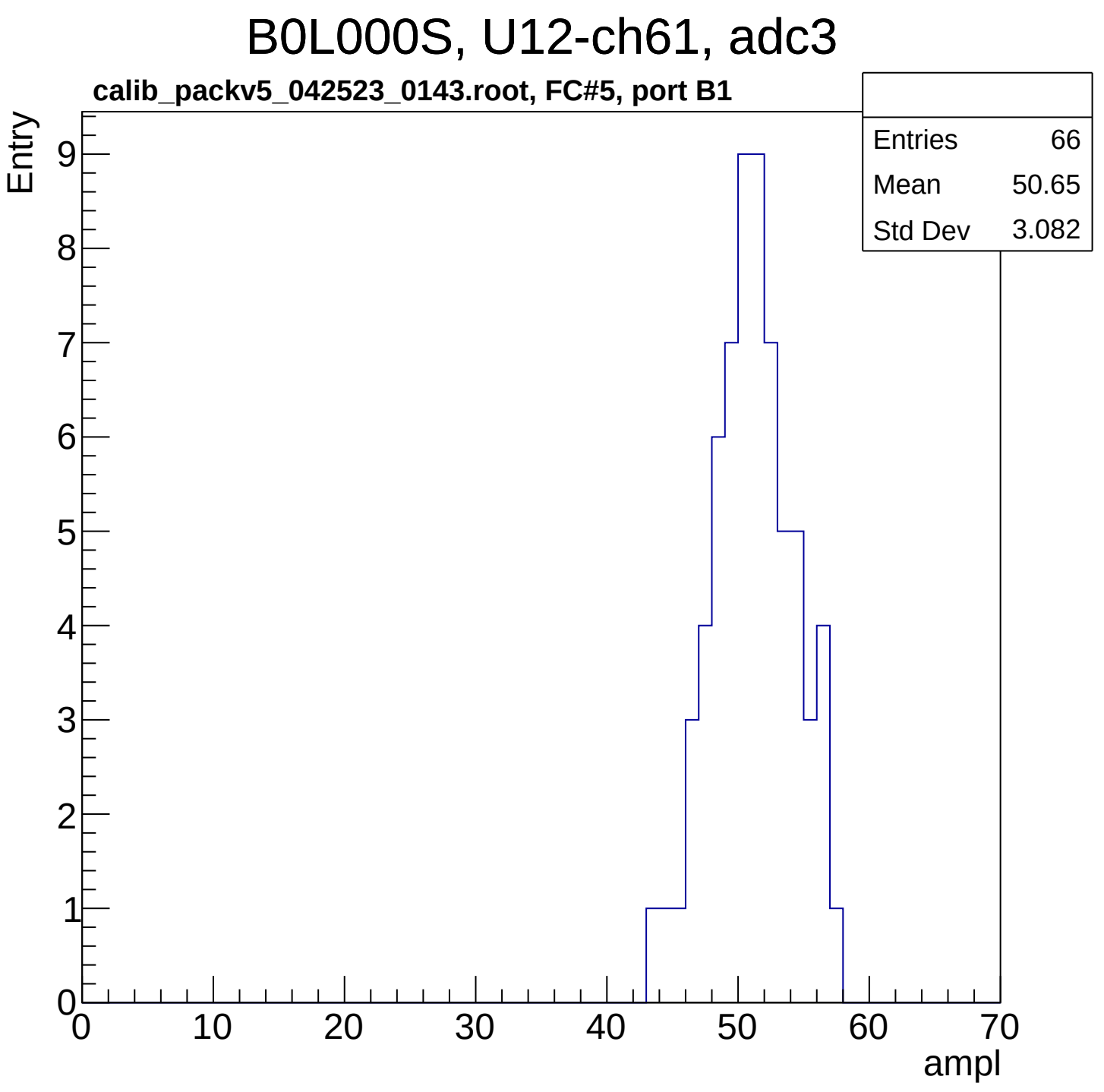
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.92
Std Dev 3.827
Gaus mean : 37.5179
Gaus Width: 3.0891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.6
Std Dev 3.088
Gaus mean : 44.0931
Gaus Width: 3.2371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.65
Std Dev 3.082
Entry
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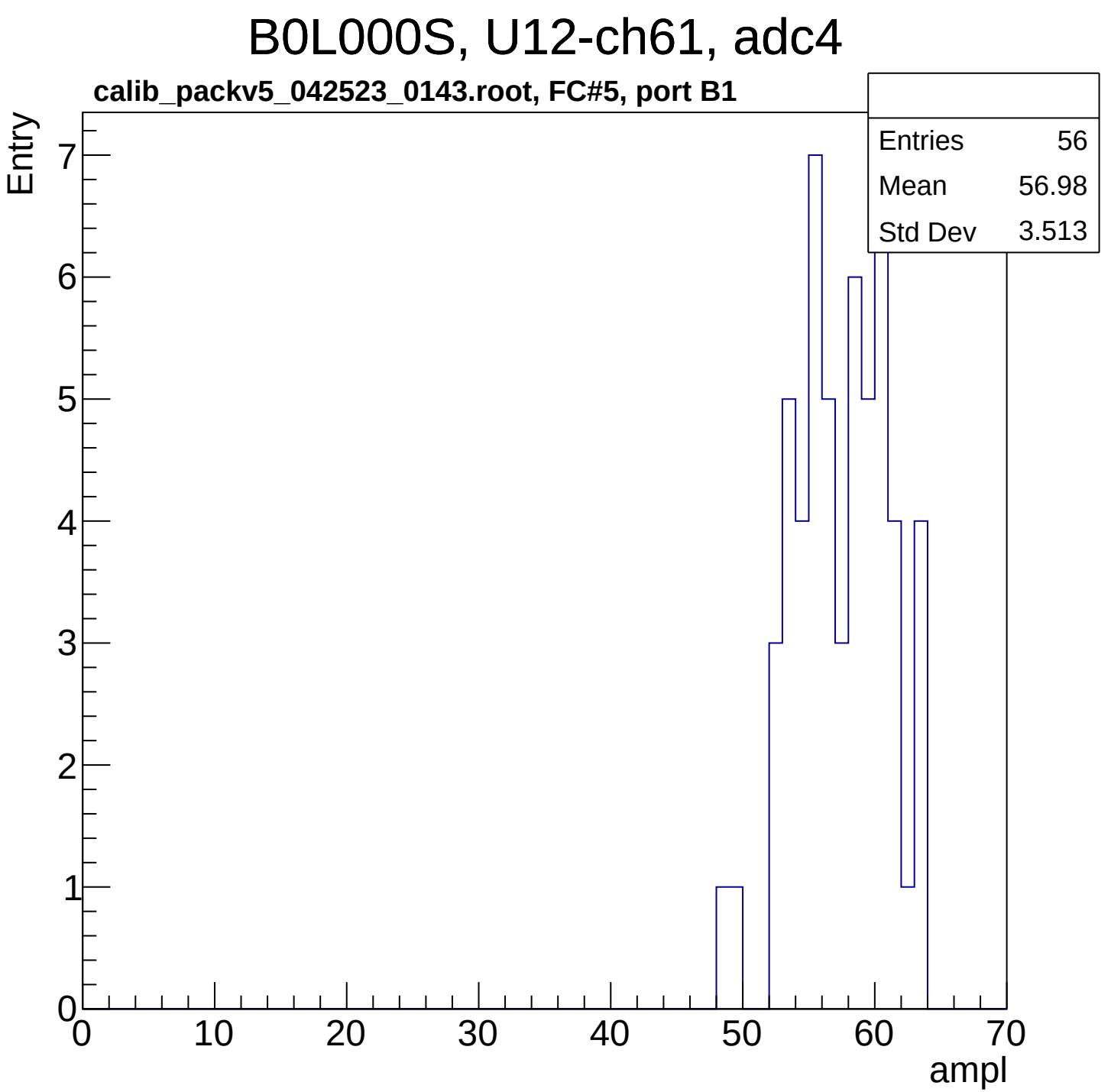
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.98
Std Dev 3.513
Entry
ampl
0
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2
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4
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6
7
0
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20
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40
50
60
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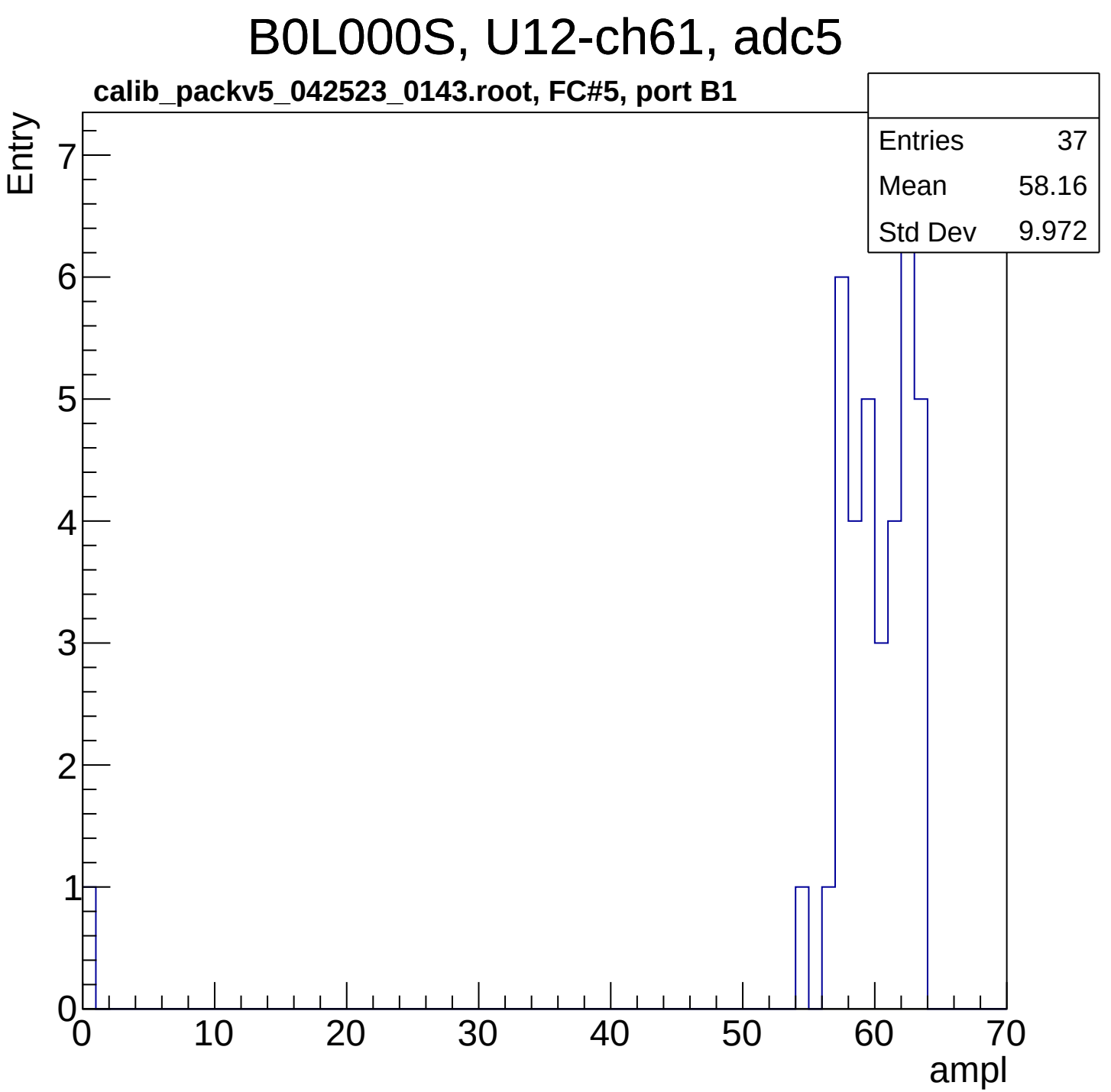
B0L000S, U12-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.16
Std Dev 9.972
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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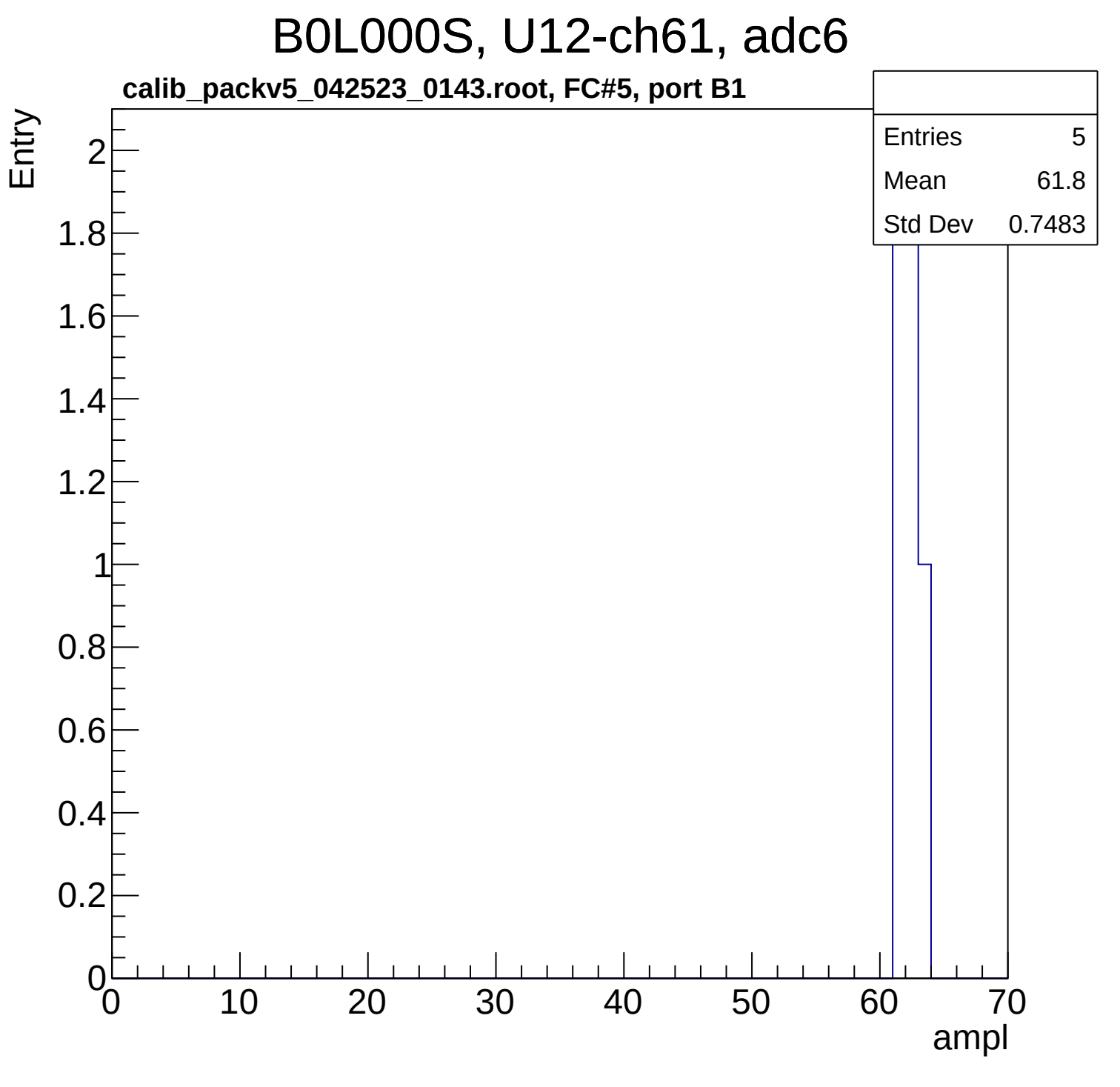
B0L000S, U12-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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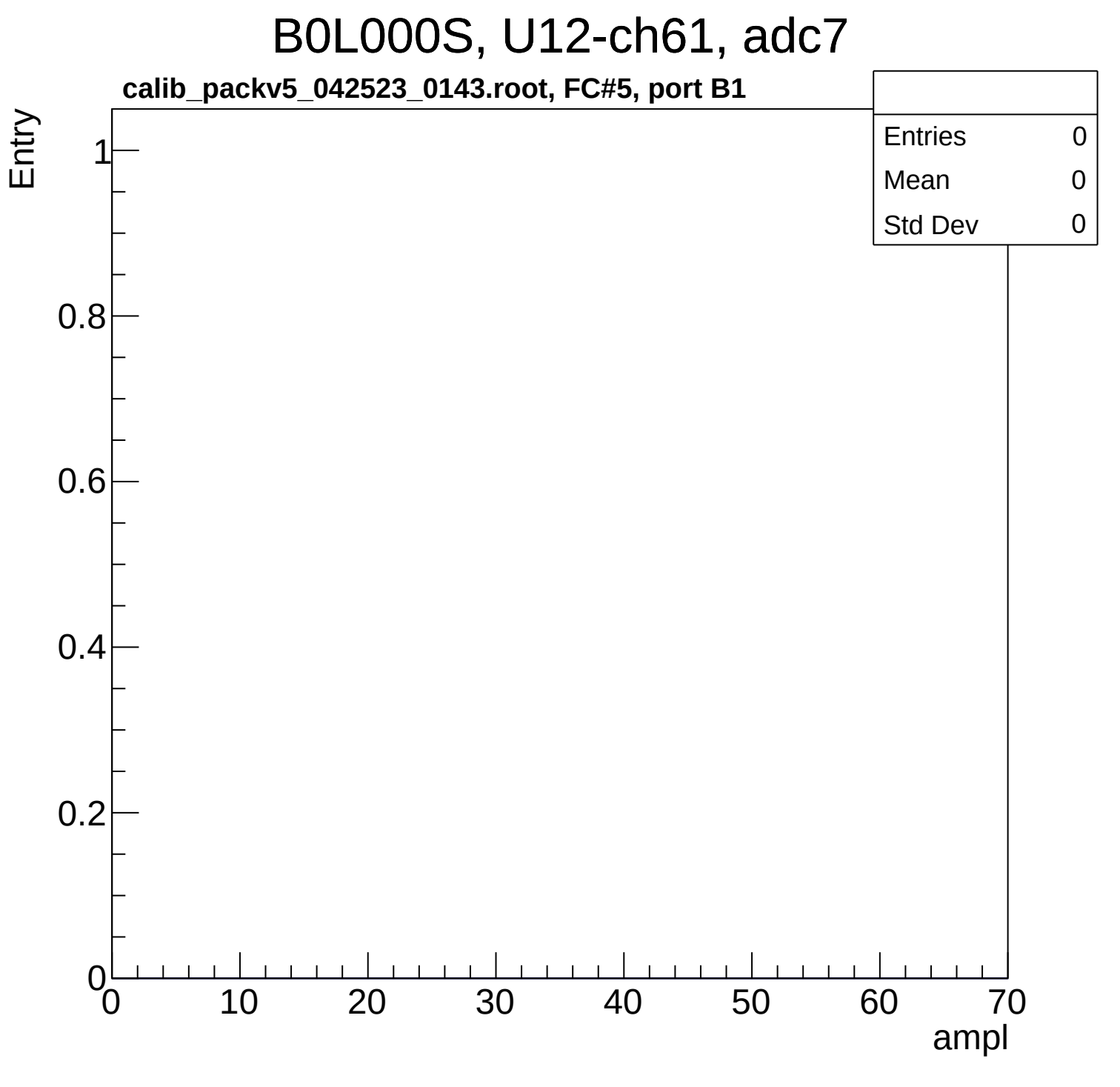
B0L000S, U12-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
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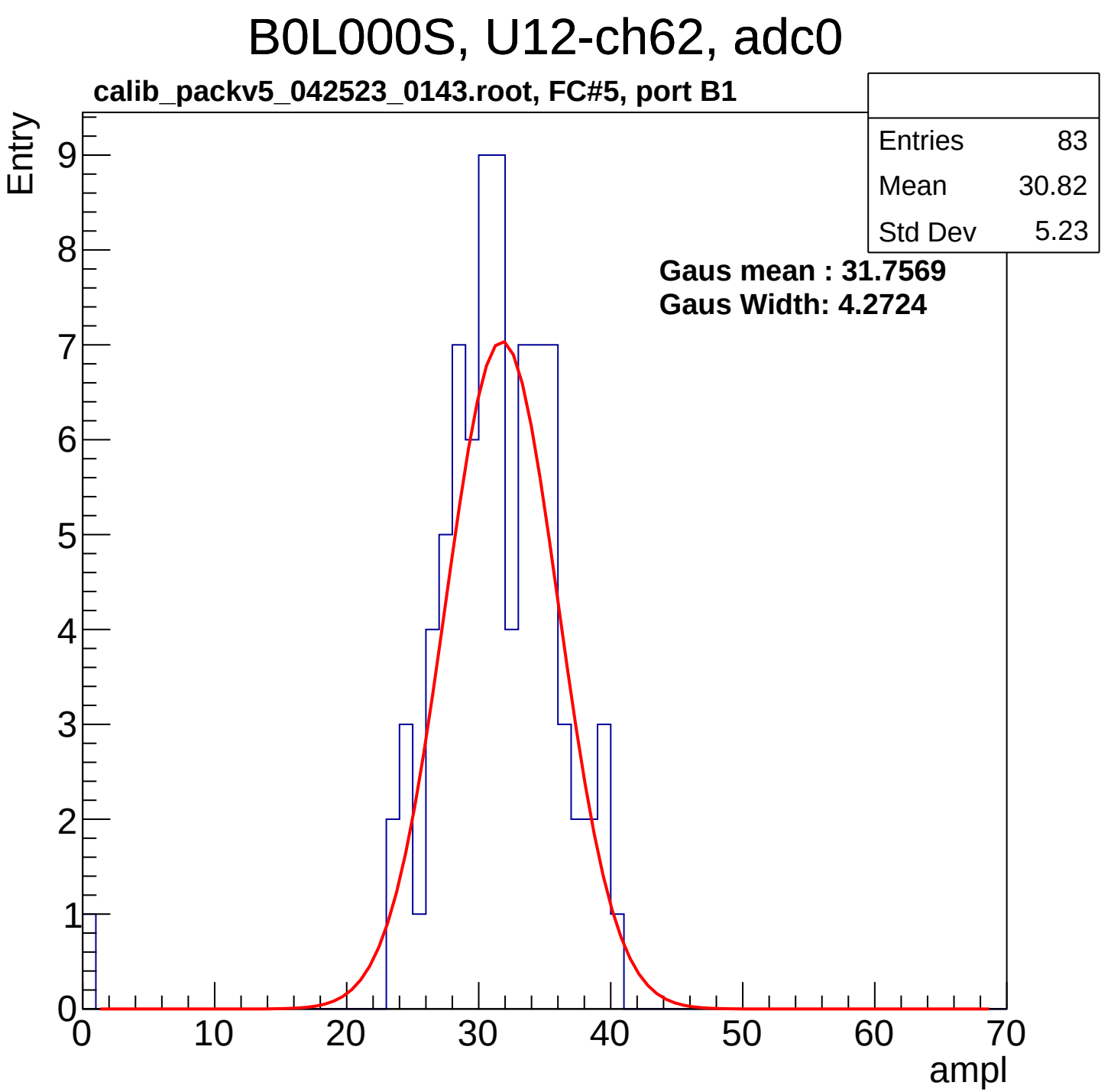
70
ampl


B0L000S, U12-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.82
Std Dev 5.23
Gaus mean : 31.7569
Gaus Width: 4.2724
Entry
ampl
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2
3
4
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6
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8
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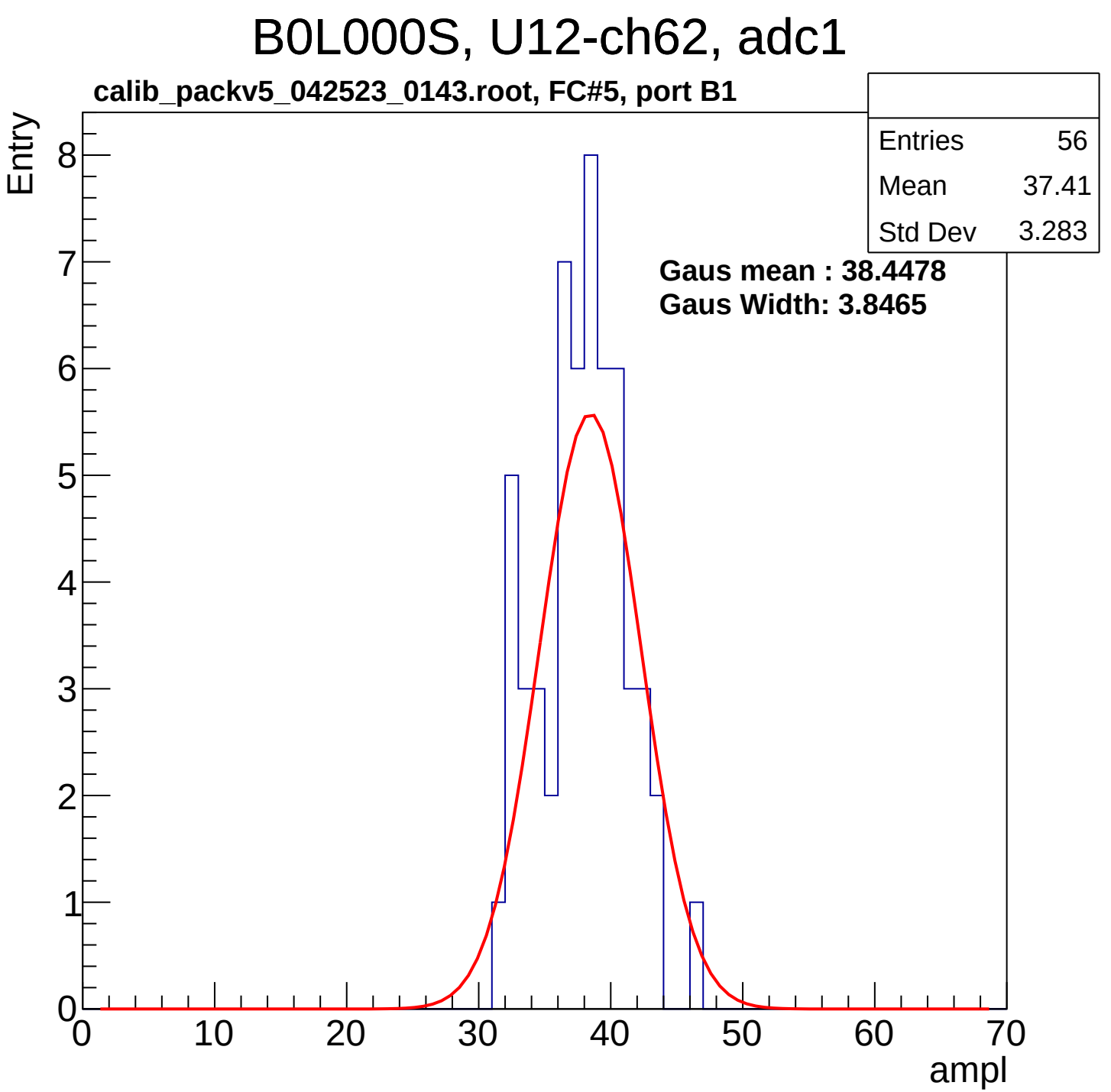
B0L000S, U12-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.41
Std Dev 3.283
Gaus mean : 38.4478
Gaus Width: 3.8465
Entry
ampl
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5
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60
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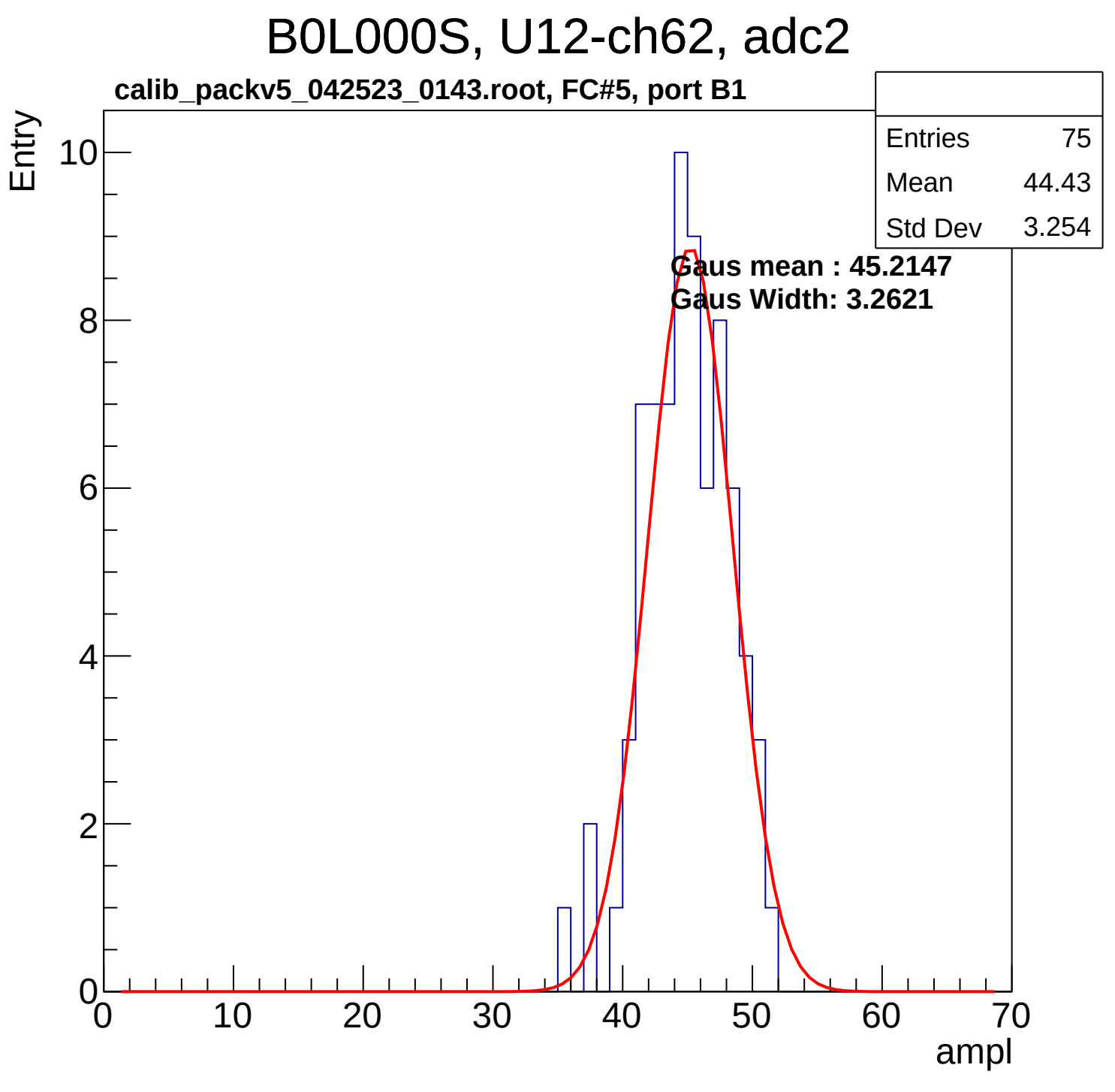
B0L000S, U12-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 44.43
Std Dev 3.254
Gaus mean : 45.2147
Gaus Width: 3.2621
Entry
ampl
0
2
4
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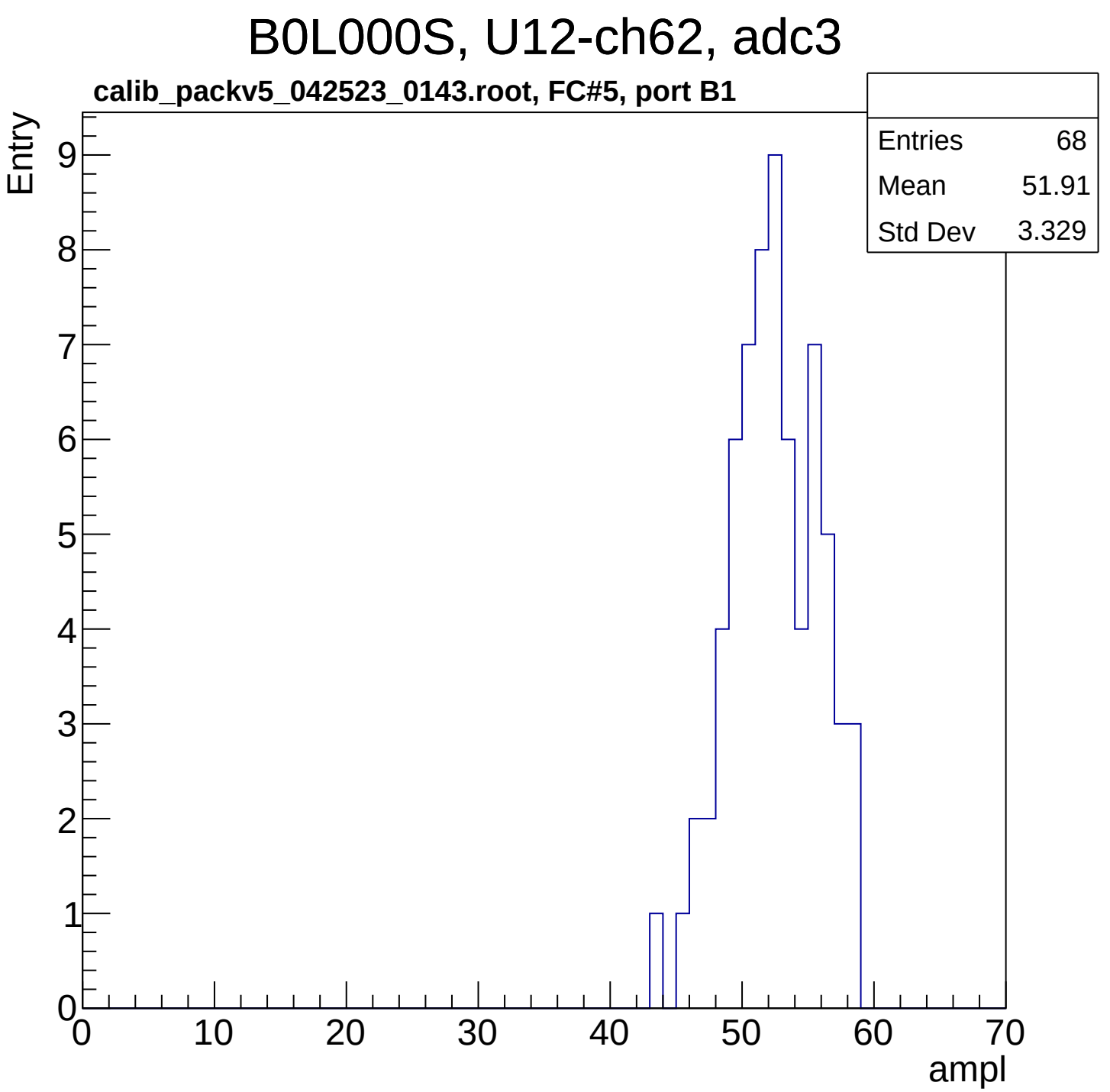
B0L000S, U12-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.91
Std Dev 3.329
Entry
ampl
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2
3
4
5
6
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0
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60
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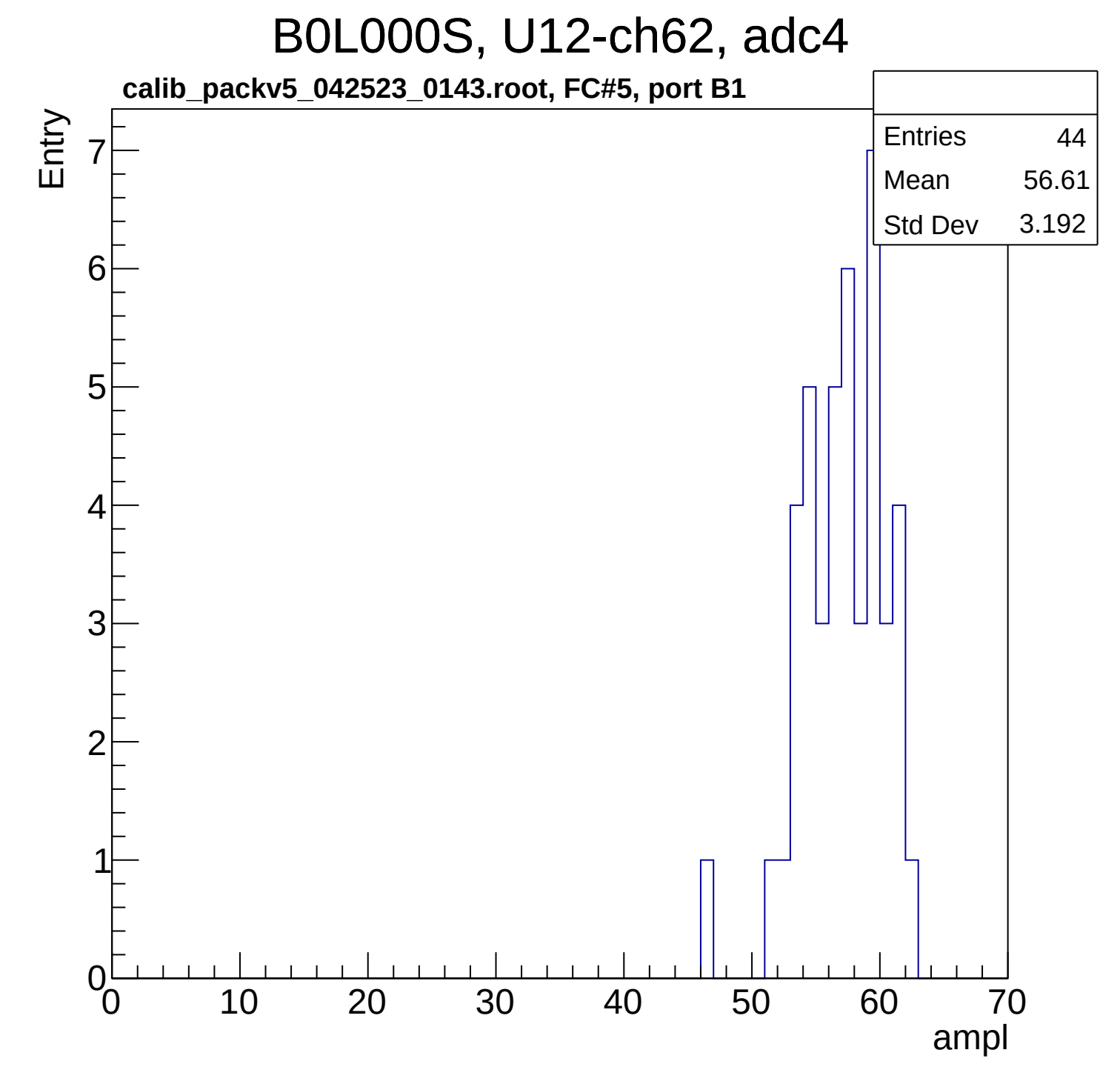
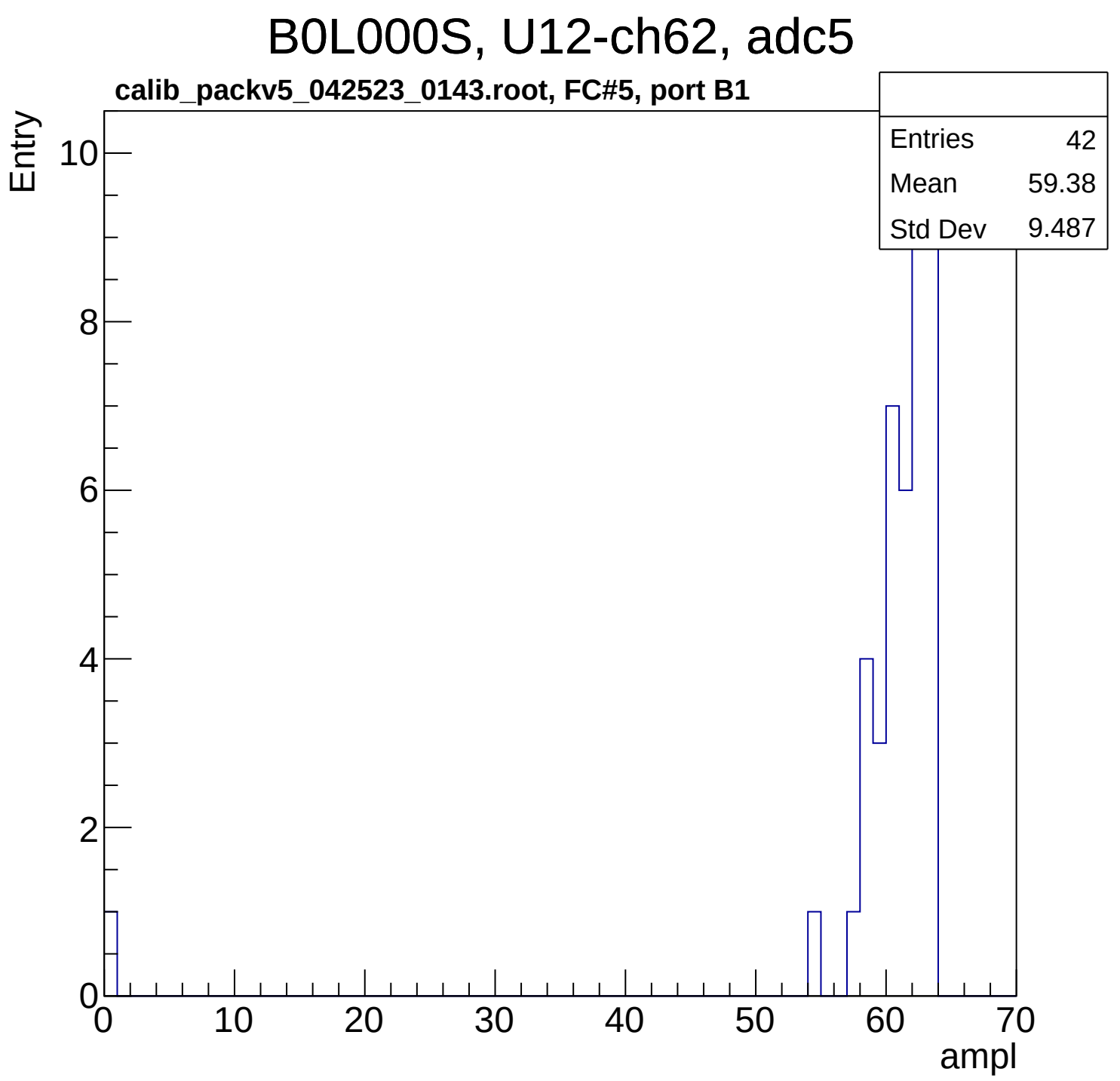

B0L000S, U12-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 56.61
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.38
Std Dev 9.487
Entry
0
2
4
6
8
10
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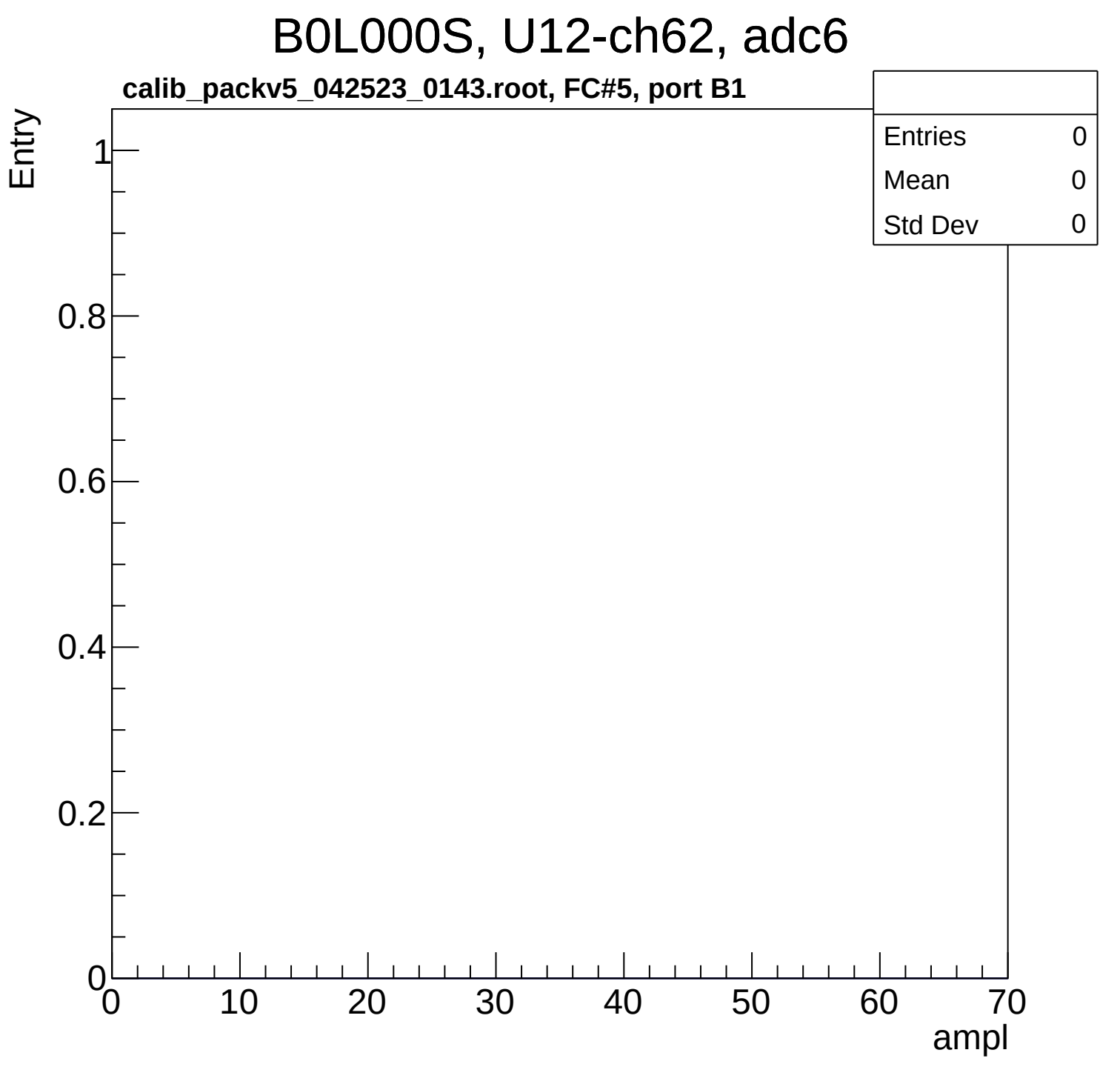
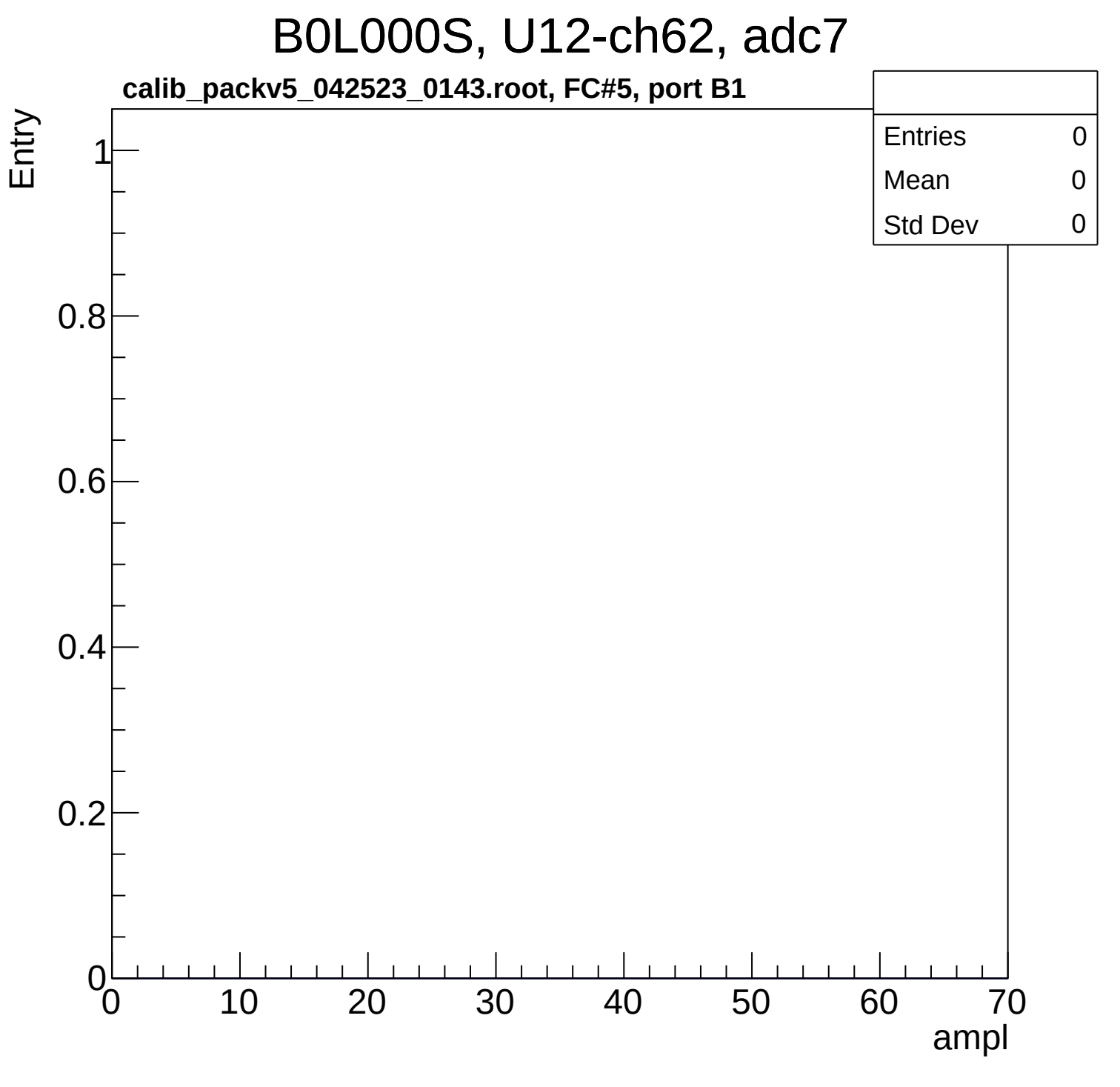
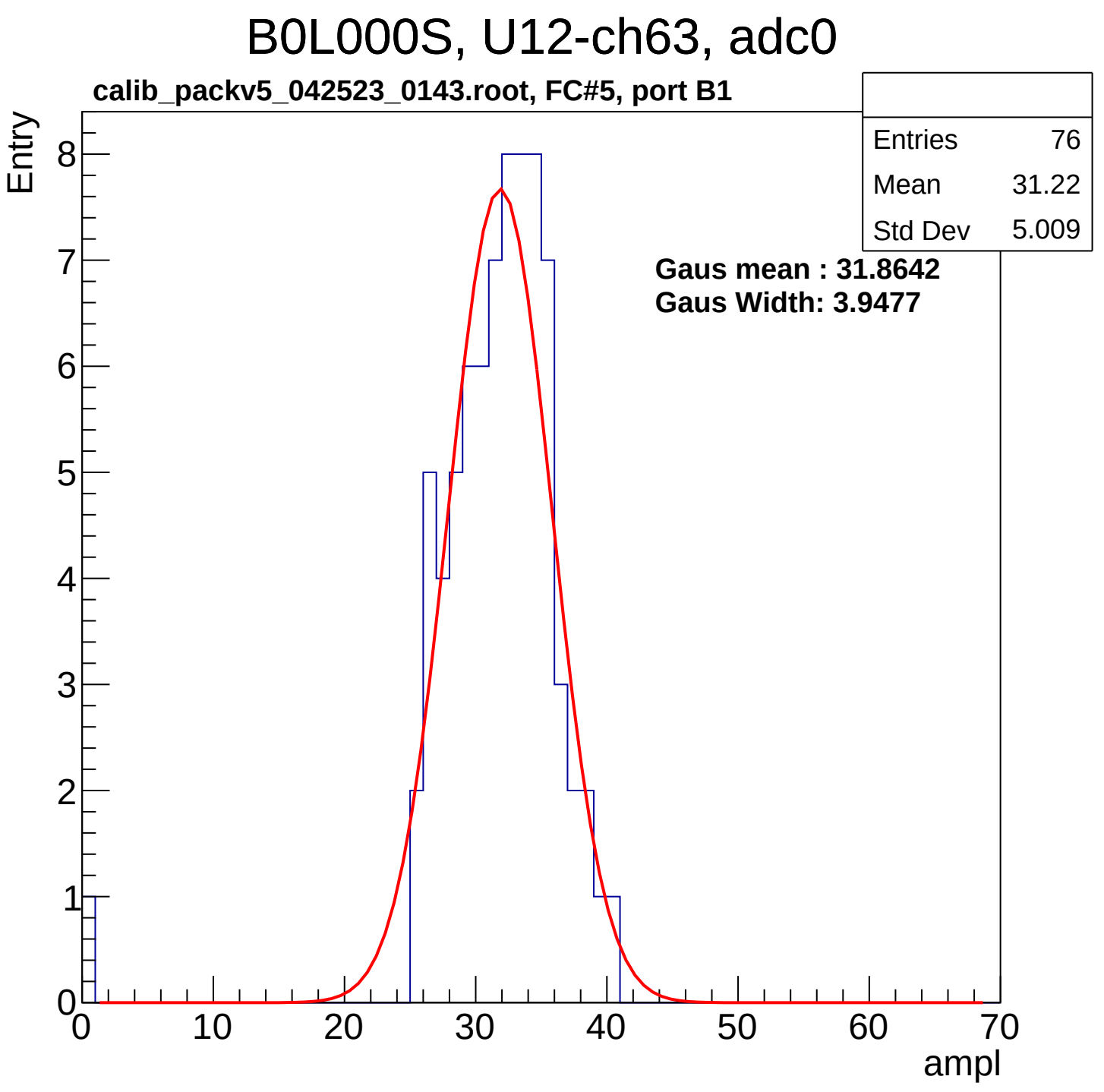
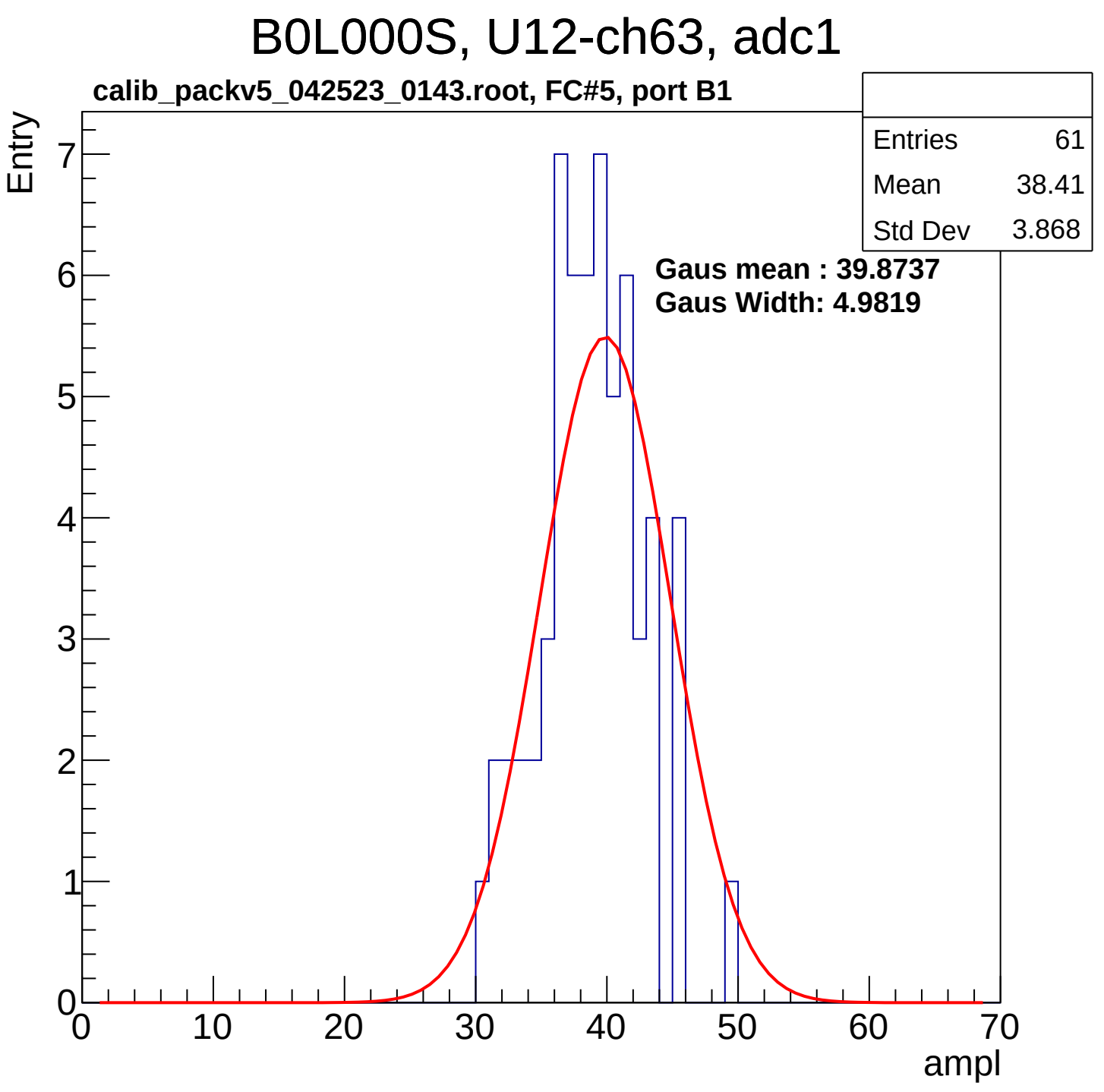
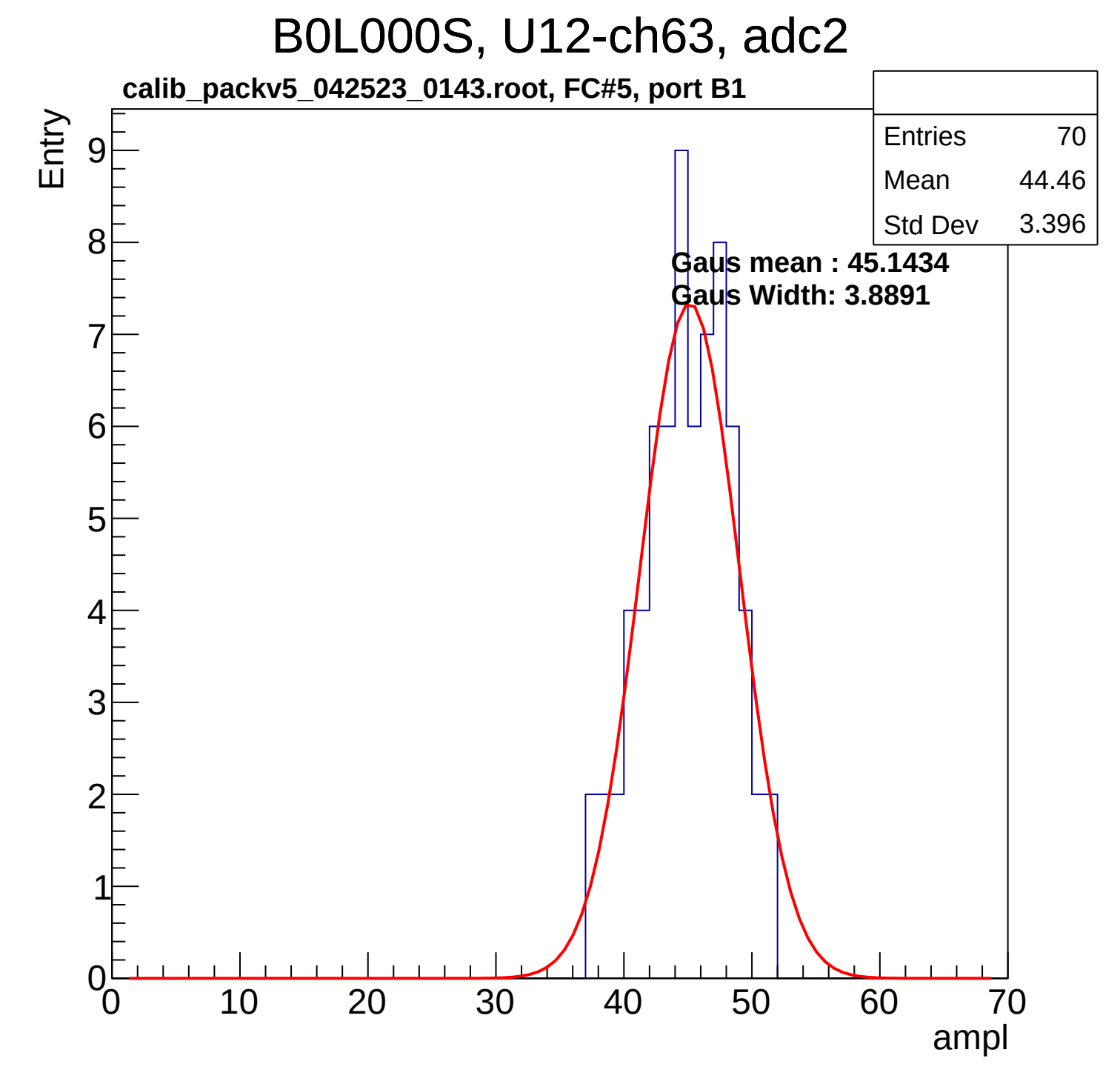
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 31.22
Std Dev 5.009
Gaus mean : 31.8642
Gaus Width: 3.9477
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 38.41
Std Dev 3.868
Gaus mean : 39.8737
Gaus Width: 4.9819
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44.46
Std Dev 3.396
Gaus mean : 45.1434
Gaus Width: 3.8891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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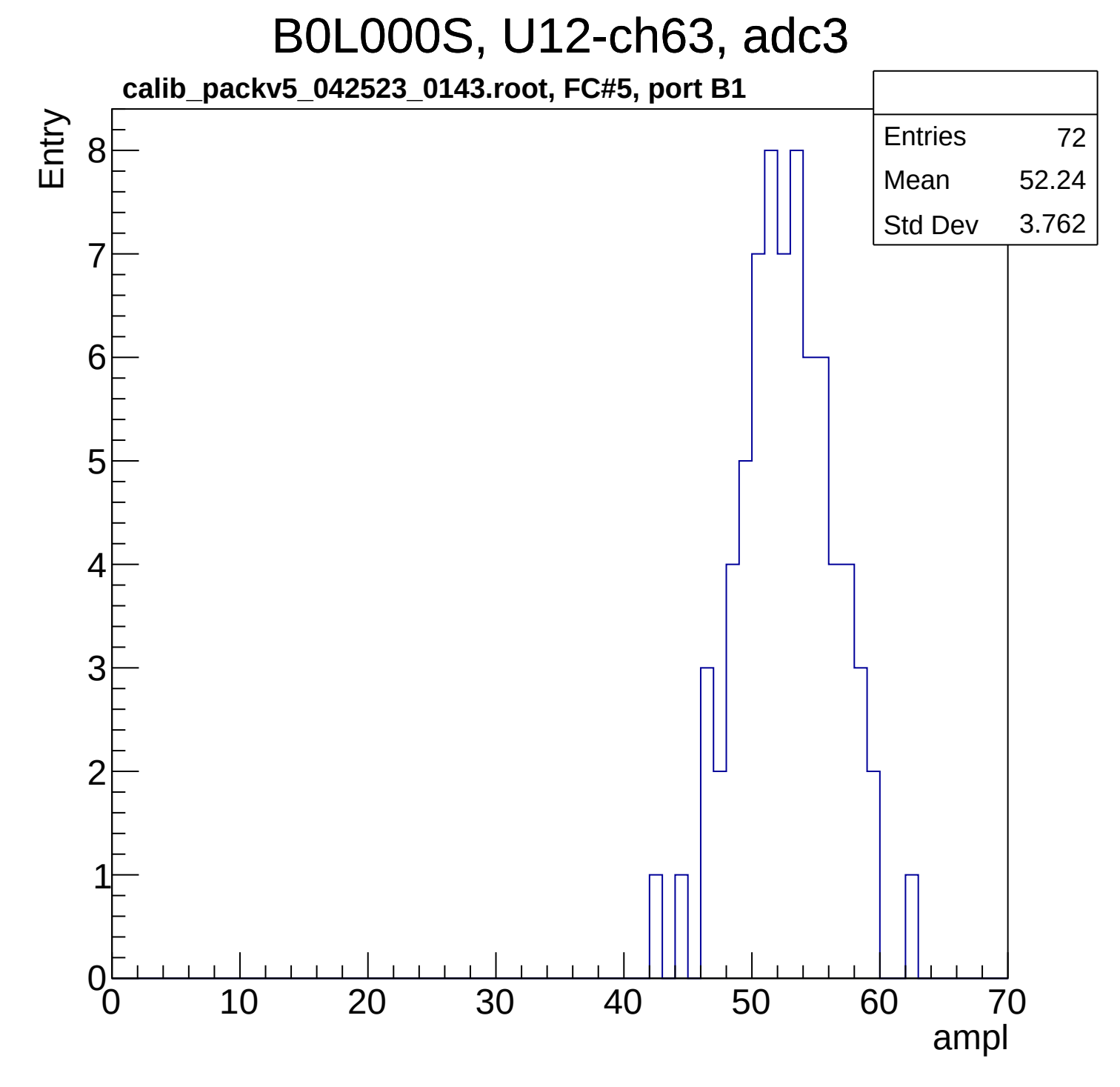
B0L000S, U12-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 52.24
Std Dev 3.762
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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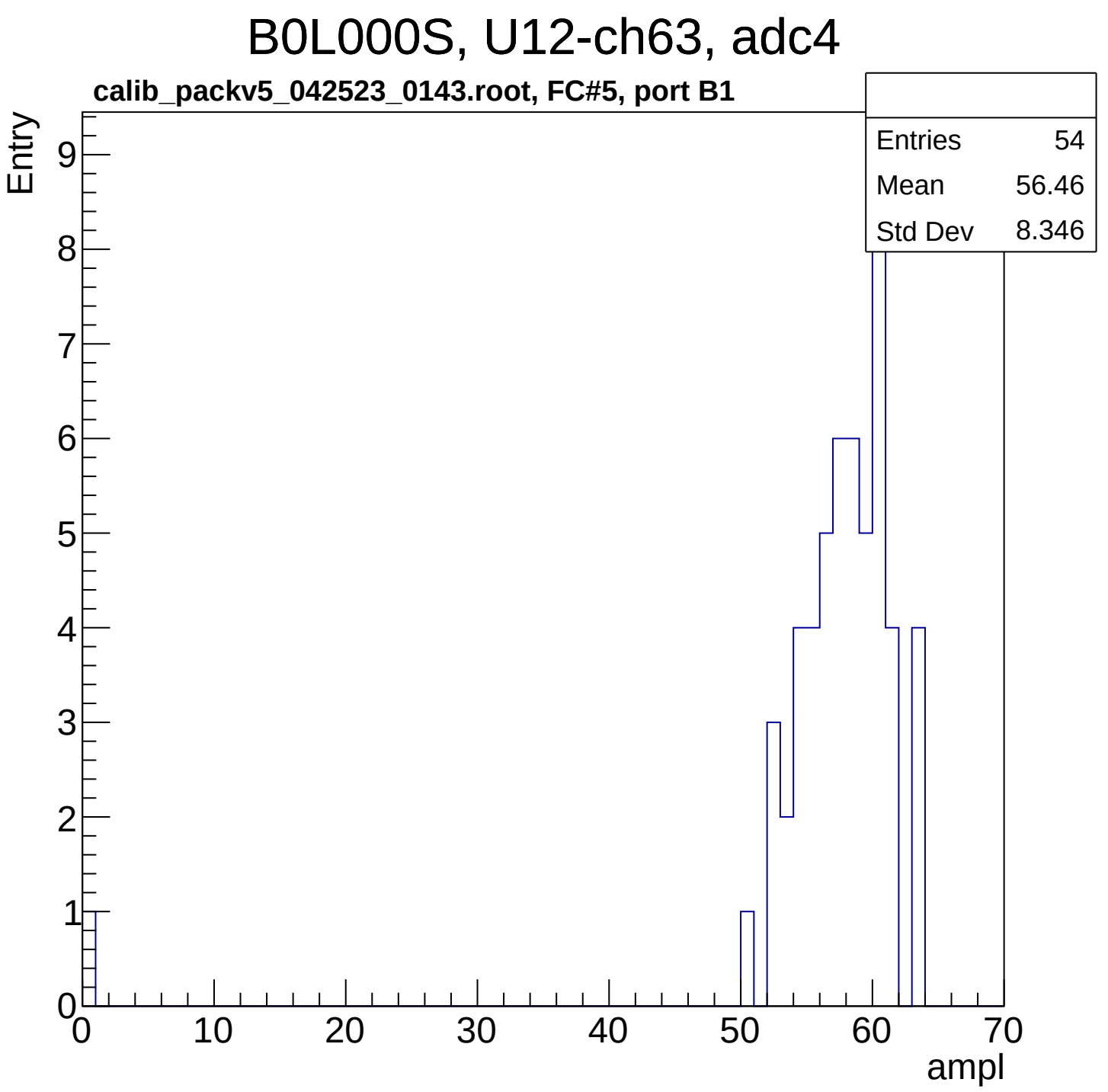
B0L000S, U12-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.46
Std Dev 8.346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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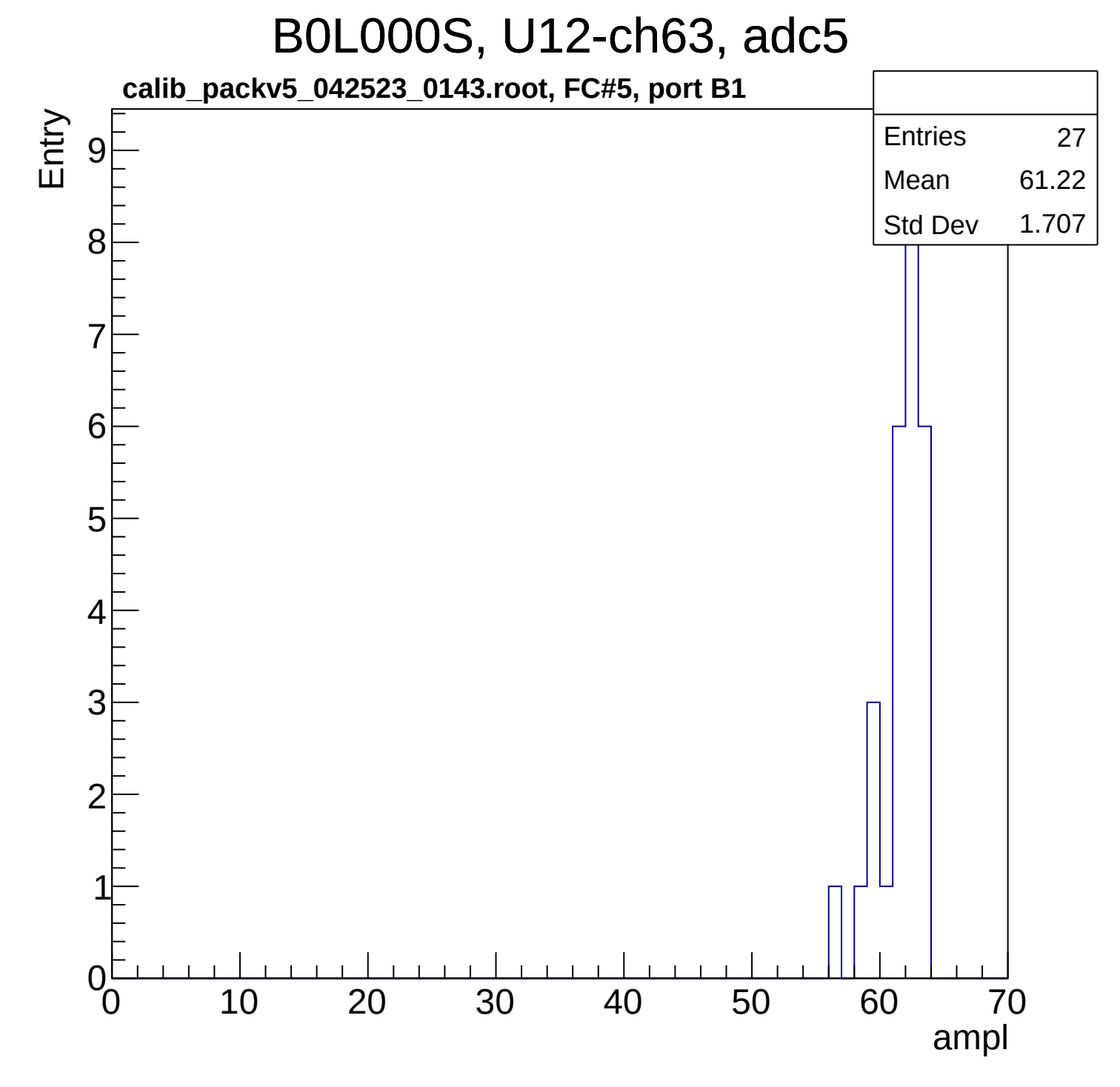
B0L000S, U12-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 61.22
Std Dev 1.707
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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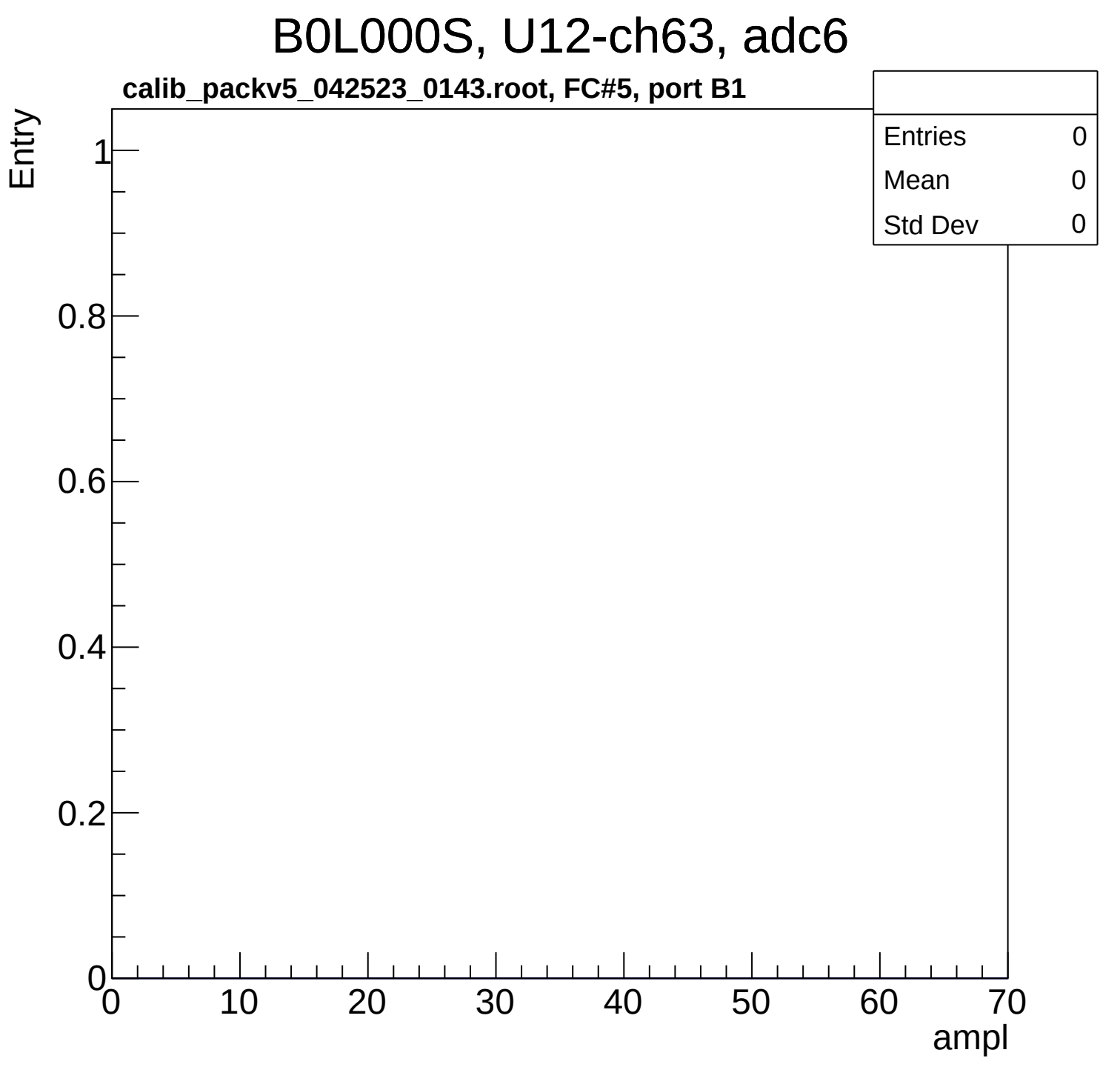
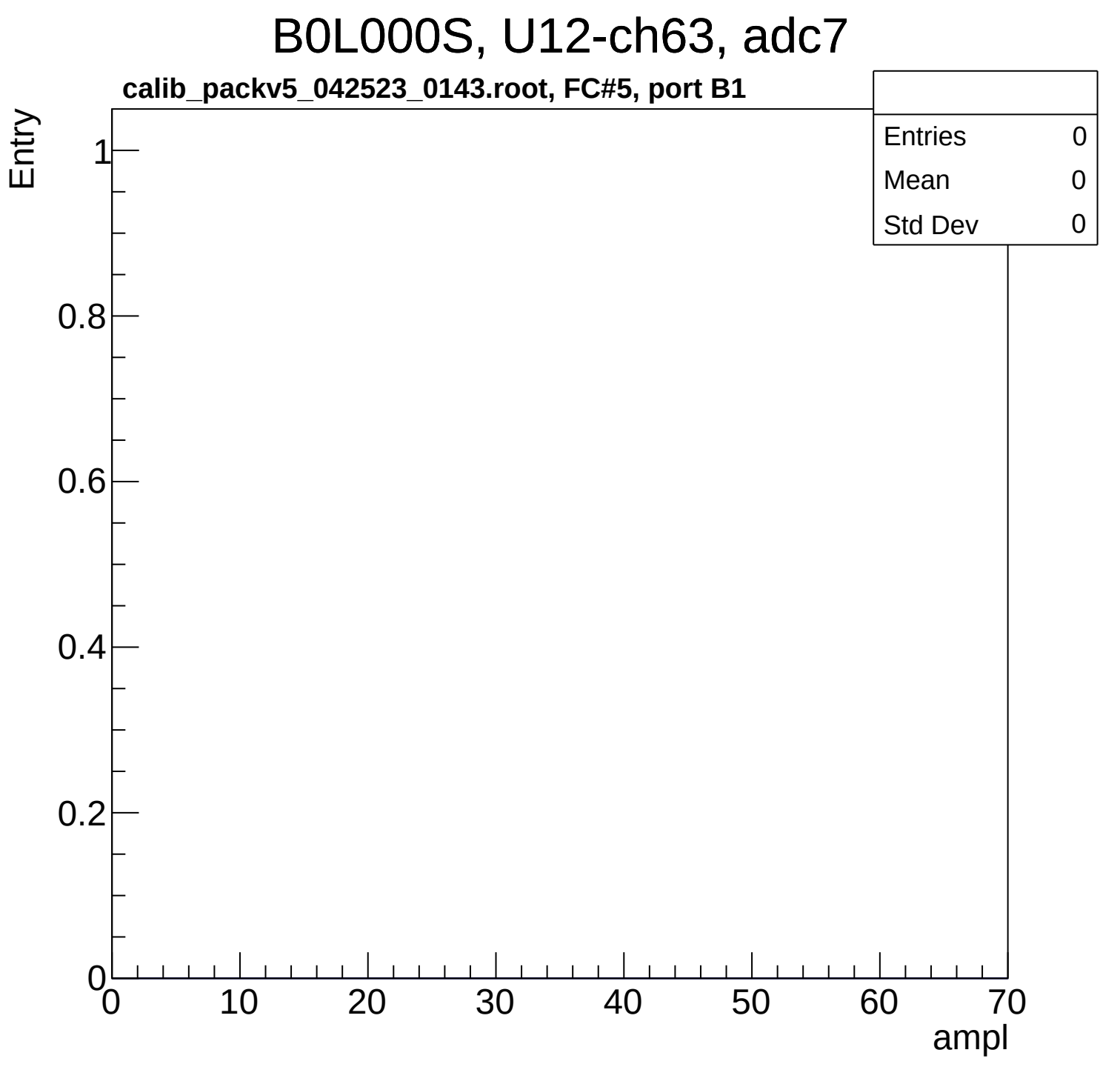

B0L000S, U12-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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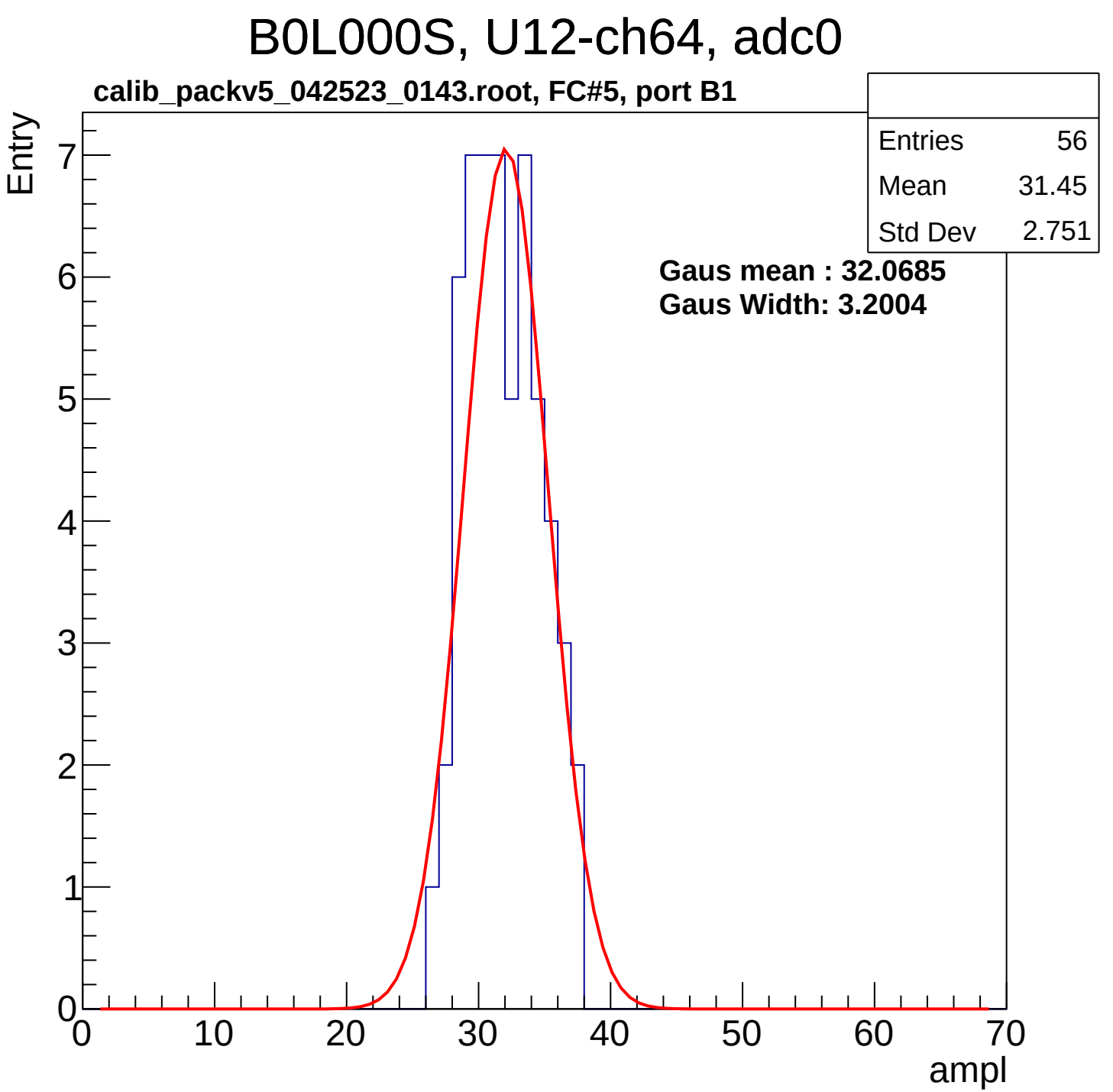
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 31.45
Std Dev 2.751
Gaus mean : 32.0685
Gaus Width: 3.2004
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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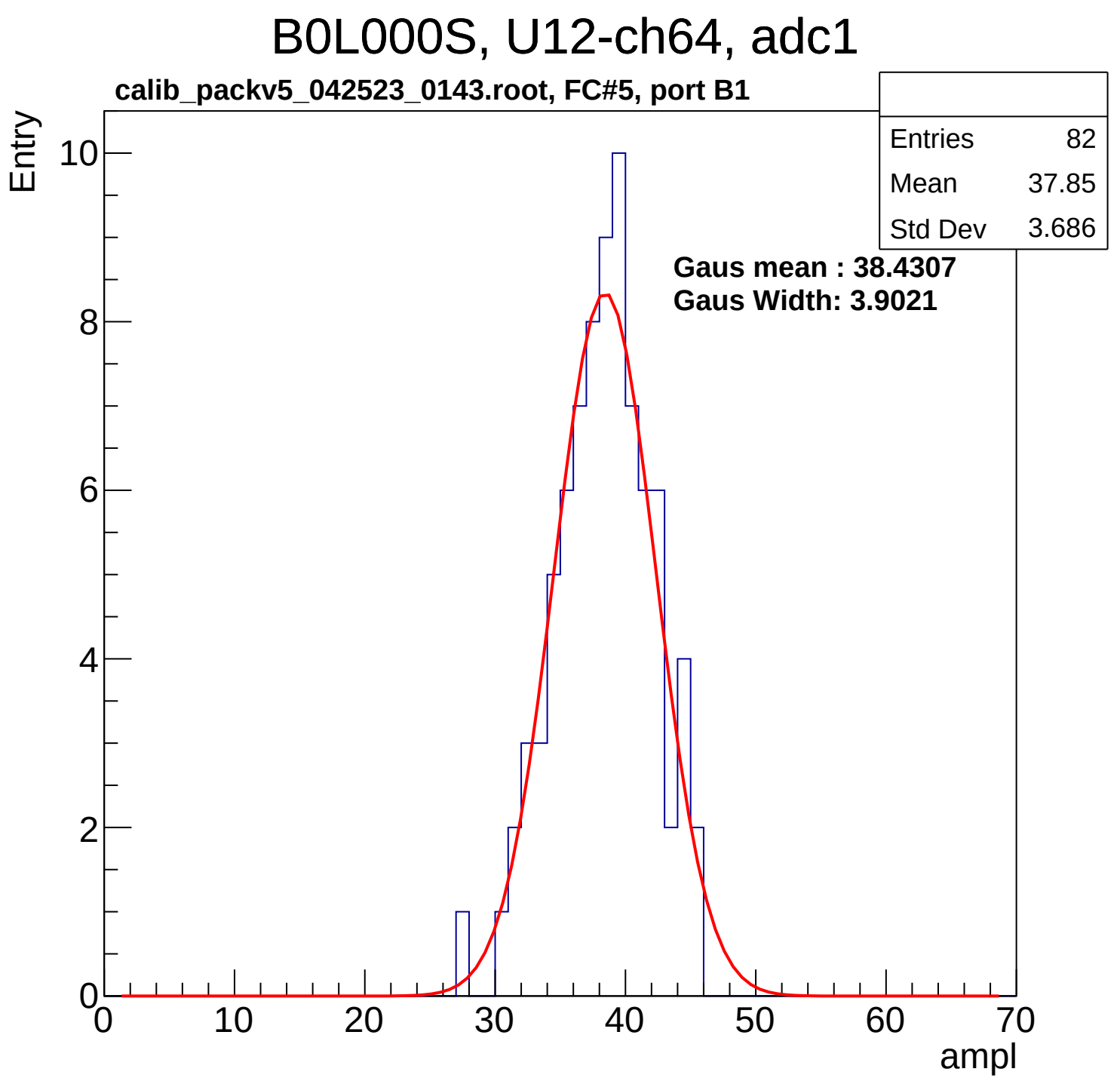
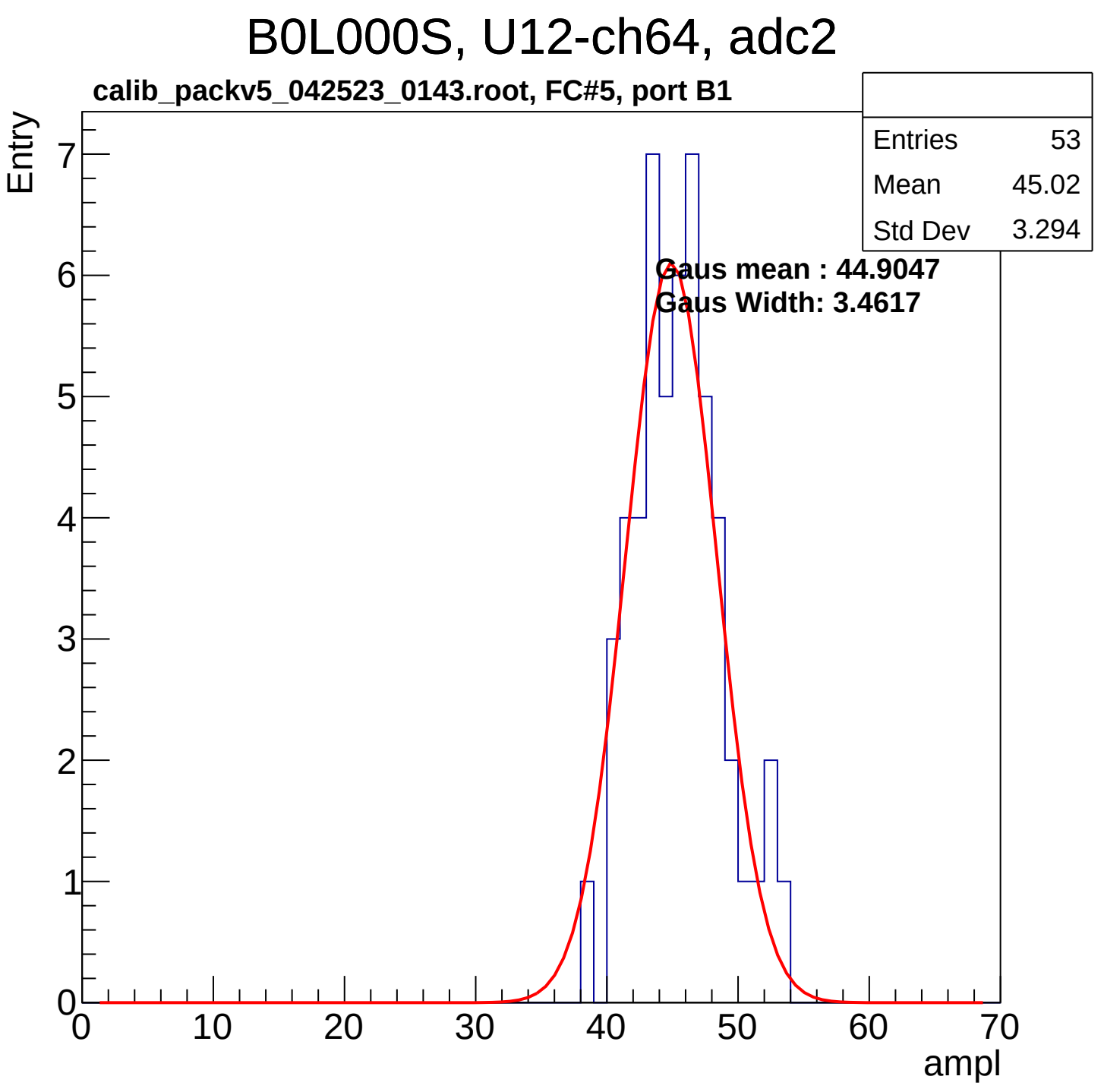
40
50
60
70


B0L000S, U12-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 37.85
Std Dev 3.686
Gaus mean : 38.4307
Gaus Width: 3.9021
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 45.02
Std Dev 3.294
Gaus mean : 44.9047
Gaus Width: 3.4617
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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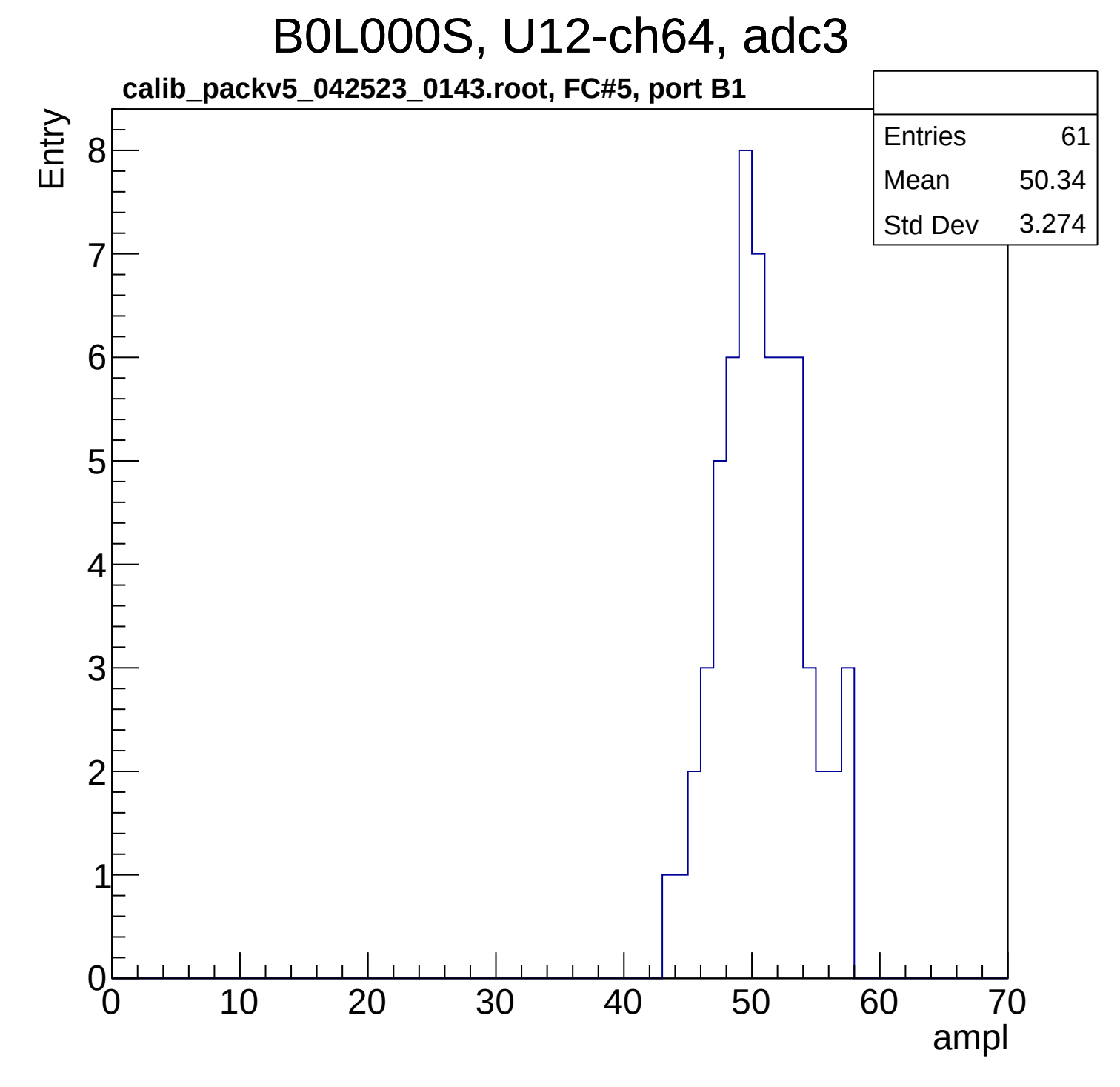
B0L000S, U12-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.34
Std Dev 3.274
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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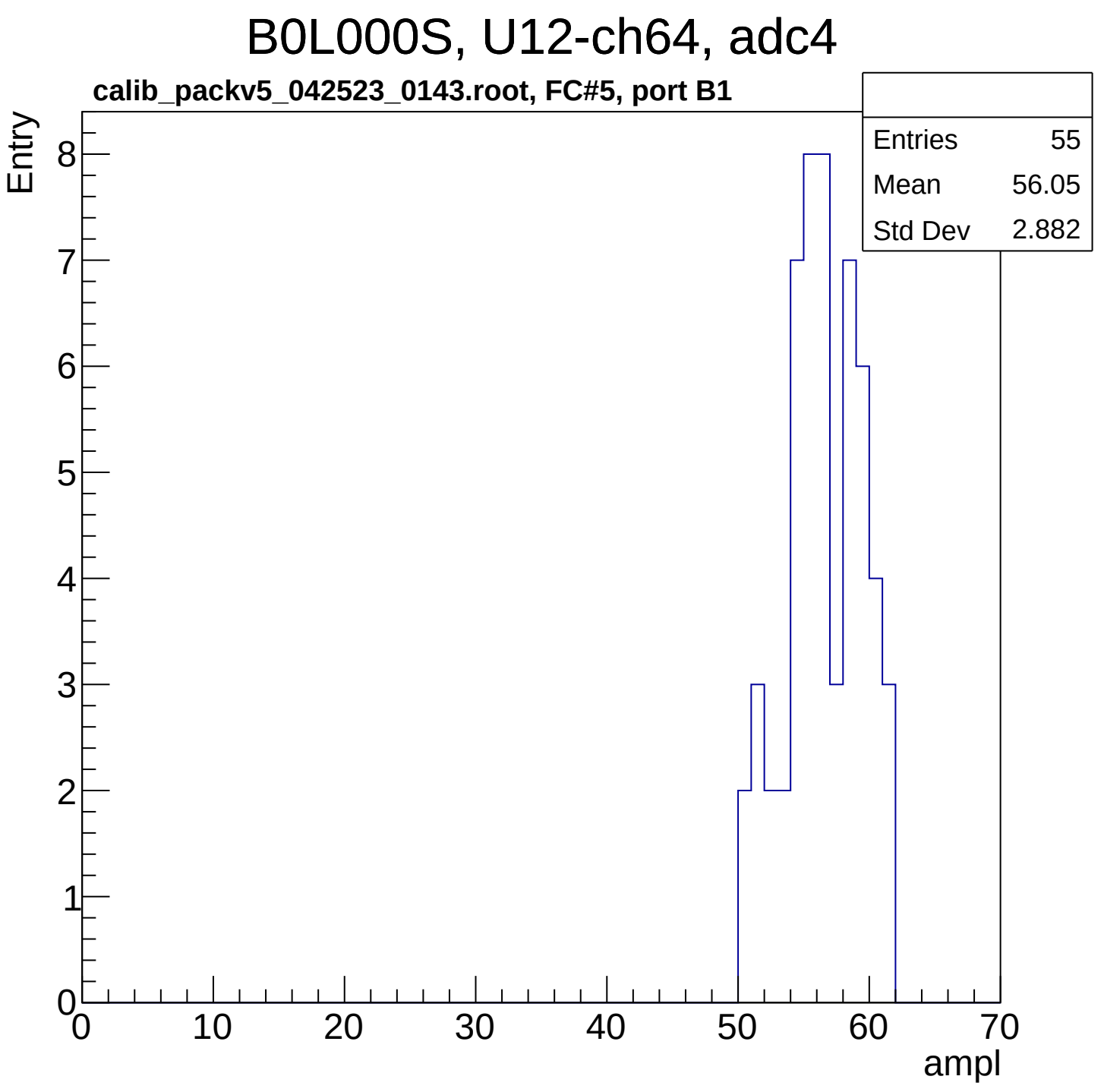
B0L000S, U12-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.05
Std Dev 2.882
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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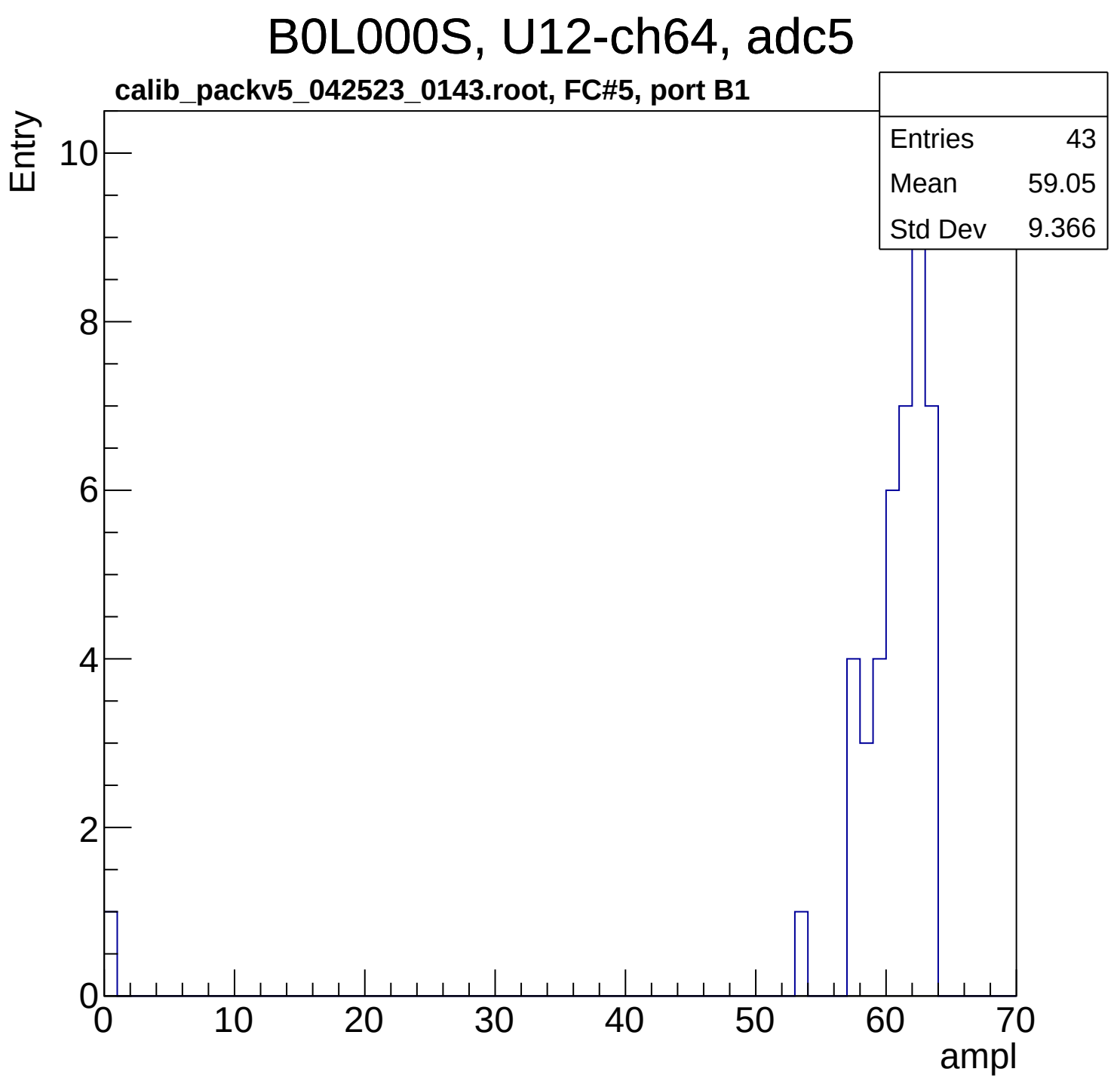
B0L000S, U12-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.05
Std Dev 9.366
Entry
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2
4
6
8
10
0
10
20
30
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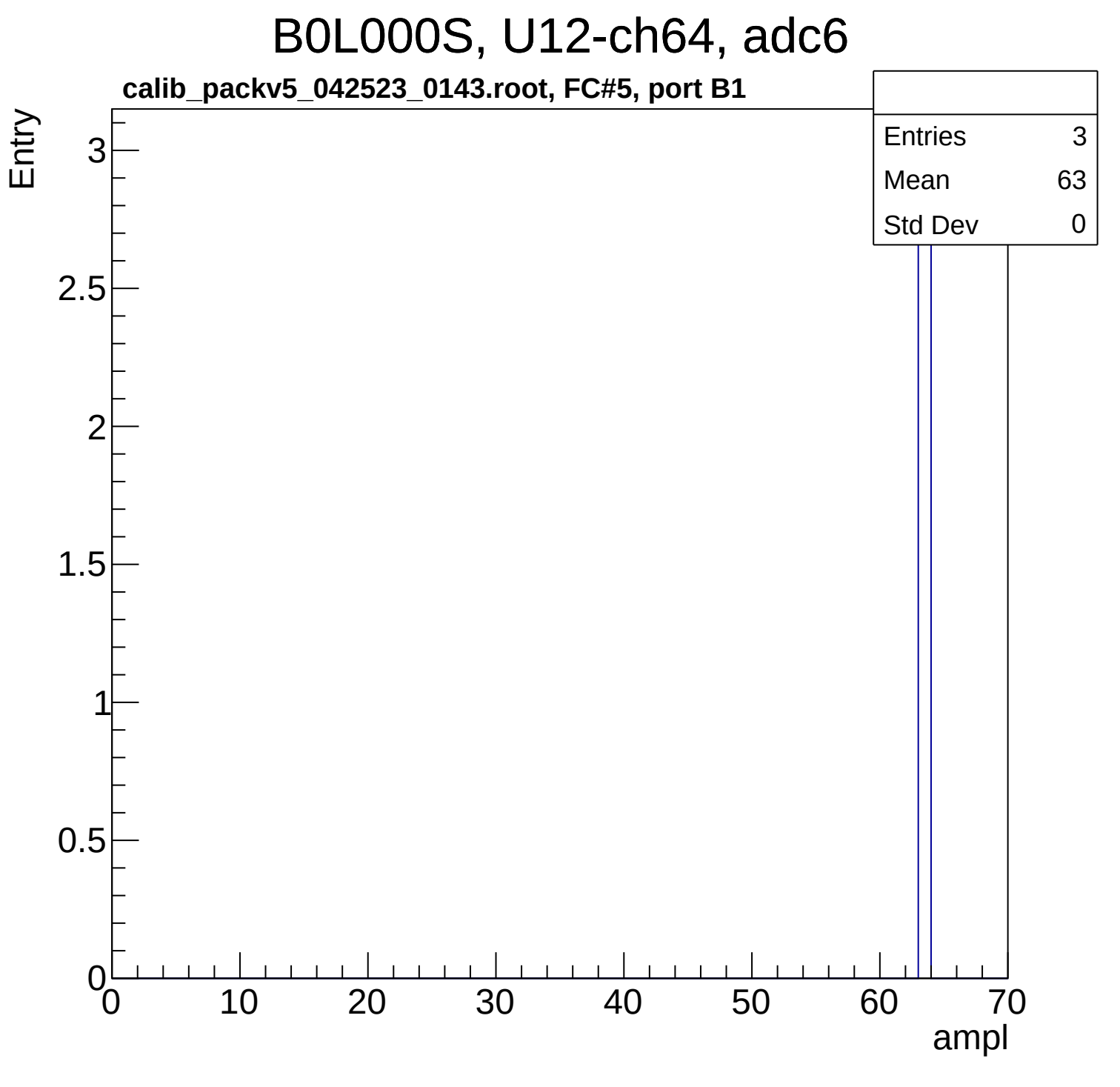
50
60
70
ampl


B0L000S, U12-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 63
Std Dev 0
Entry
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0.5
1
1.5
2
2.5
3
0
10
20
30
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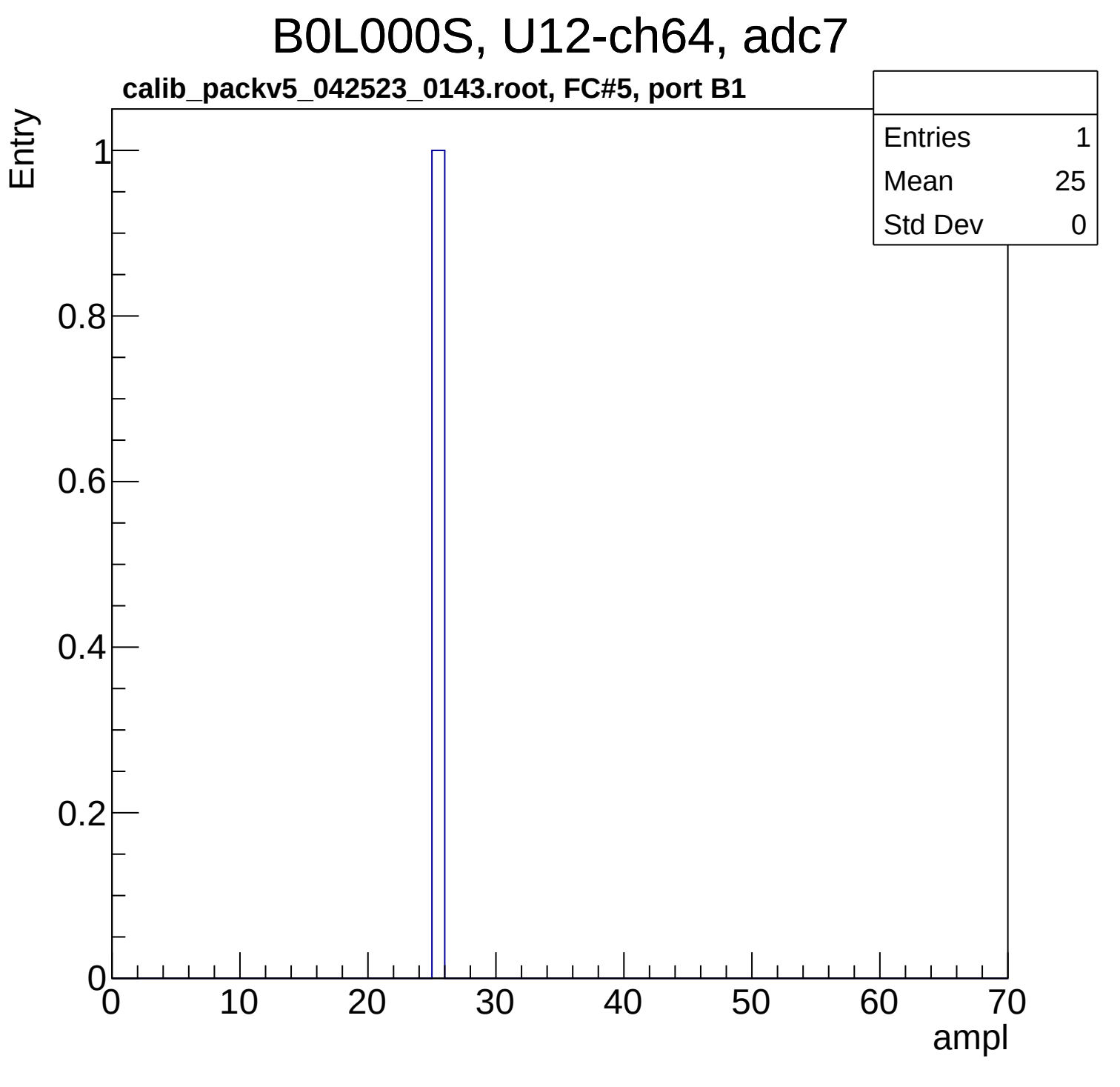
50
60
70
ampl


B0L000S, U12-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 25
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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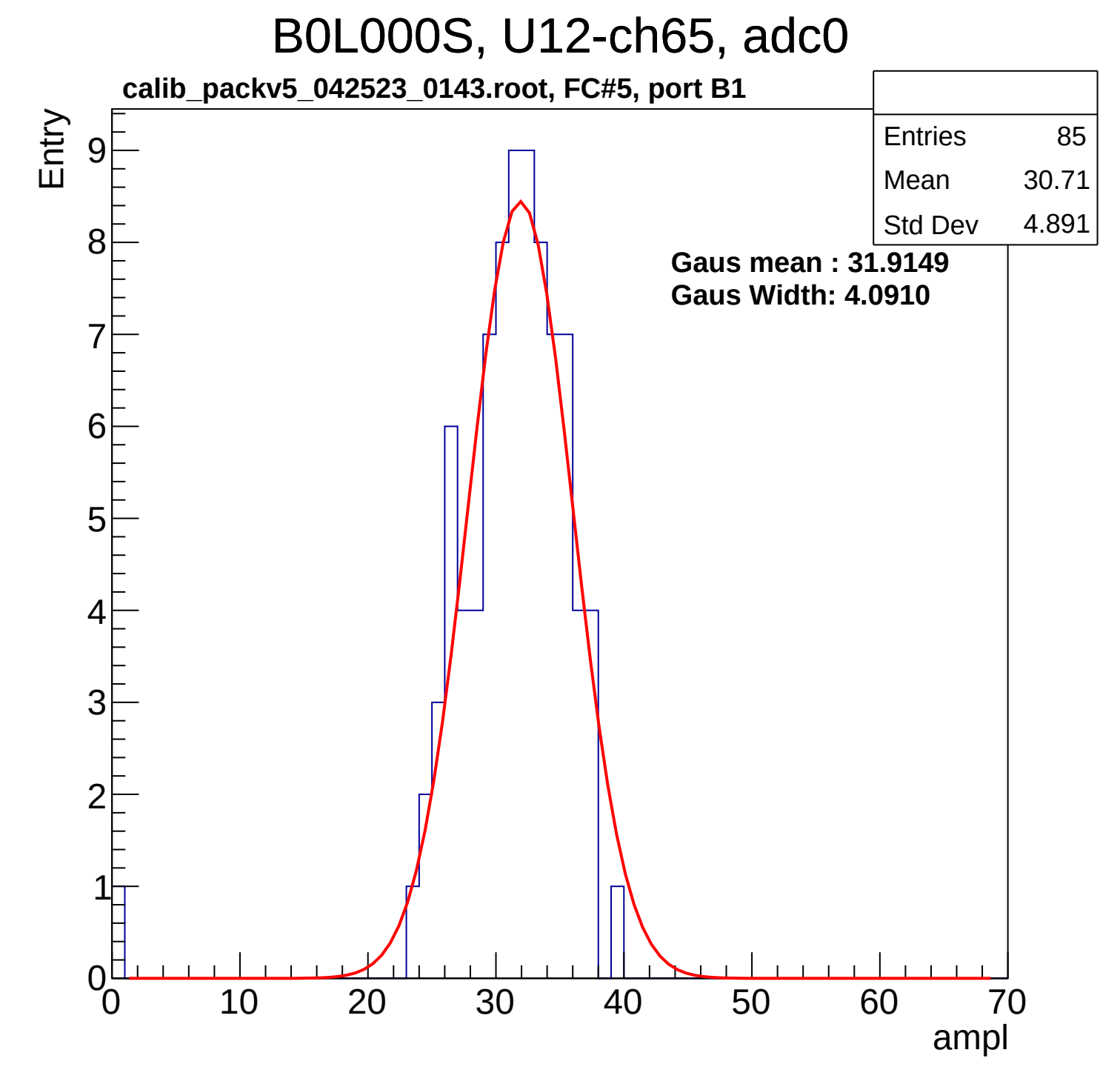
50
60
70
ampl


B0L000S, U12-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.71
Std Dev 4.891
Gaus mean : 31.9149
Gaus Width: 4.0910
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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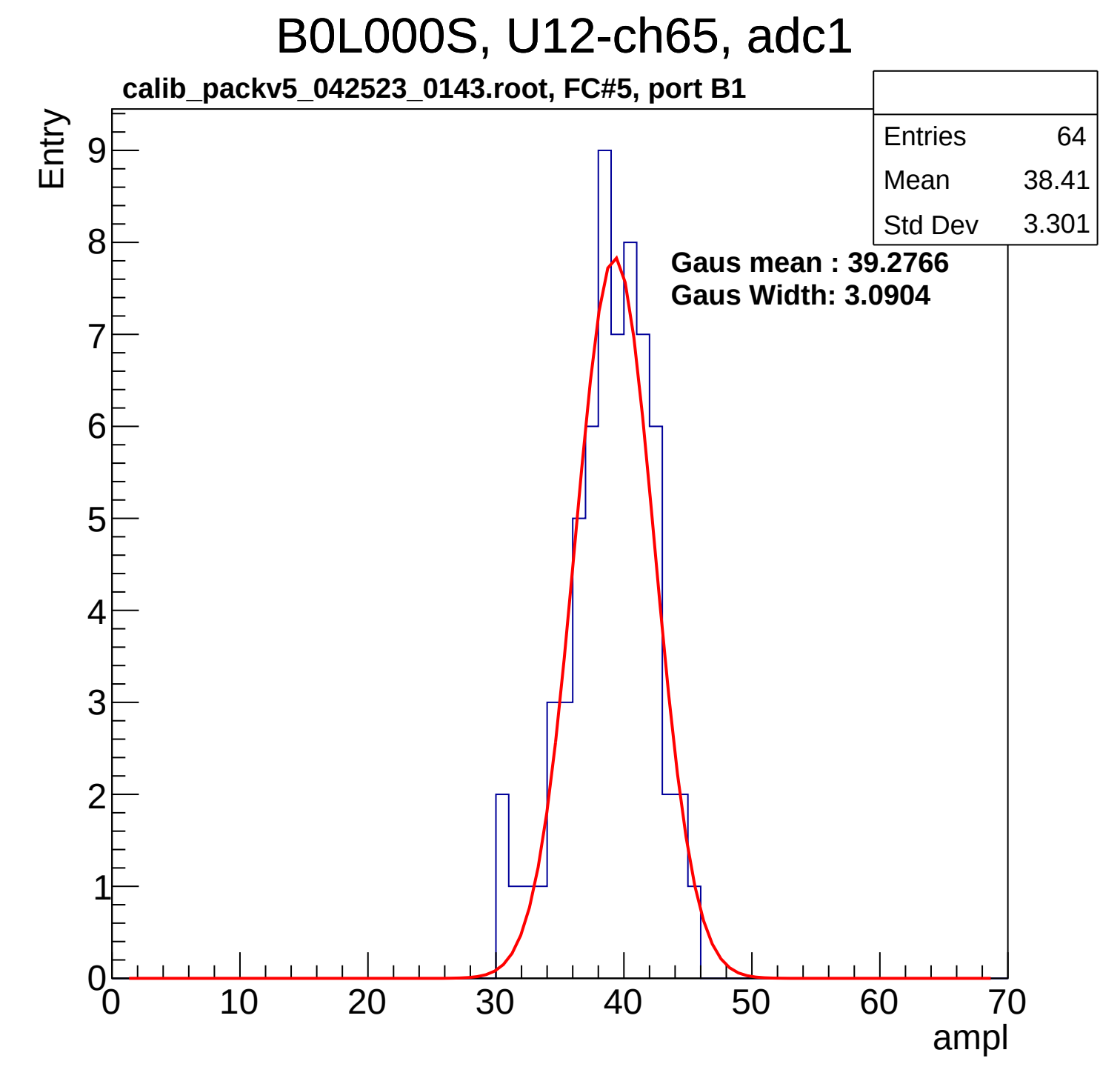
B0L000S, U12-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.41
Std Dev 3.301
Gaus mean : 39.2766
Gaus Width: 3.0904
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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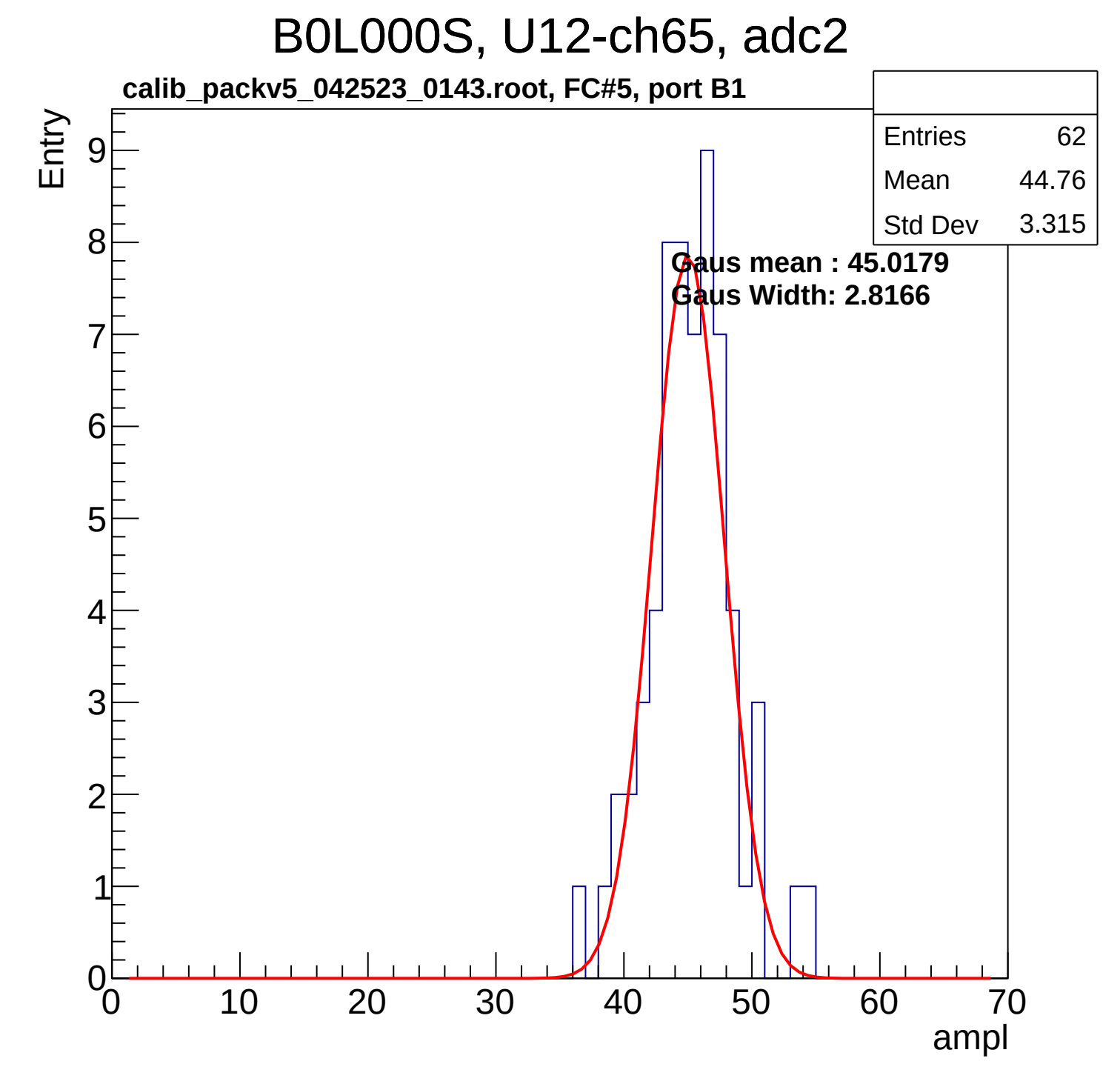
40
50
60
70


B0L000S, U12-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 44.76
Std Dev 3.315
Gaus mean : 45.0179
Gaus Width: 2.8166
Entry
ampl
0
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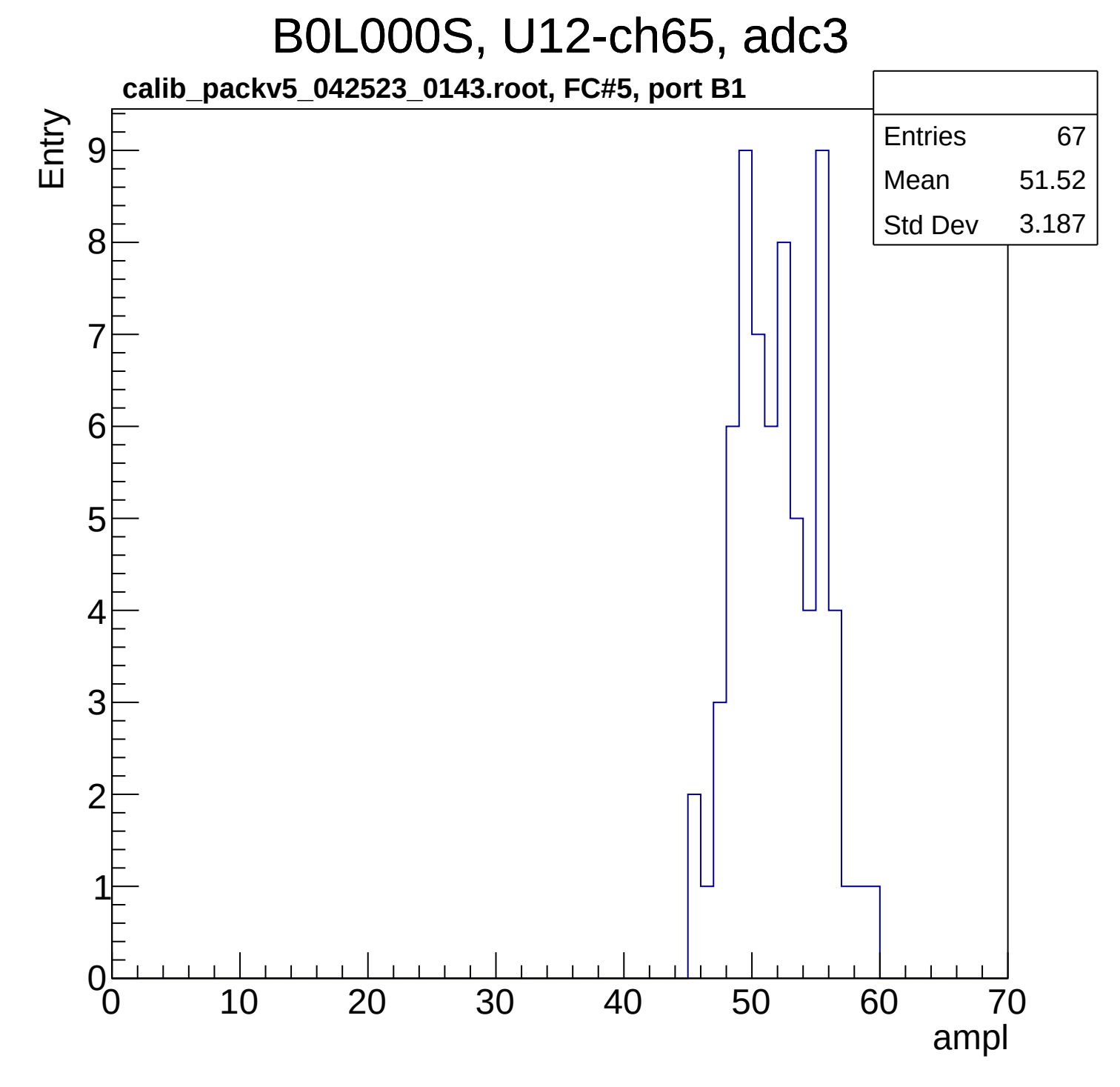
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 51.52
Std Dev 3.187
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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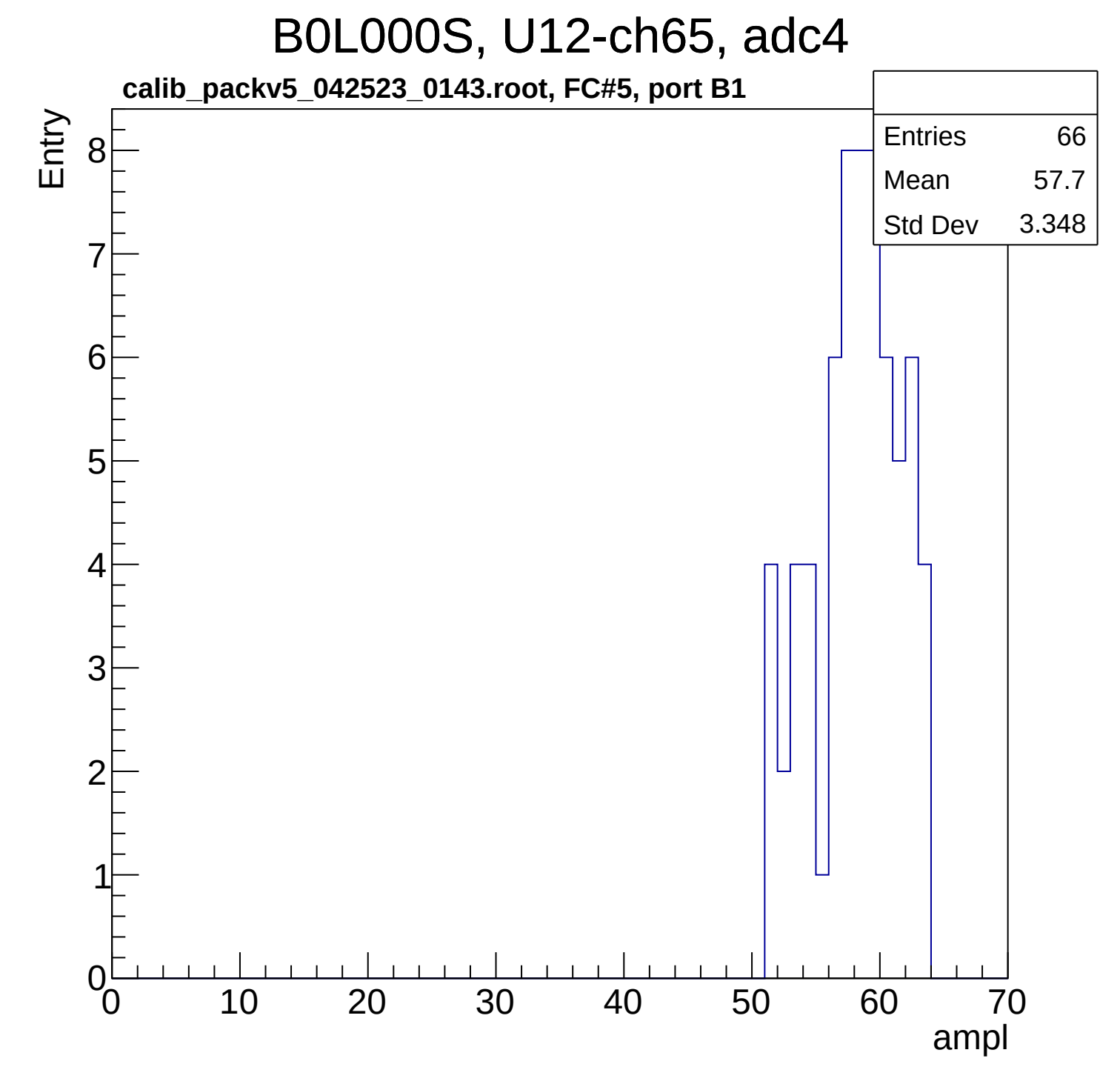
B0L000S, U12-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.7
Std Dev 3.348
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
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50
60
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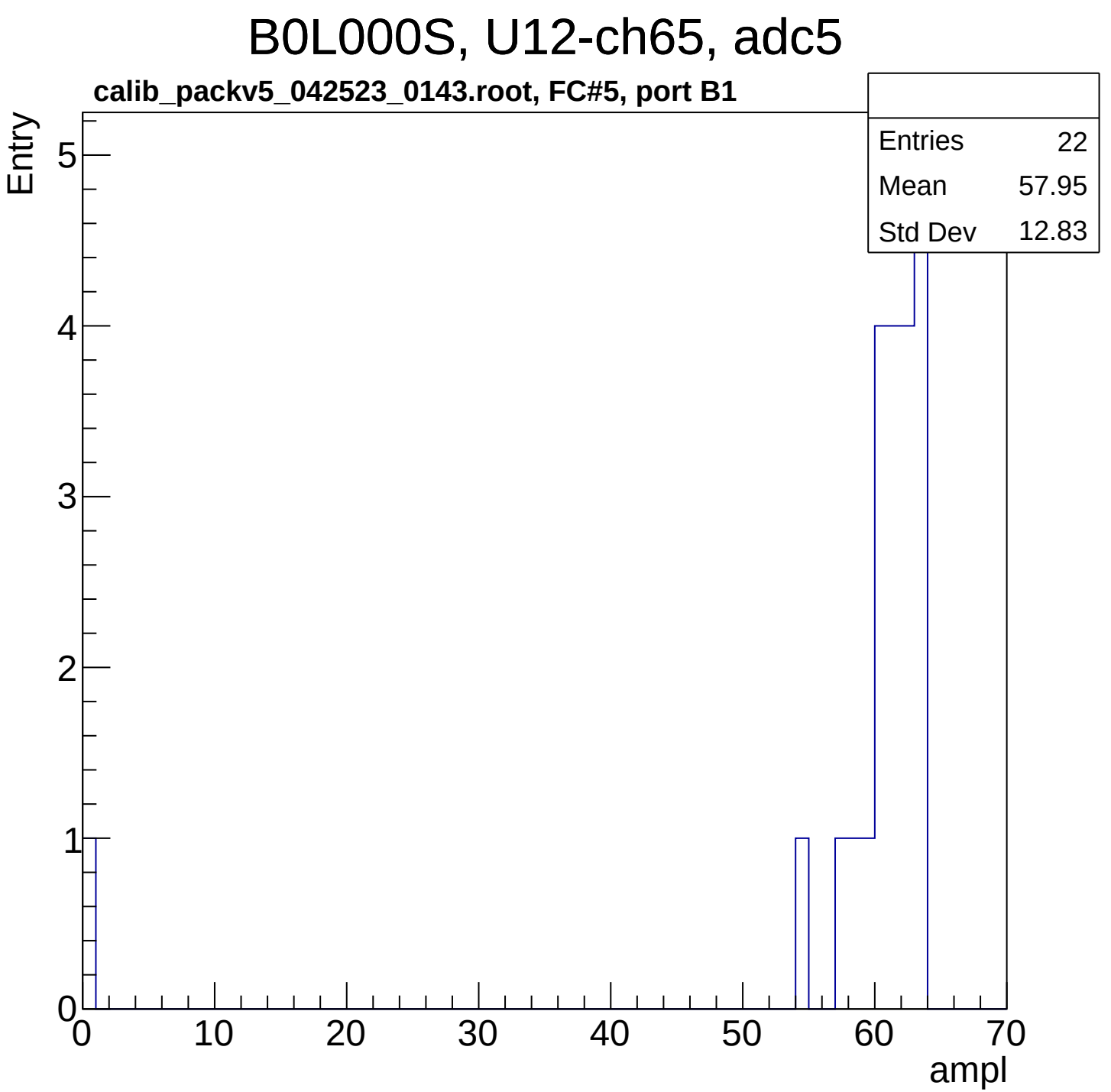
B0L000S, U12-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 57.95
Std Dev 12.83
Entry
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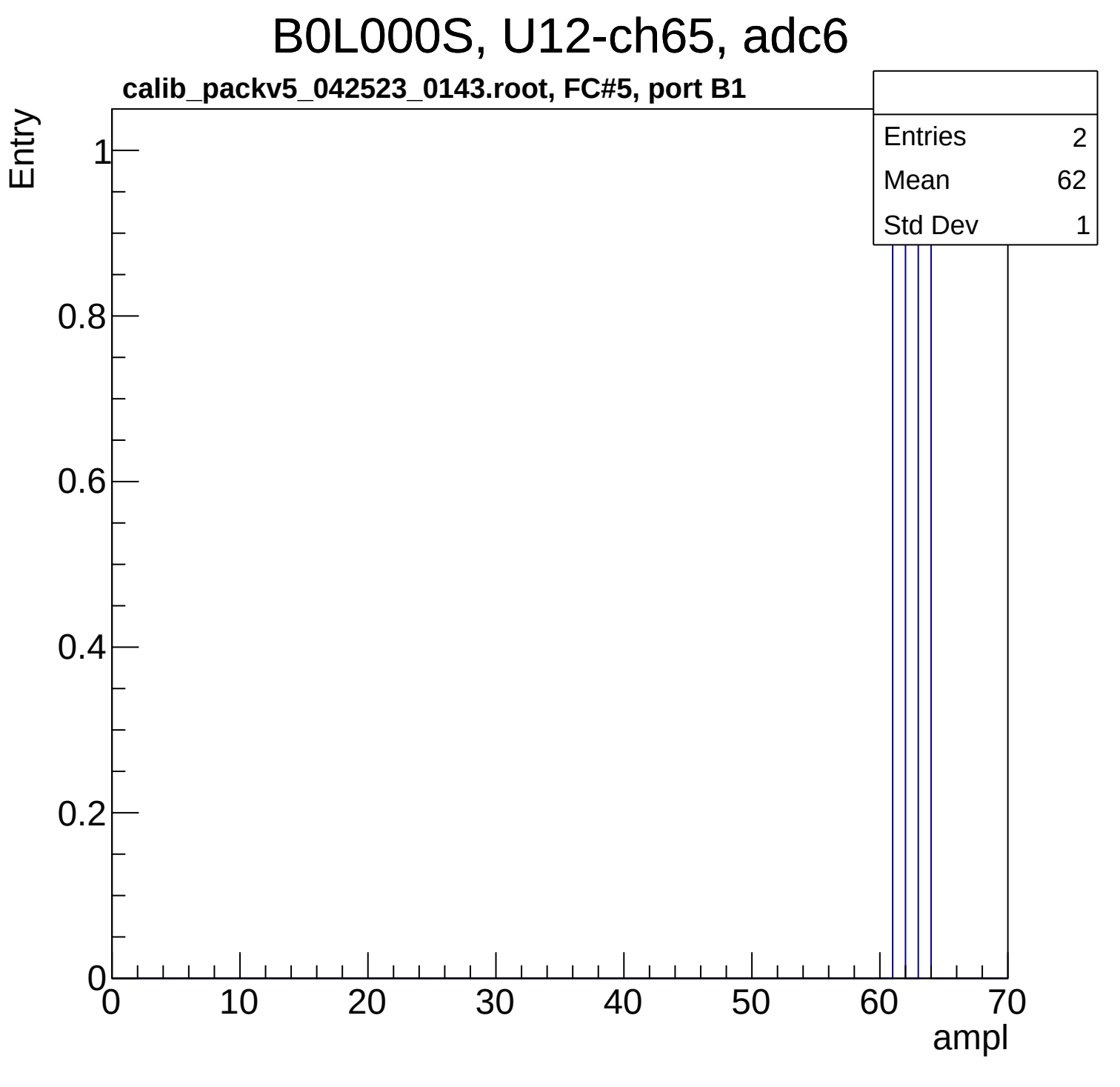
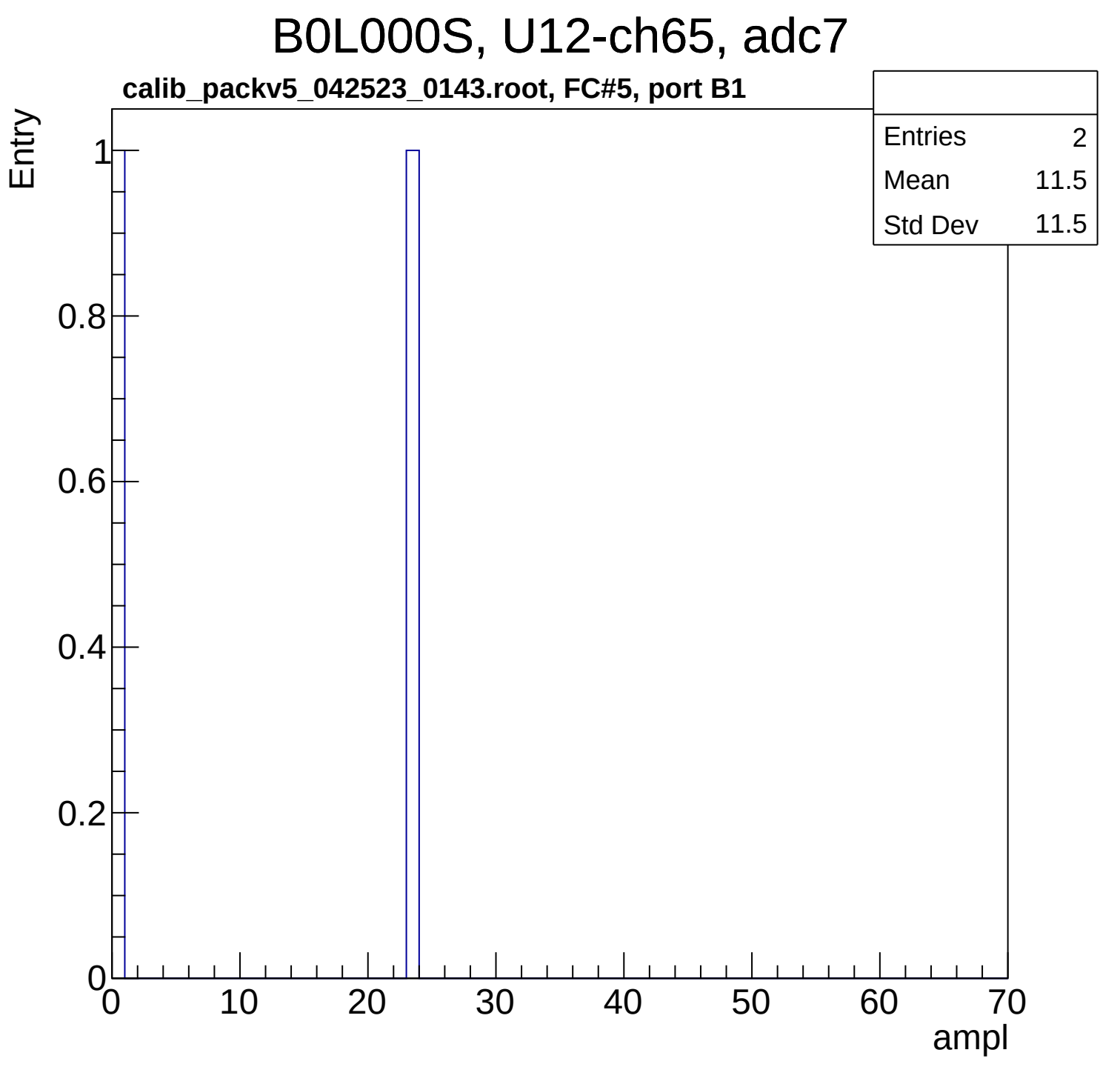
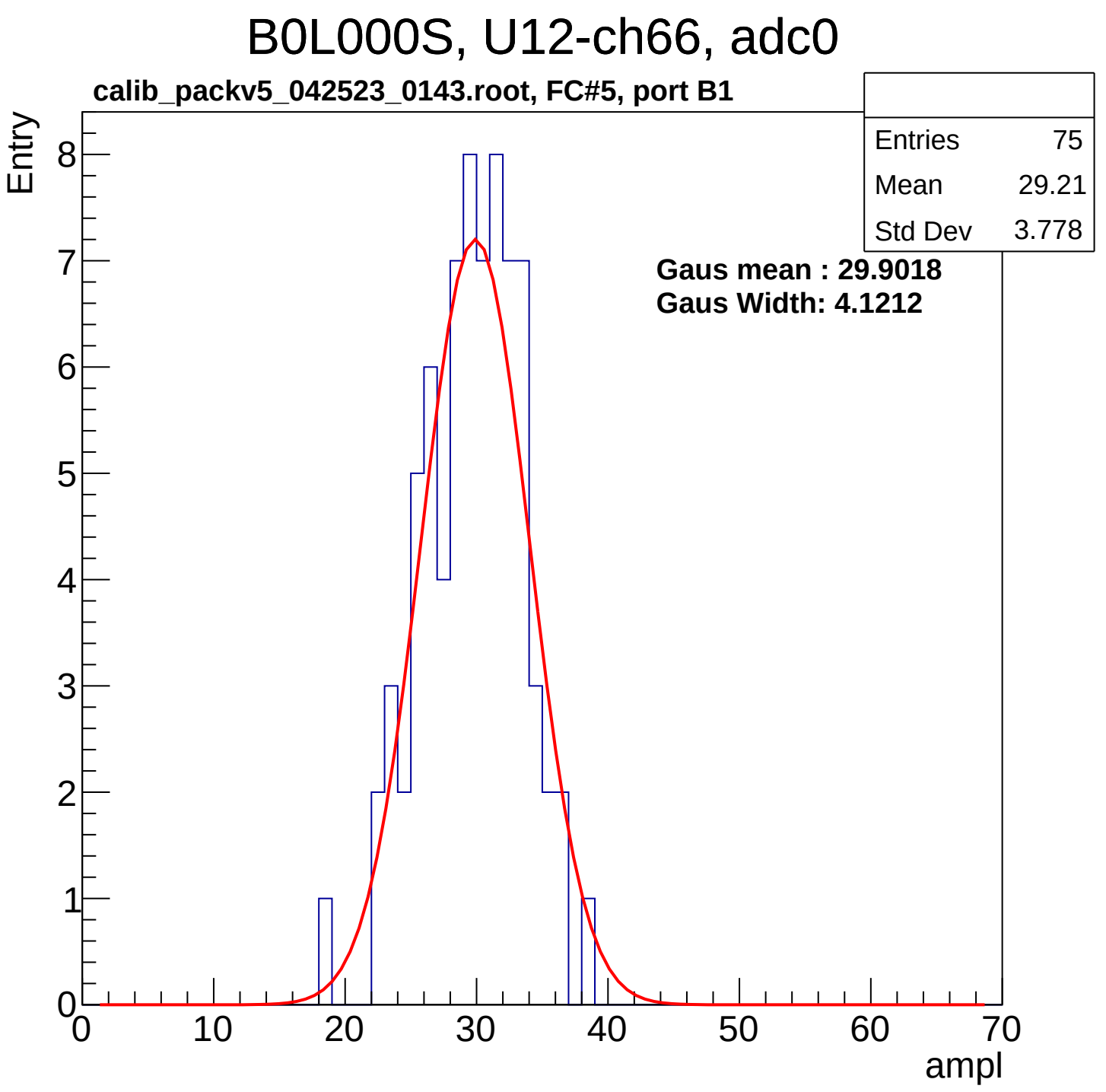
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.21
Std Dev 3.778
Gaus mean : 29.9018
Gaus Width: 4.1212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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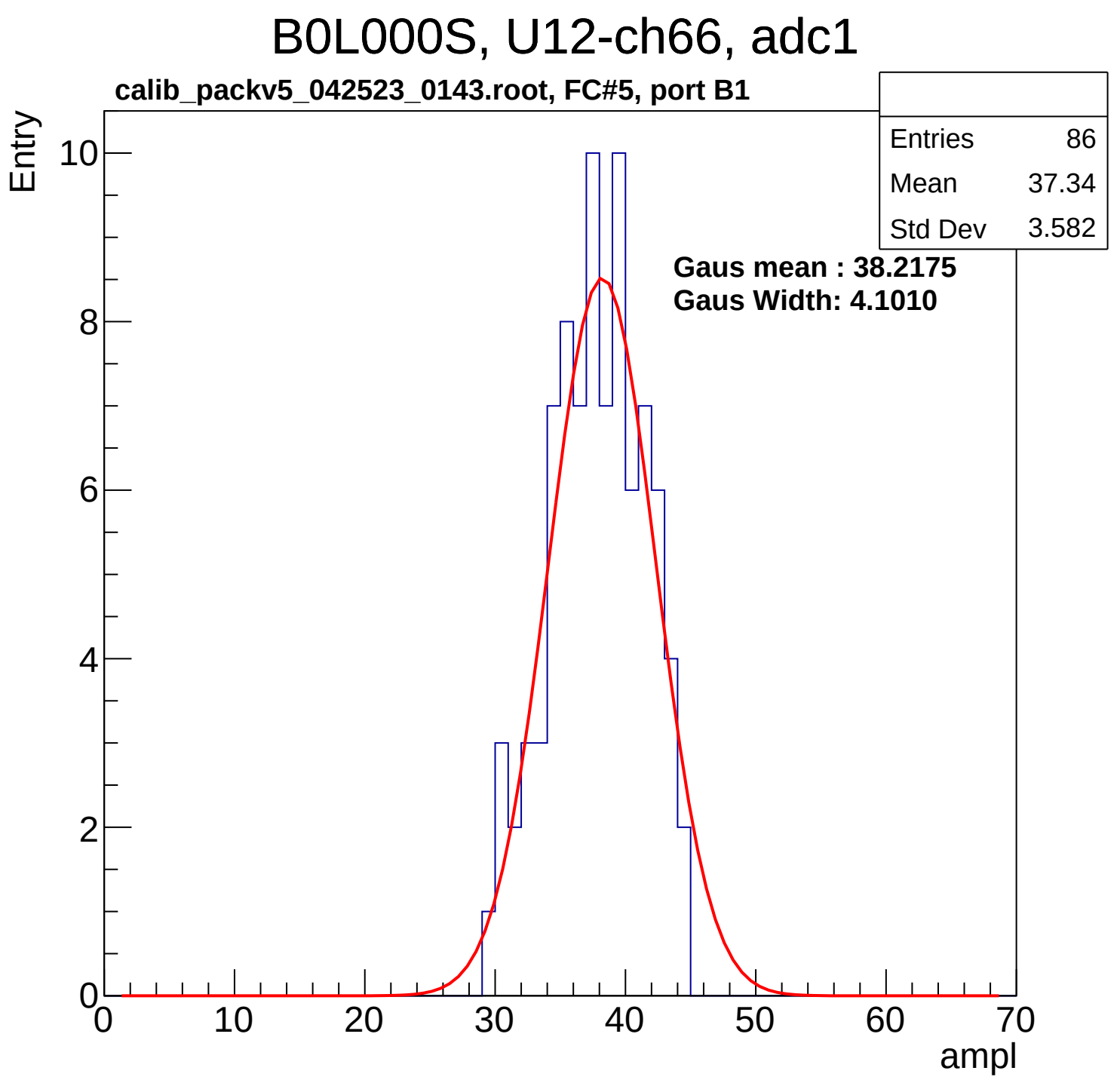
B0L000S, U12-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 37.34
Std Dev 3.582
Gaus mean : 38.2175
Gaus Width: 4.1010
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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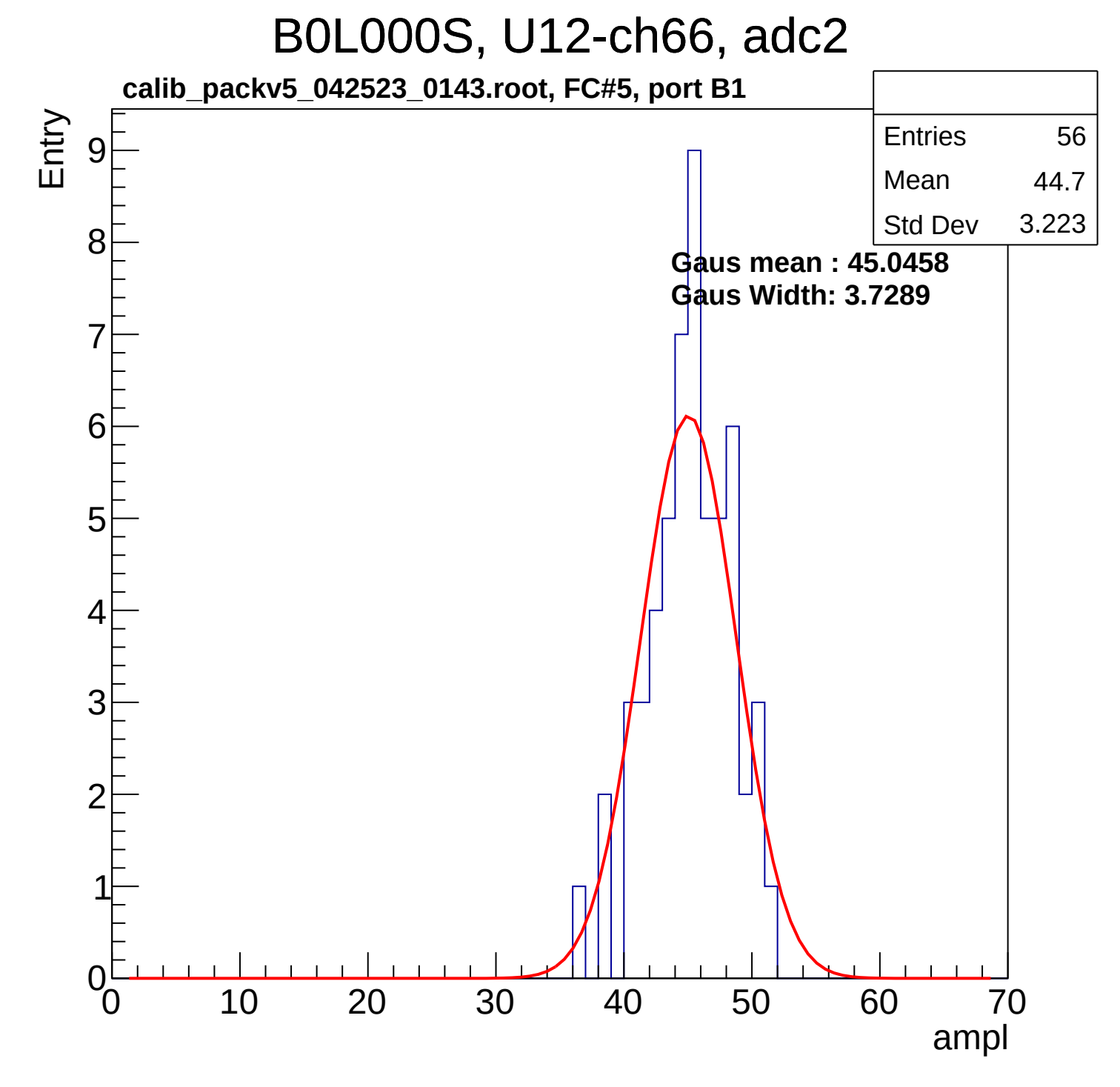
B0L000S, U12-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.7
Std Dev 3.223
Gaus mean : 45.0458
Gaus Width: 3.7289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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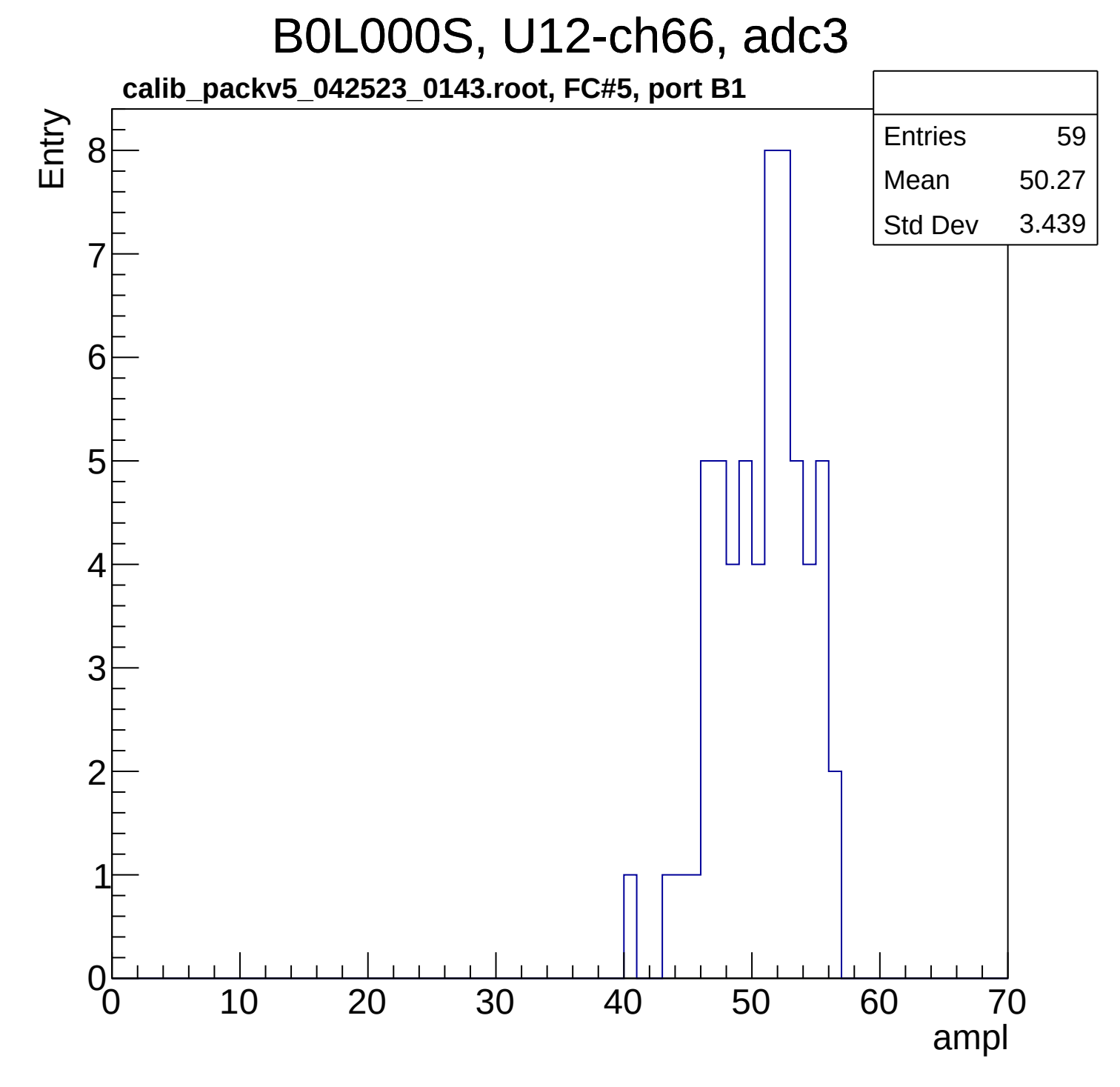
B0L000S, U12-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 50.27
Std Dev 3.439
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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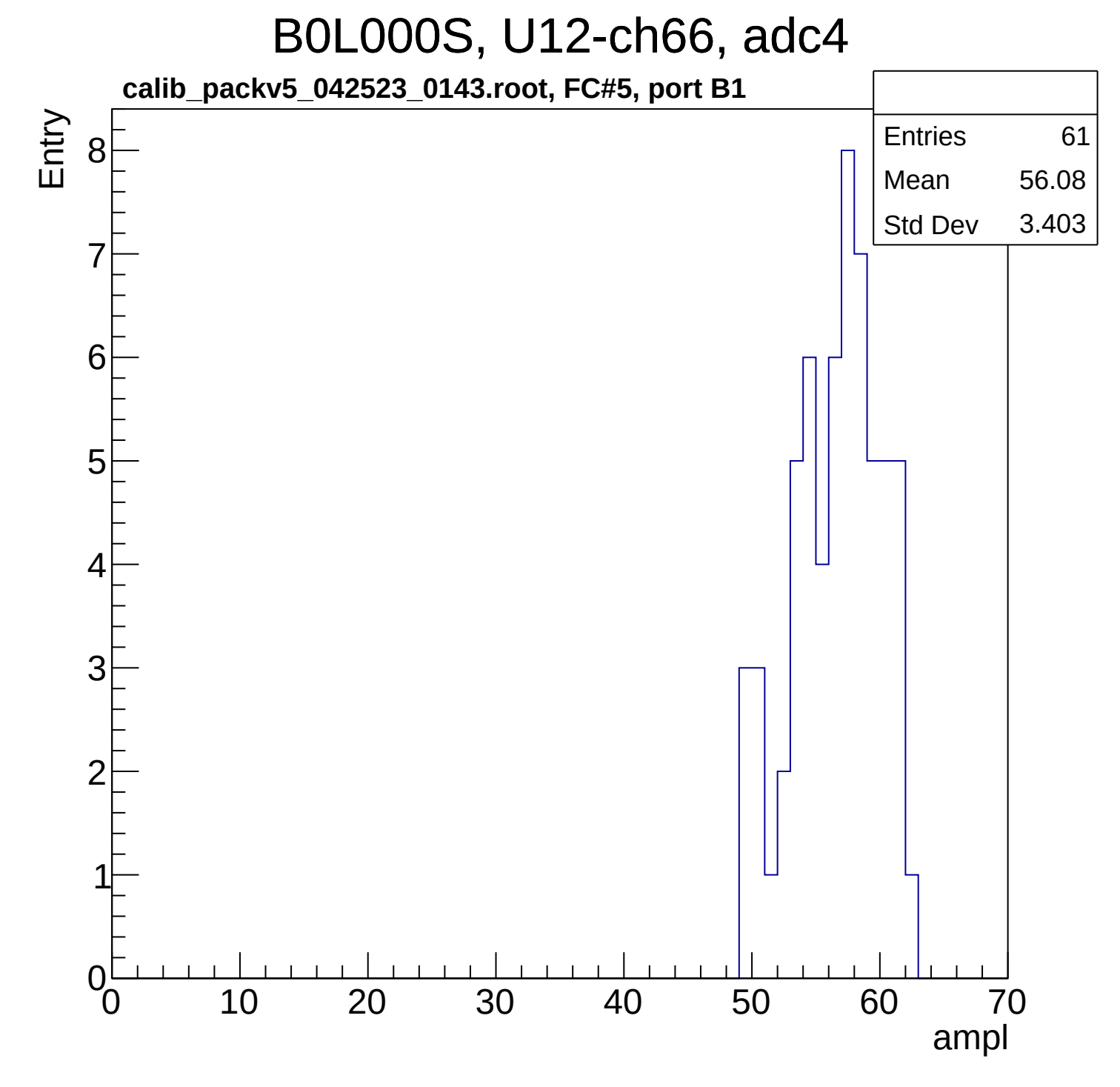
B0L000S, U12-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.08
Std Dev 3.403
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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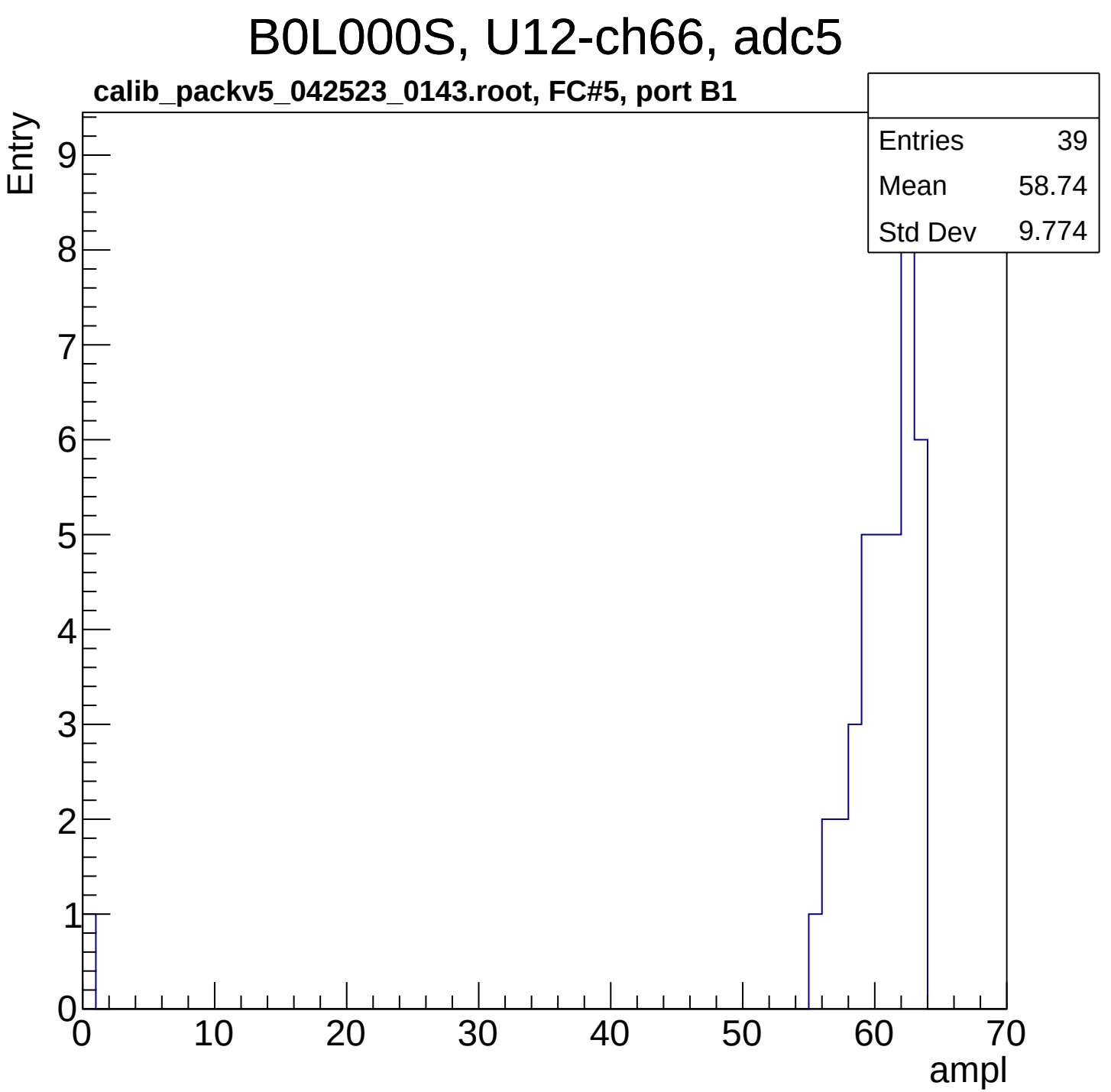
B0L000S, U12-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.74
Std Dev 9.774
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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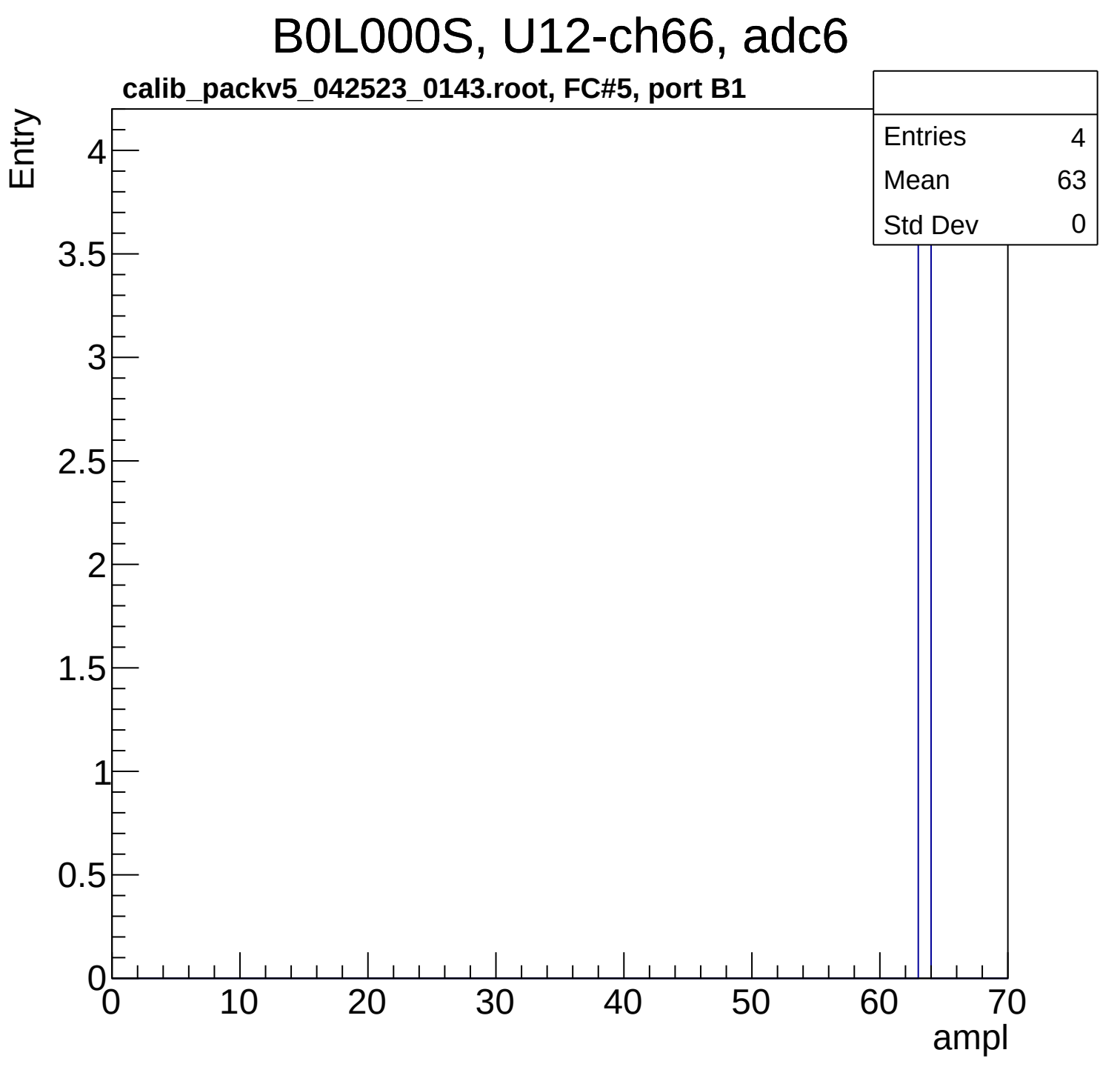
B0L000S, U12-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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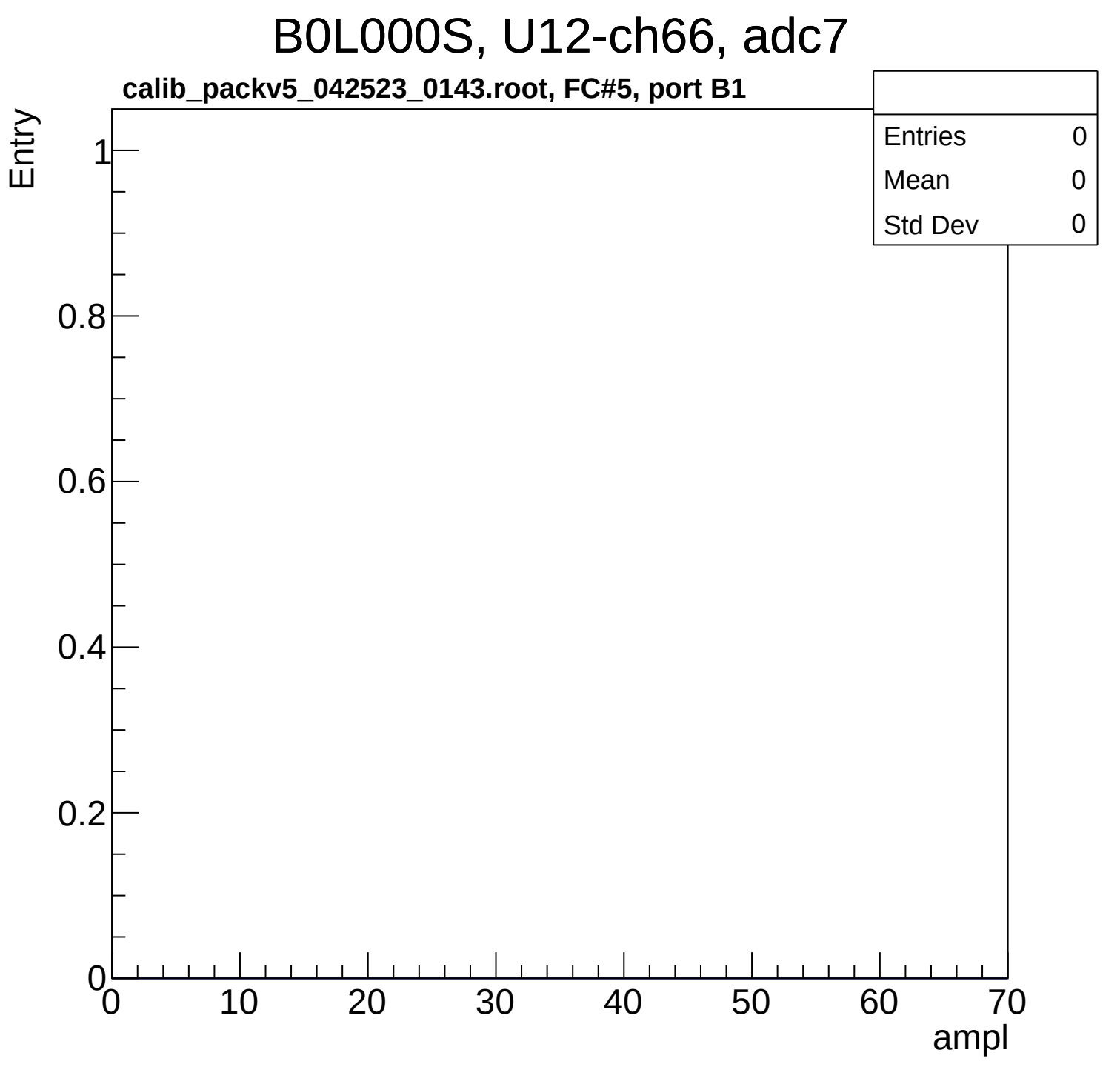
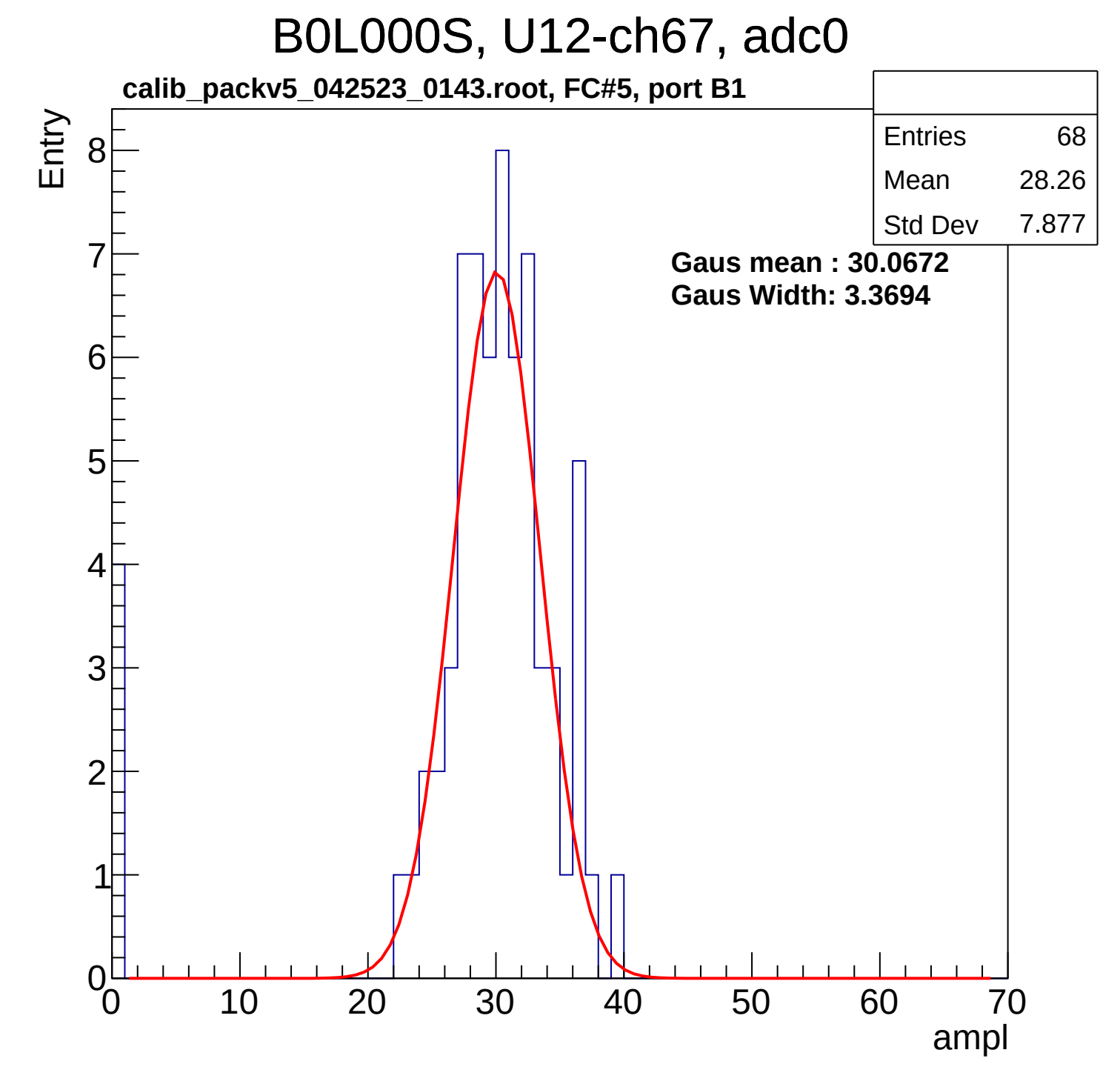
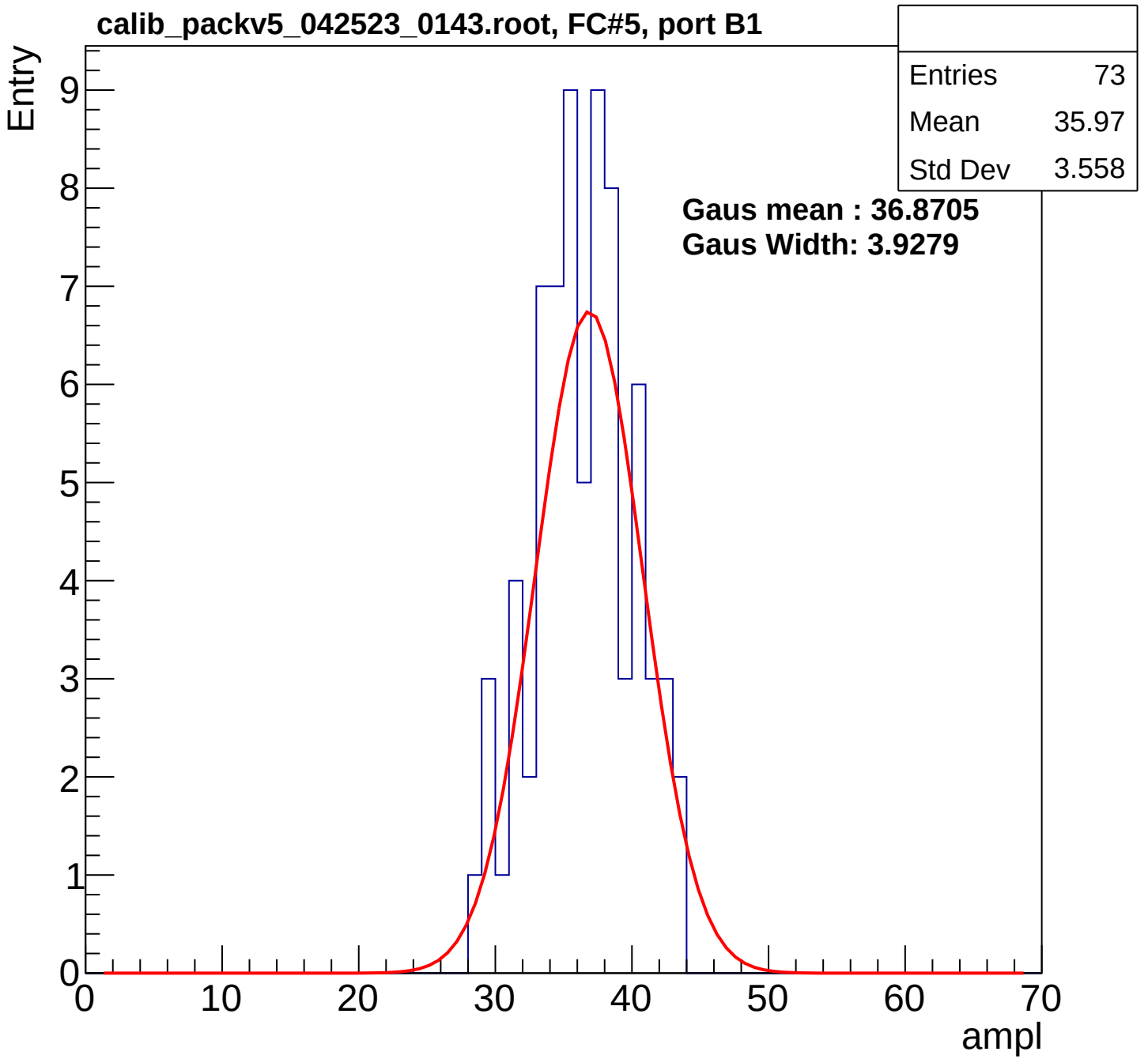

B0L000S, U12-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.26
Std Dev 7.877
Gaus mean : 30.0672
Gaus Width: 3.3694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.97
Std Dev 3.558
Gaus mean : 36.8705
Gaus Width: 3.9279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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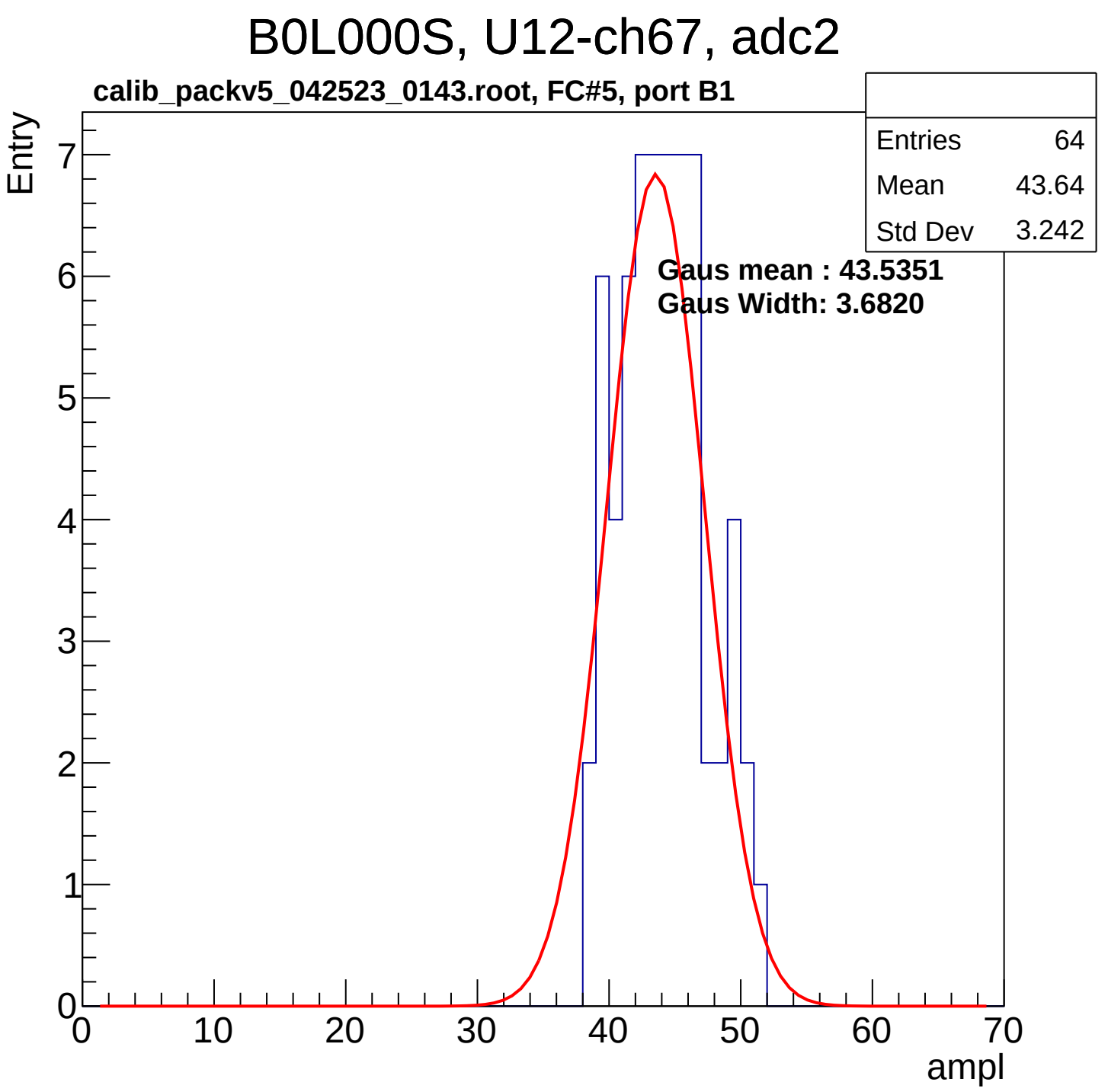
B0L000S, U12-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.64
Std Dev 3.242
Gaus mean : 43.5351
Gaus Width: 3.6820
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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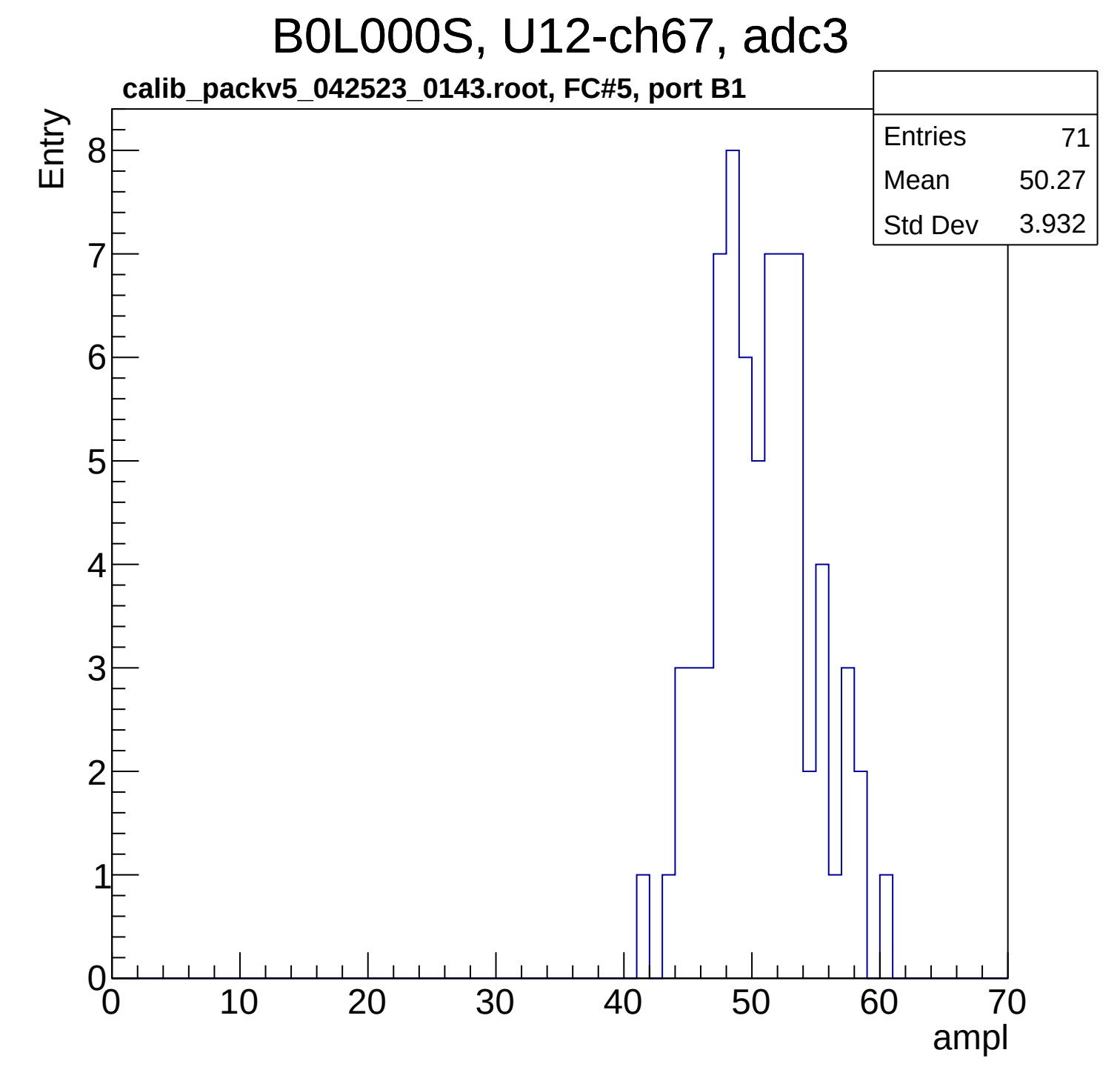
B0L000S, U12-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.27
Std Dev 3.932
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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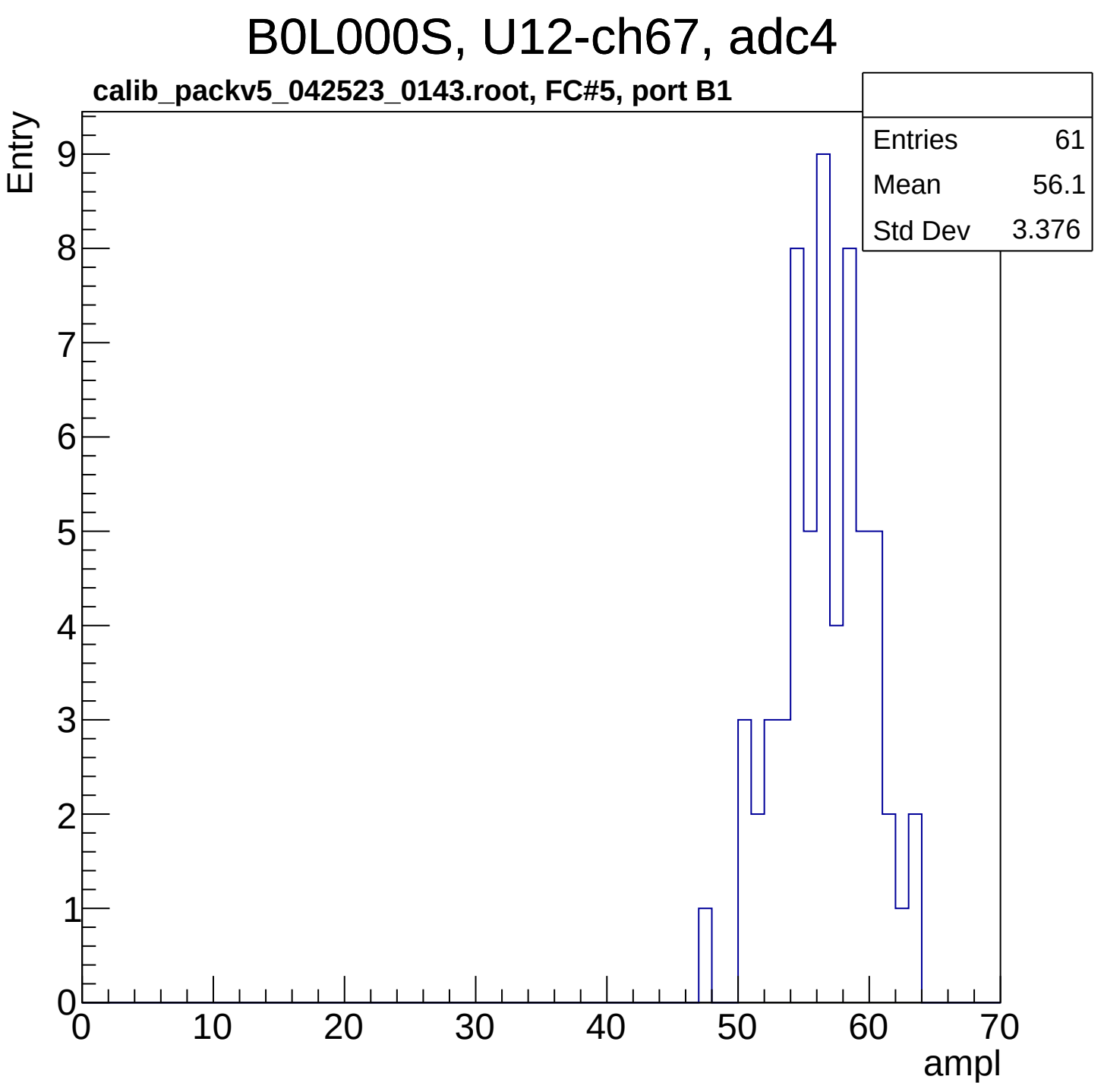
B0L000S, U12-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.1
Std Dev 3.376
Entry
ampl
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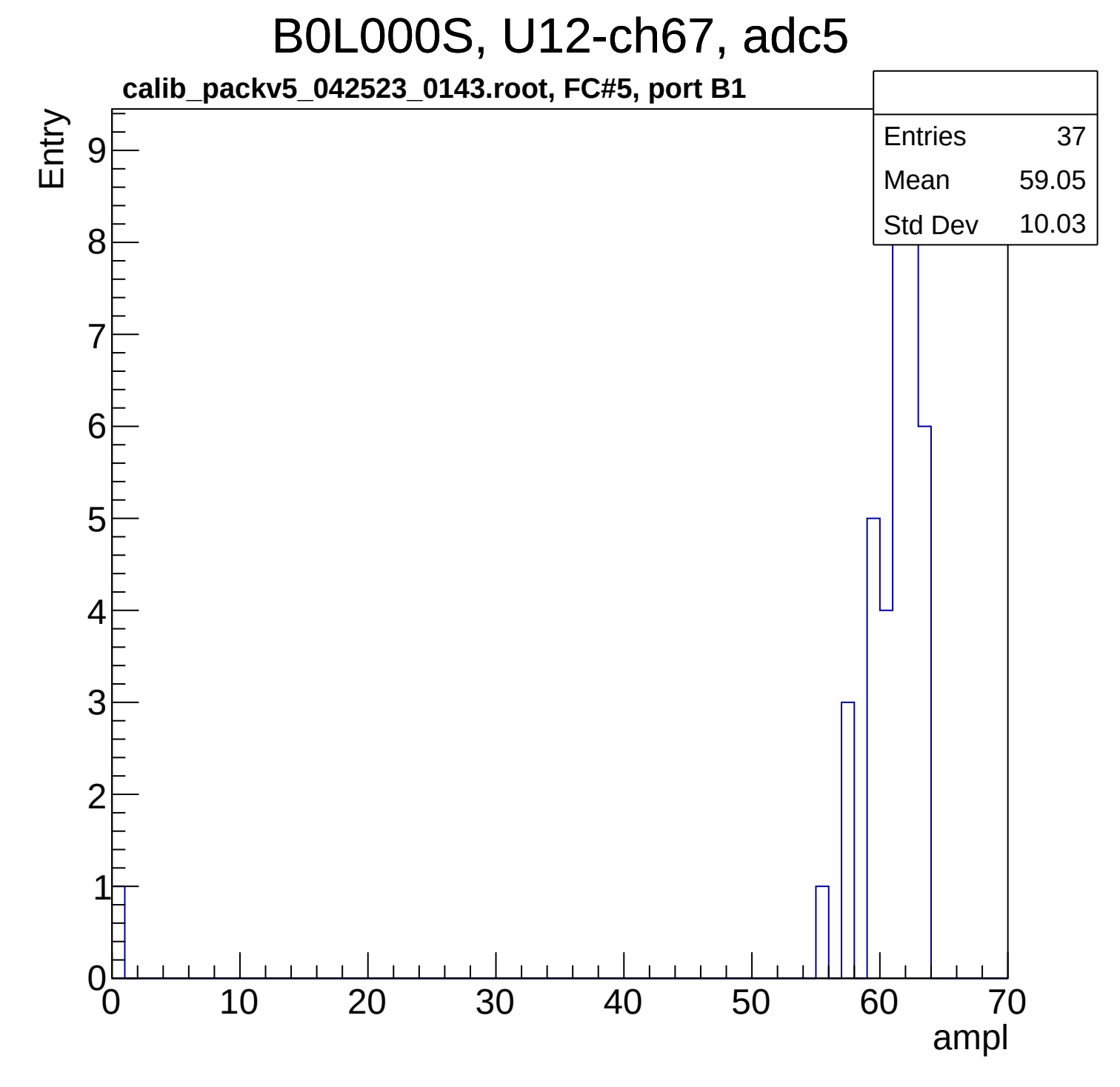
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.05
Std Dev 10.03
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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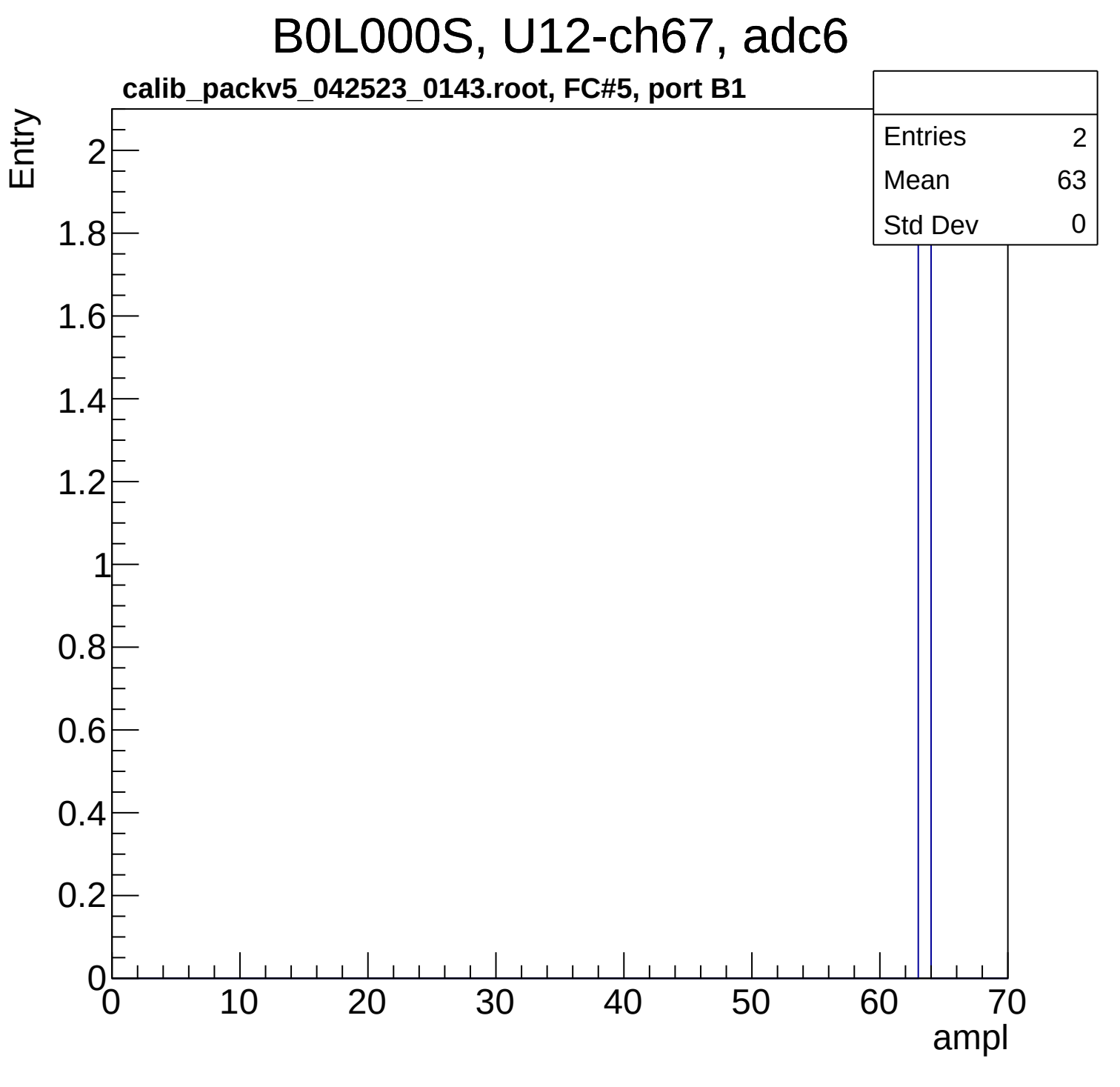
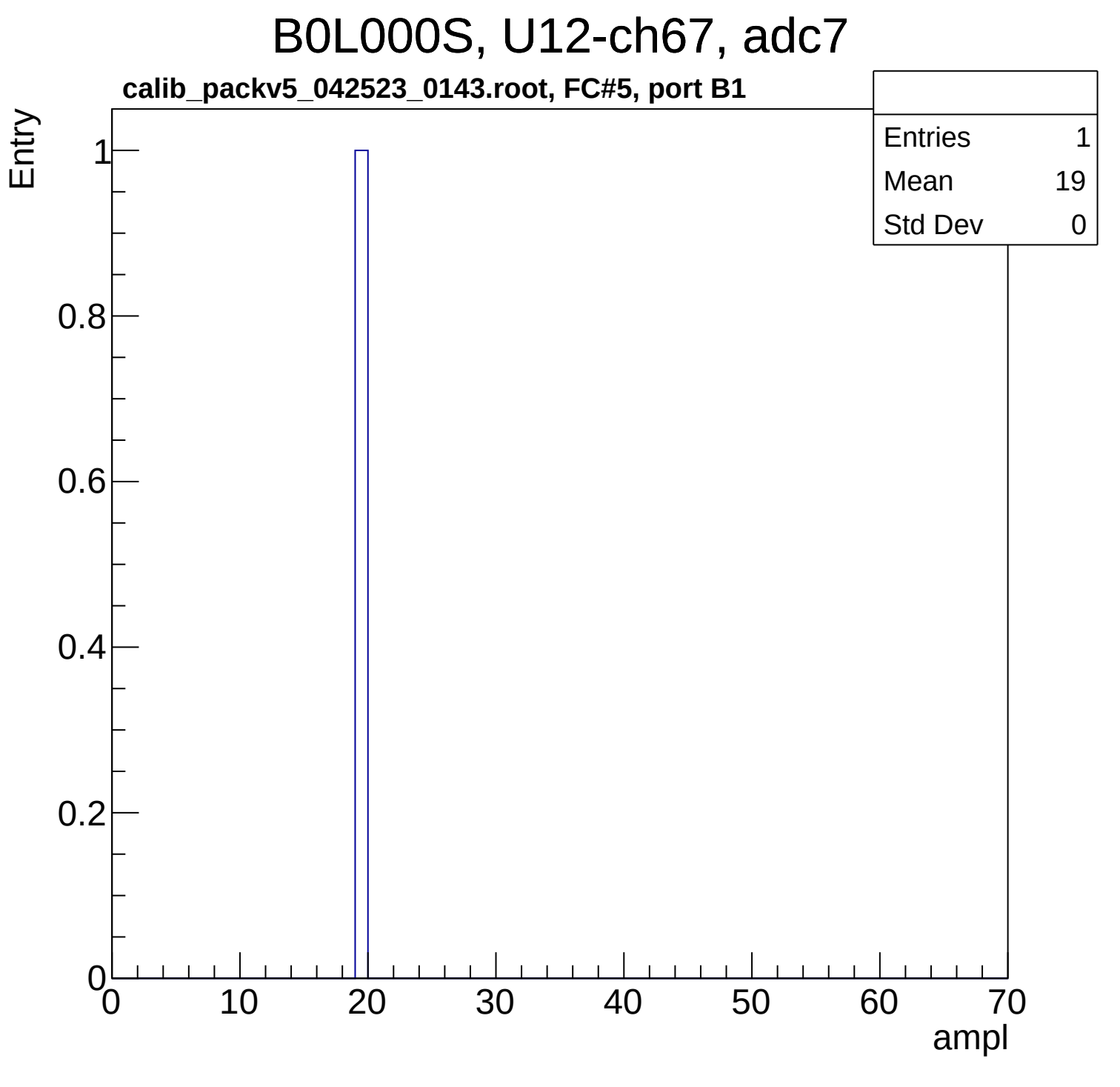

B0L000S, U12-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
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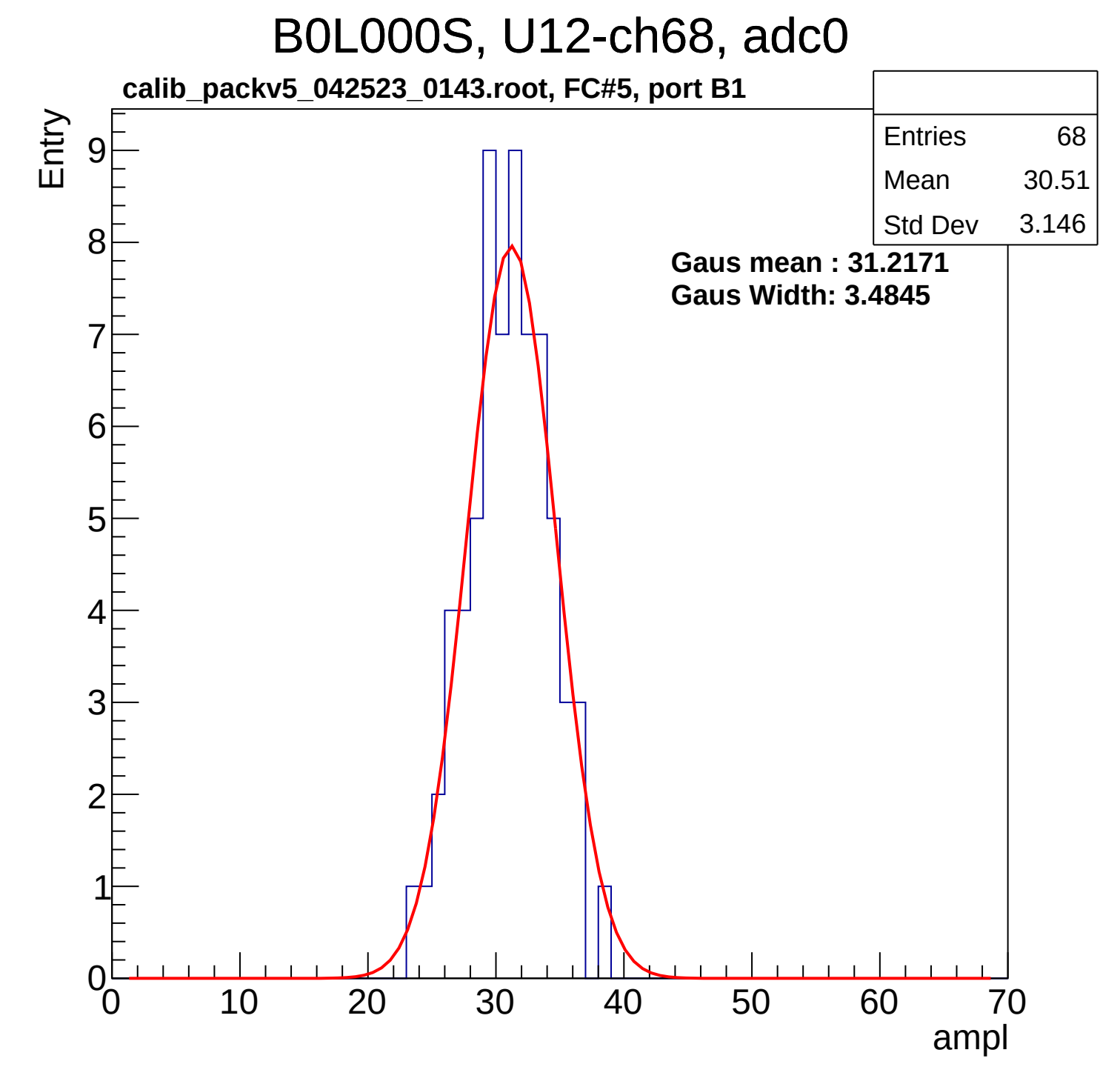
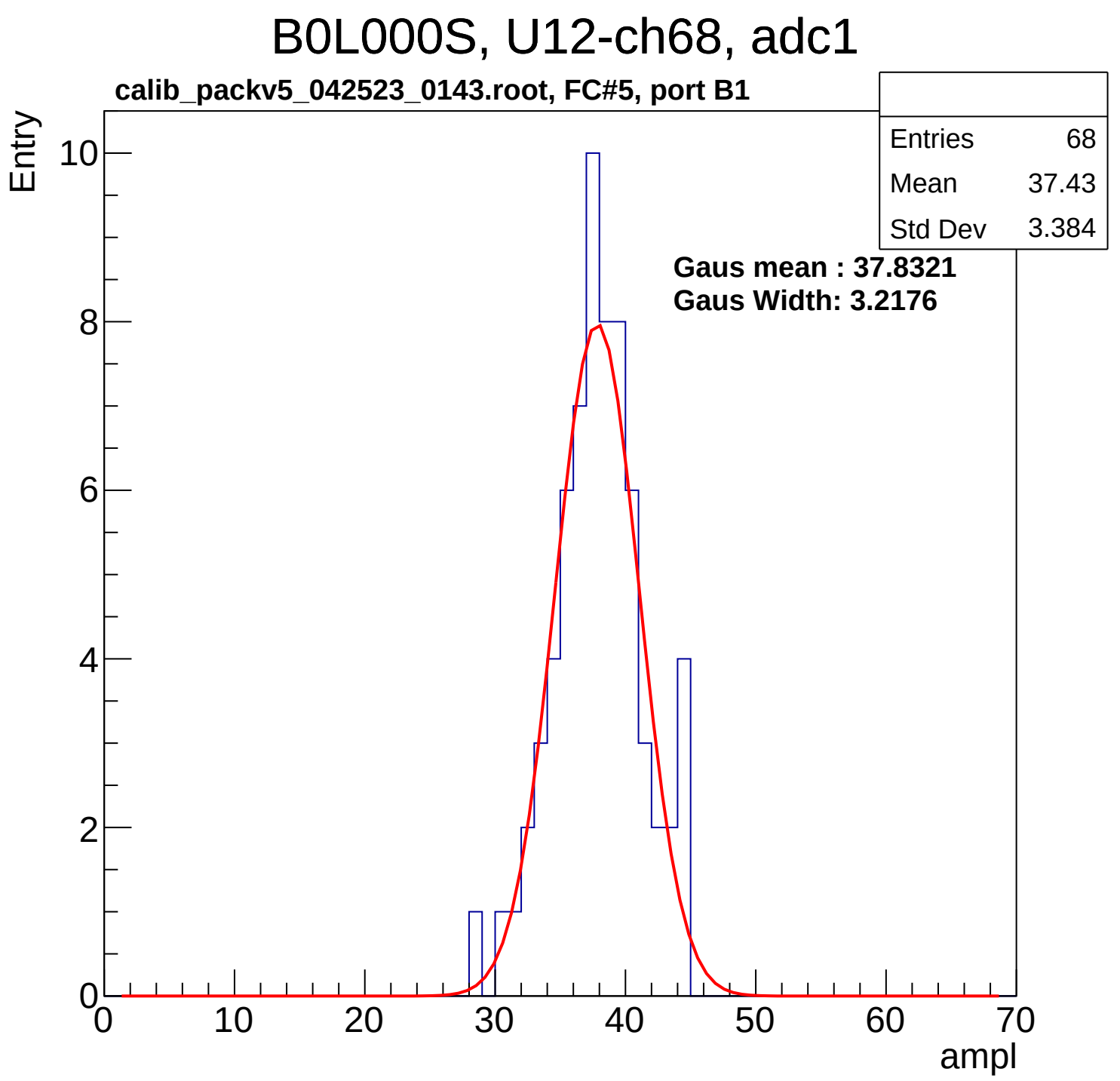
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.51
Std Dev 3.146
Gaus mean : 31.2171
Gaus Width: 3.4845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 37.43
Std Dev 3.384
Gaus mean : 37.8321
Gaus Width: 3.2176
Entry
ampl
0
2
4
6
8
10
0
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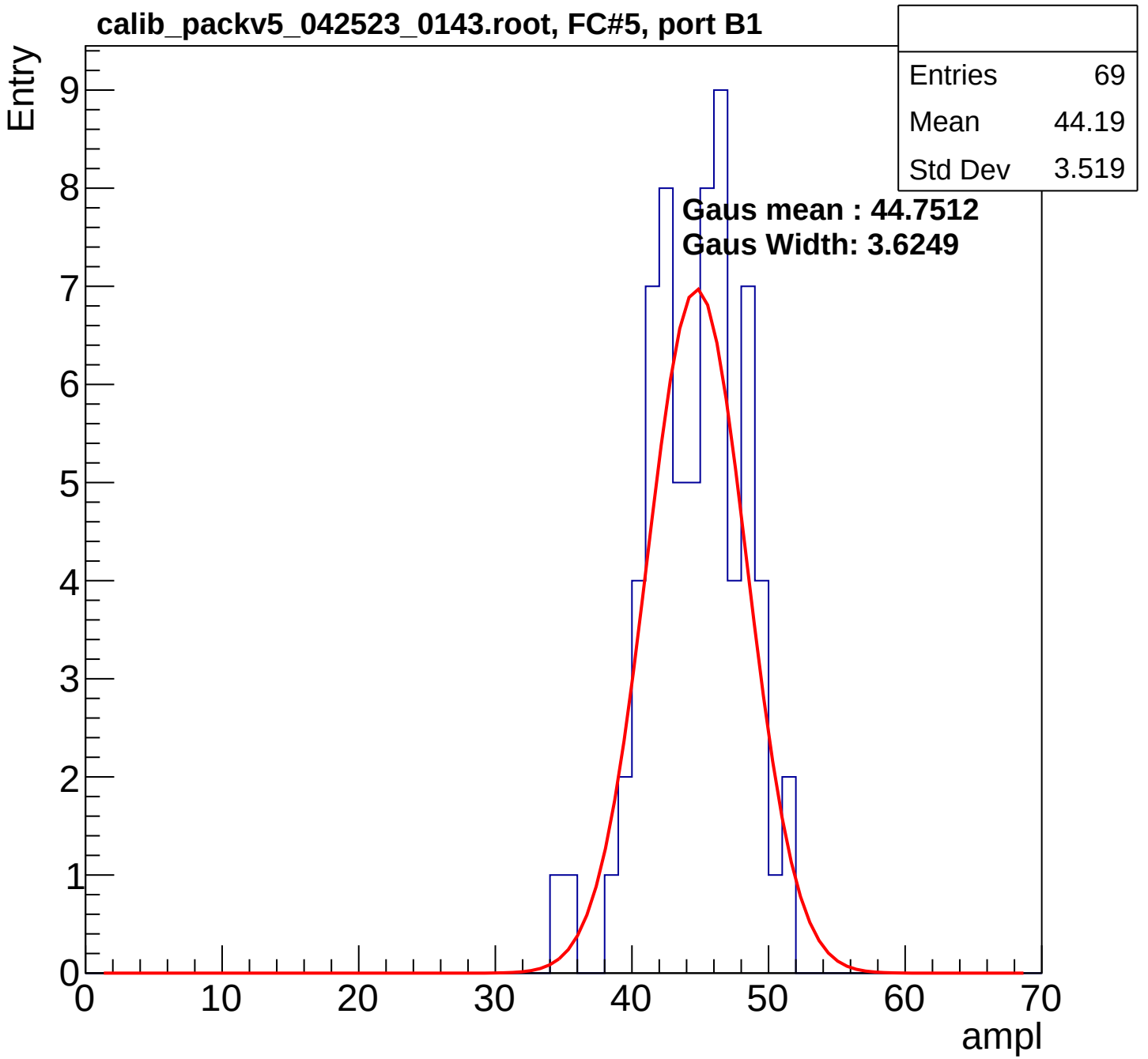
20
30
40
50
60
70


B0L000S, U12-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.19
Std Dev 3.519
Gaus mean : 44.7512
Gaus Width: 3.6249
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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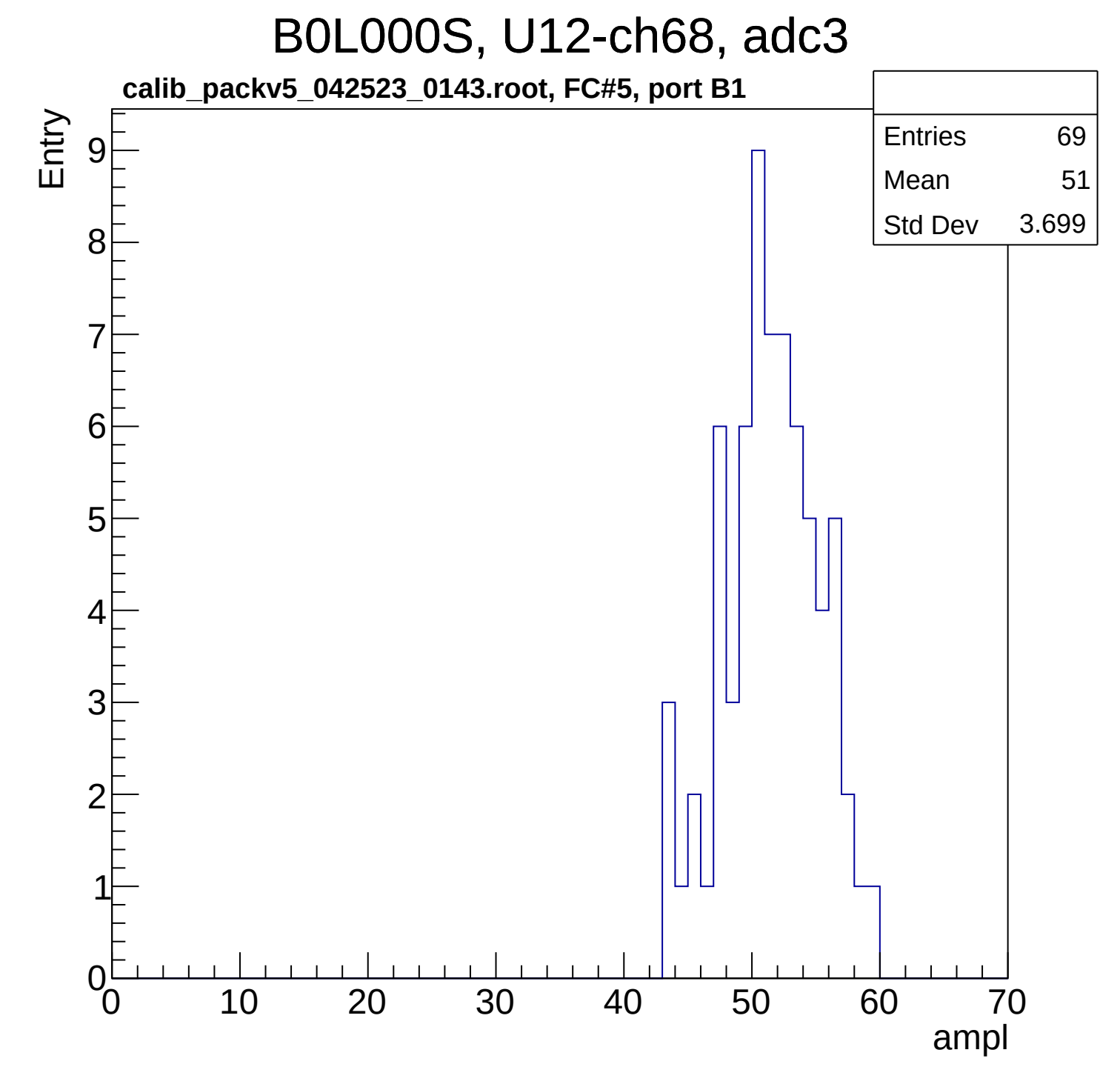
B0L000S, U12-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 51
Std Dev 3.699
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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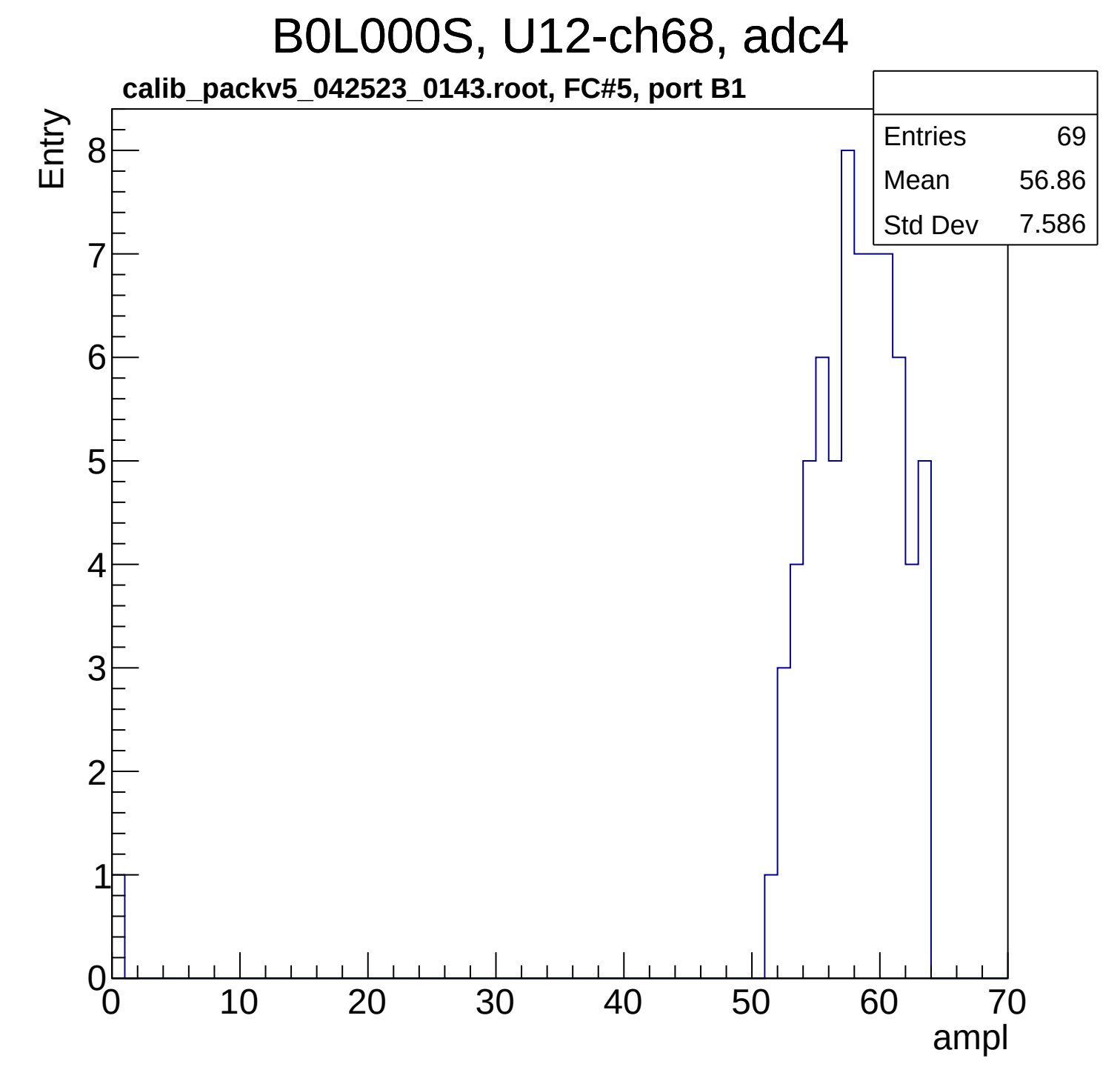
B0L000S, U12-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 56.86
Std Dev 7.586
Entry
ampl
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5
6
7
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0
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20
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40
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60
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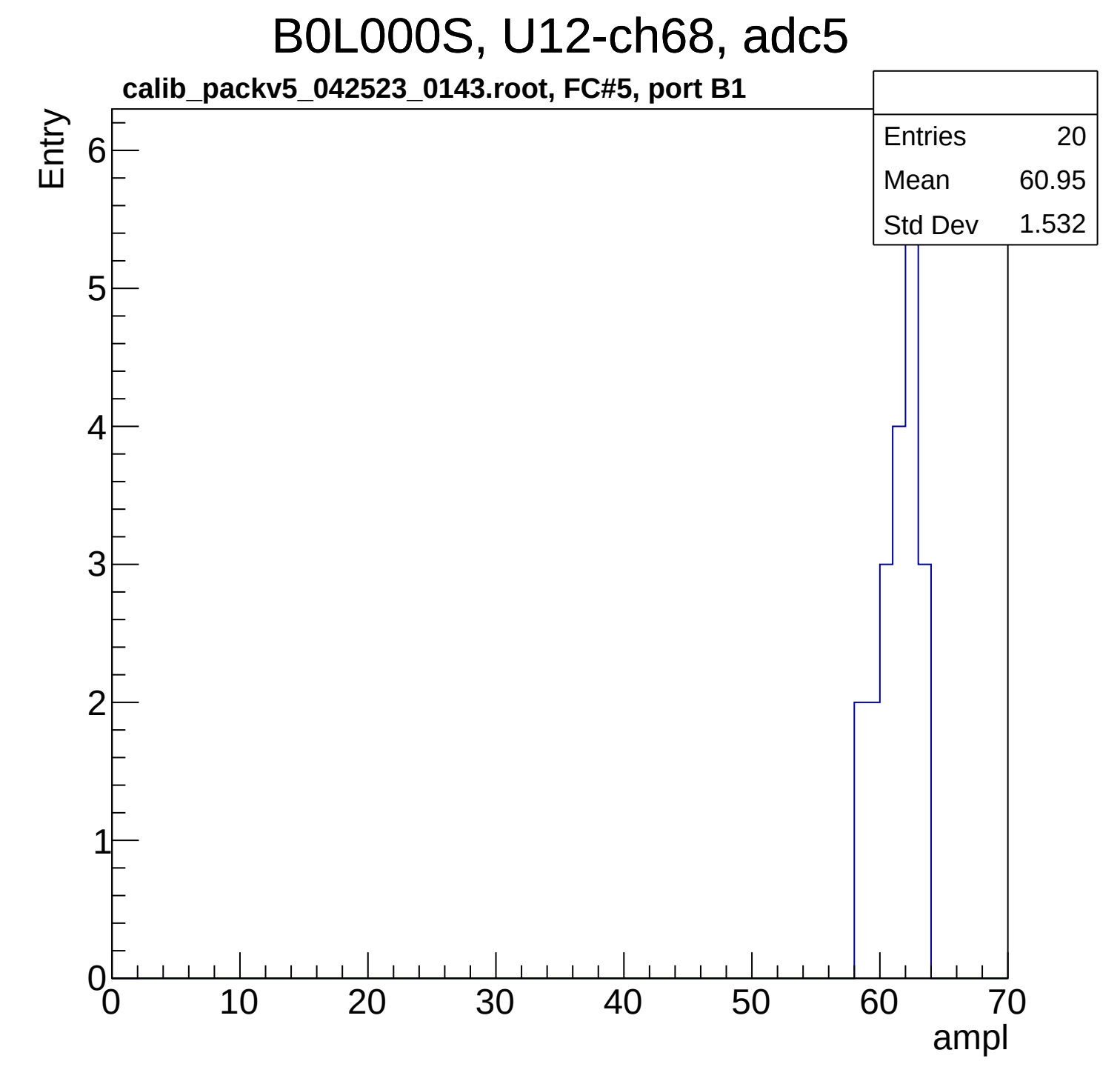
B0L000S, U12-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 60.95
Std Dev 1.532
Entry
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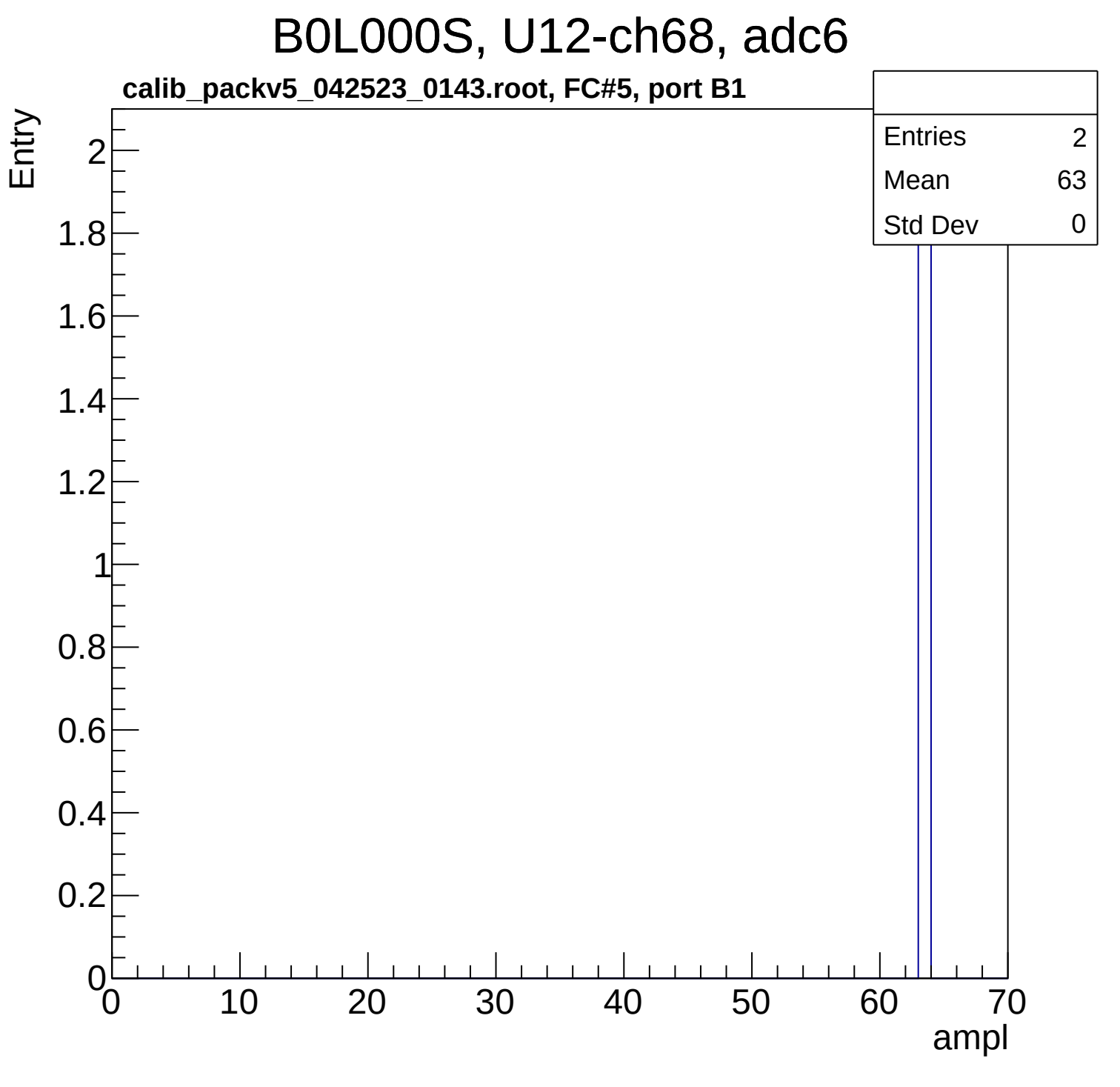
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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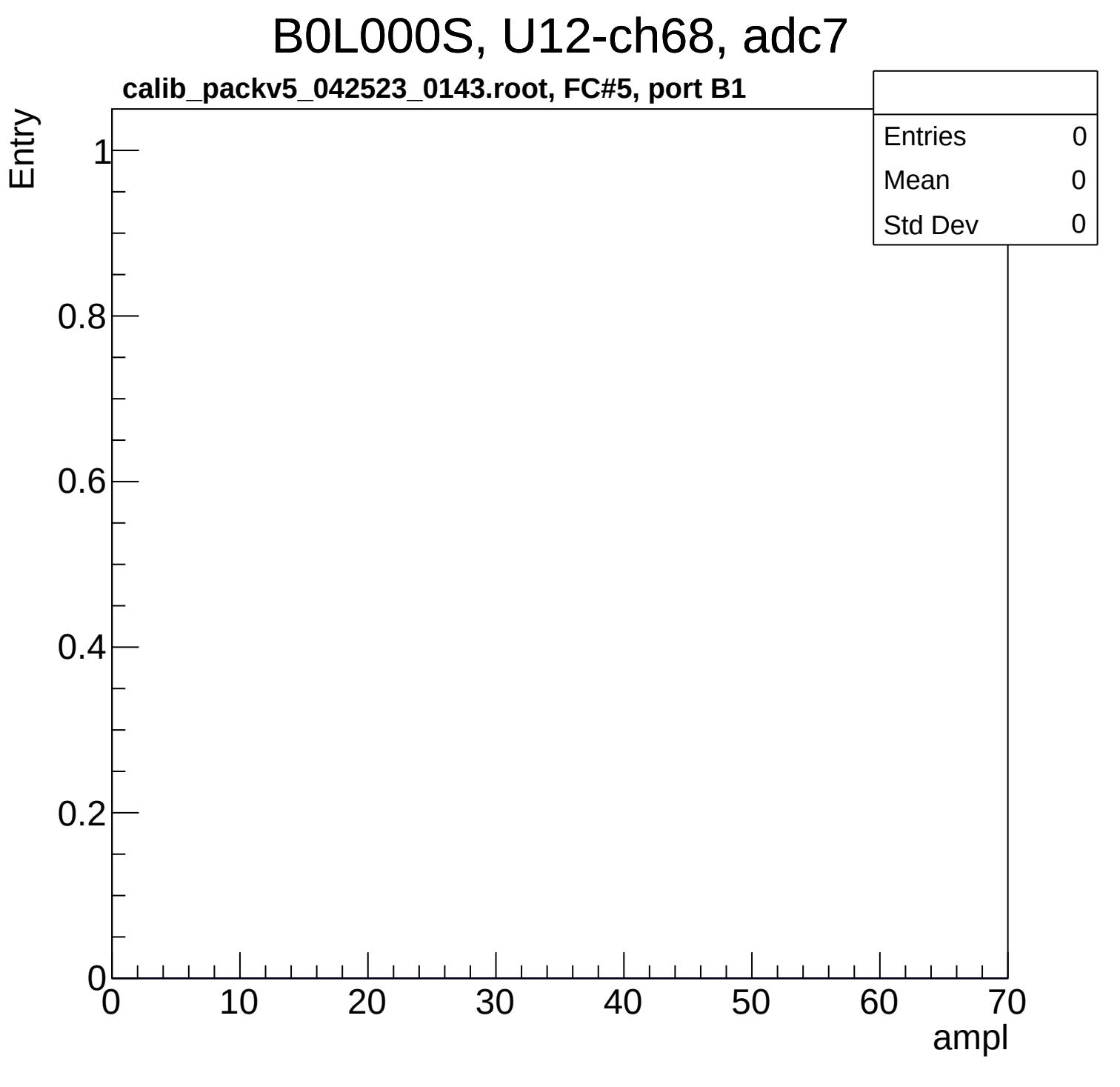
60
70


B0L000S, U12-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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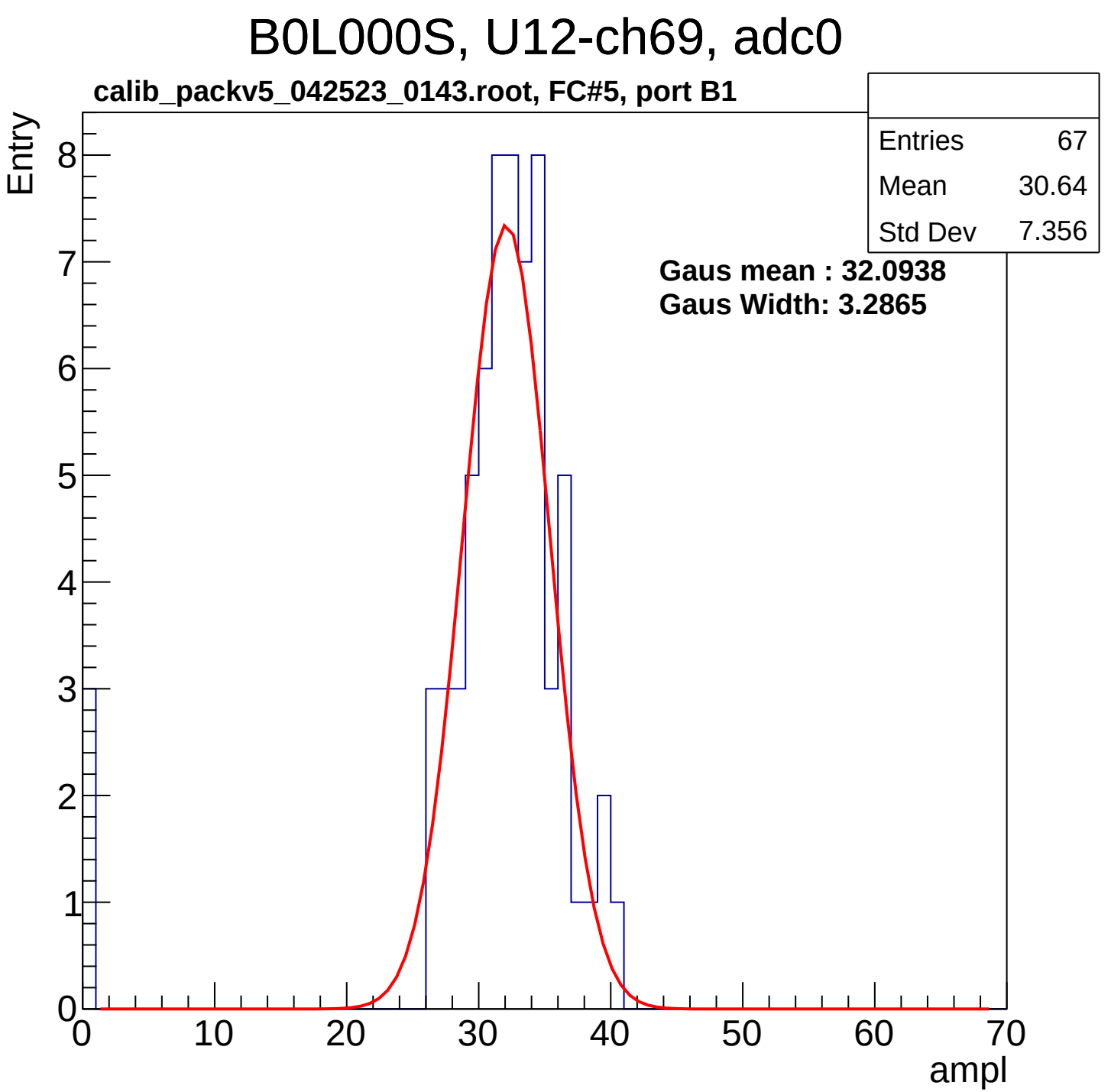
40
50
60
70
ampl


B0L000S, U12-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.64
Std Dev 7.356
Gaus mean : 32.0938
Gaus Width: 3.2865
Entry
ampl
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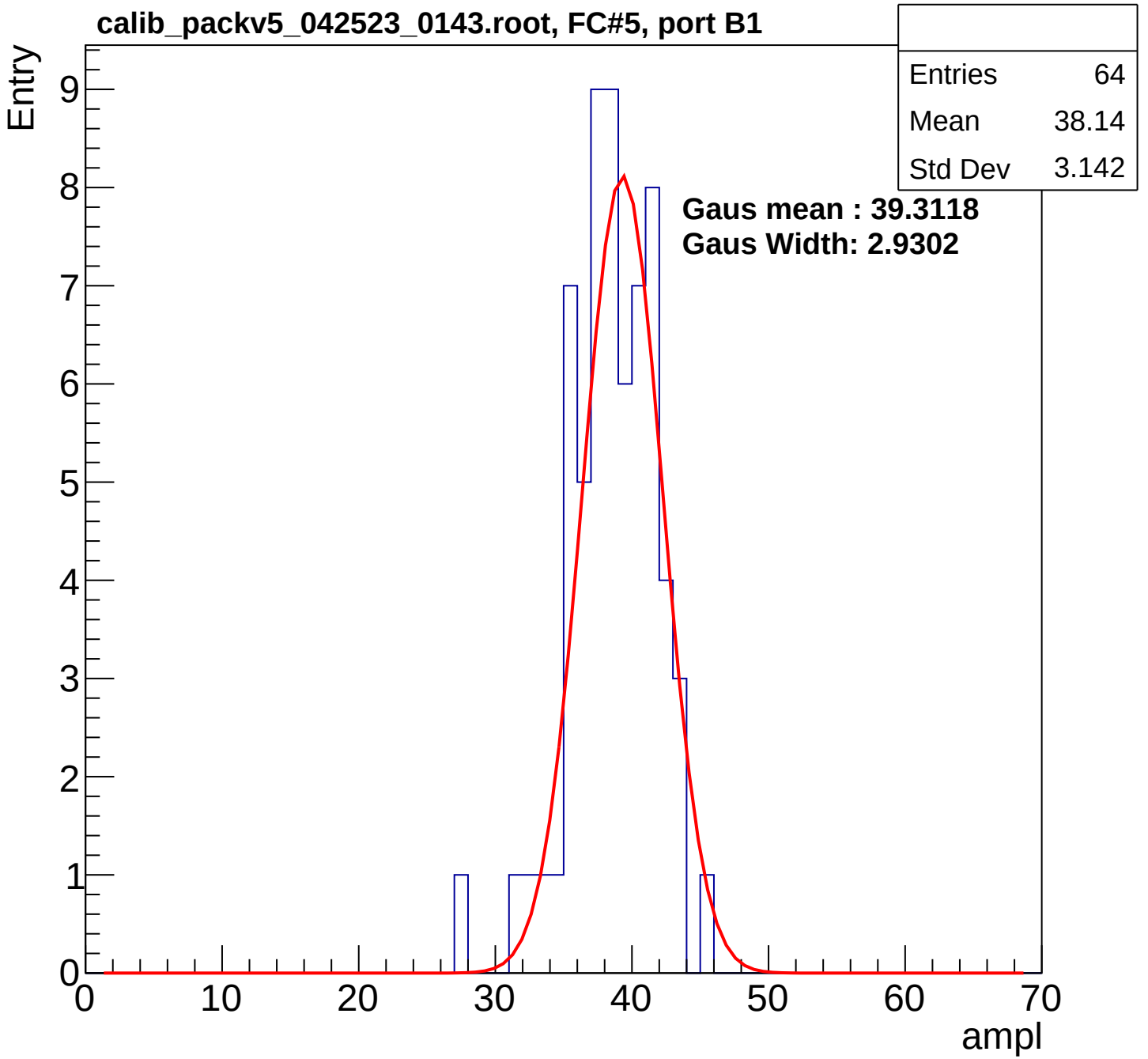
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.14
Std Dev 3.142
Gaus mean : 39.3118
Gaus Width: 2.9302
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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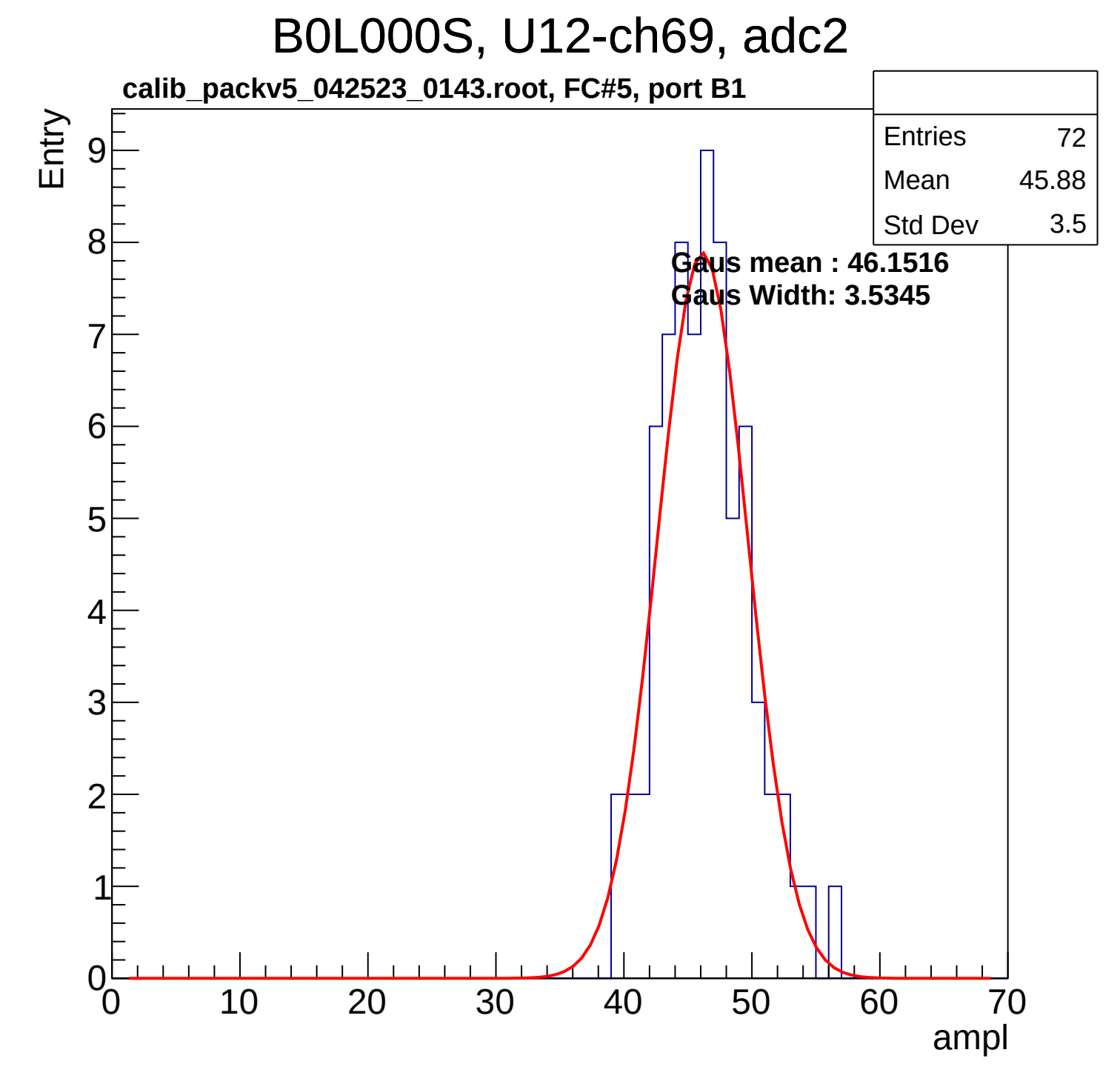
B0L000S, U12-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 45.88
Std Dev 3.5
Gaus mean : 46.1516
Gaus Width: 3.5345
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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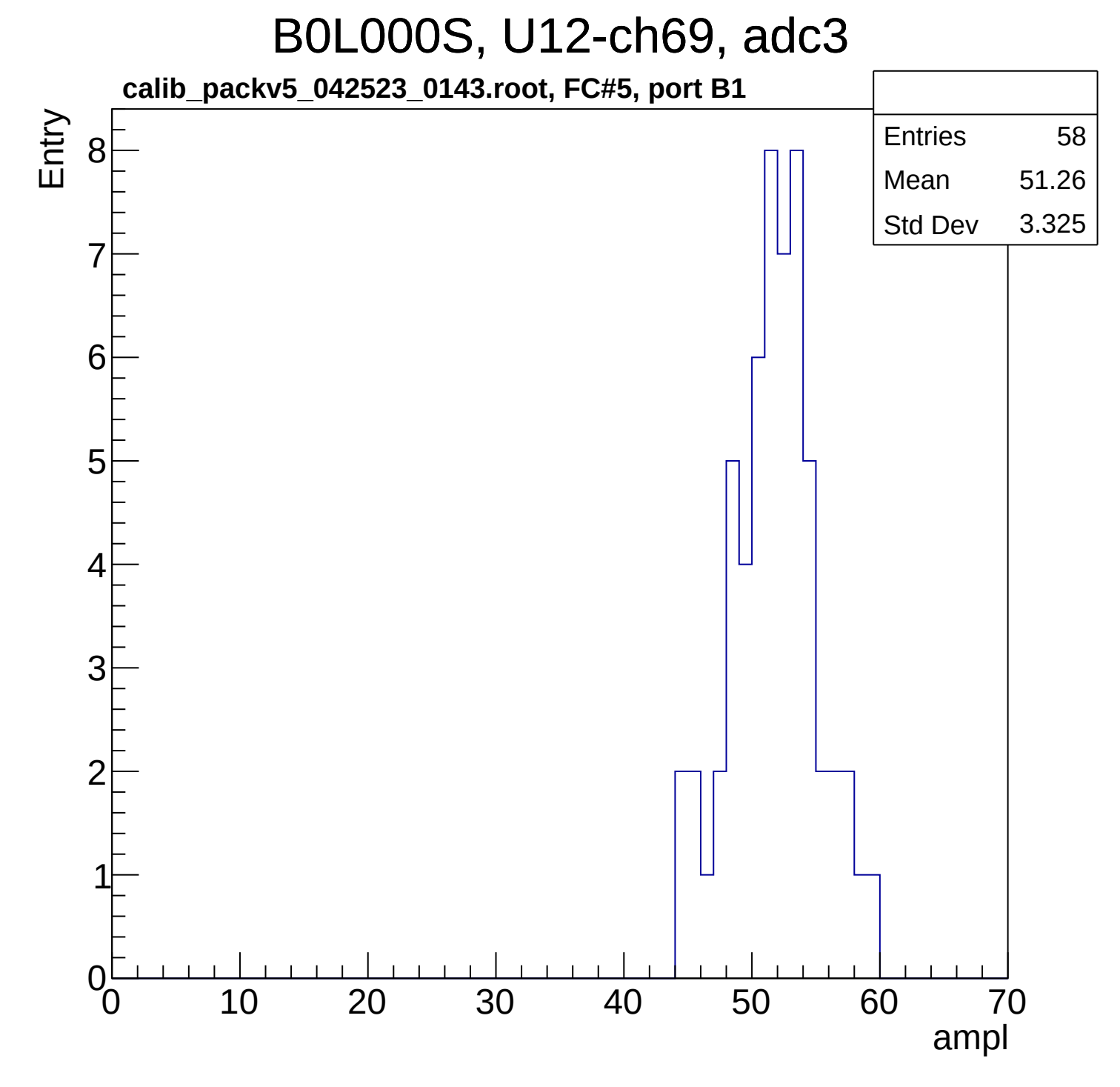
B0L000S, U12-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.26
Std Dev 3.325
Entry
ampl
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2
3
4
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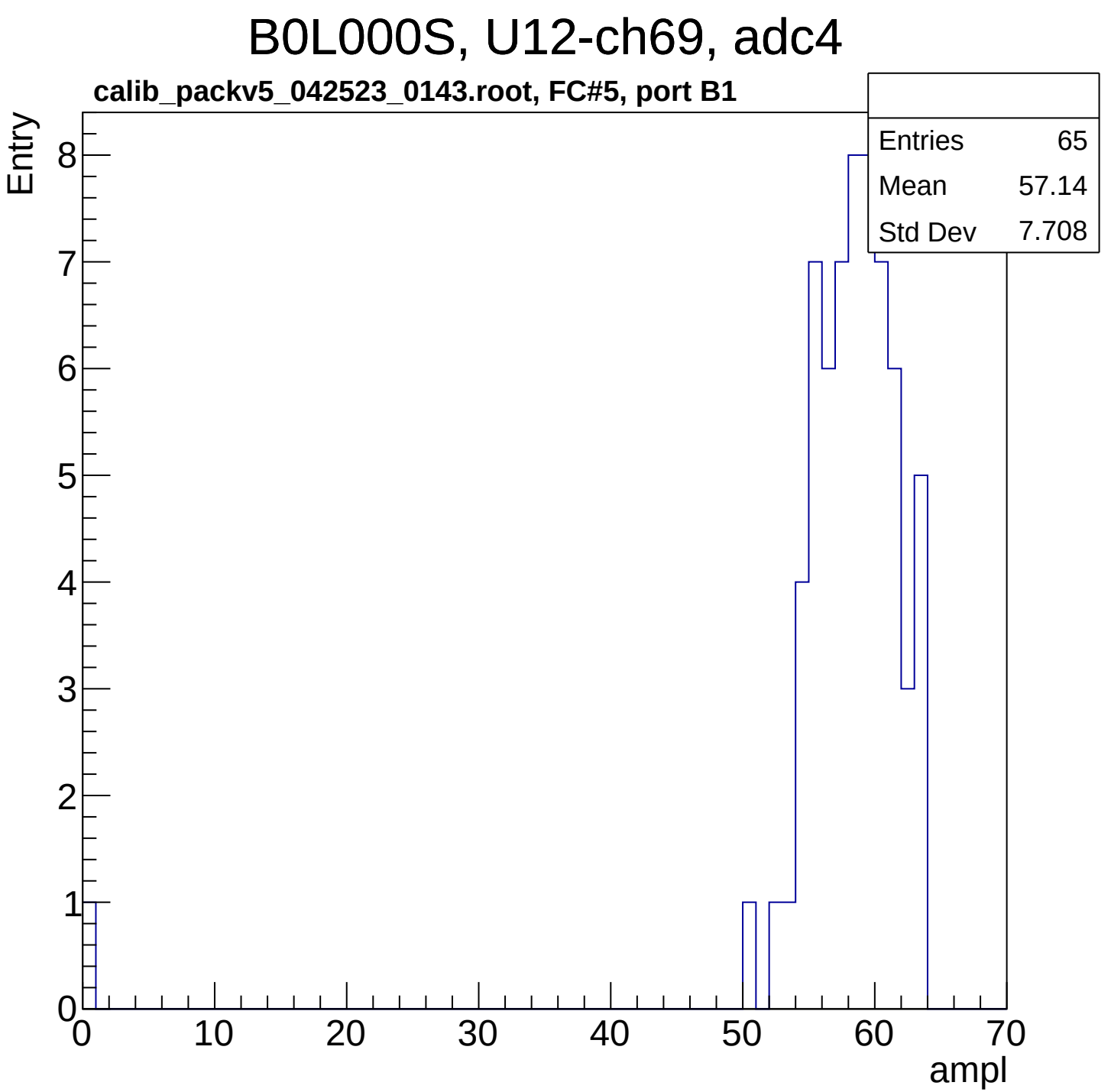
B0L000S, U12-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 57.14
Std Dev 7.708
Entry
ampl
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60
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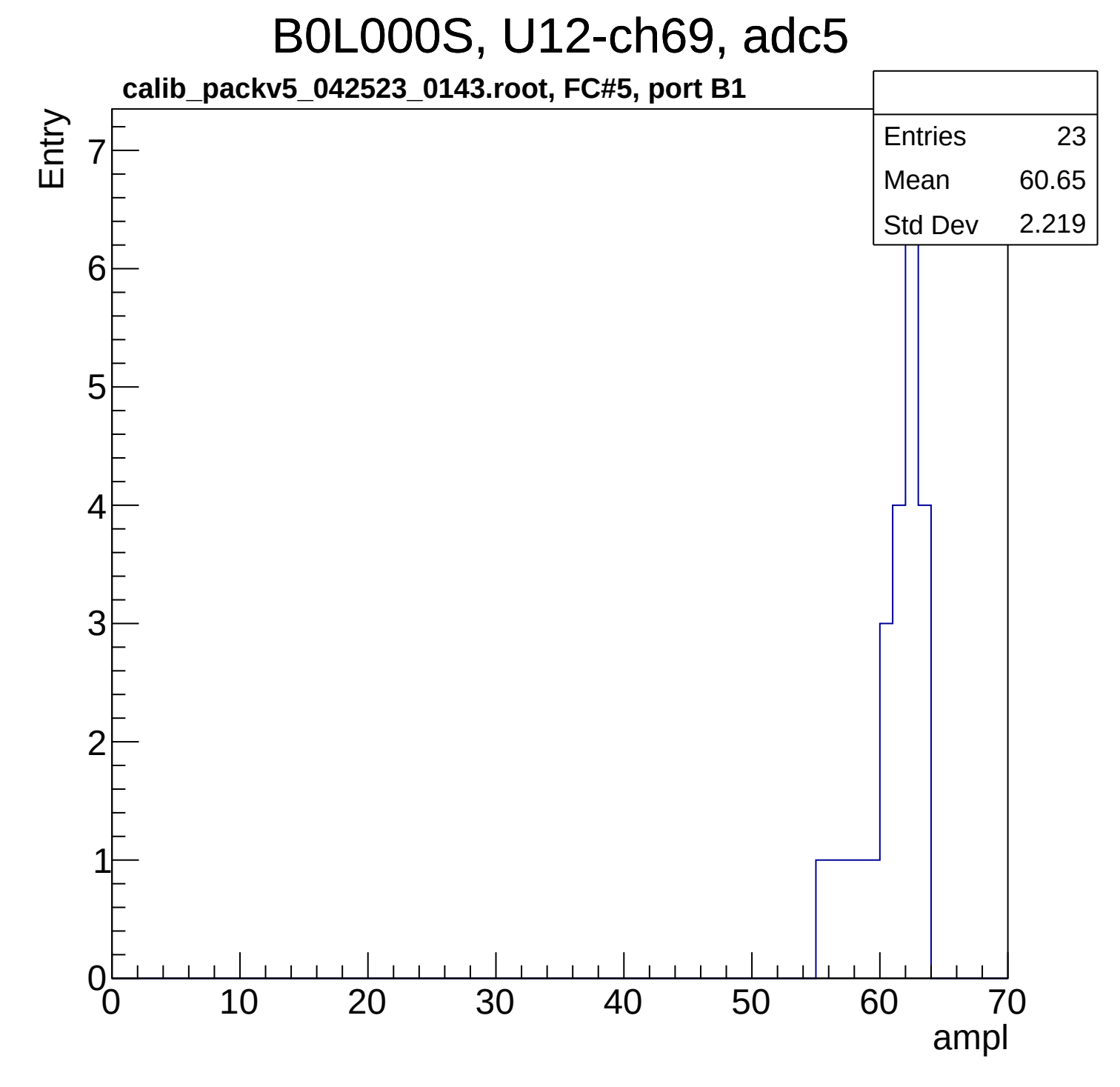
B0L000S, U12-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 60.65
Std Dev 2.219
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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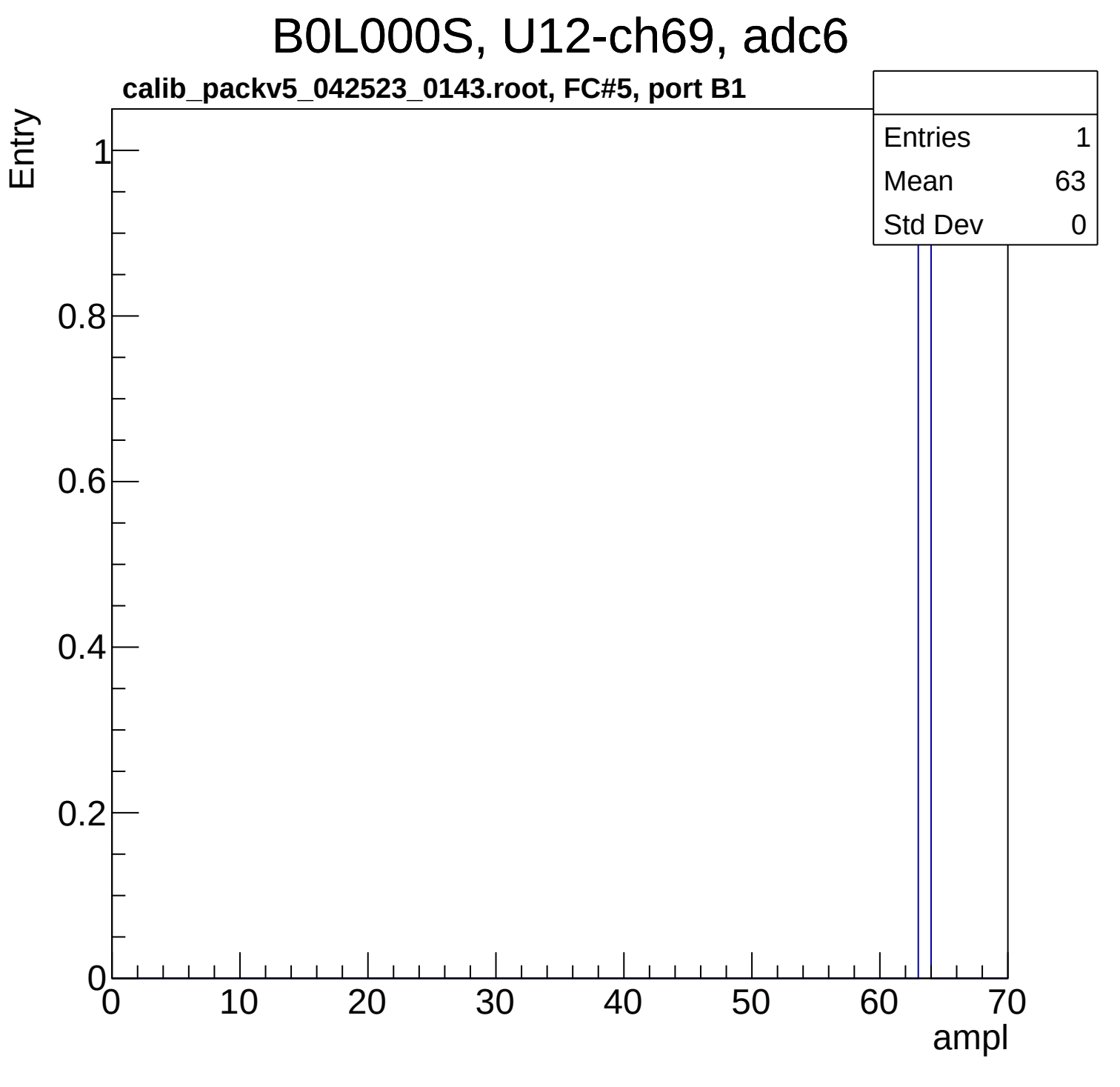
40
50
60
70


B0L000S, U12-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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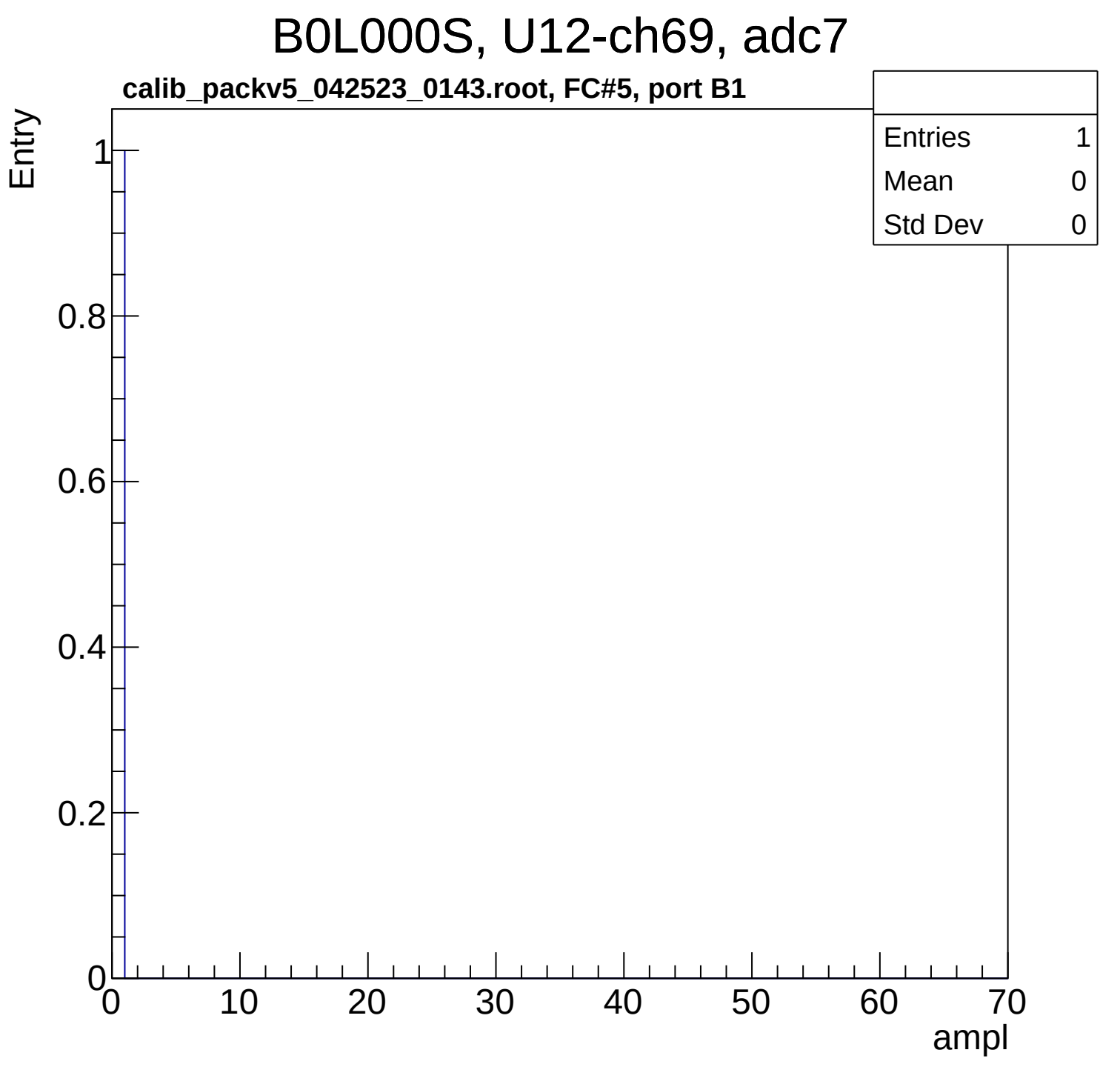
40
50
60
70
ampl


B0L000S, U12-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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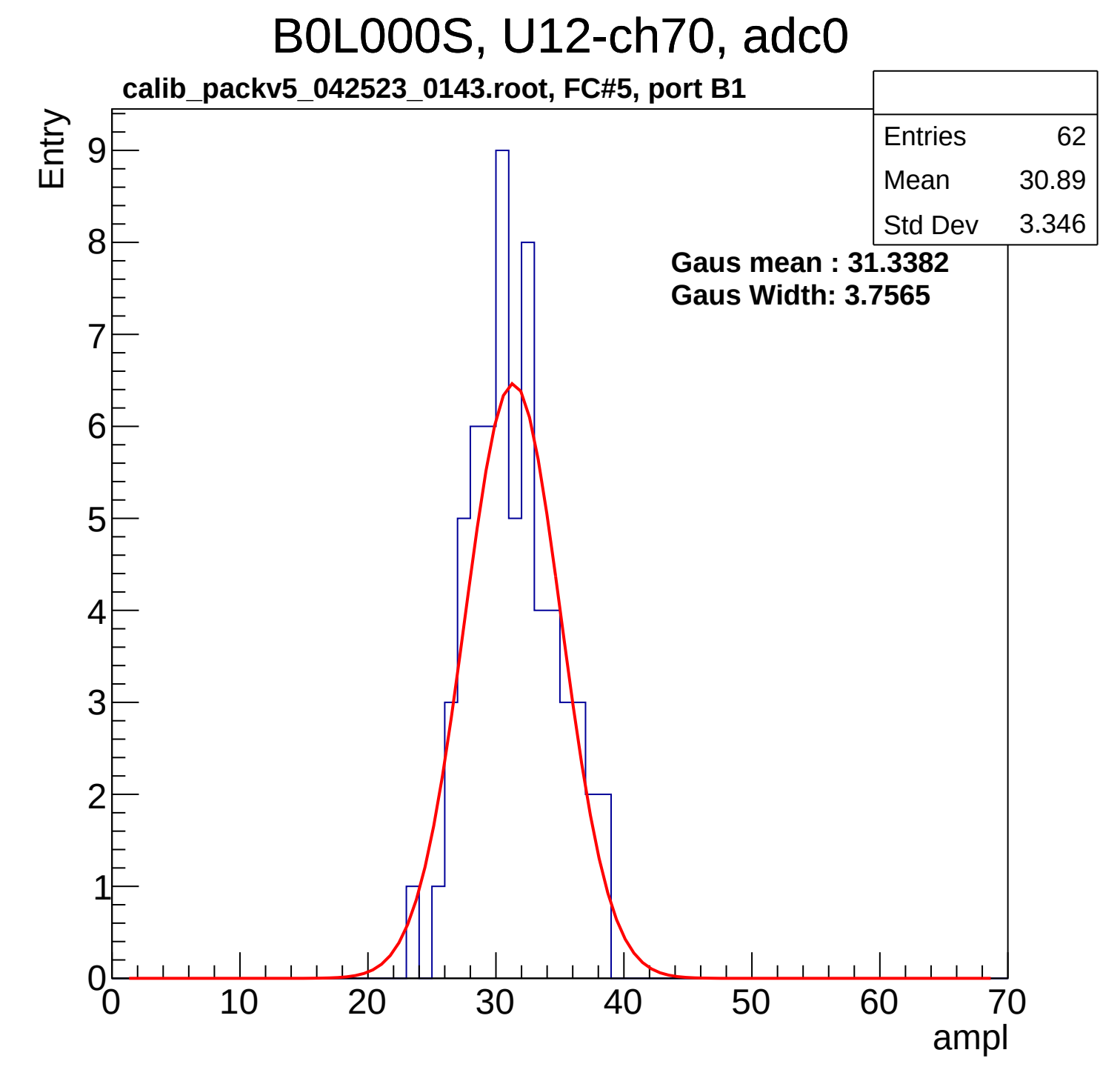
40
50
60
70
ampl


B0L000S, U12-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 30.89
Std Dev 3.346
Gaus mean : 31.3382
Gaus Width: 3.7565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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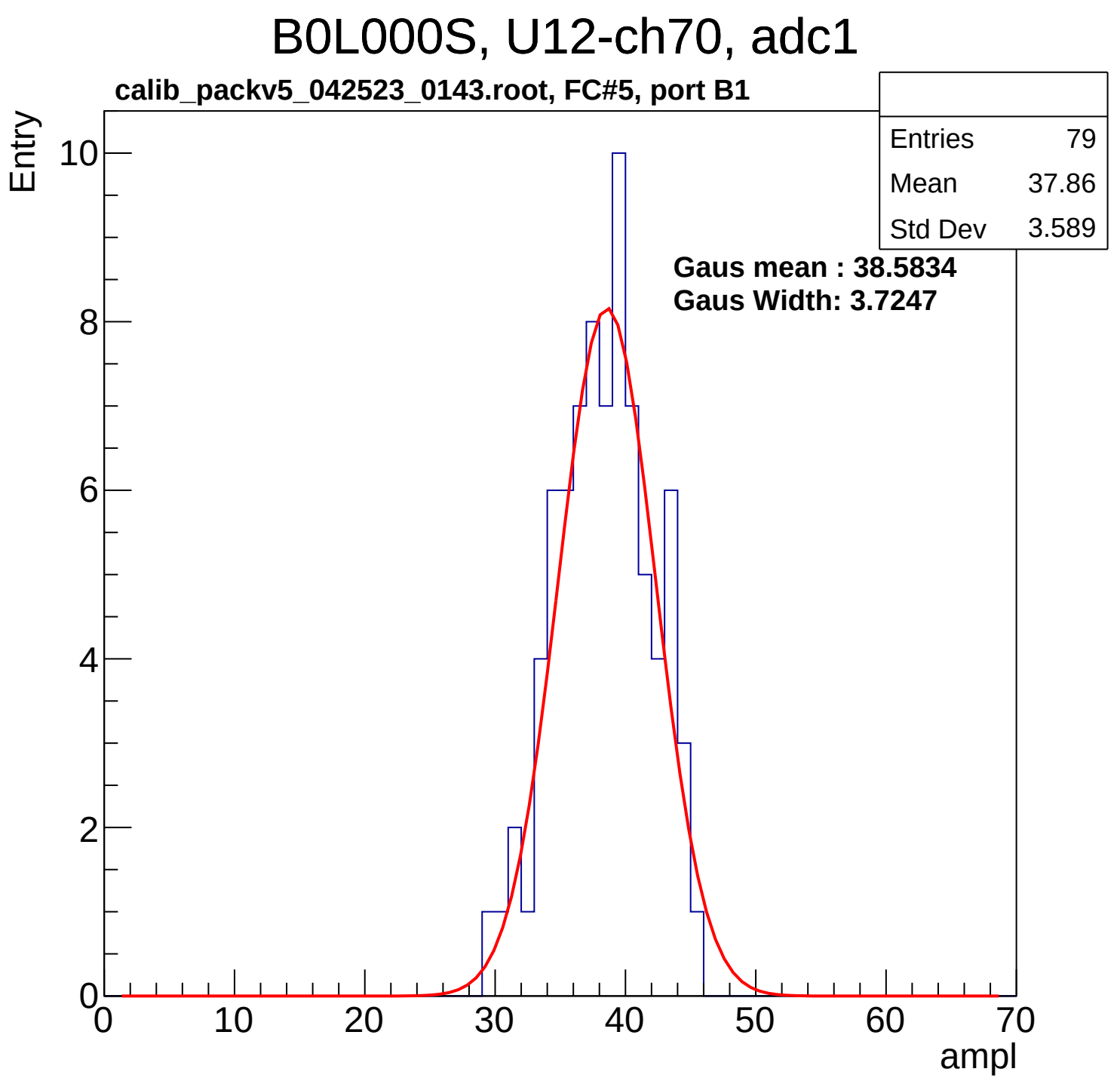
B0L000S, U12-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 37.86
Std Dev 3.589
Gaus mean : 38.5834
Gaus Width: 3.7247
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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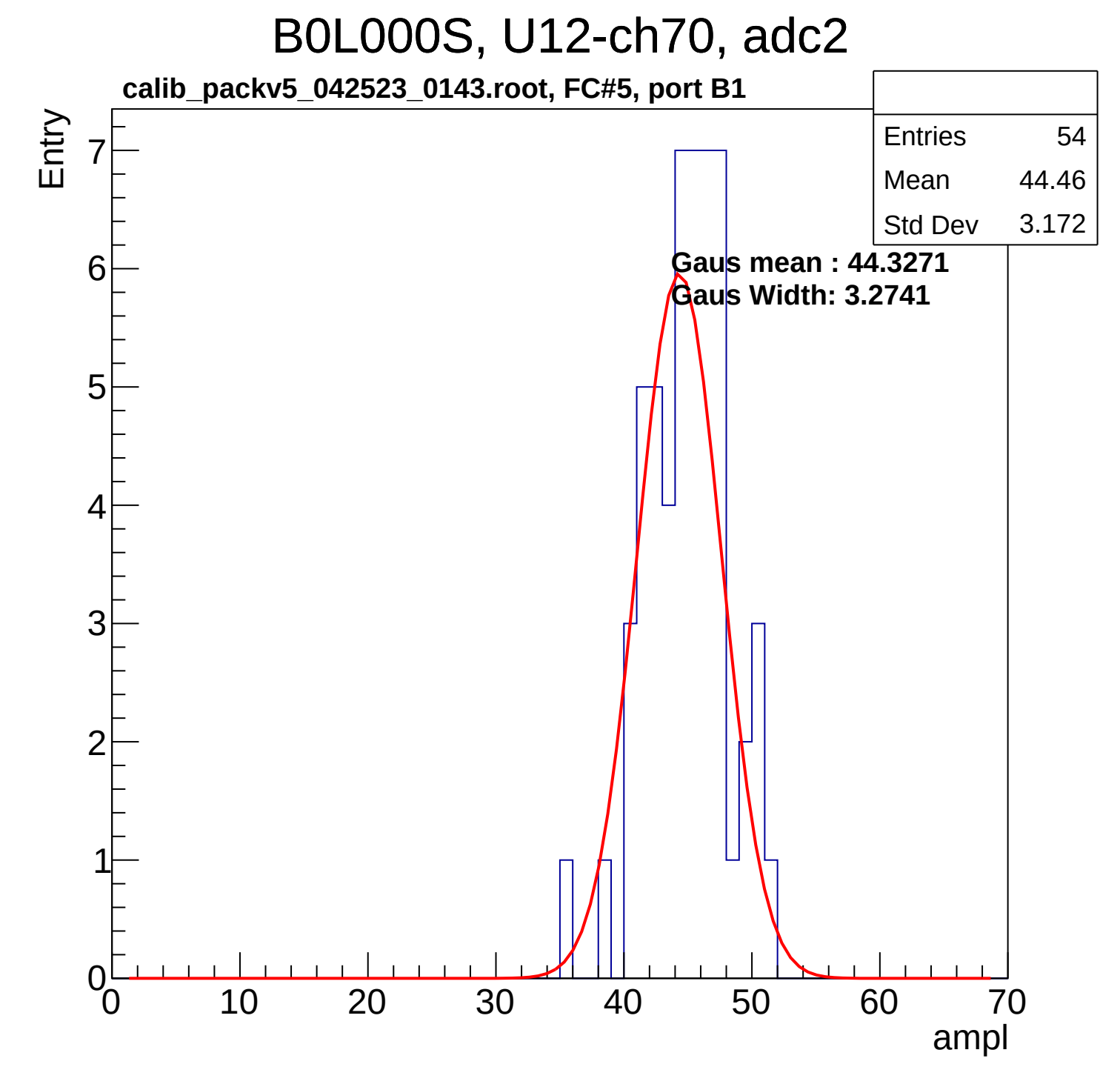
B0L000S, U12-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 44.46
Std Dev 3.172
Gaus mean : 44.3271
Gaus Width: 3.2741
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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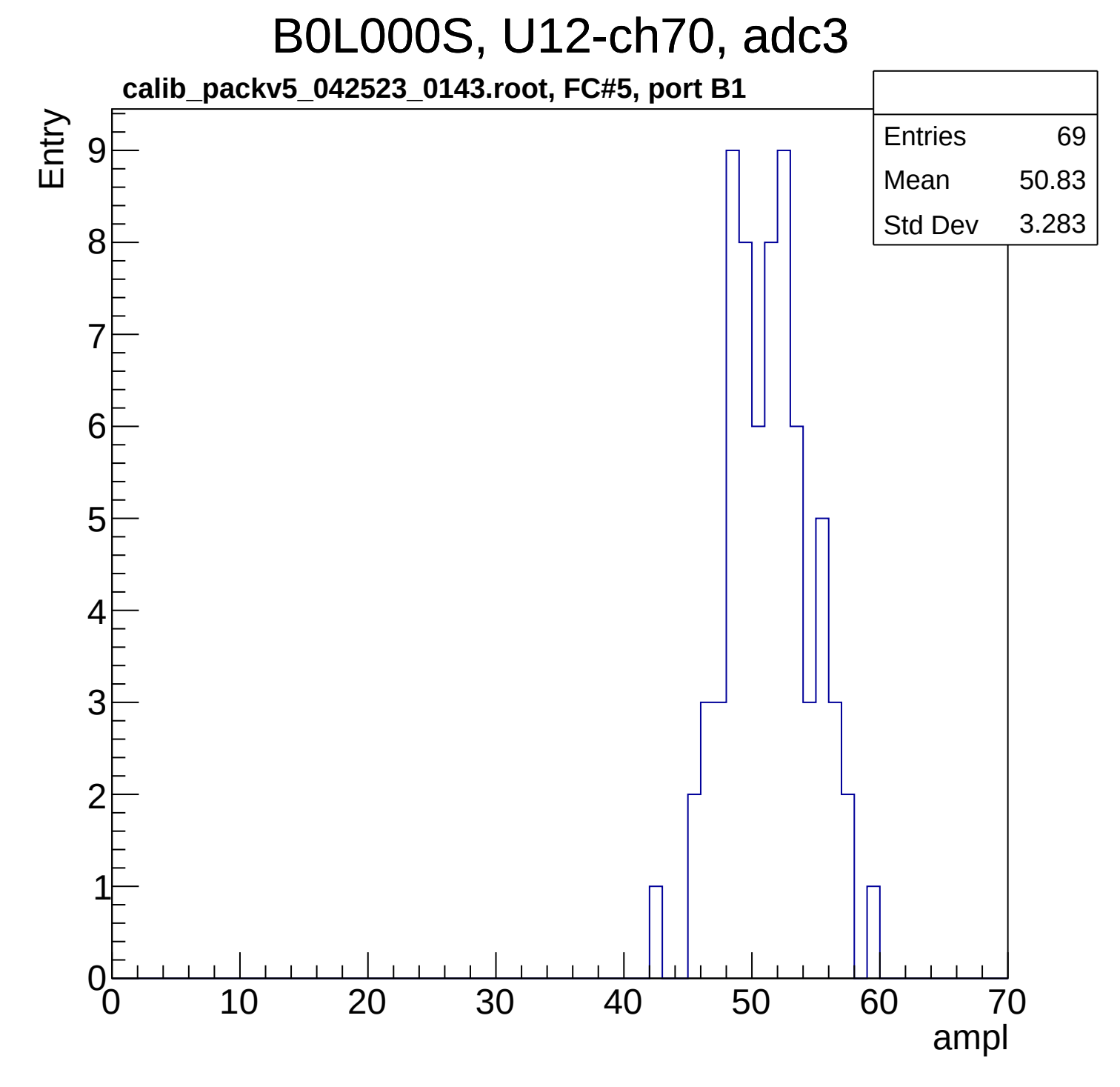
B0L000S, U12-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.83
Std Dev 3.283
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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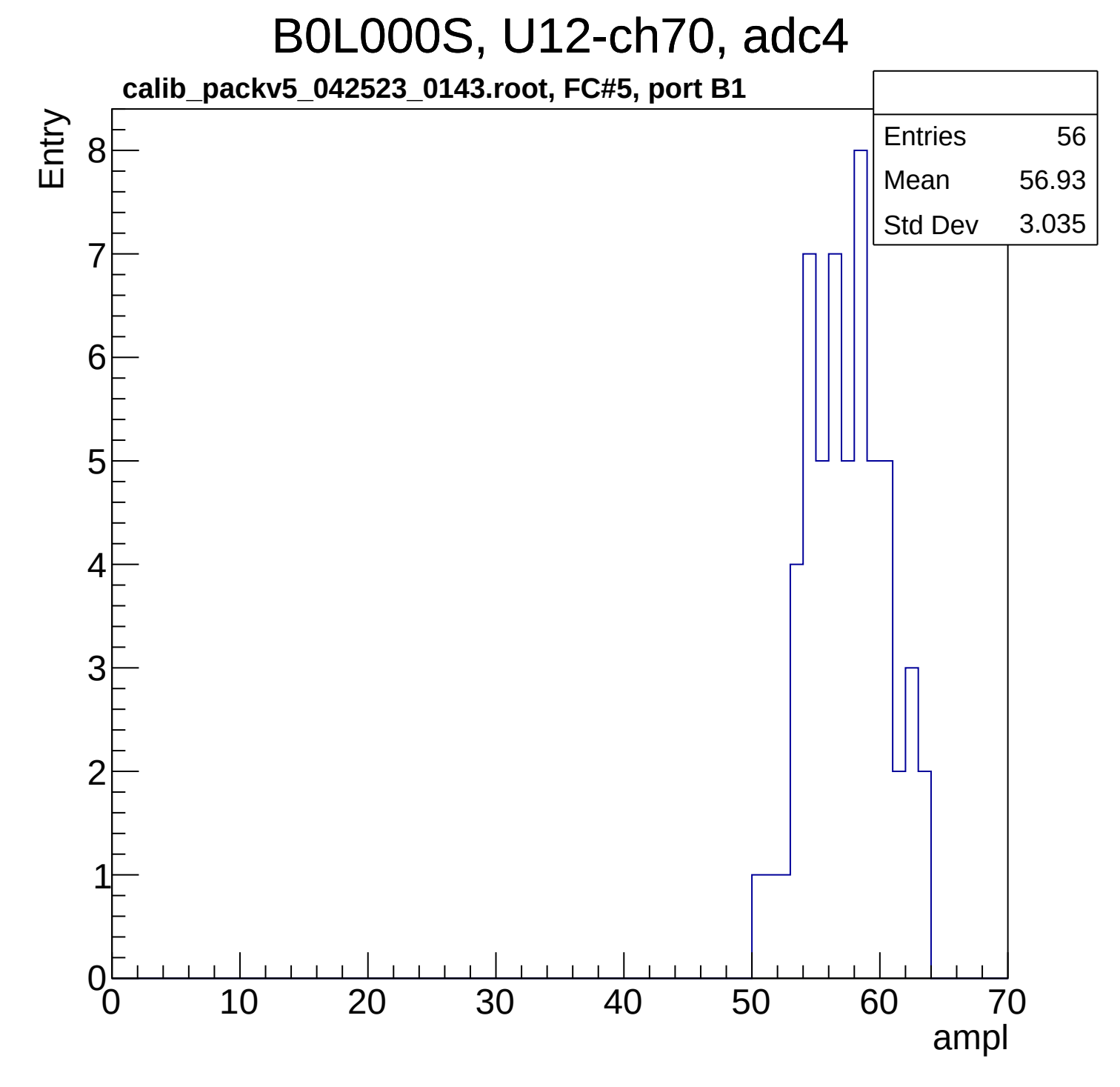
B0L000S, U12-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.93
Std Dev 3.035
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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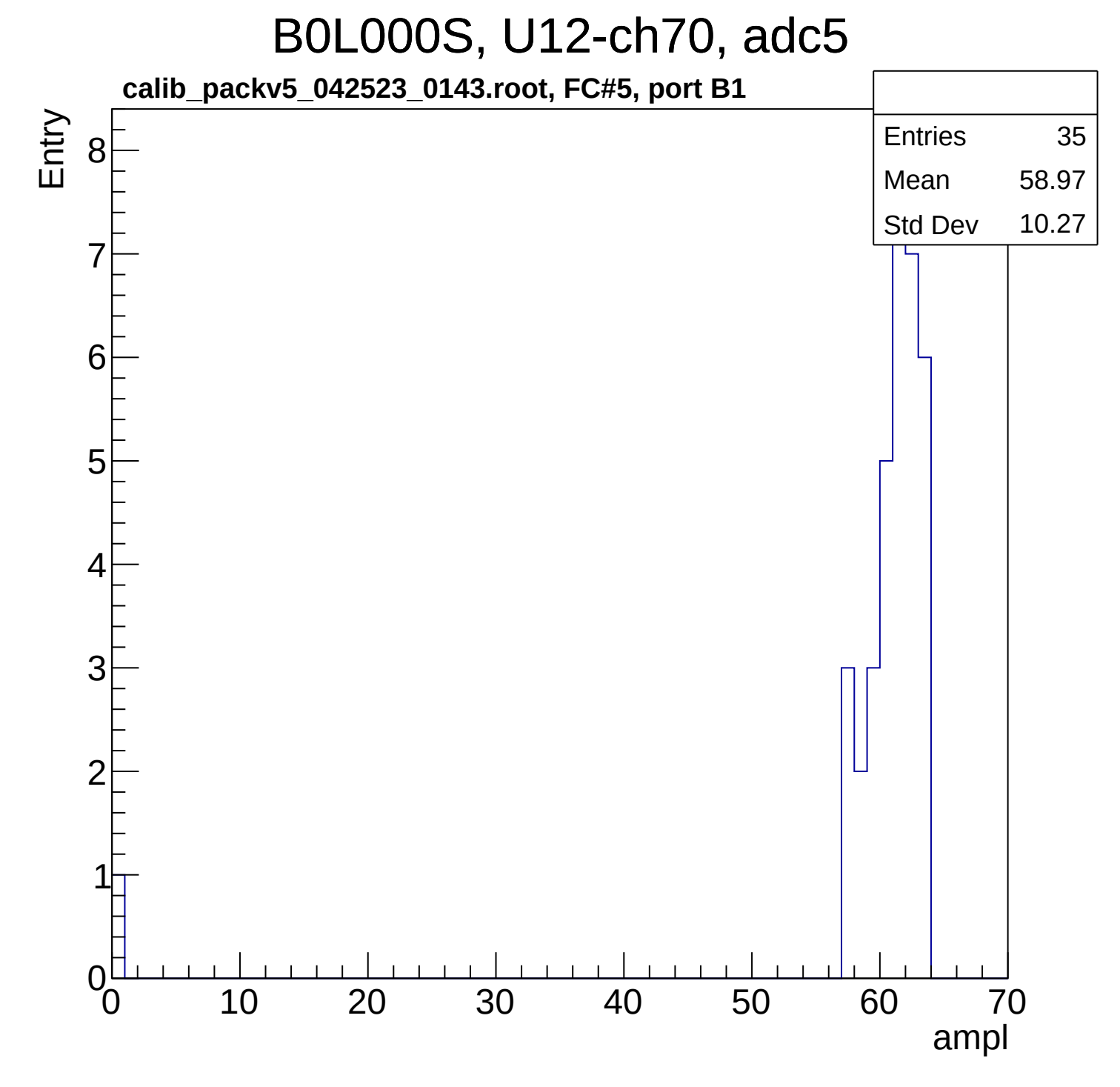
B0L000S, U12-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.97
Std Dev 10.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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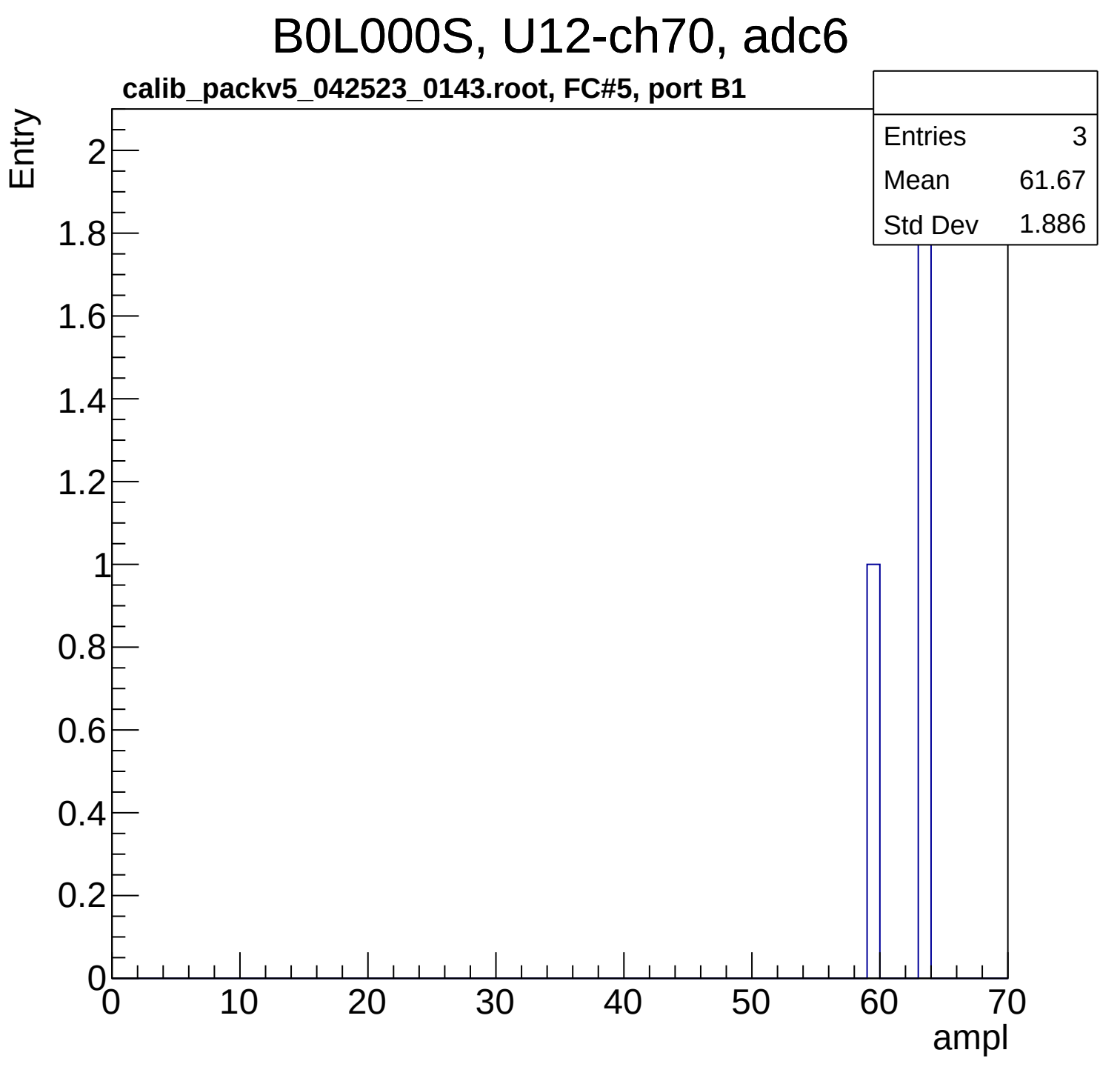
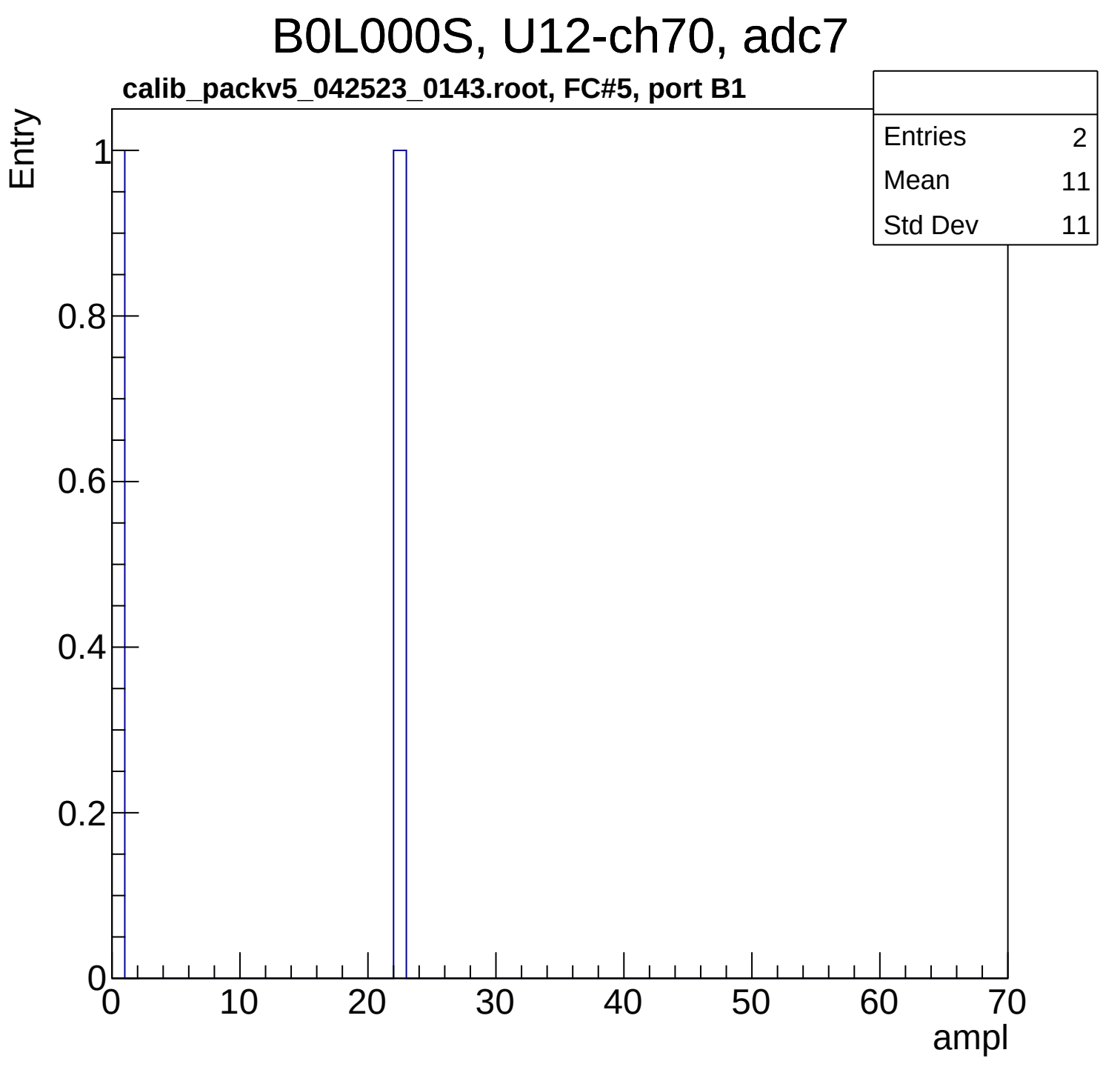

B0L000S, U12-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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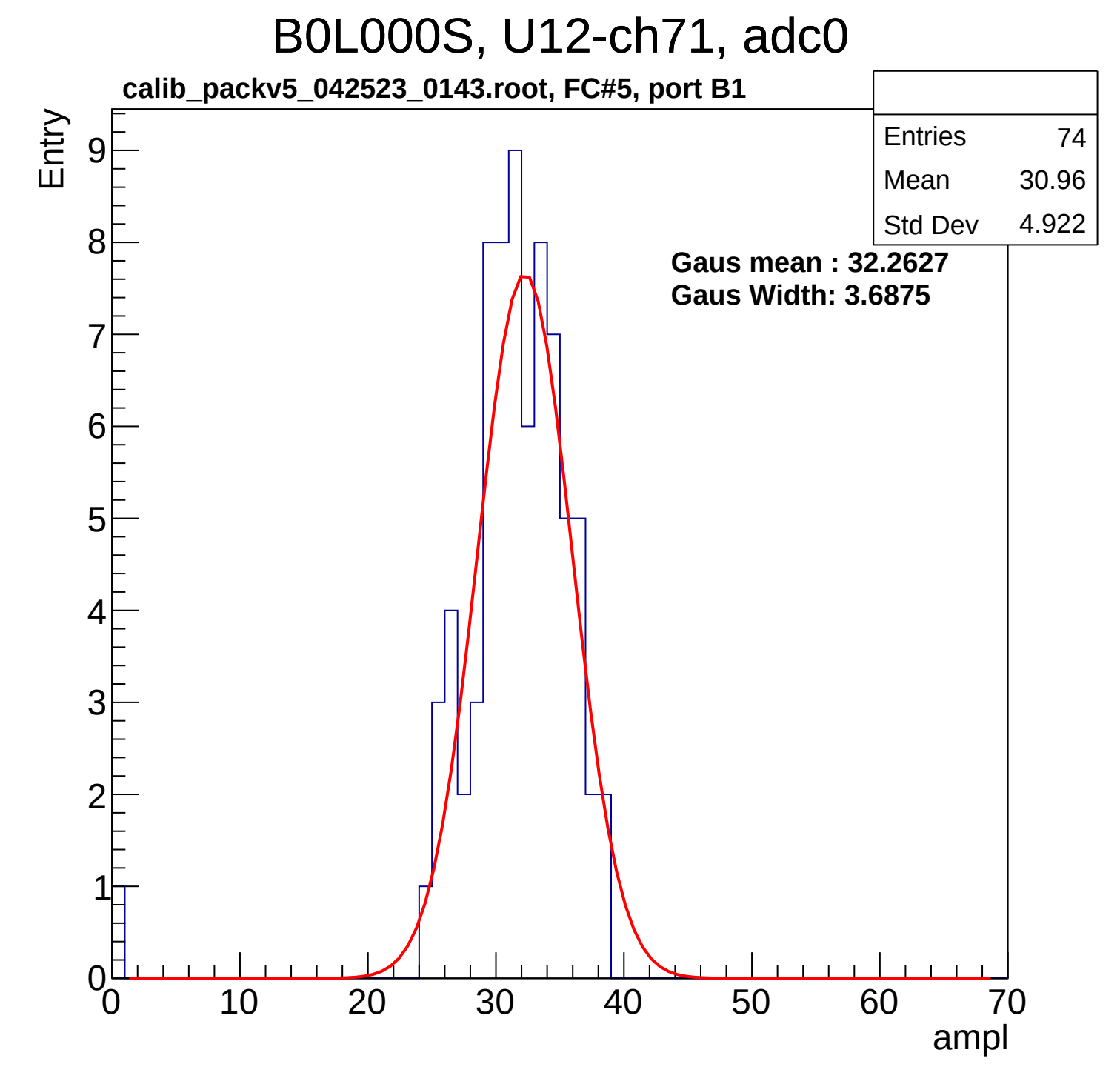
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 30.96
Std Dev 4.922
Gaus mean : 32.2627
Gaus Width: 3.6875
Entry
ampl
0
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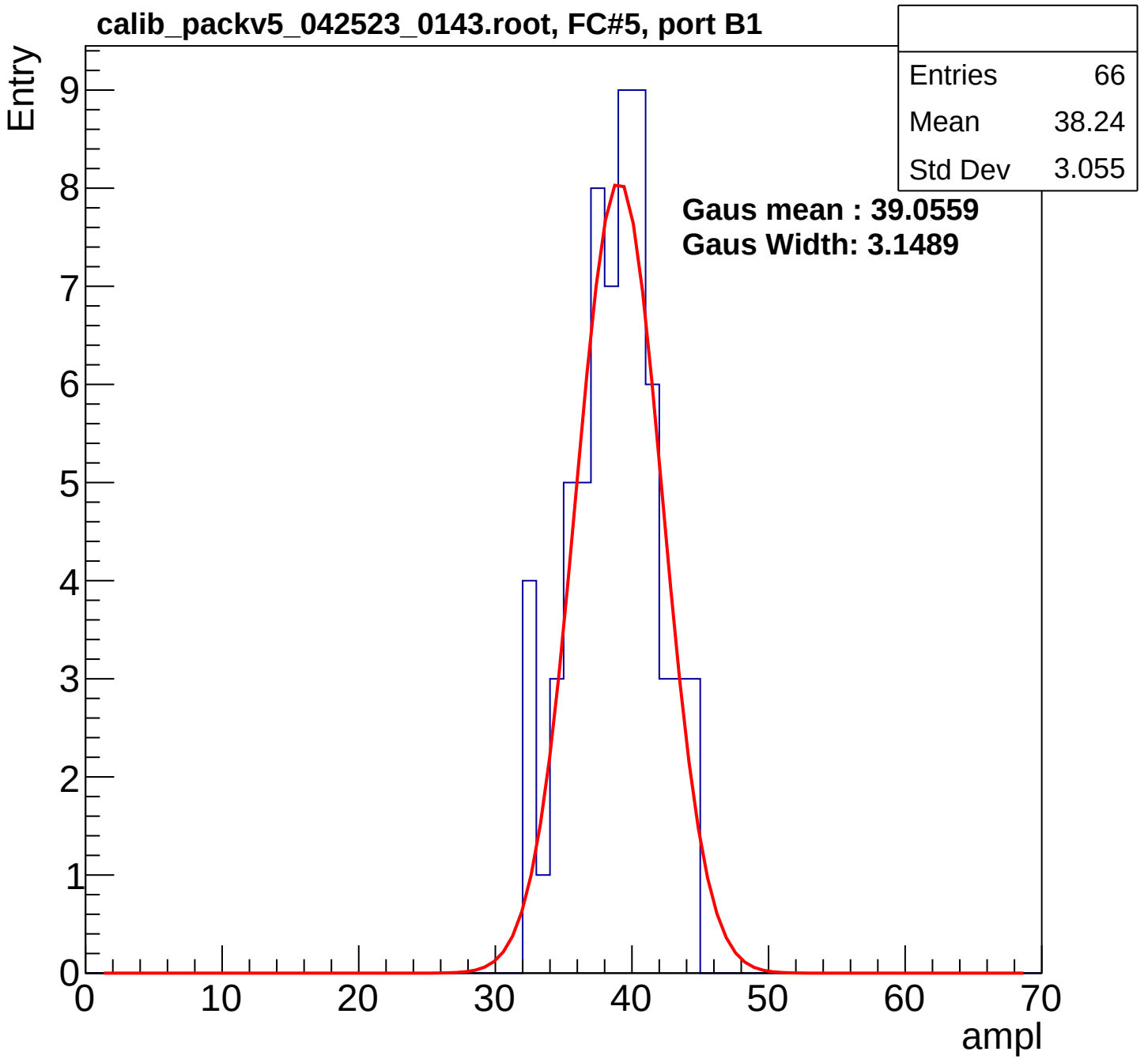
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 38.24
Std Dev 3.055
Gaus mean : 39.0559
Gaus Width: 3.1489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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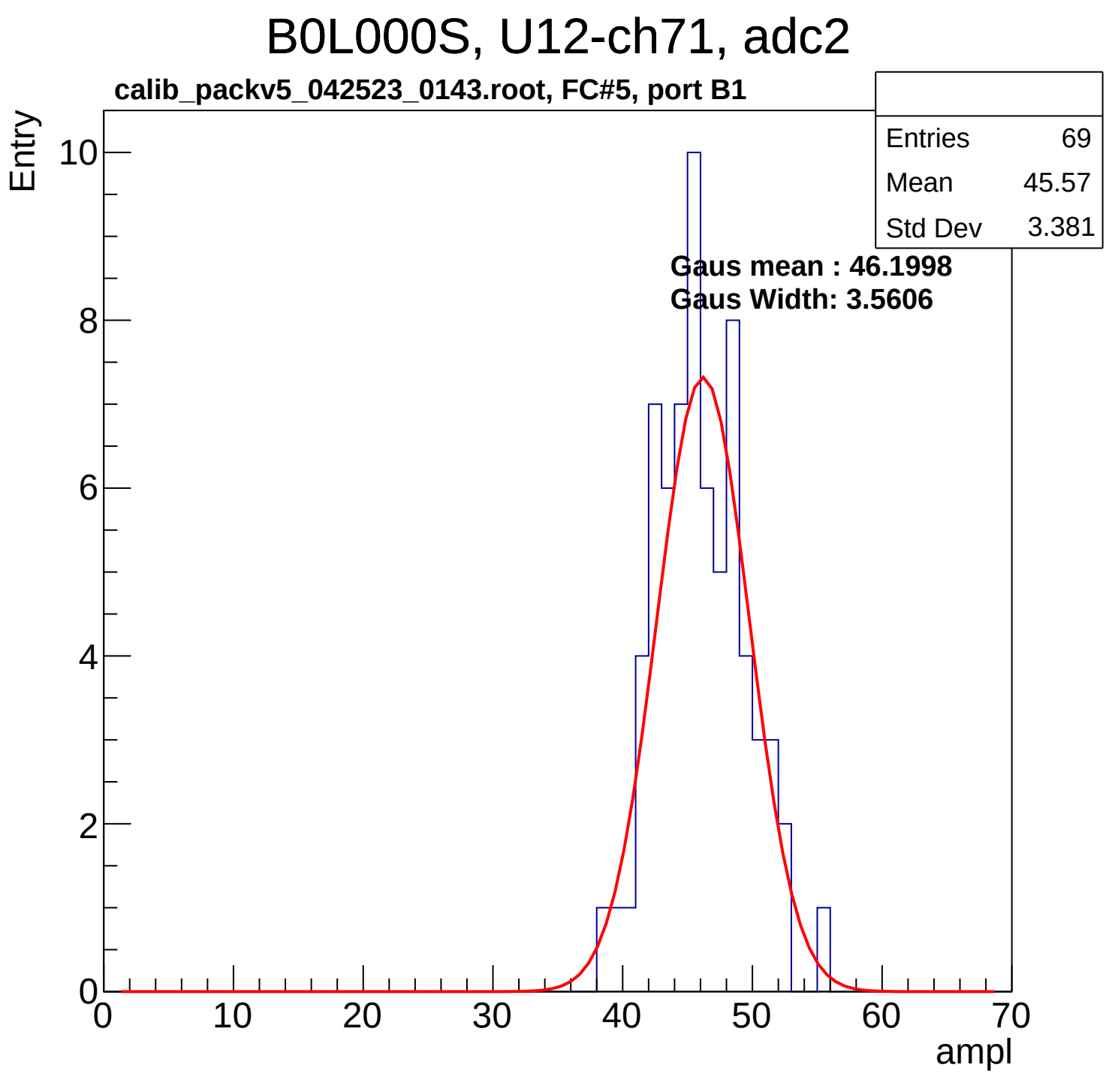
B0L000S, U12-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.57
Std Dev 3.381
Gaus mean : 46.1998
Gaus Width: 3.5606
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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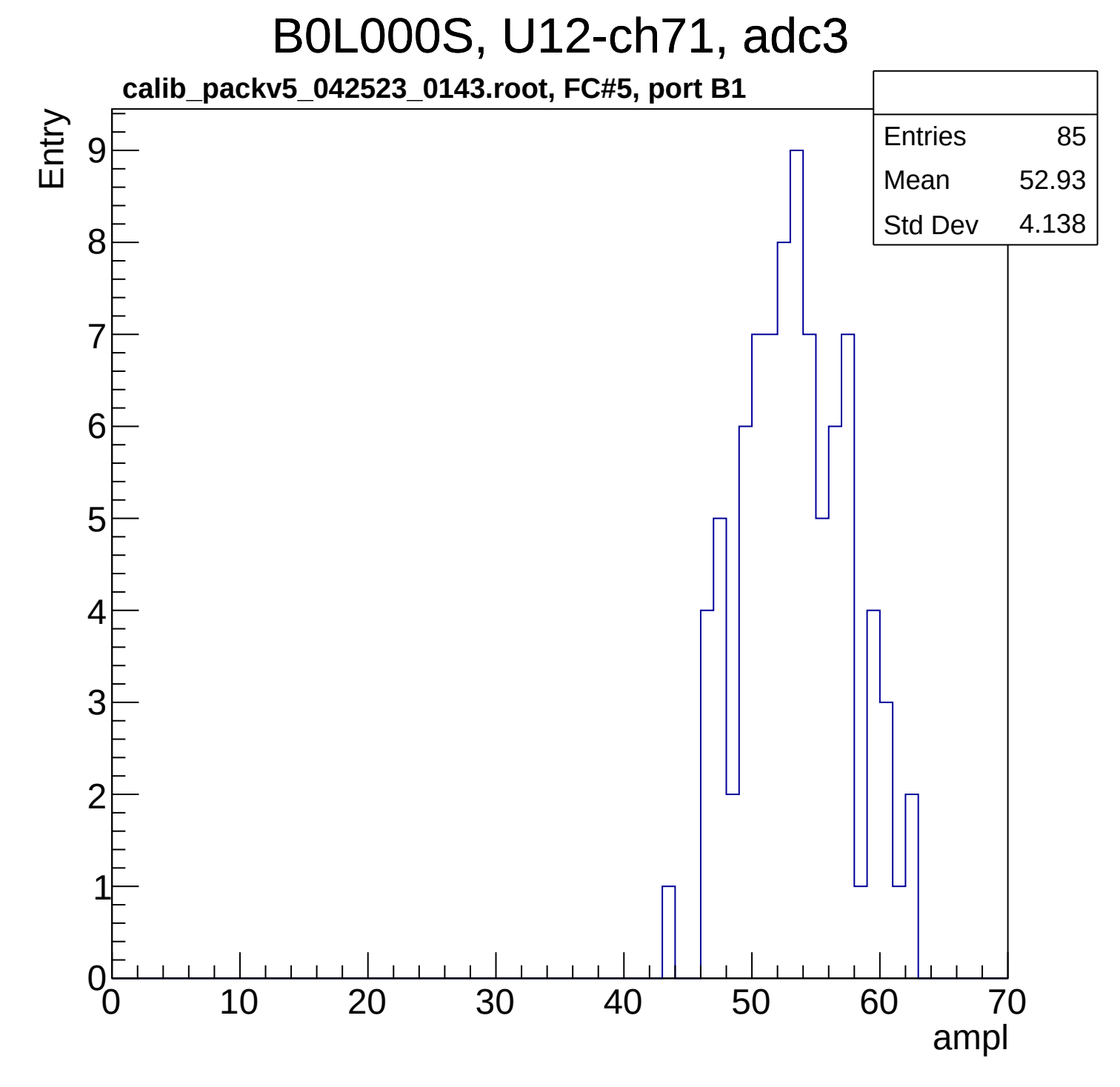
B0L000S, U12-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 52.93
Std Dev 4.138
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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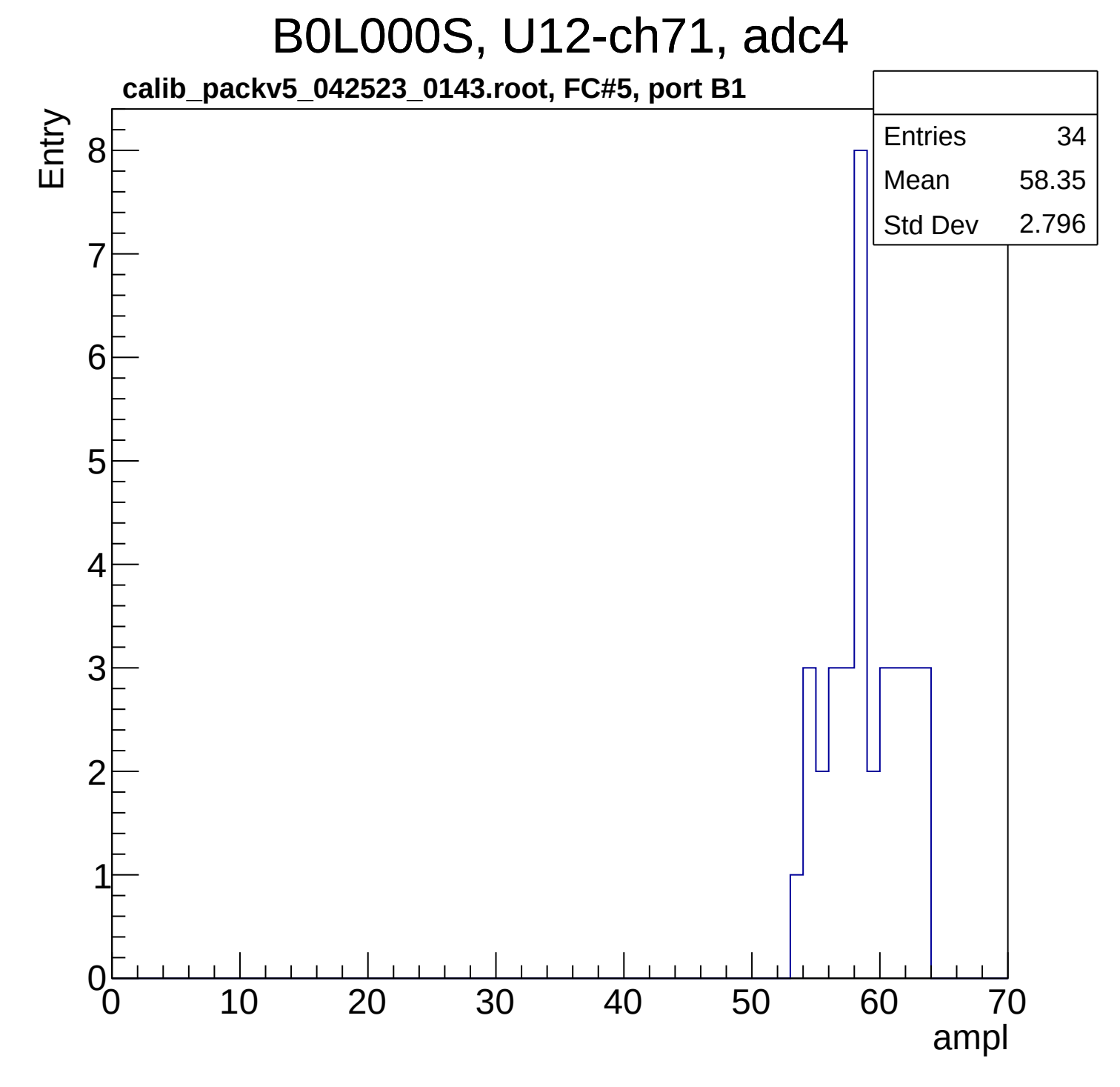
B0L000S, U12-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 58.35
Std Dev 2.796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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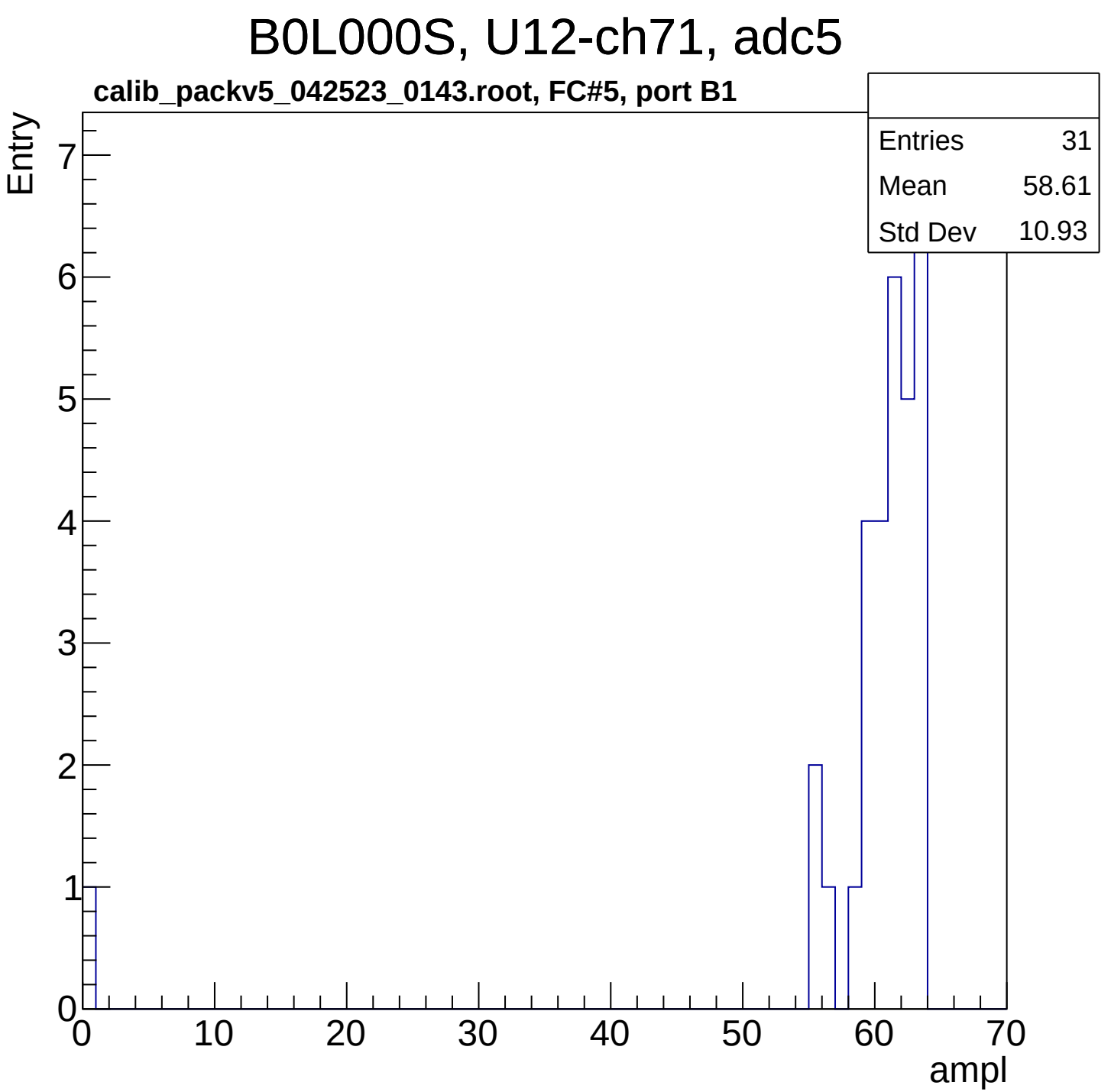
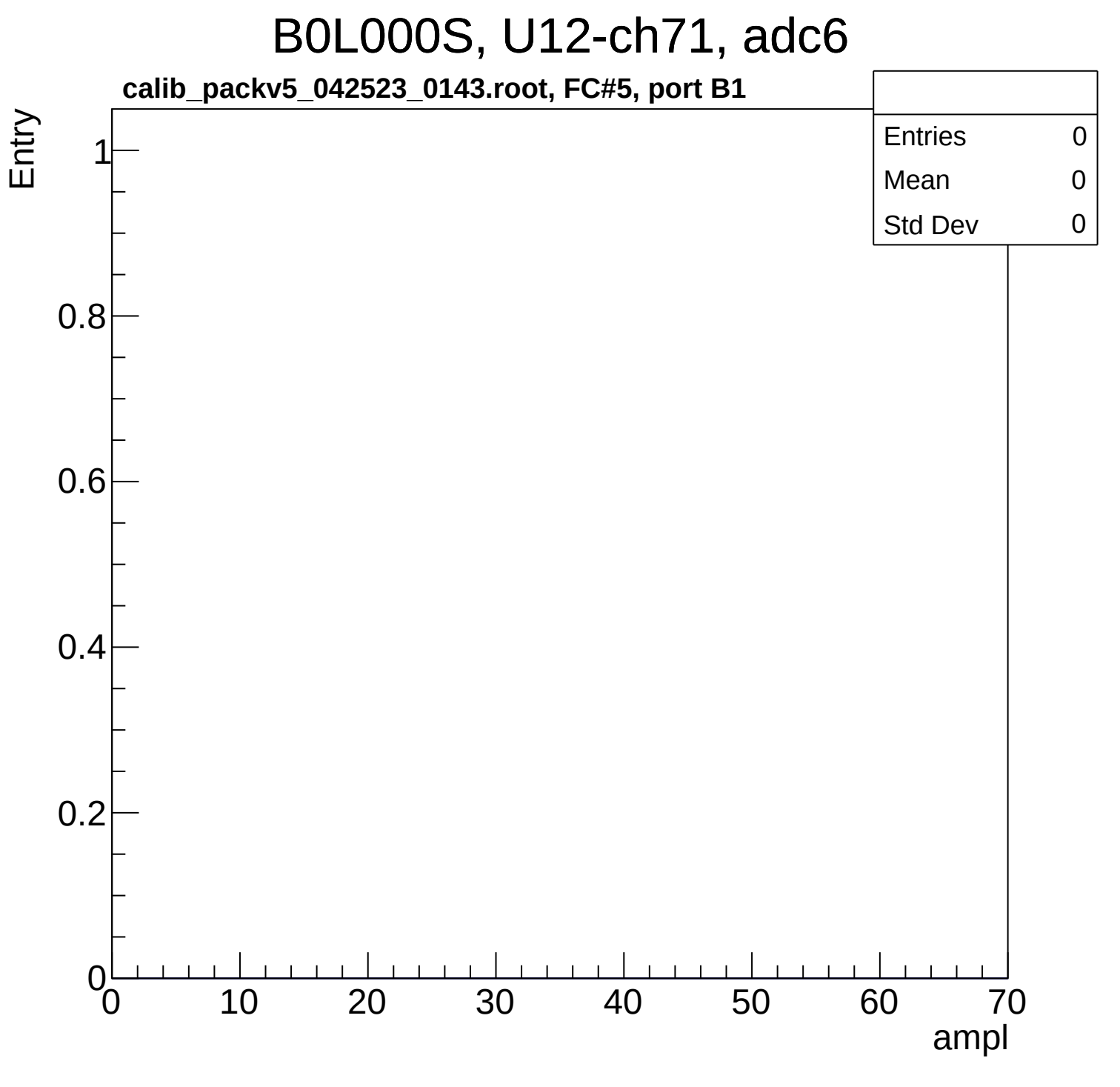
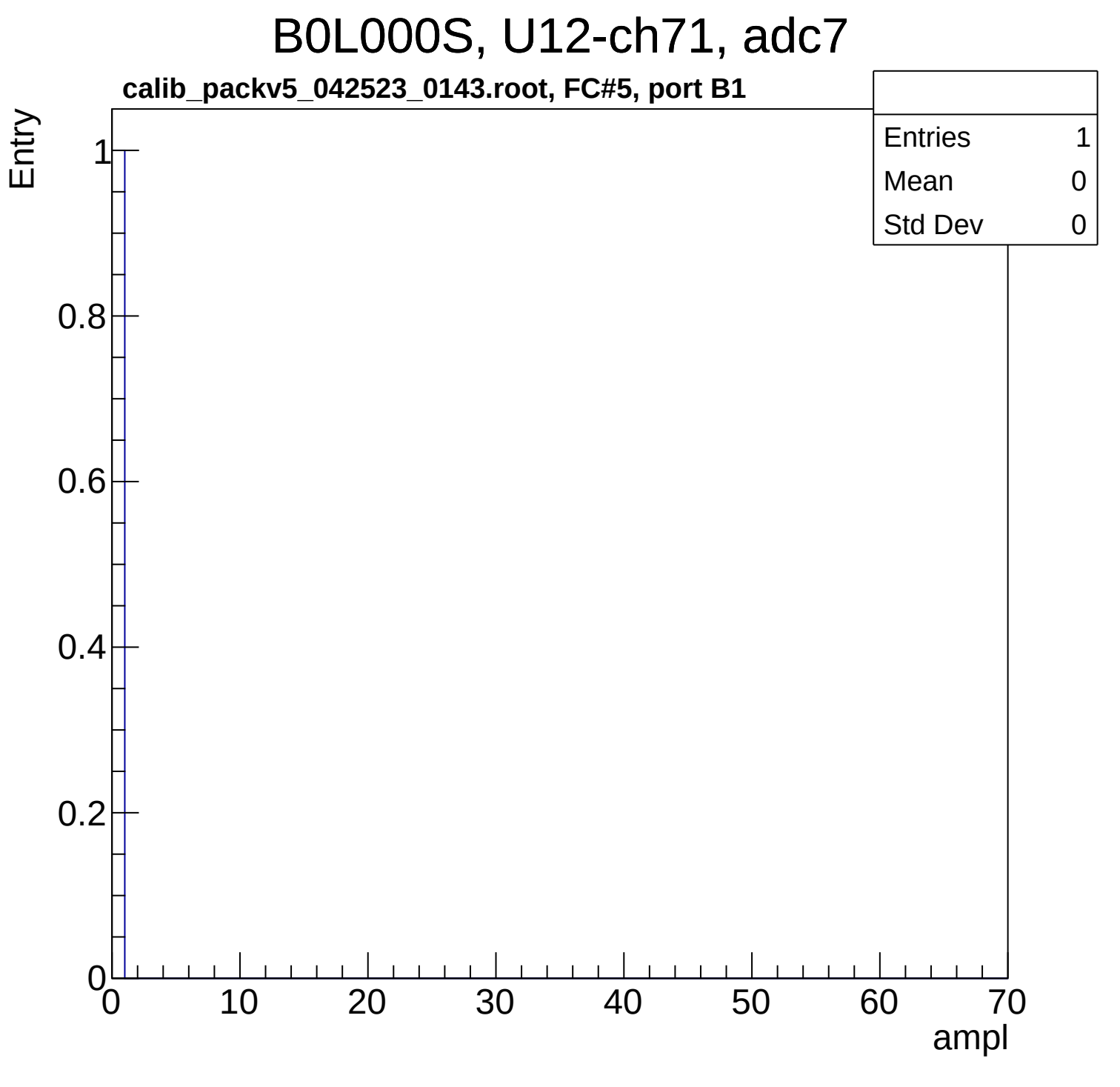
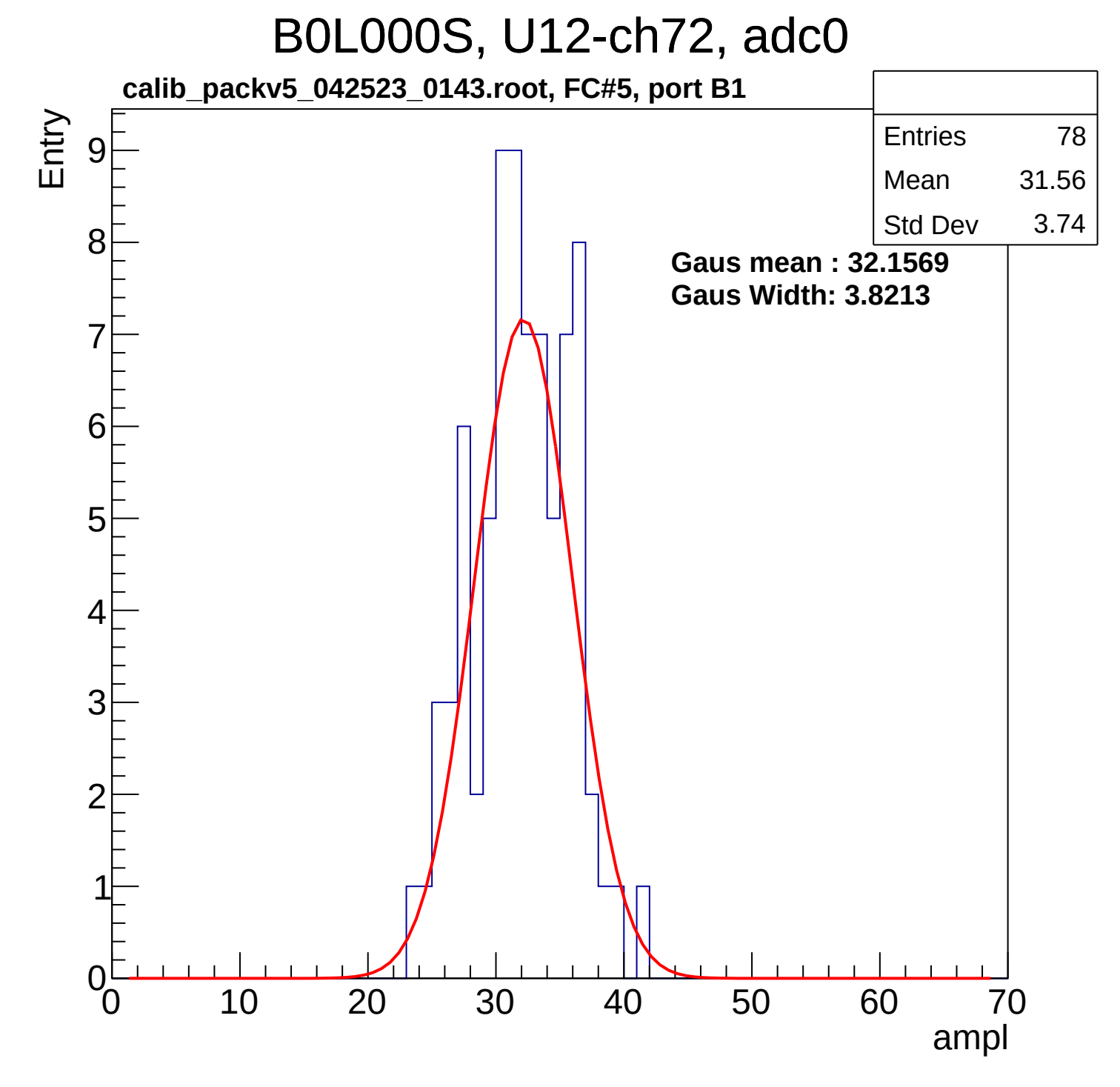

B0L000S, U12-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.61
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 31.56
Std Dev 3.74
Gaus mean : 32.1569
Gaus Width: 3.8213
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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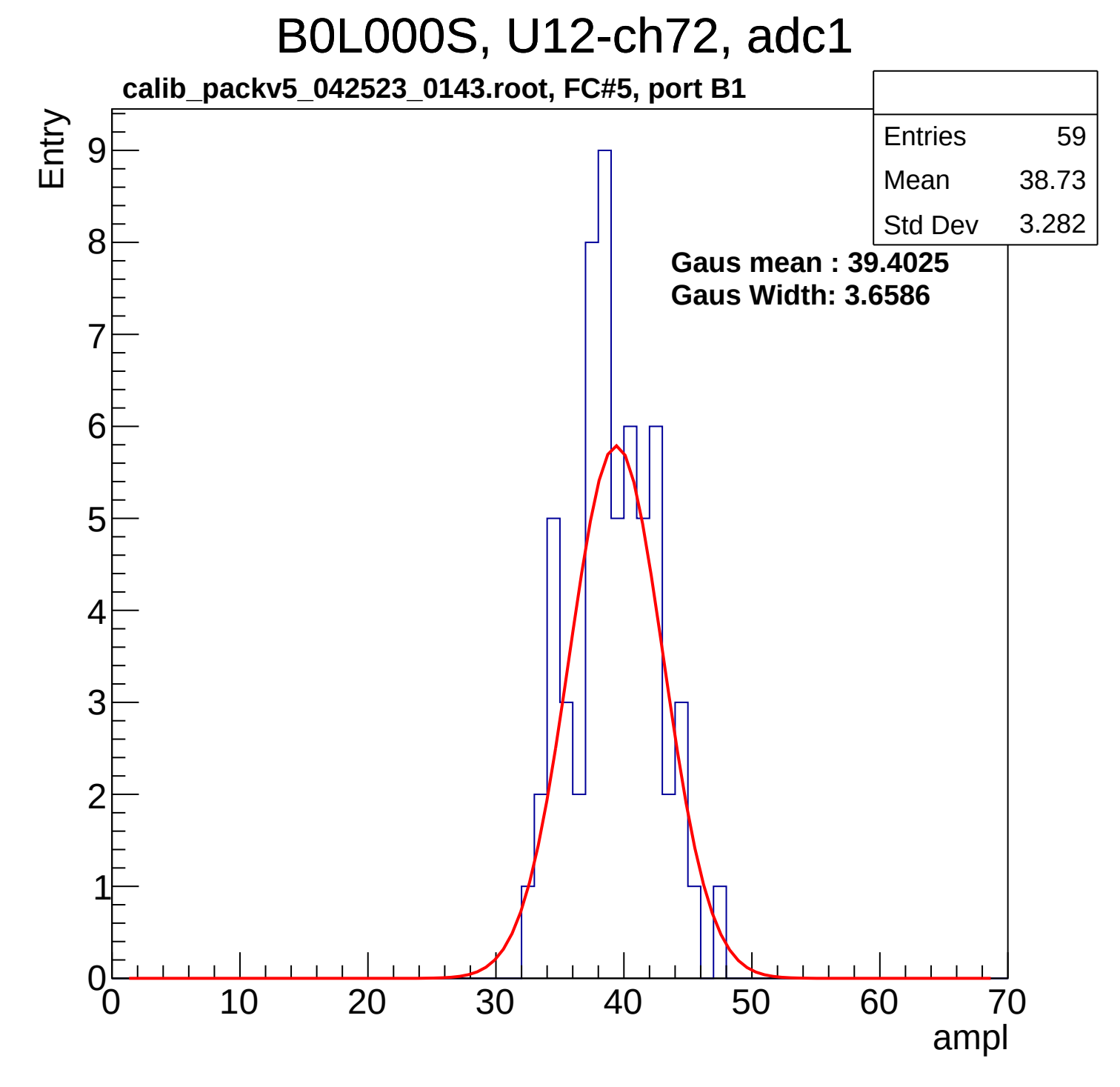
B0L000S, U12-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 38.73
Std Dev 3.282
Gaus mean : 39.4025
Gaus Width: 3.6586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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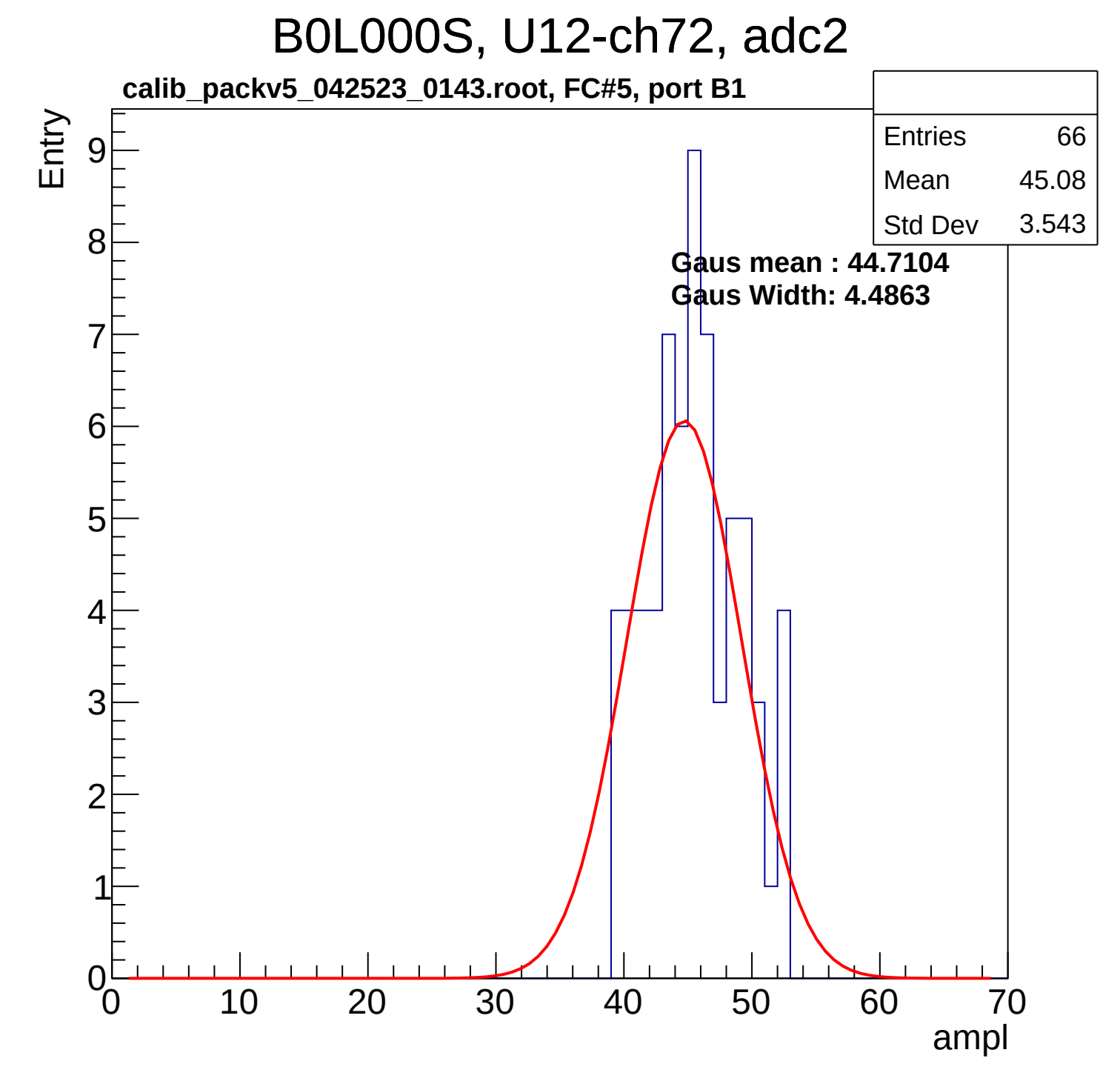
40
50
60
70


B0L000S, U12-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 45.08
Std Dev 3.543
Gaus mean : 44.7104
Gaus Width: 4.4863
Entry
ampl
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2
3
4
5
6
7
8
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0
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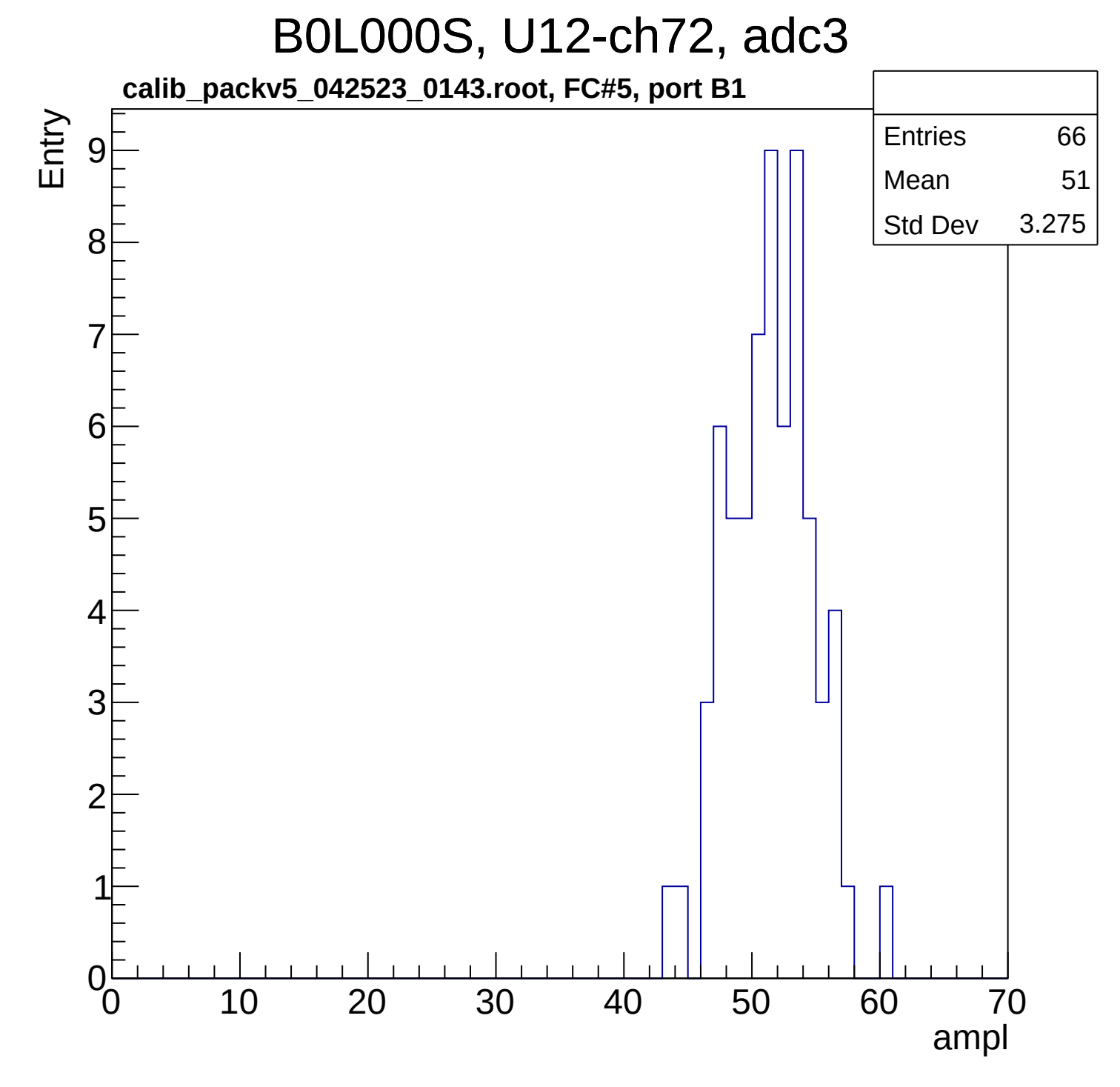
60
70


B0L000S, U12-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51
Std Dev 3.275
Entry
ampl
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5
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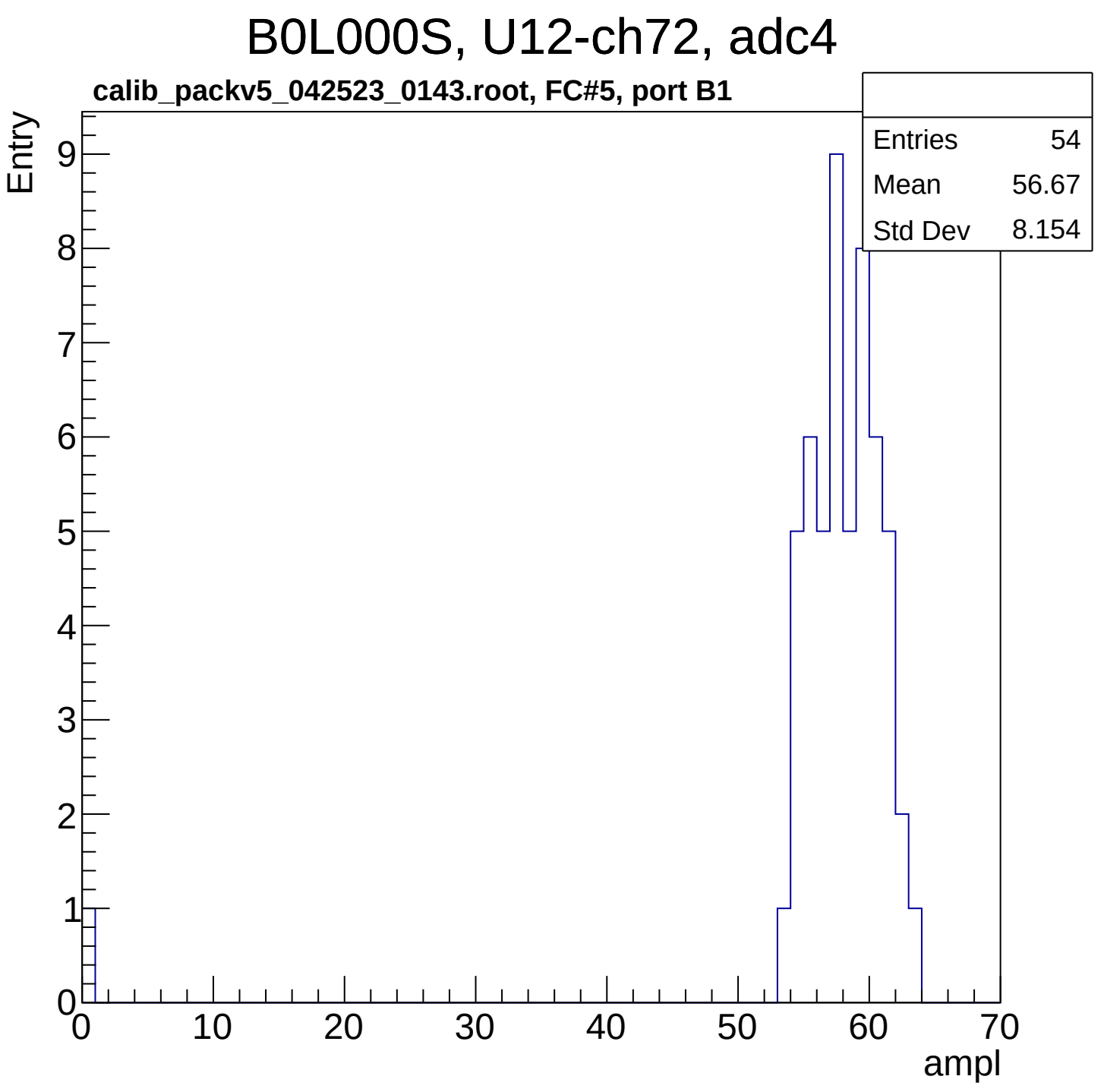
B0L000S, U12-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.67
Std Dev 8.154
Entry
ampl
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3
4
5
6
7
8
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0
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40
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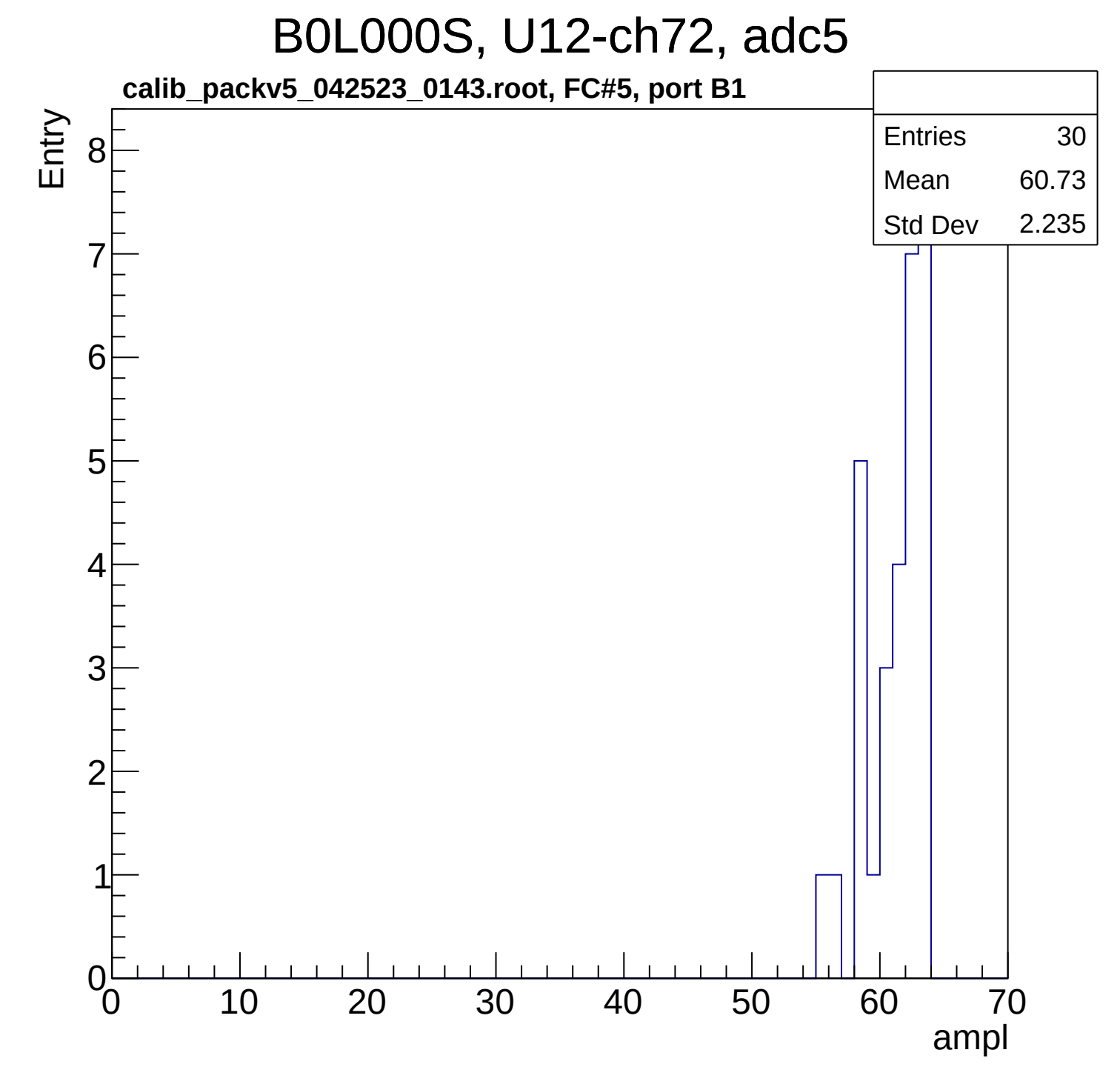
60
70


B0L000S, U12-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.73
Std Dev 2.235
Entry
ampl
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2
3
4
5
6
7
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0
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20
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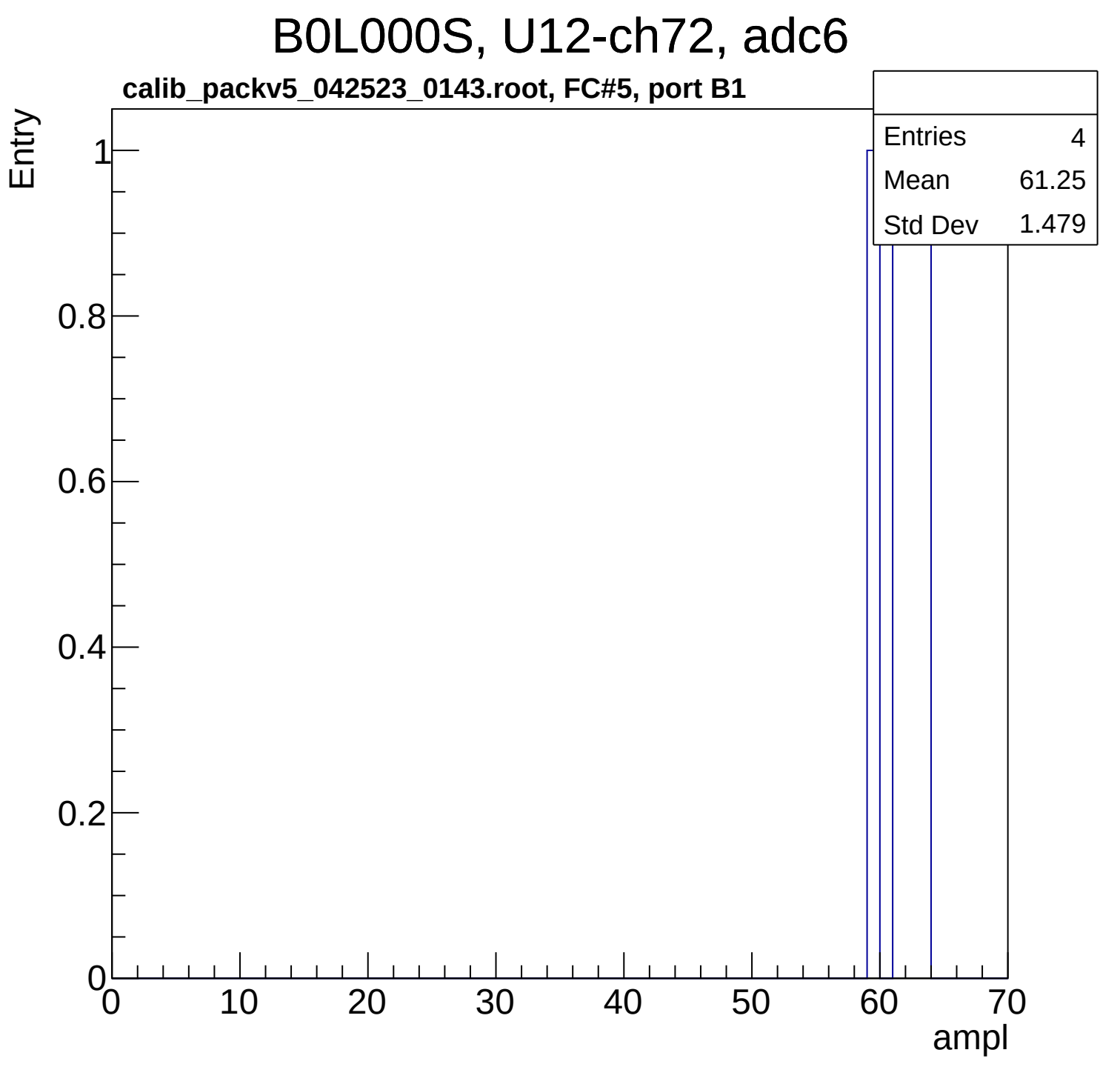
40
50
60
70


B0L000S, U12-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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0.2
0.4
0.6
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0
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20
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40
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60
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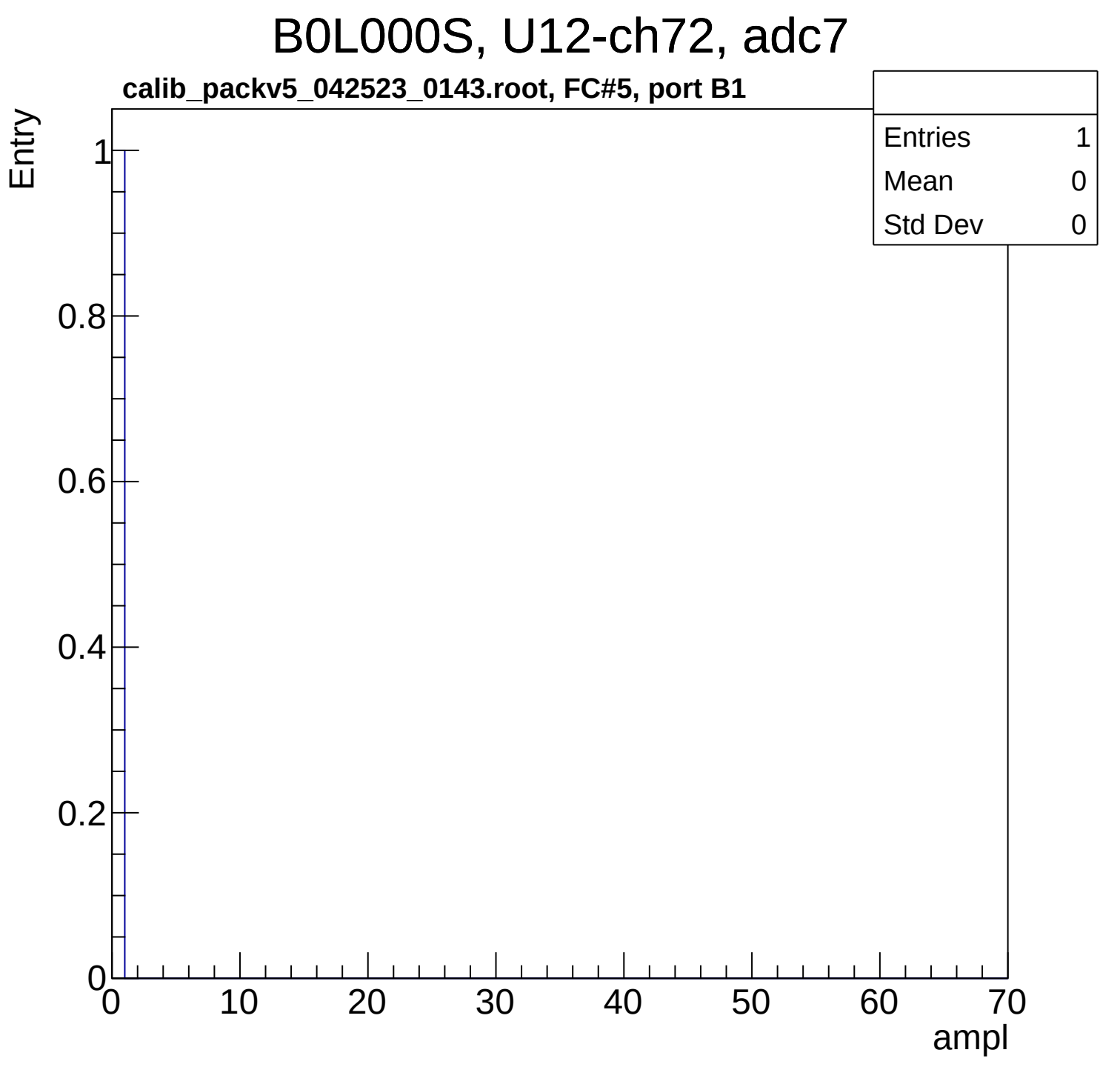
B0L000S, U12-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
30
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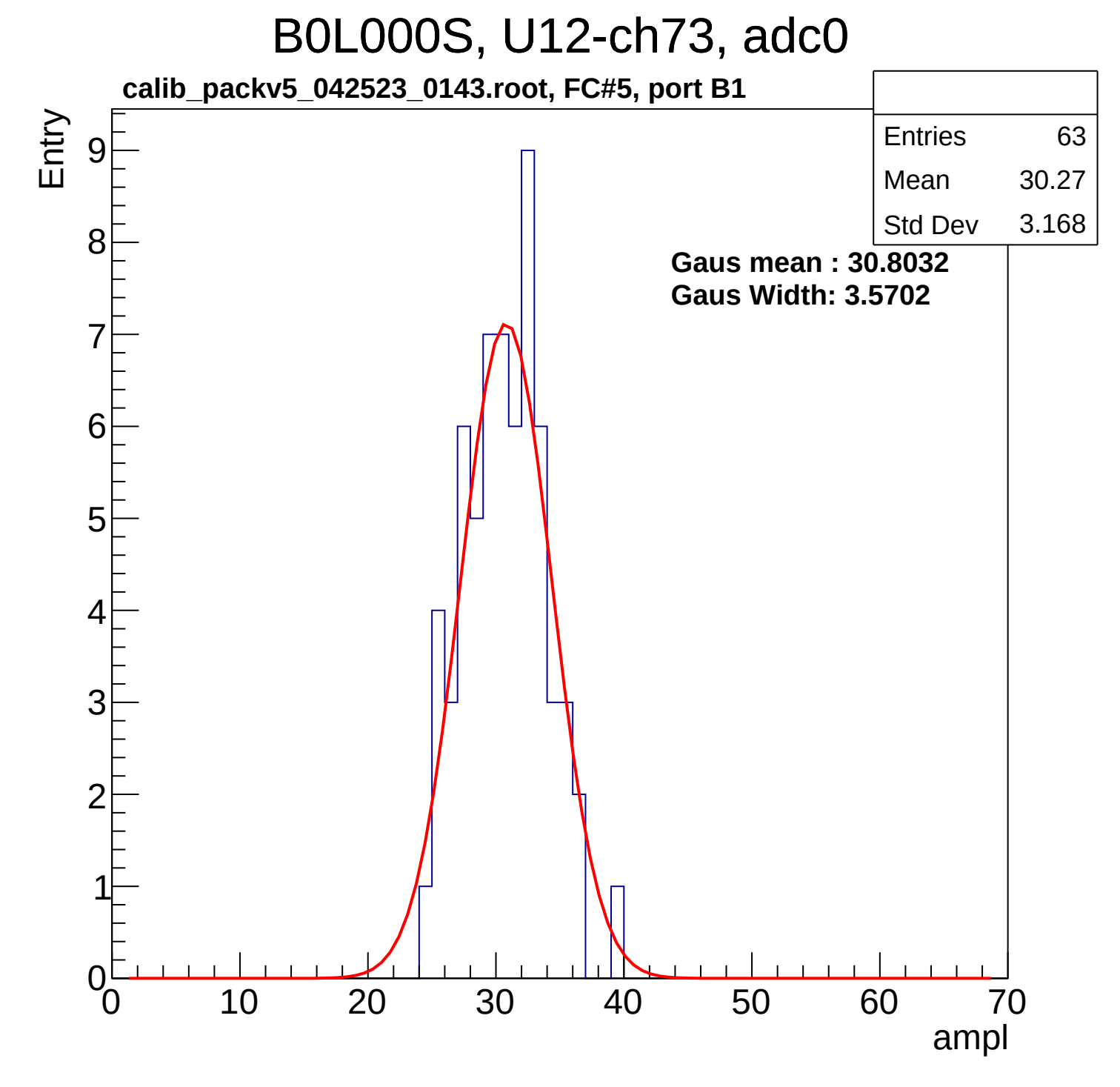
50
60
70
ampl


B0L000S, U12-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.27
Std Dev 3.168
Gaus mean : 30.8032
Gaus Width: 3.5702
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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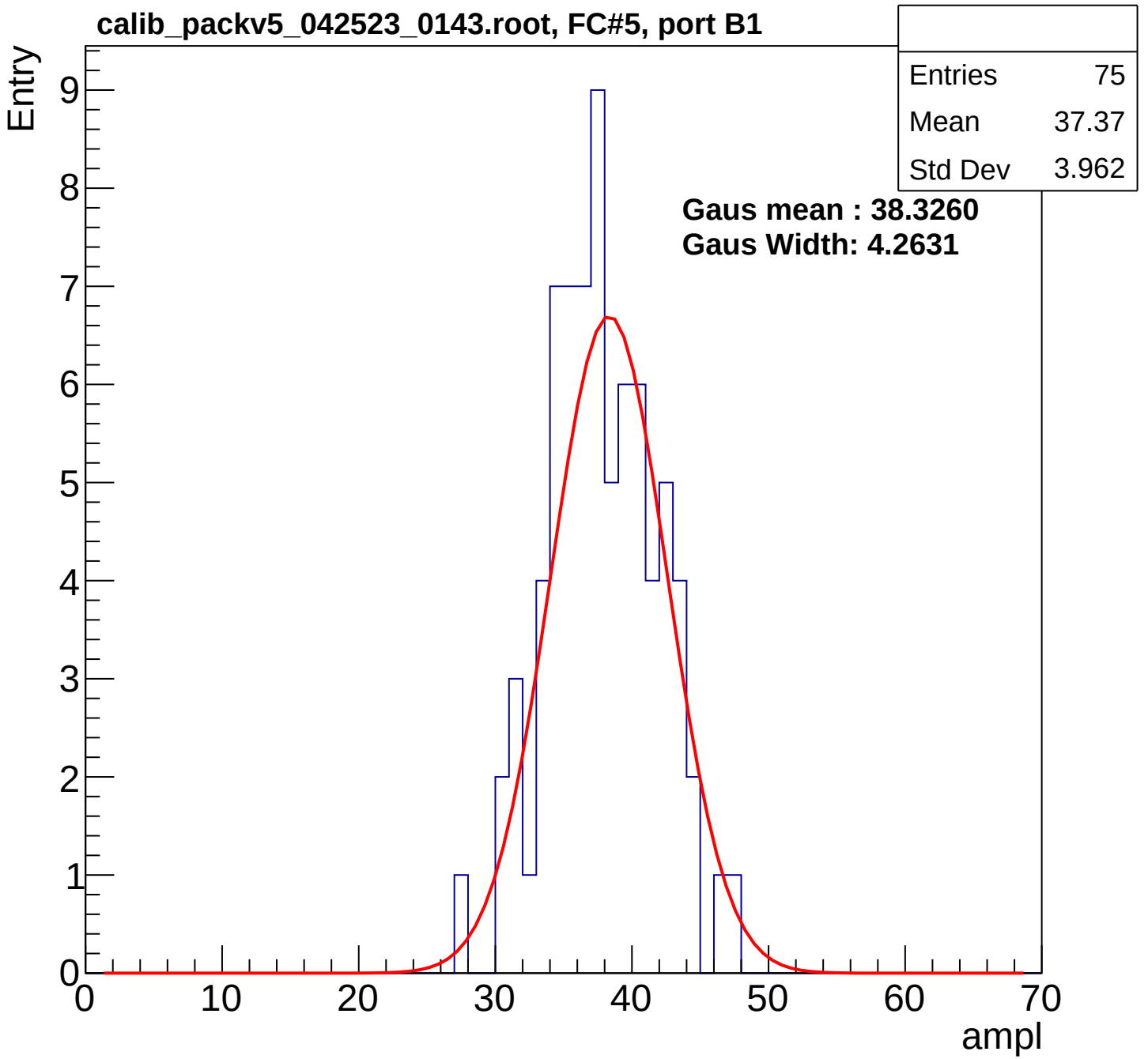
B0L000S, U12-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 37.37
Std Dev 3.962
Gaus mean : 38.3260
Gaus Width: 4.2631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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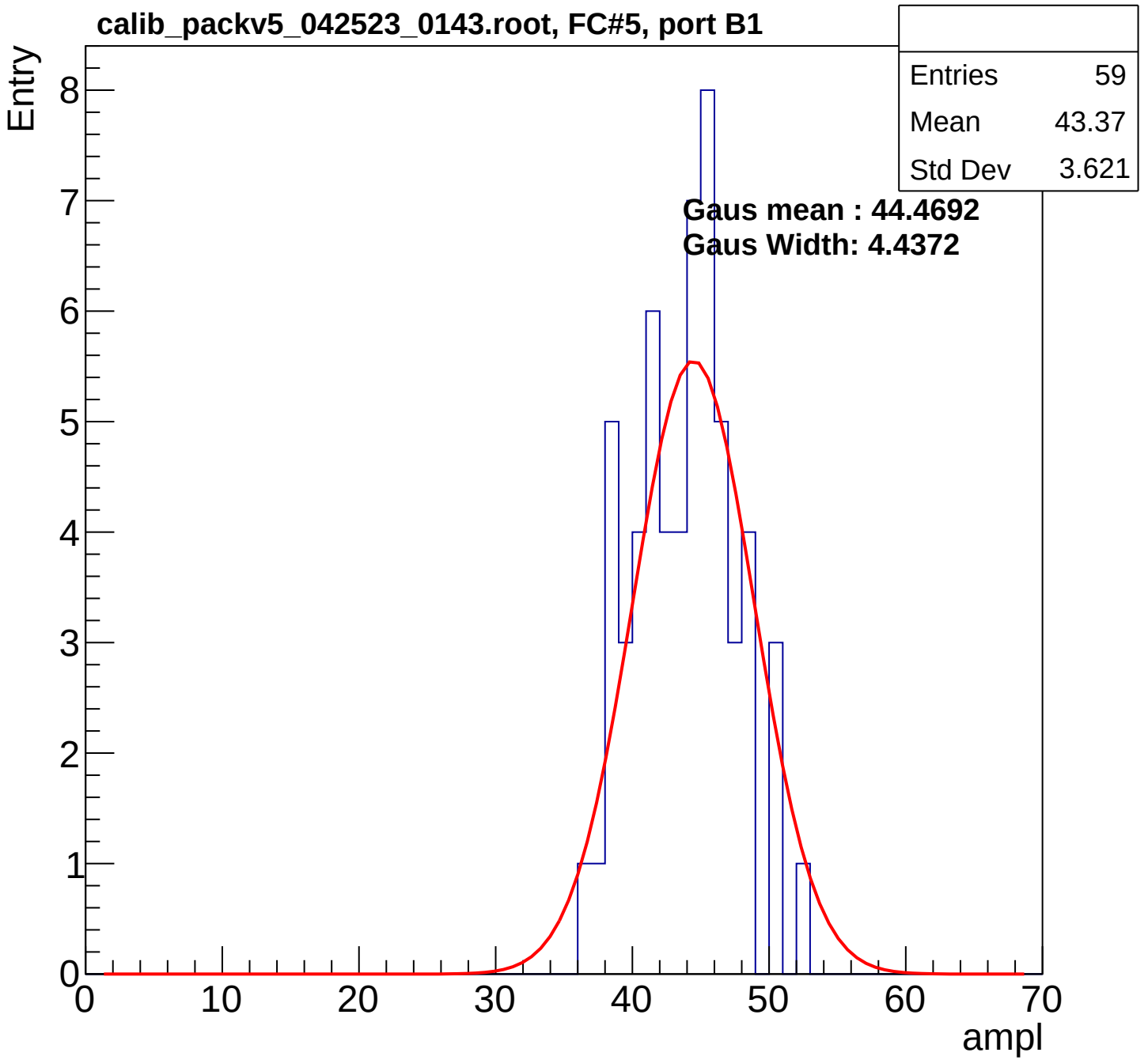
B0L000S, U12-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.37
Std Dev 3.621
Gaus mean : 44.4692
Gaus Width: 4.4372
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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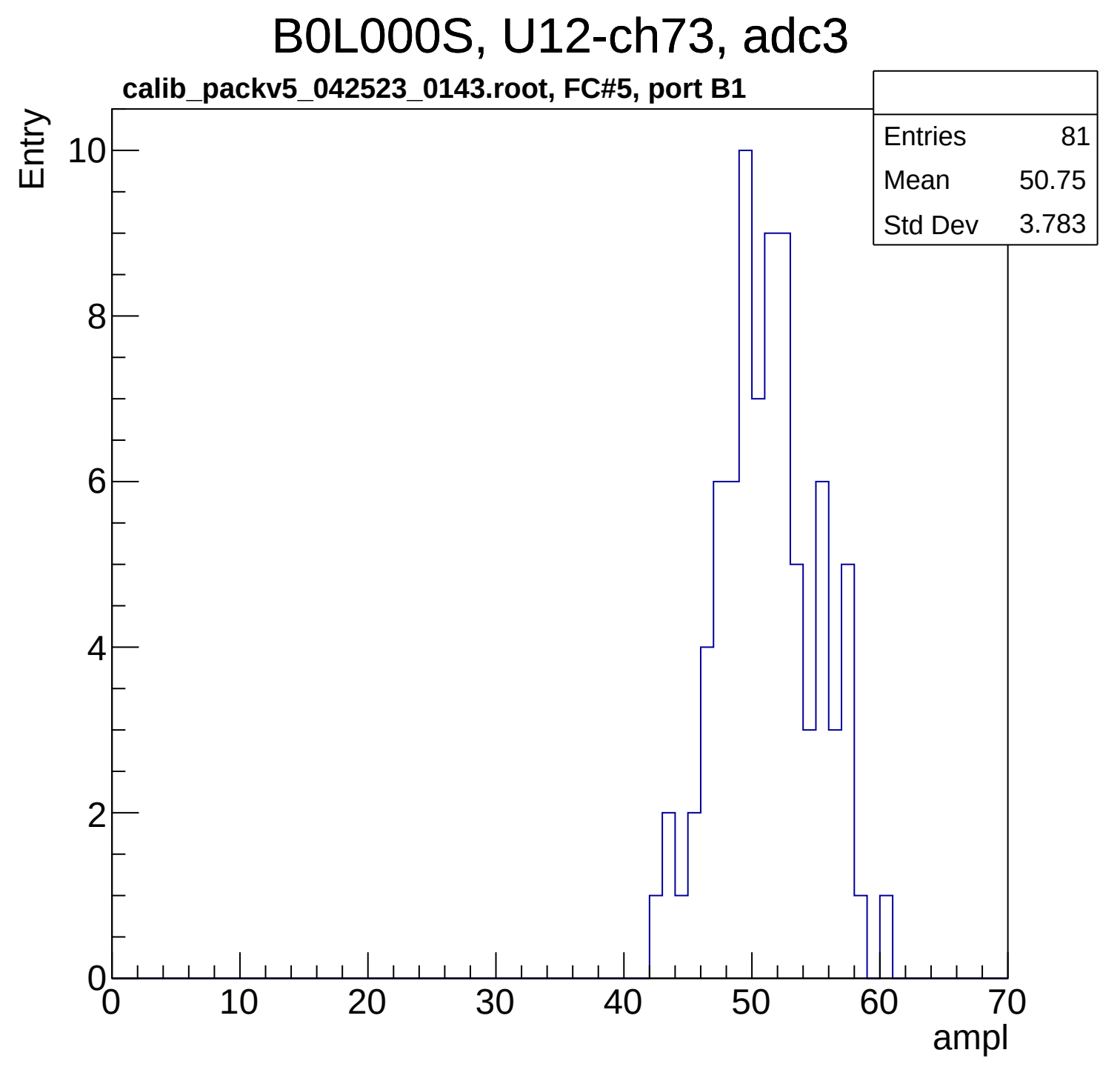
B0L000S, U12-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 50.75
Std Dev 3.783
Entry
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6
8
10
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10
20
30
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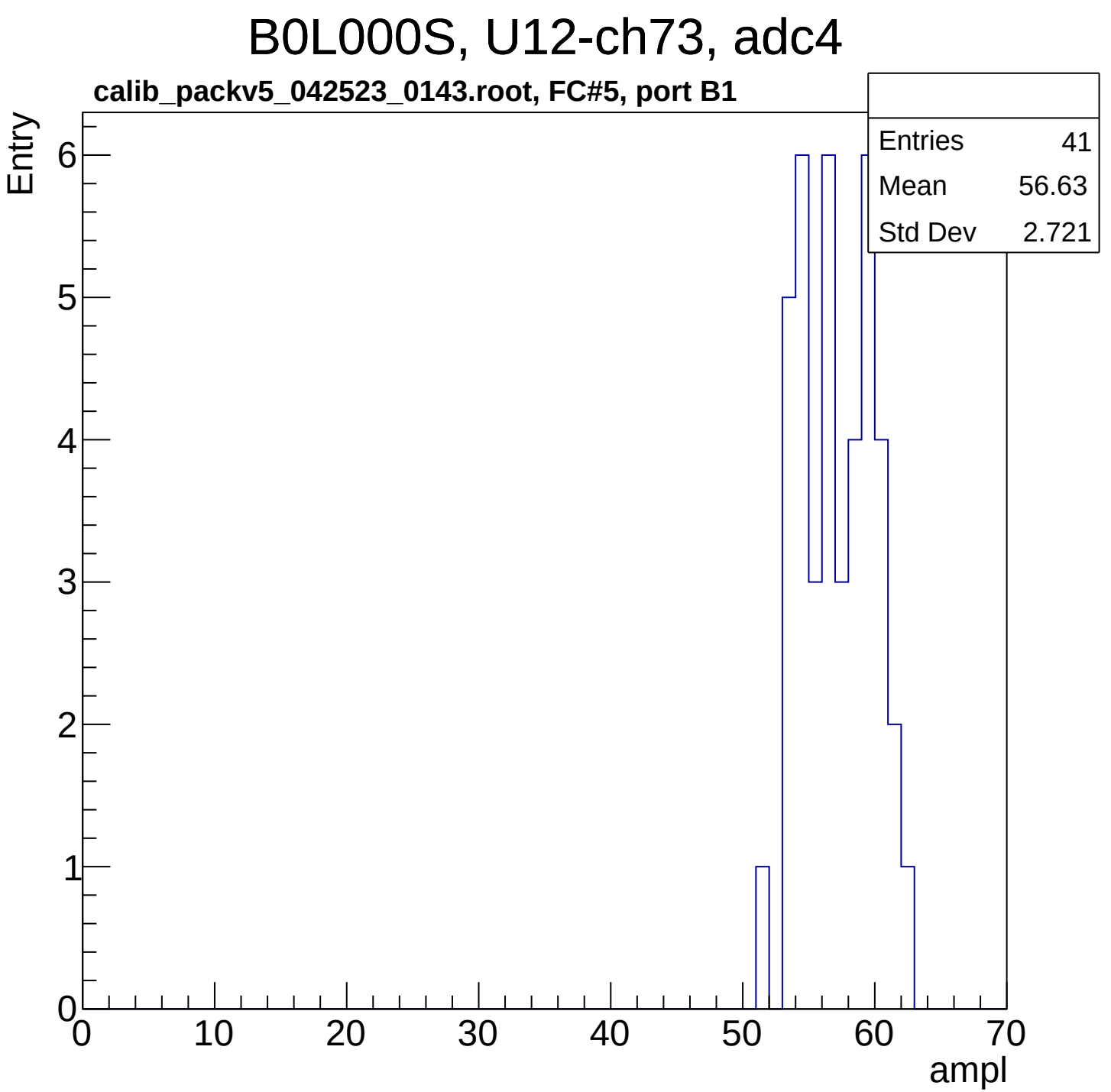
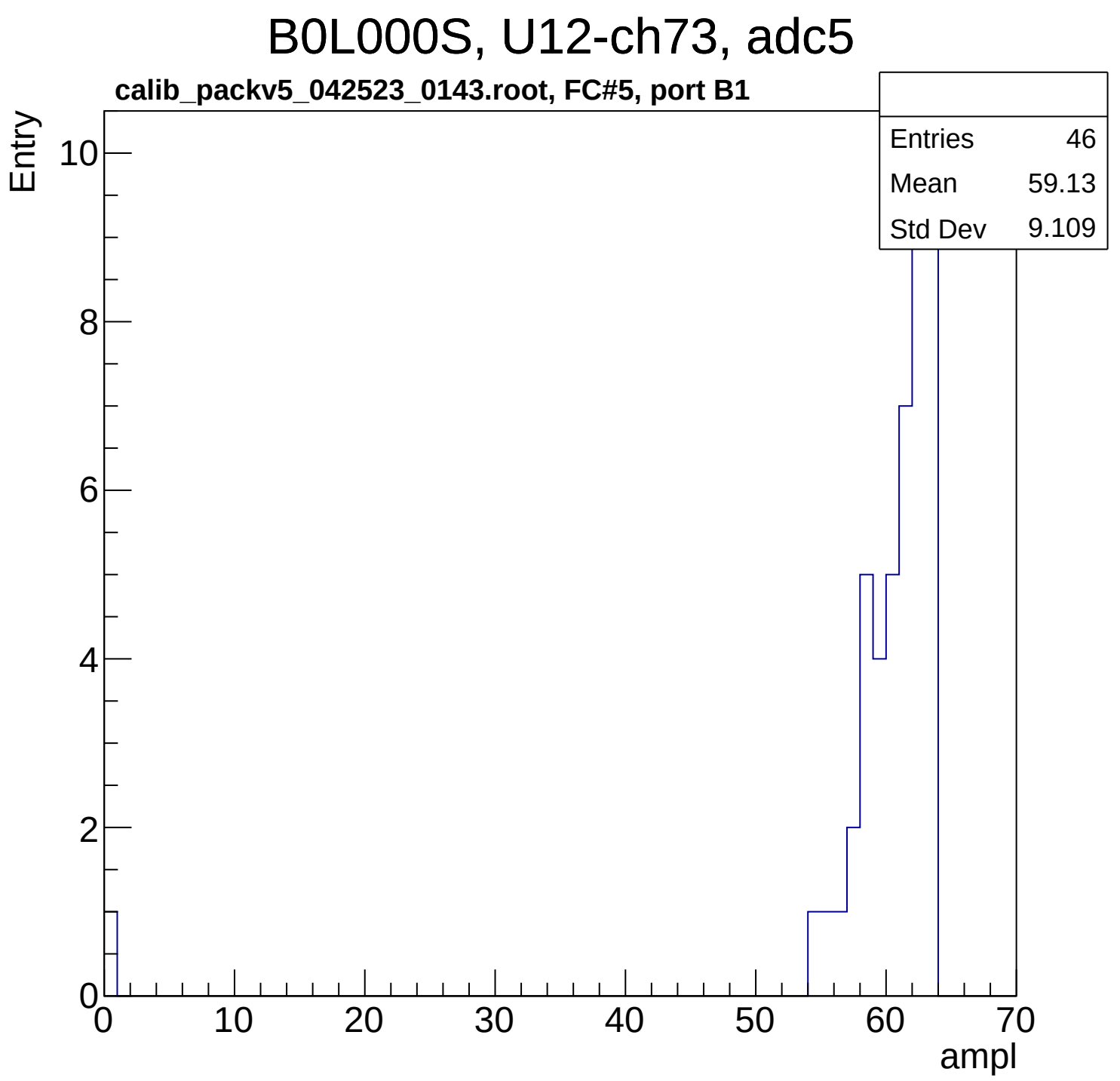
50
60
70
ampl


B0L000S, U12-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 56.63
Std Dev 2.721
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.13
Std Dev 9.109
Entry
0
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4
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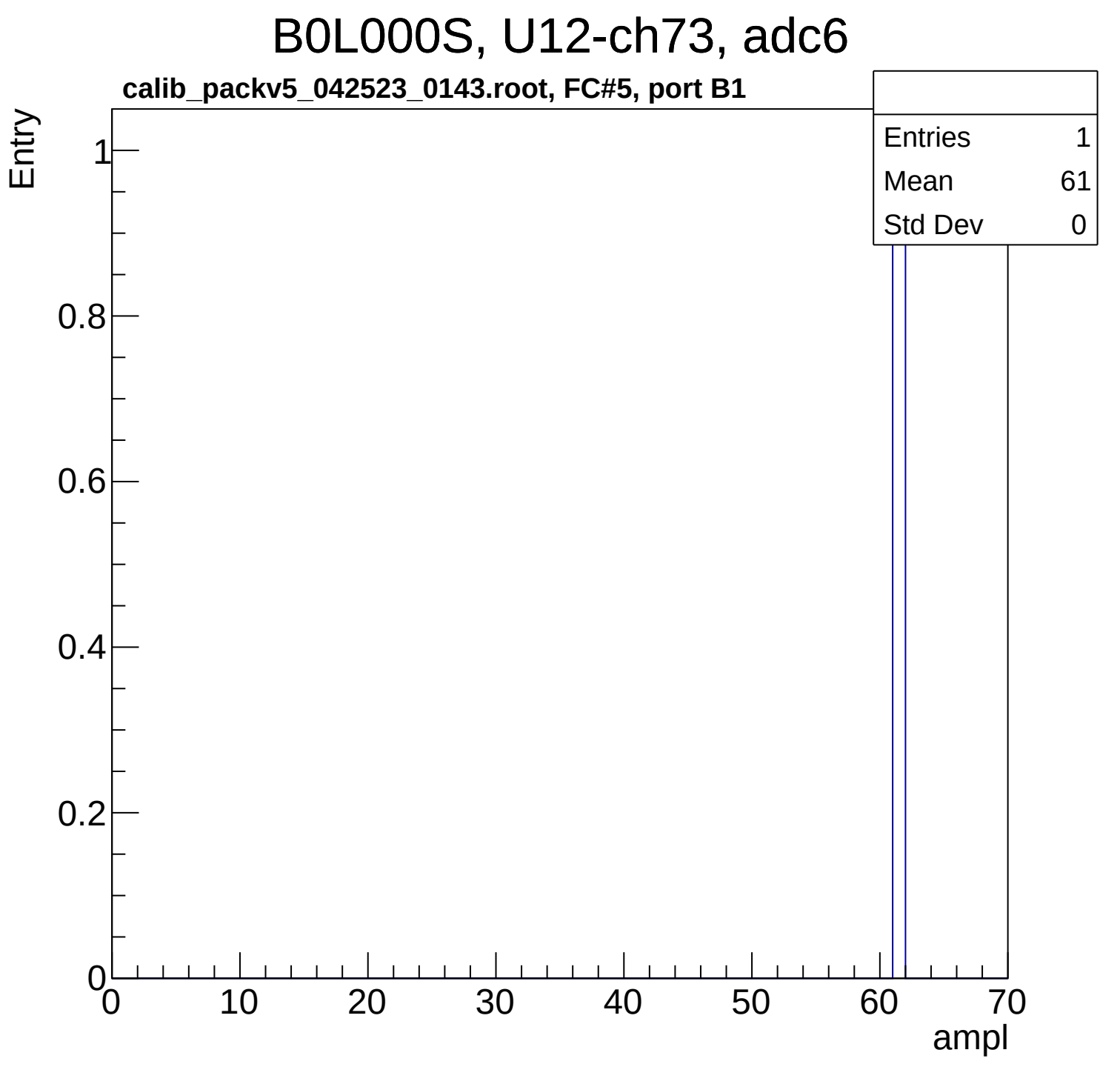
ampl


B0L000S, U12-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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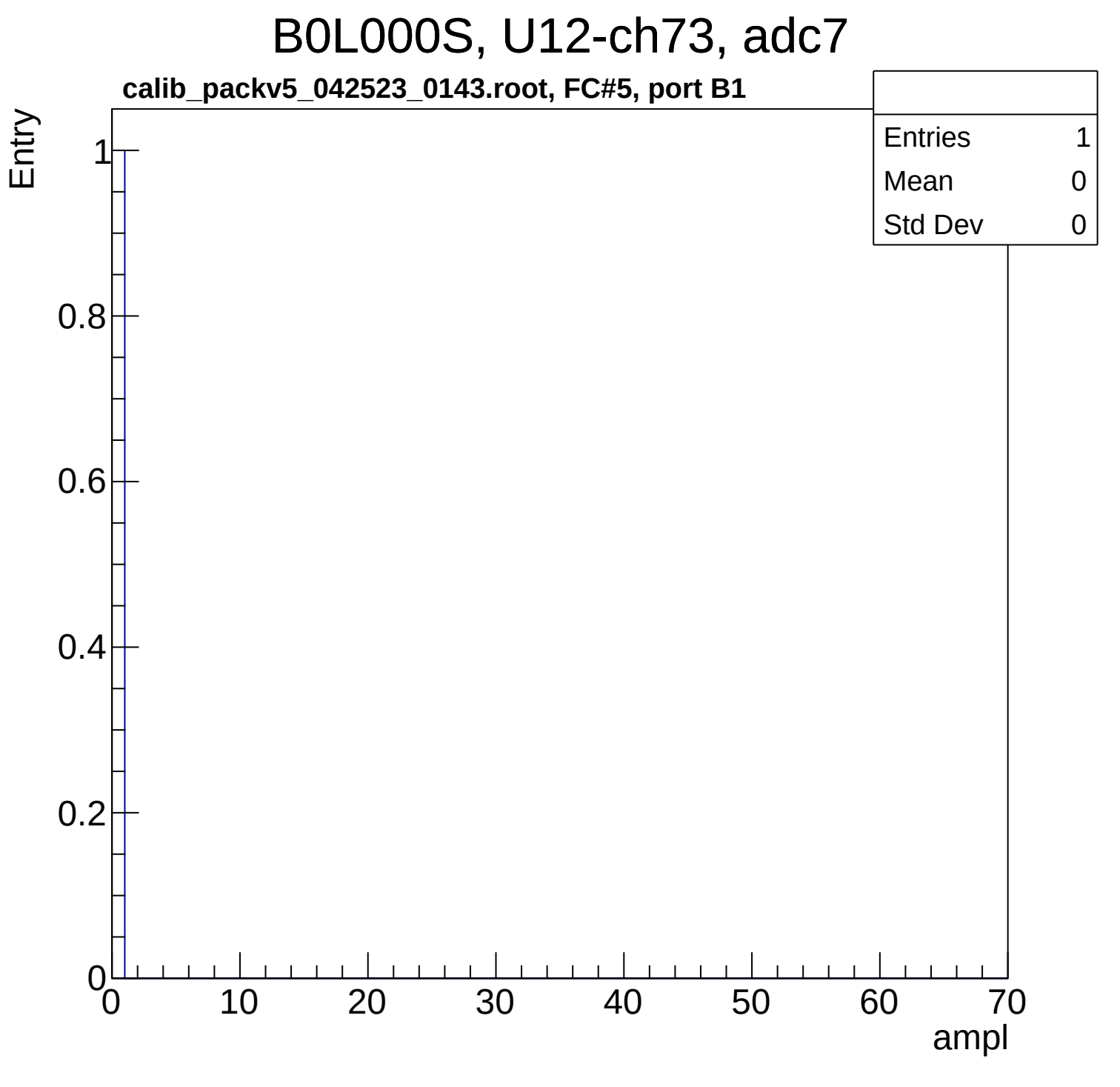
40
50
60
70
ampl


B0L000S, U12-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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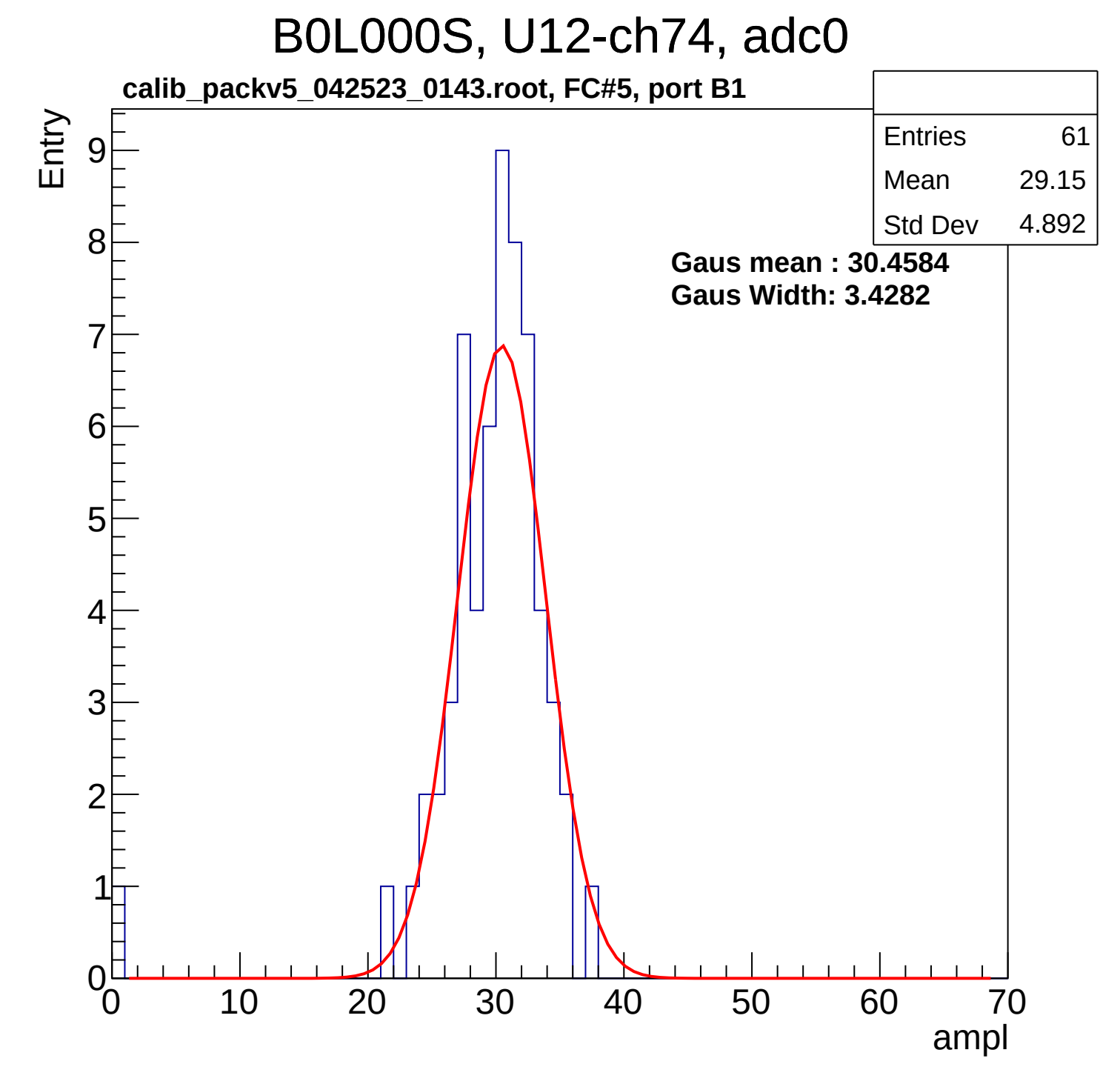
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.15
Std Dev 4.892
Gaus mean : 30.4584
Gaus Width: 3.4282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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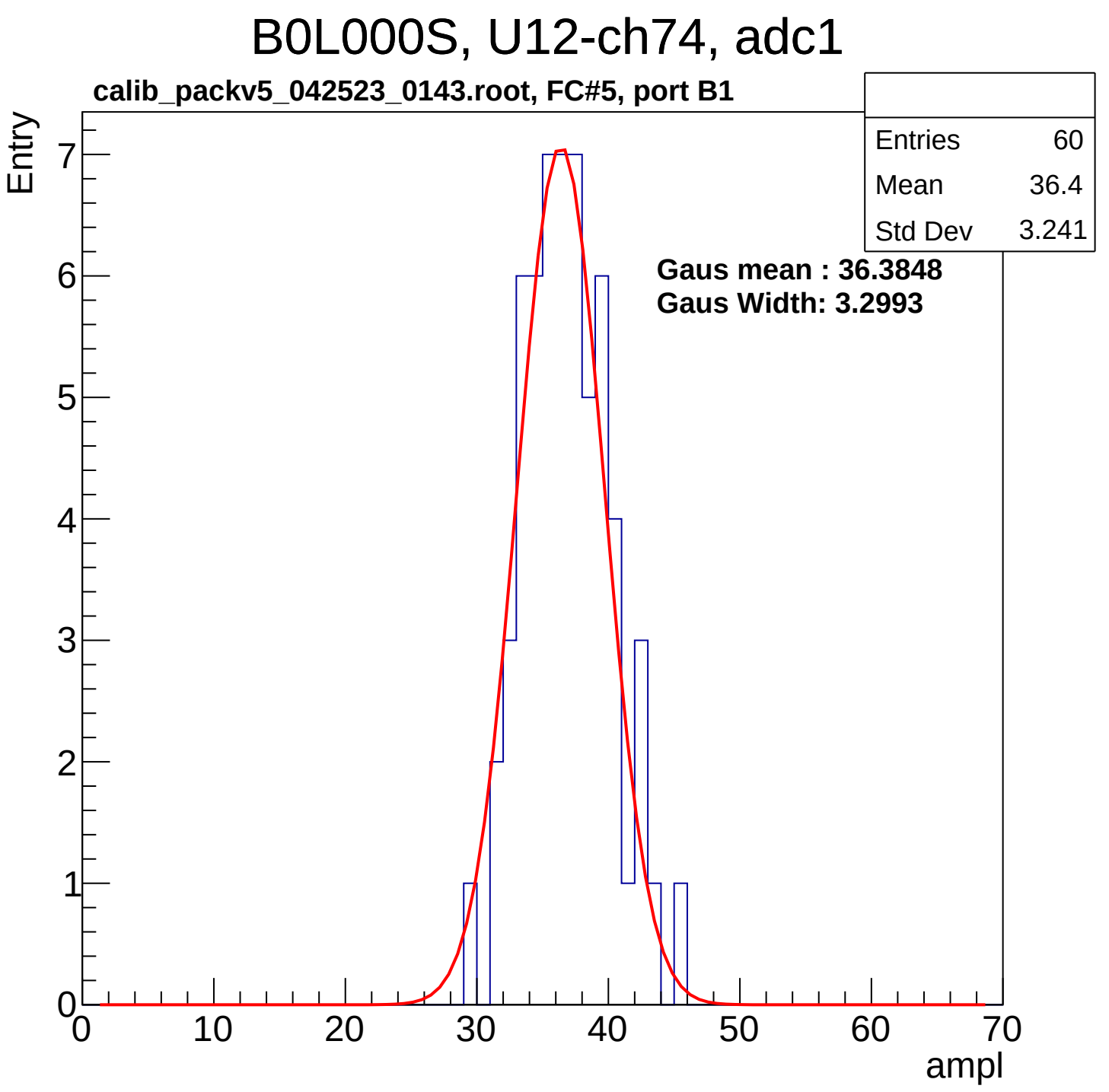
B0L000S, U12-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.4
Std Dev 3.241
Gaus mean : 36.3848
Gaus Width: 3.2993
Entry
ampl
0
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2
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4
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6
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0
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40
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60
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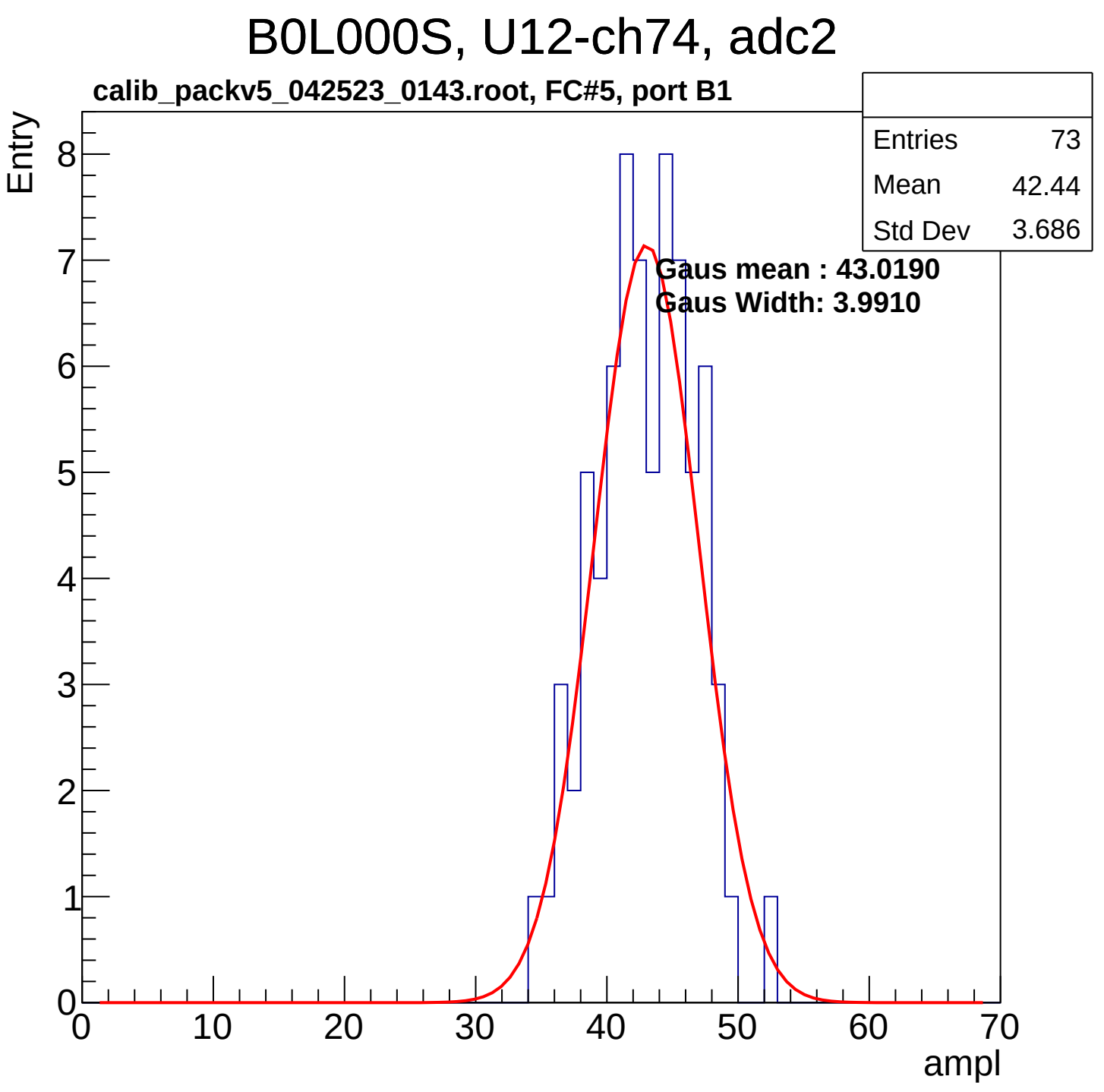
B0L000S, U12-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.44
Std Dev 3.686
Gaus mean : 43.0190
Gaus Width: 3.9910
Entry
ampl
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1
2
3
4
5
6
7
8
0
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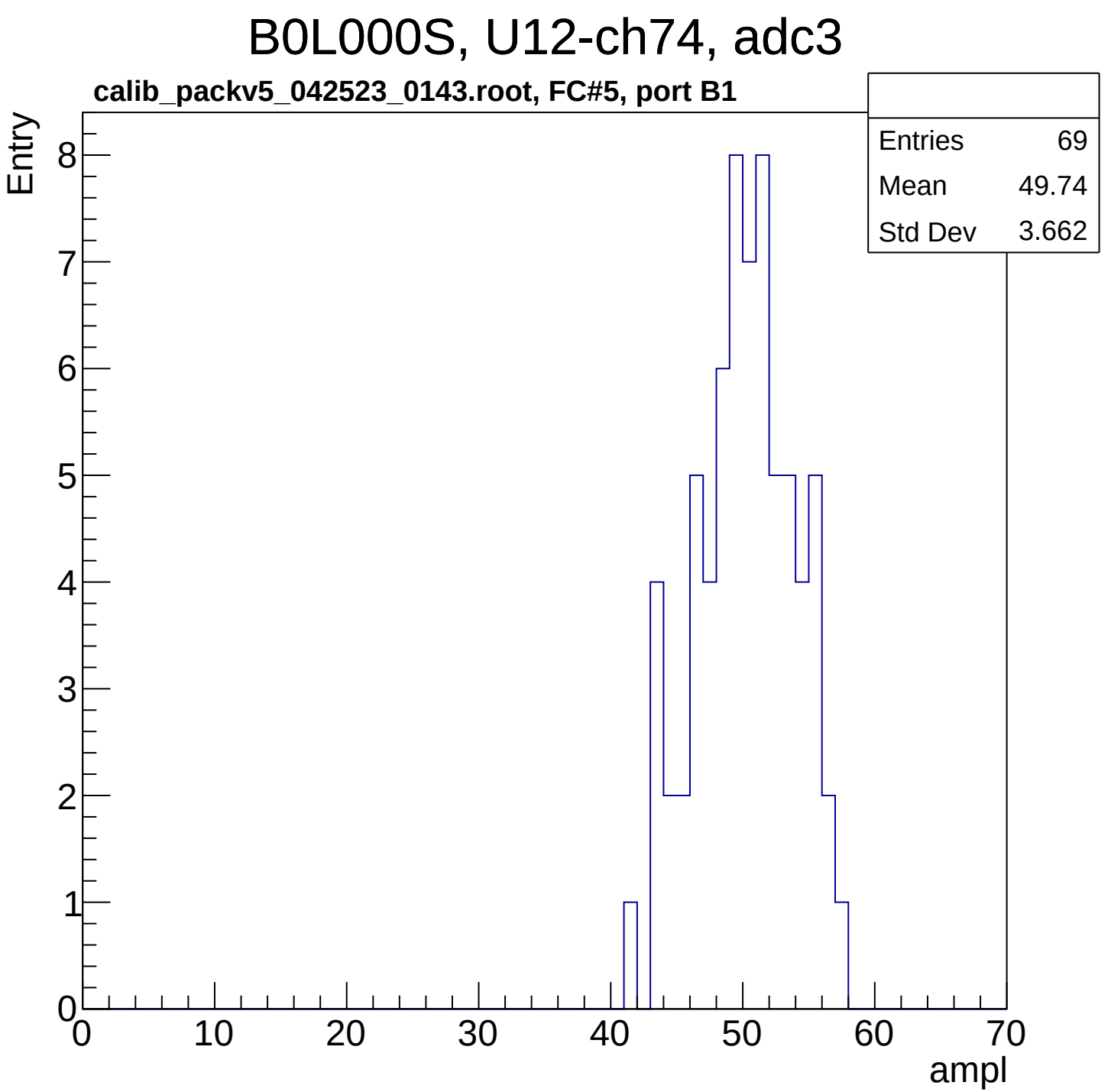
B0L000S, U12-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.74
Std Dev 3.662
Entry
ampl
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1
2
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4
5
6
7
8
0
10
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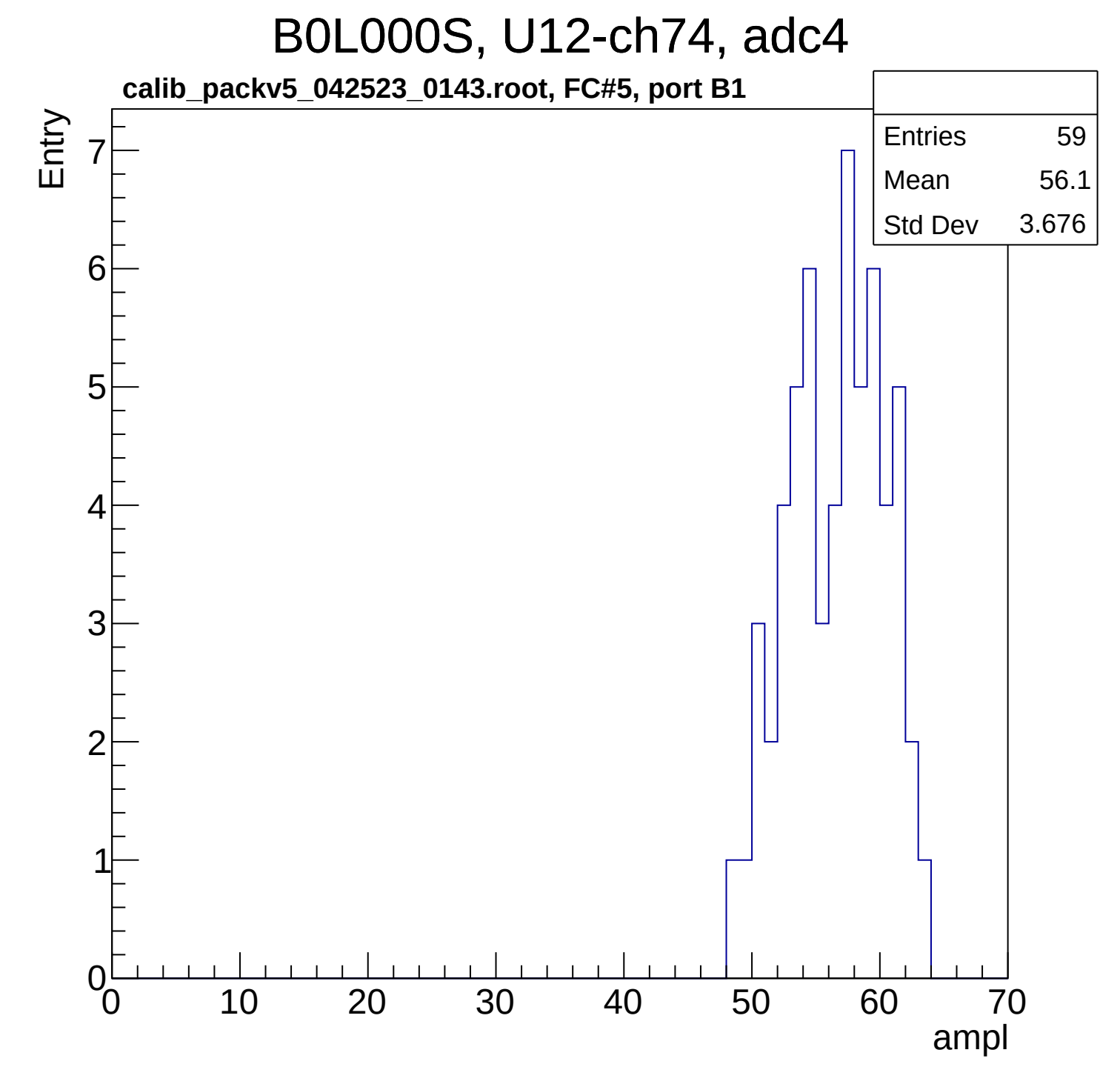
B0L000S, U12-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.1
Std Dev 3.676
Entry
ampl
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1
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4
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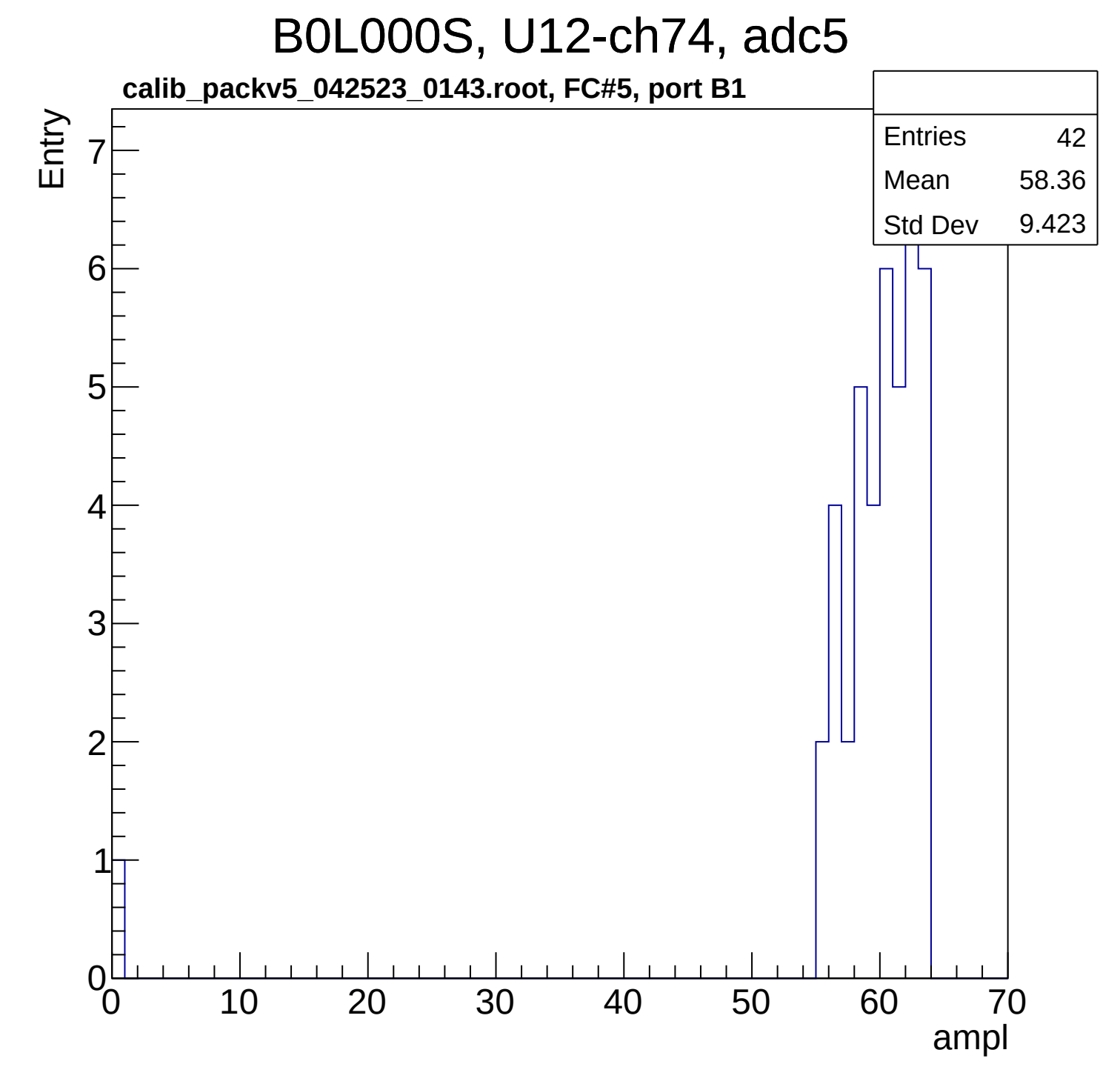
B0L000S, U12-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.36
Std Dev 9.423
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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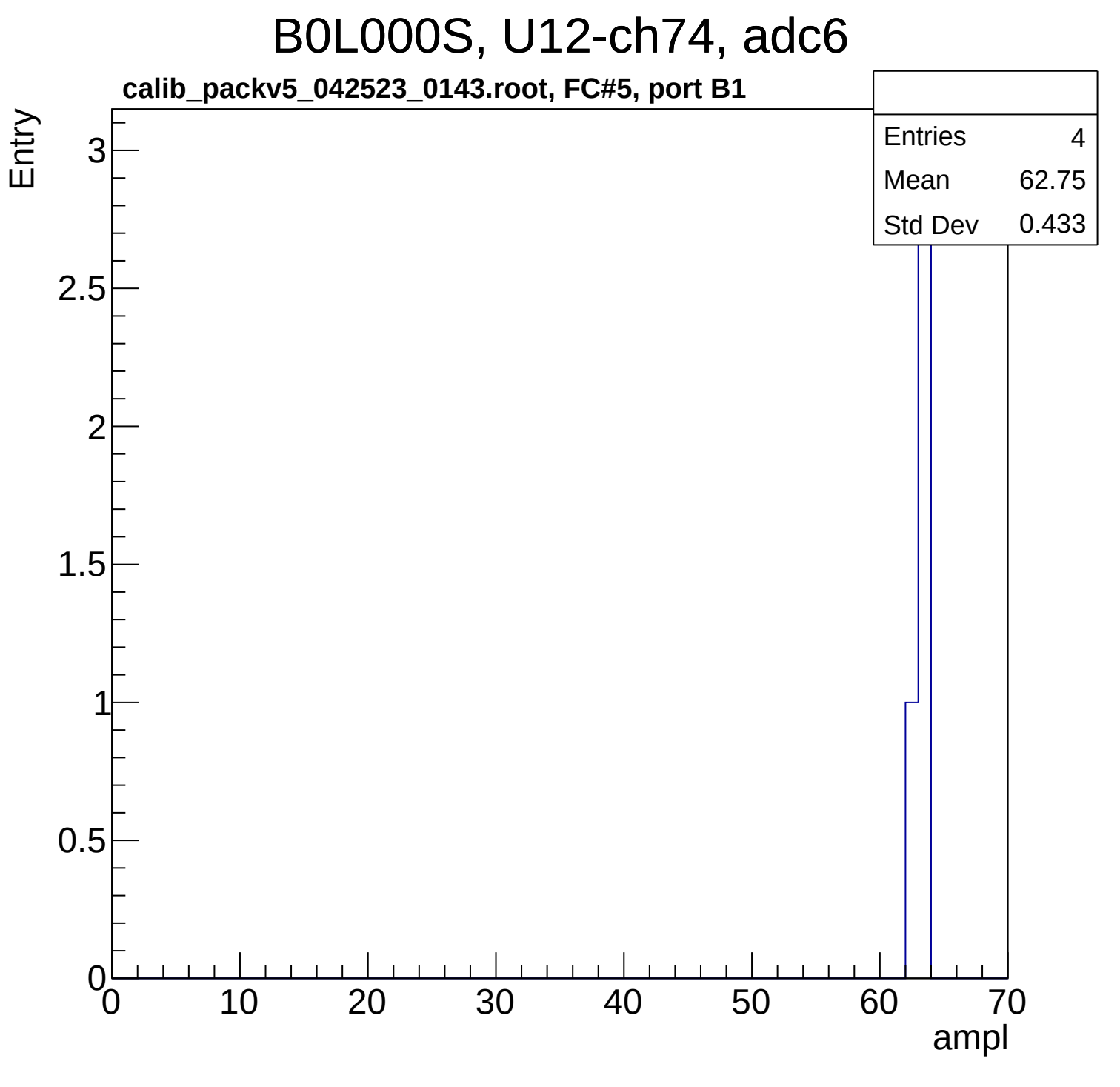
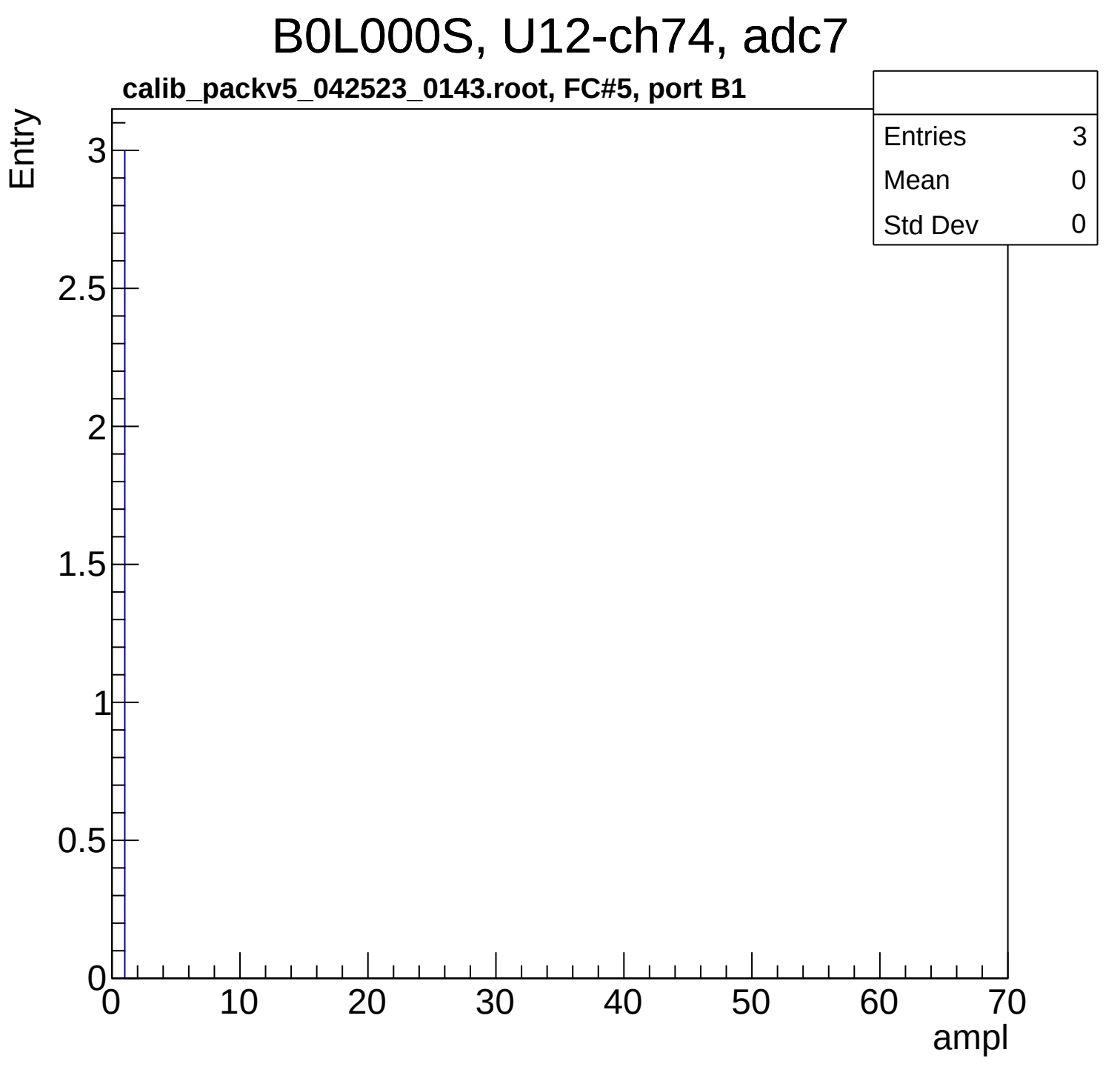

B0L000S, U12-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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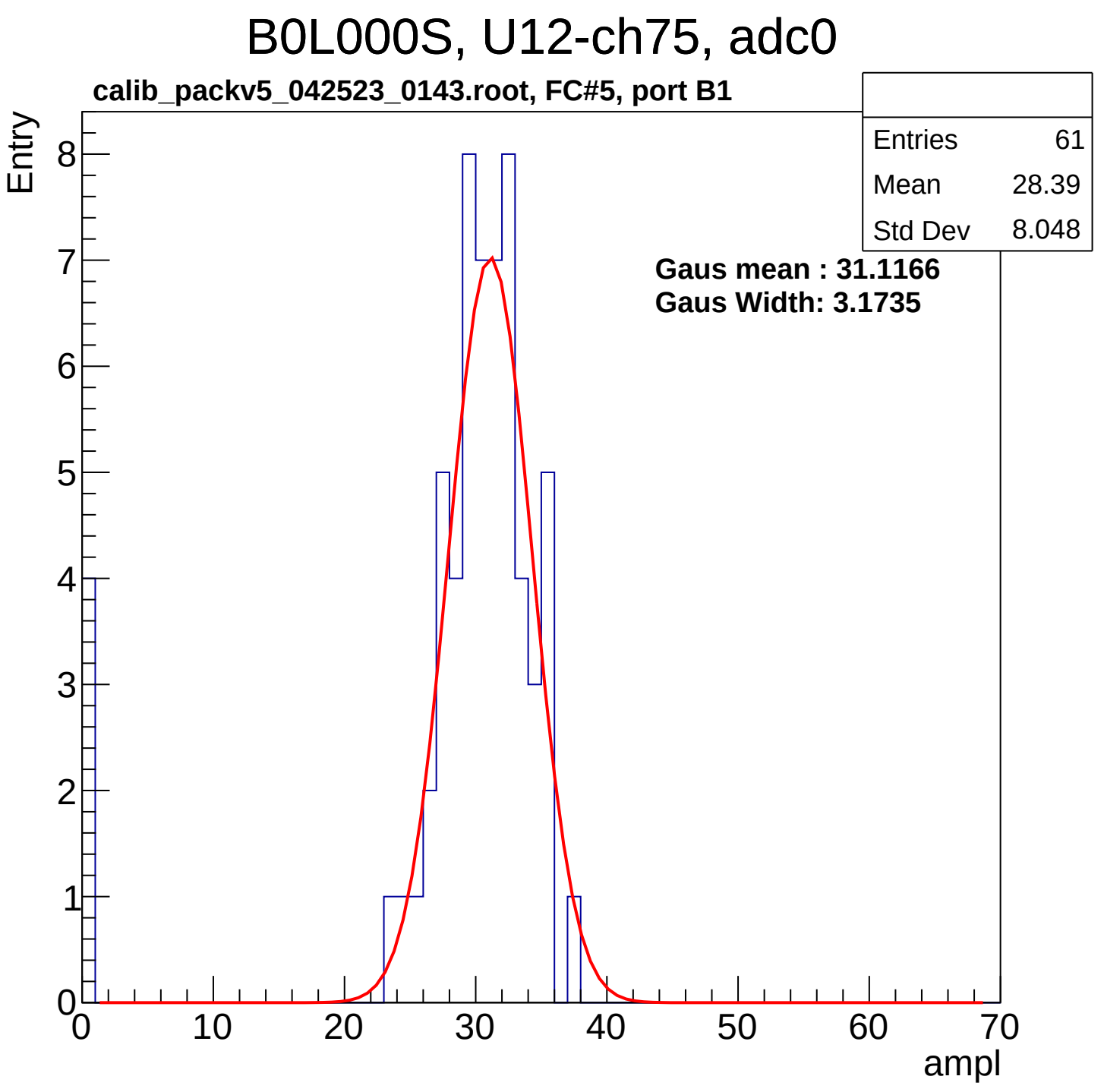
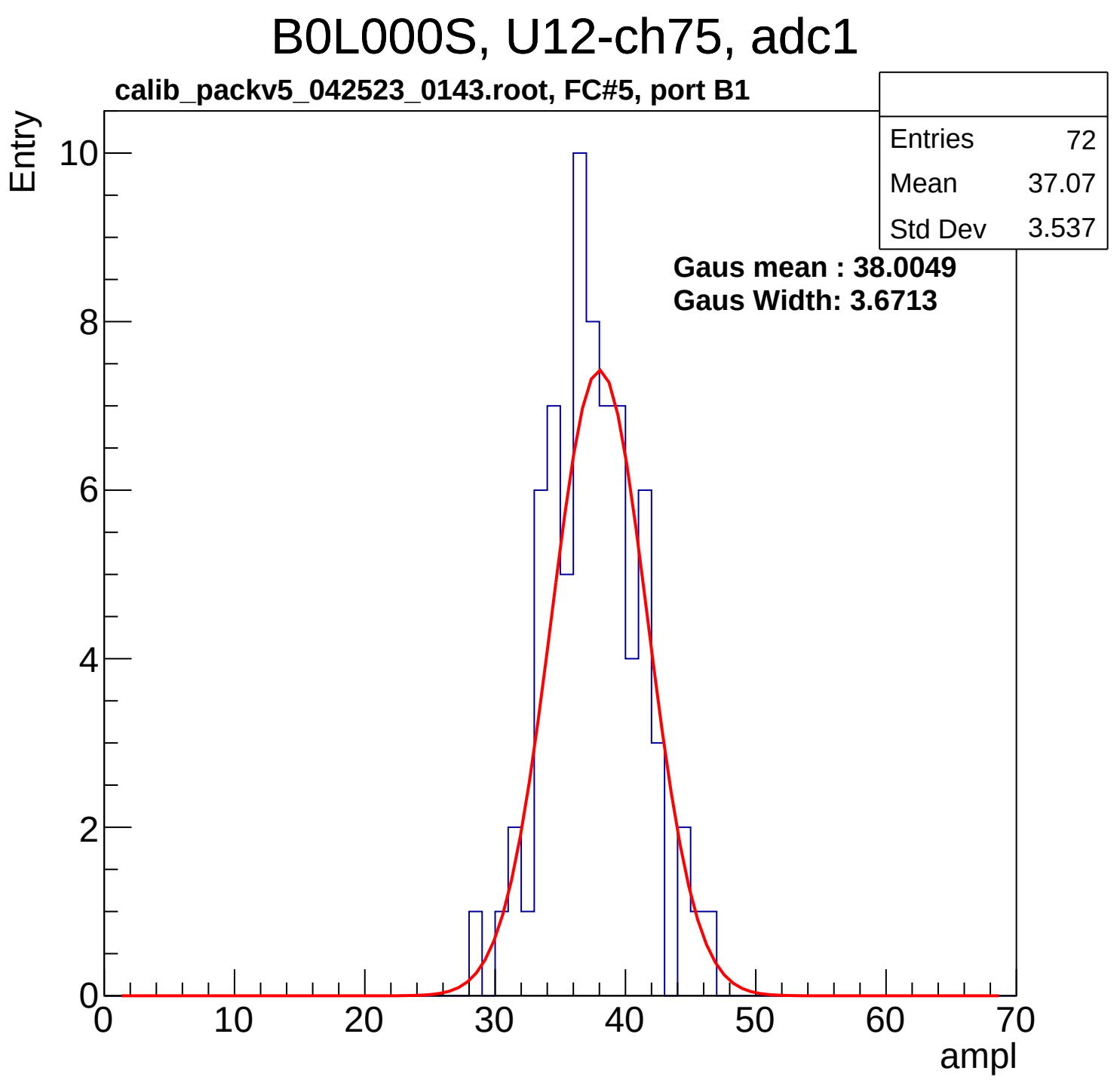
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.39
Std Dev 8.048
Gaus mean : 31.1166
Gaus Width: 3.1735
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.07
Std Dev 3.537
Gaus mean : 38.0049
Gaus Width: 3.6713
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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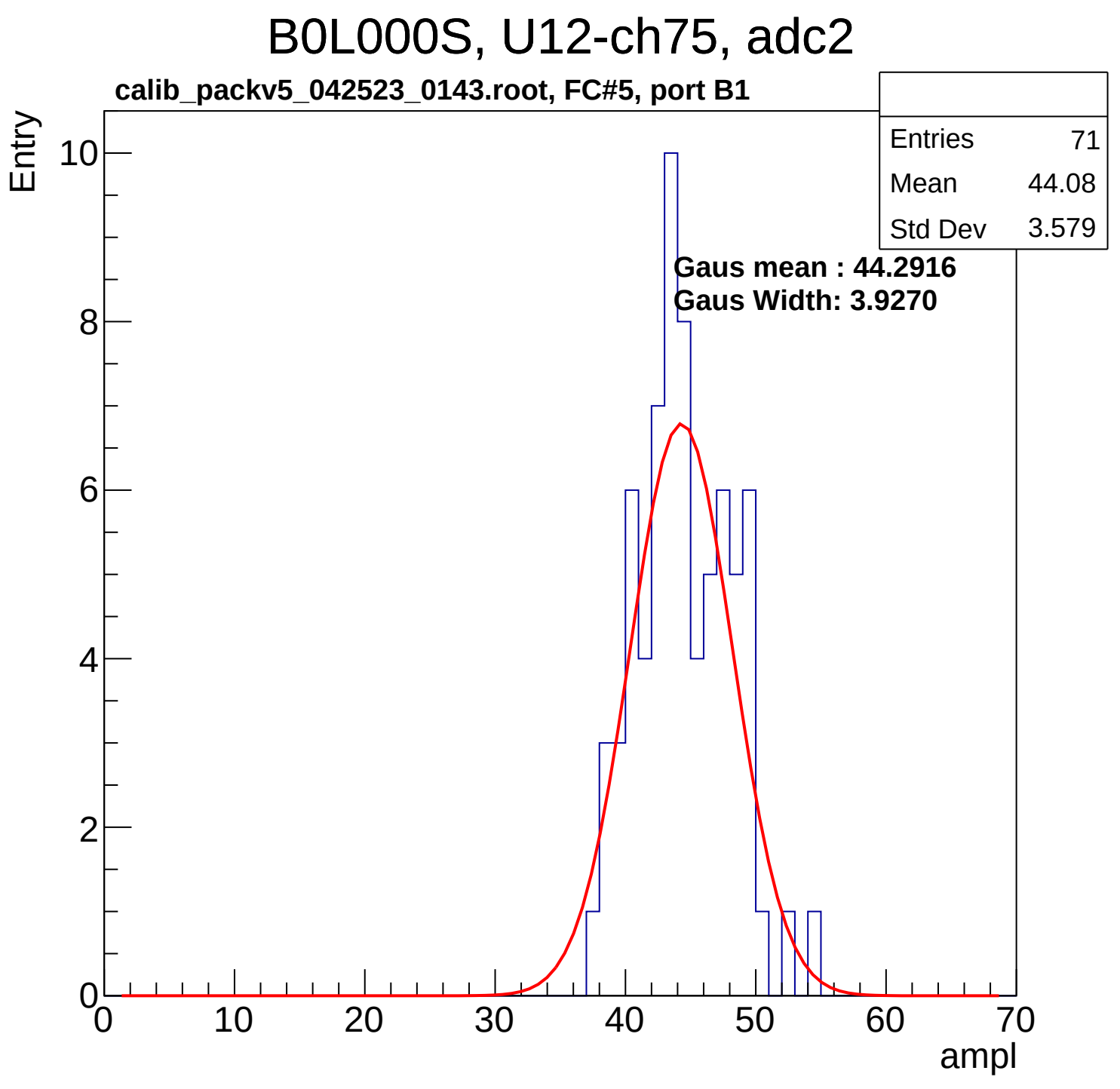
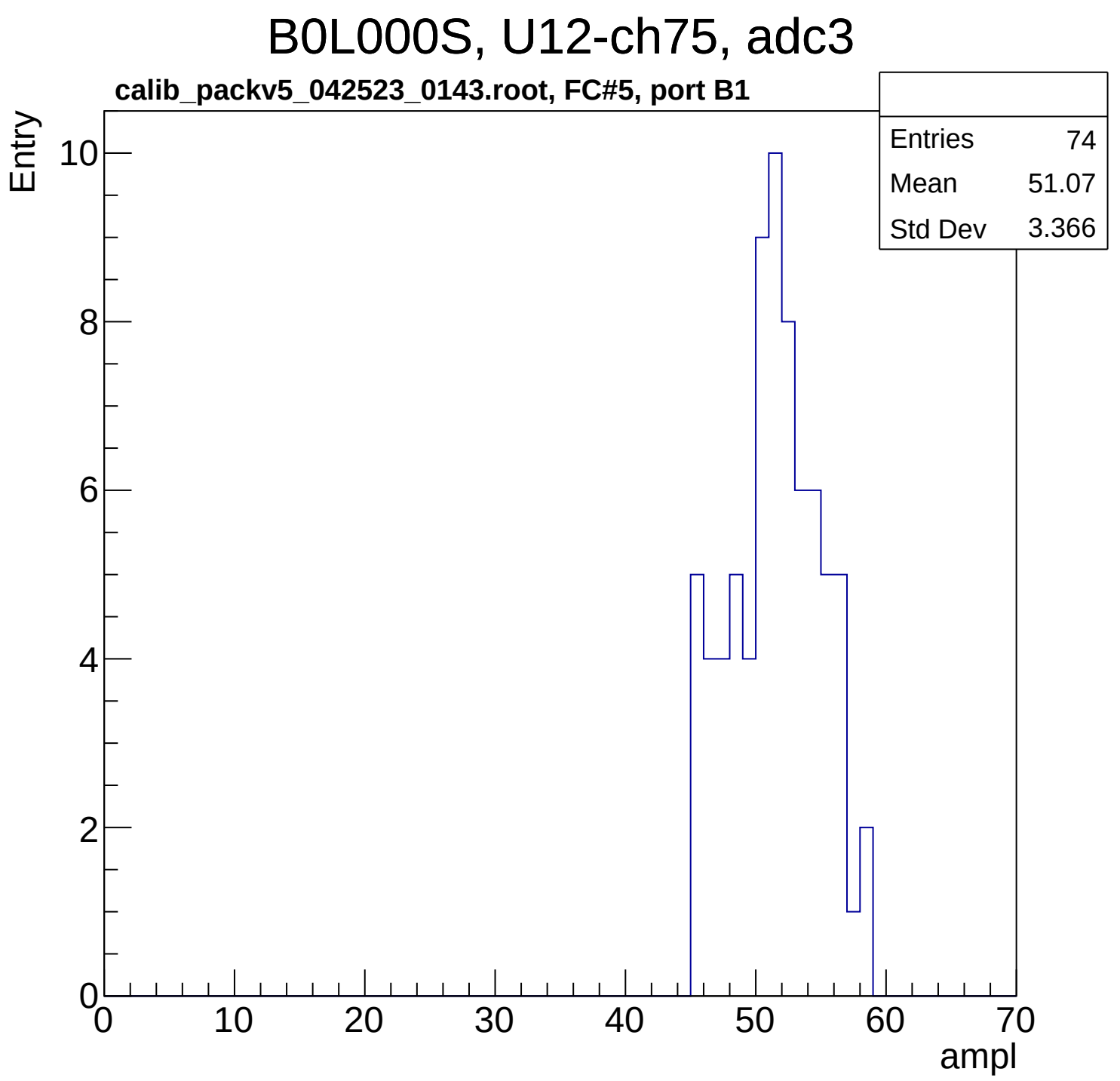
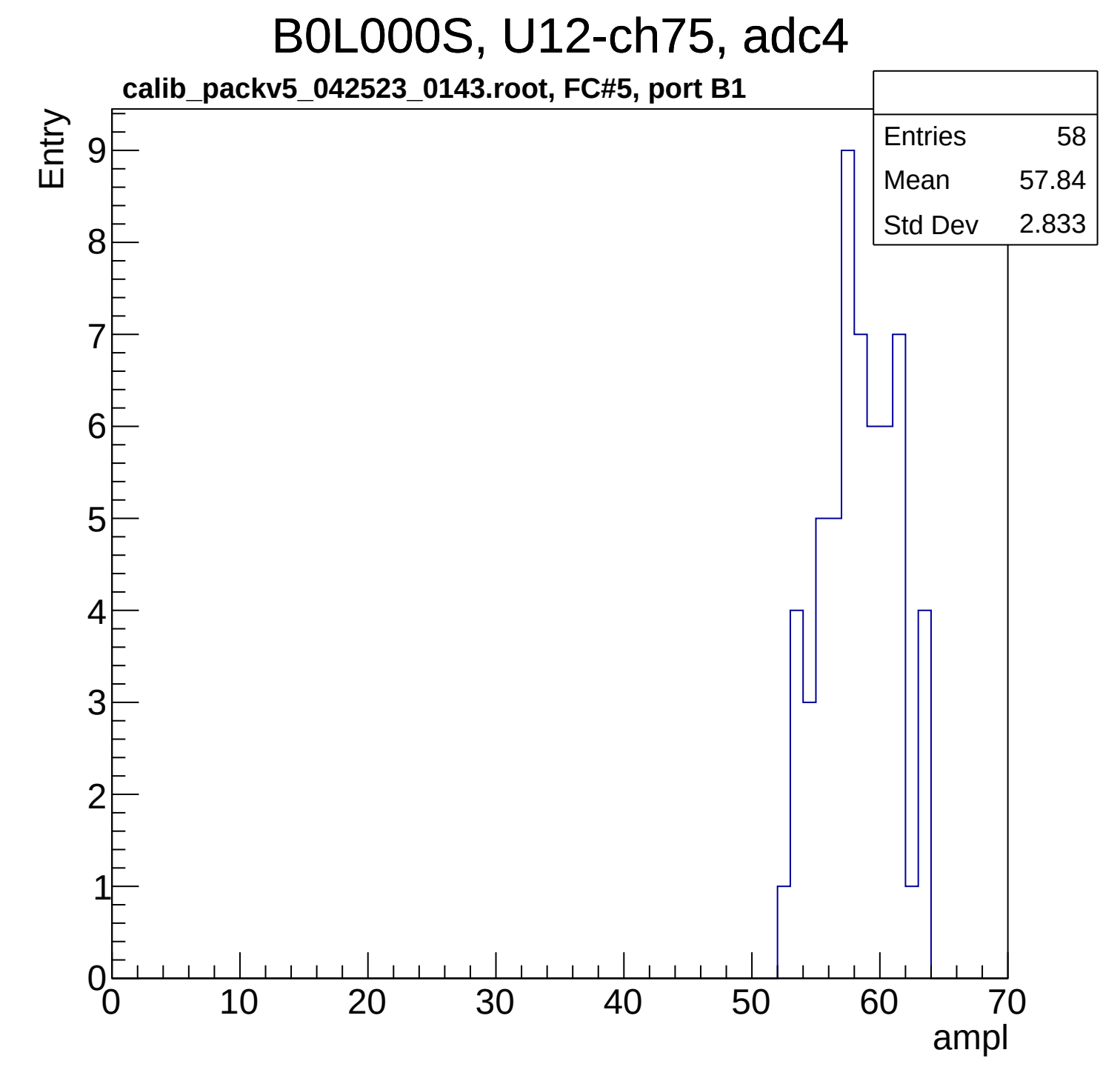
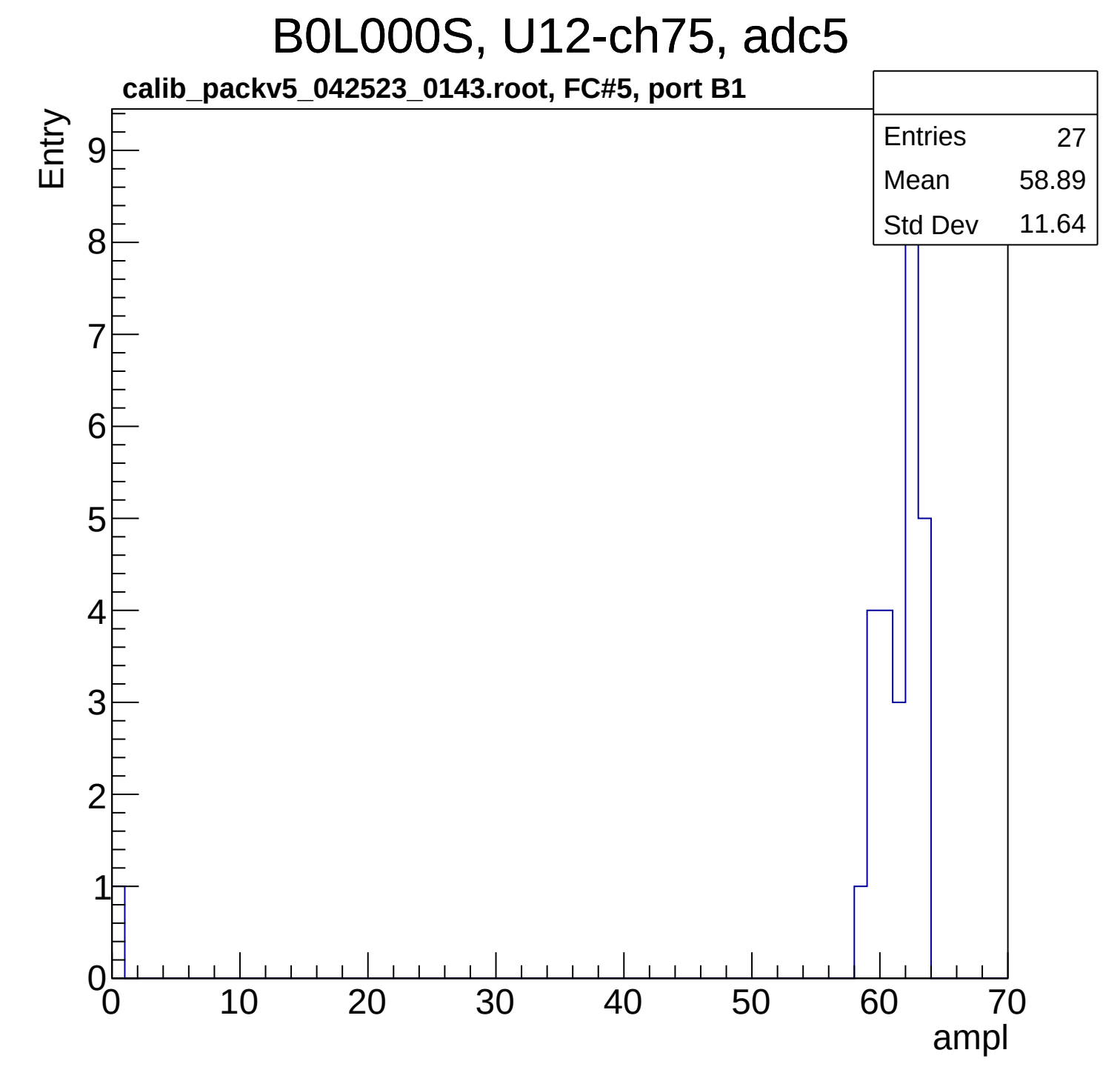

B0L000S, U12-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 44.08
Std Dev 3.579
Gaus mean : 44.2916
Gaus Width: 3.9270
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 51.07
Std Dev 3.366
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.84
Std Dev 2.833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.89
Std Dev 11.64
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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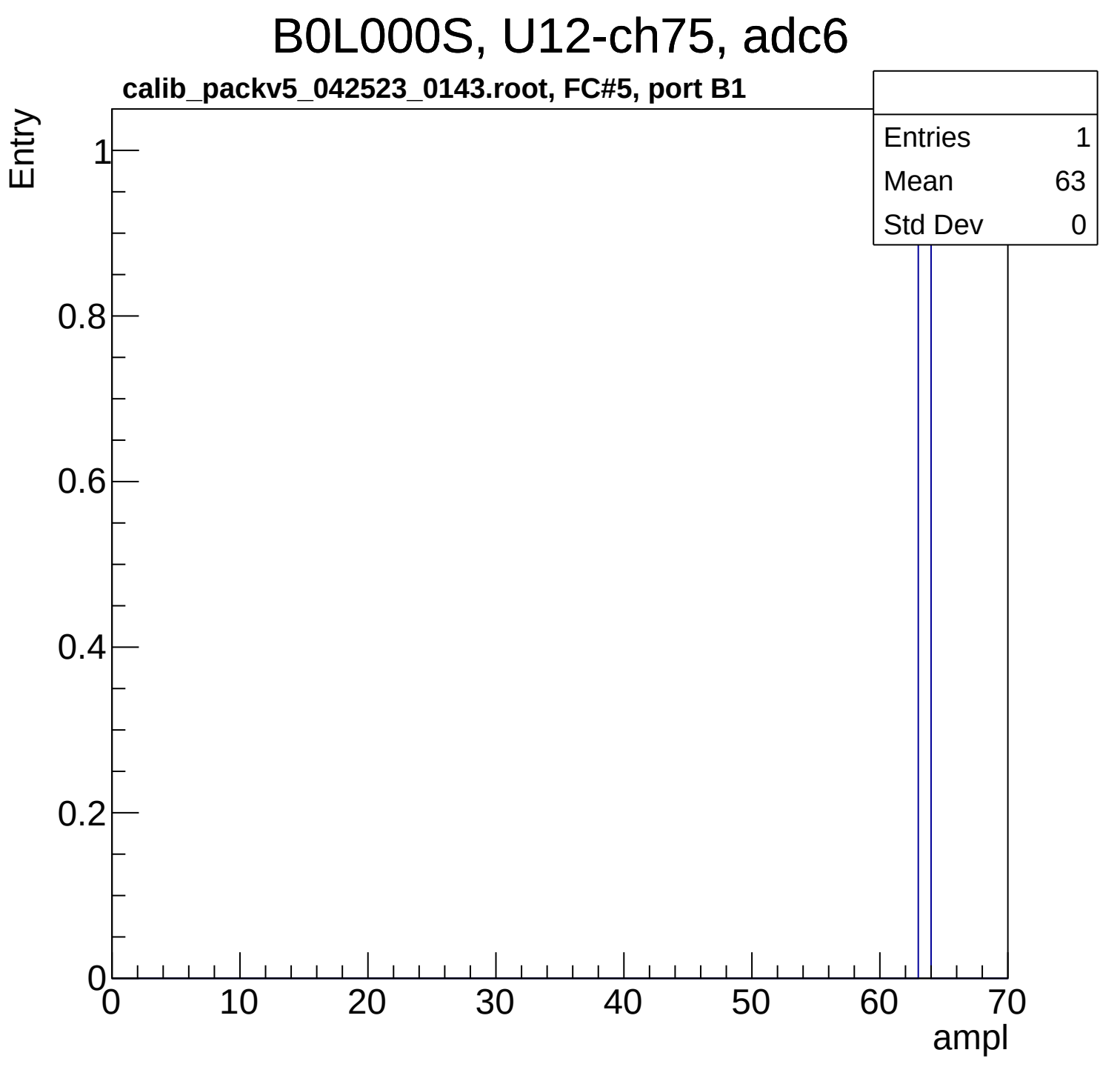
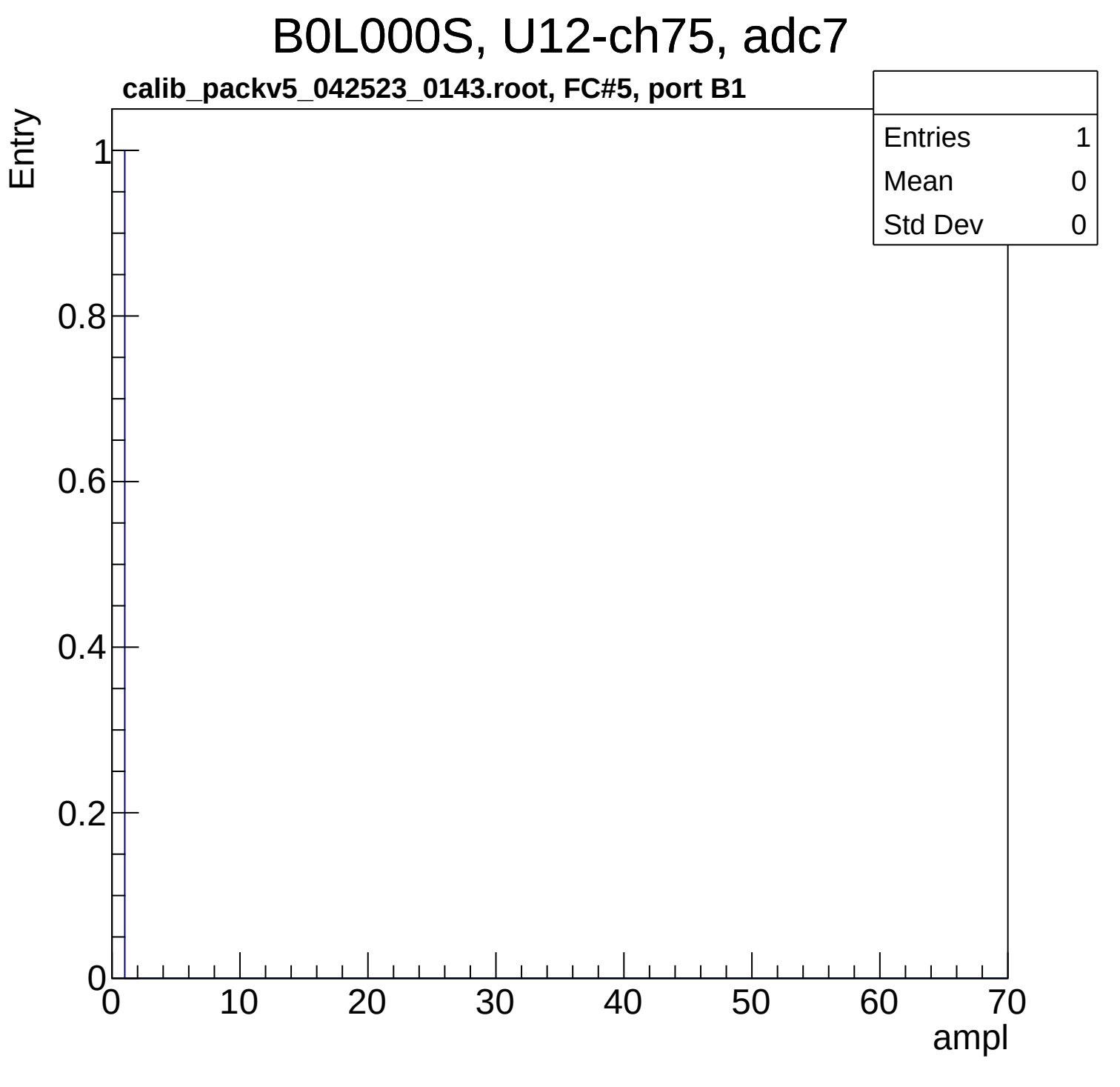

B0L000S, U12-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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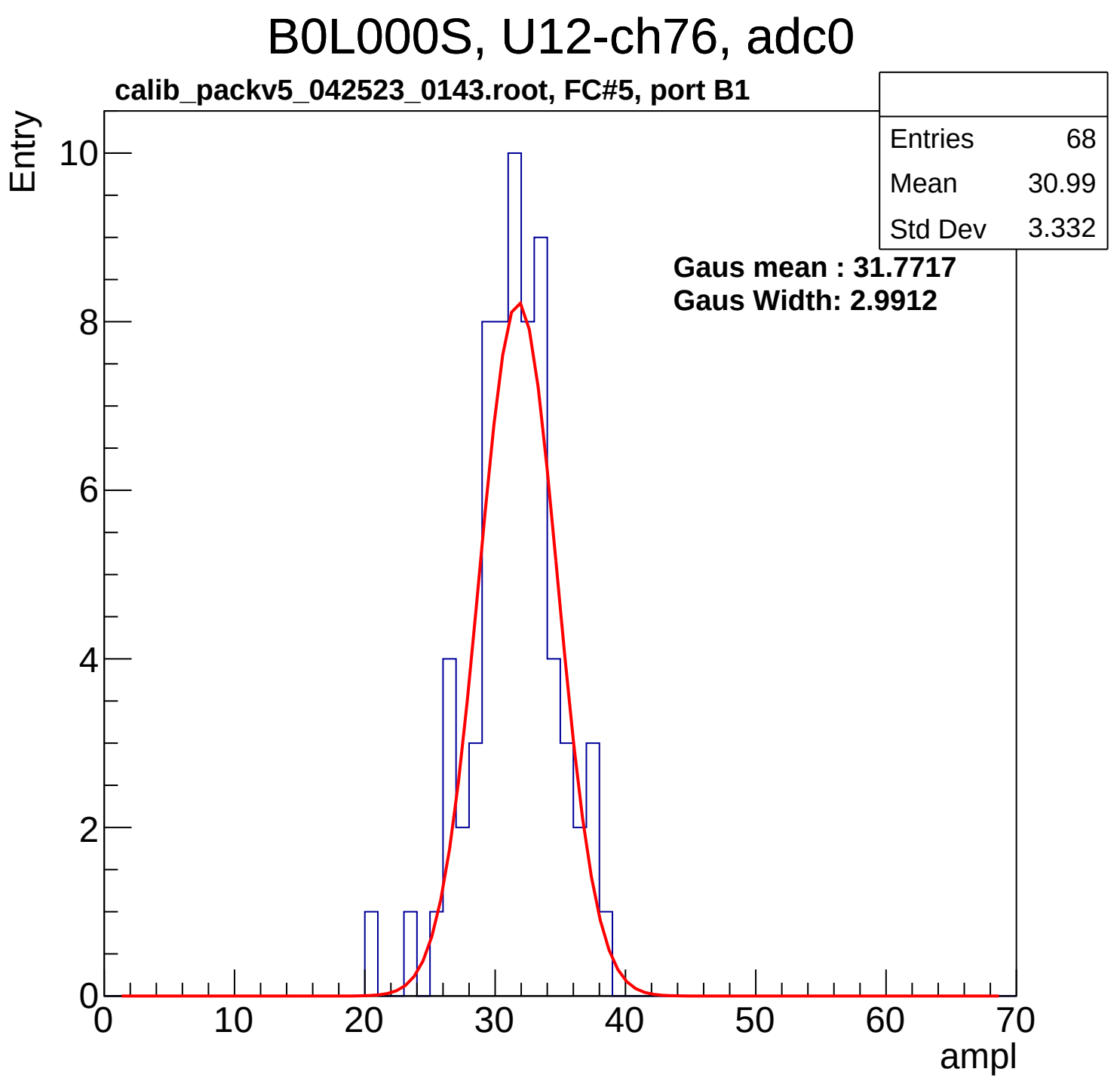
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.99
Std Dev 3.332
Gaus mean : 31.7717
Gaus Width: 2.9912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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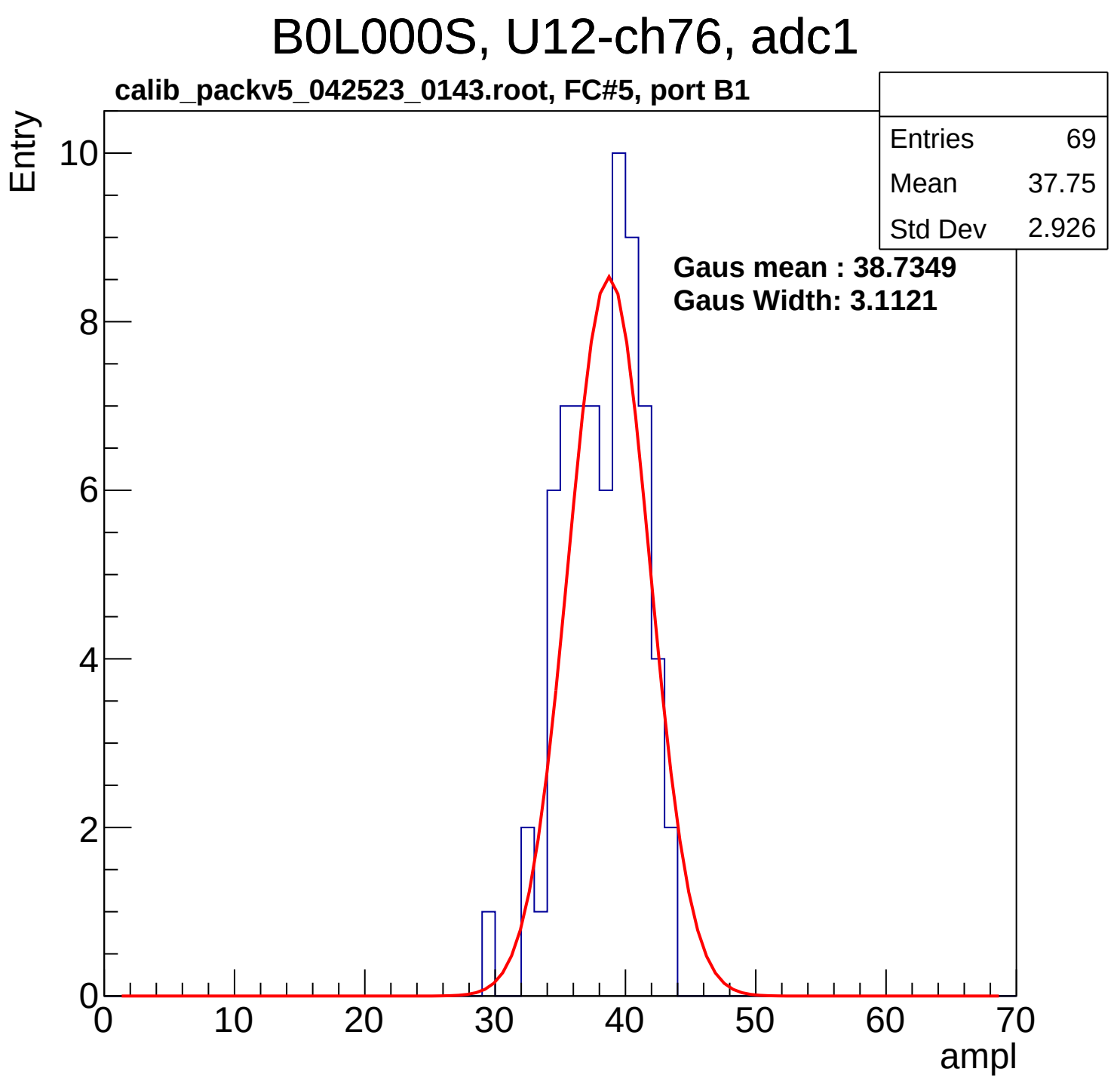
60
70


B0L000S, U12-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.75
Std Dev 2.926
Gaus mean : 38.7349
Gaus Width: 3.1121
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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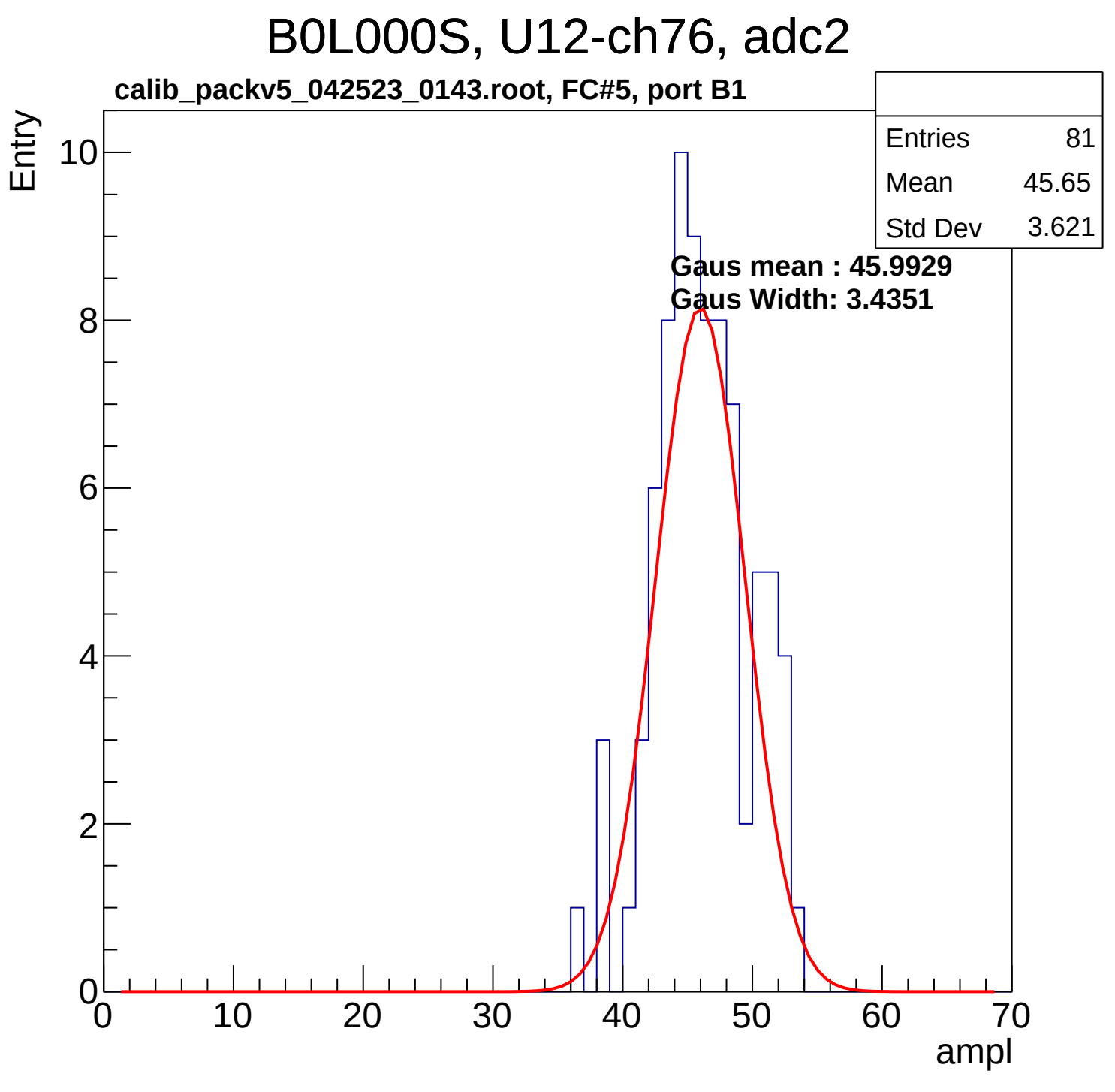
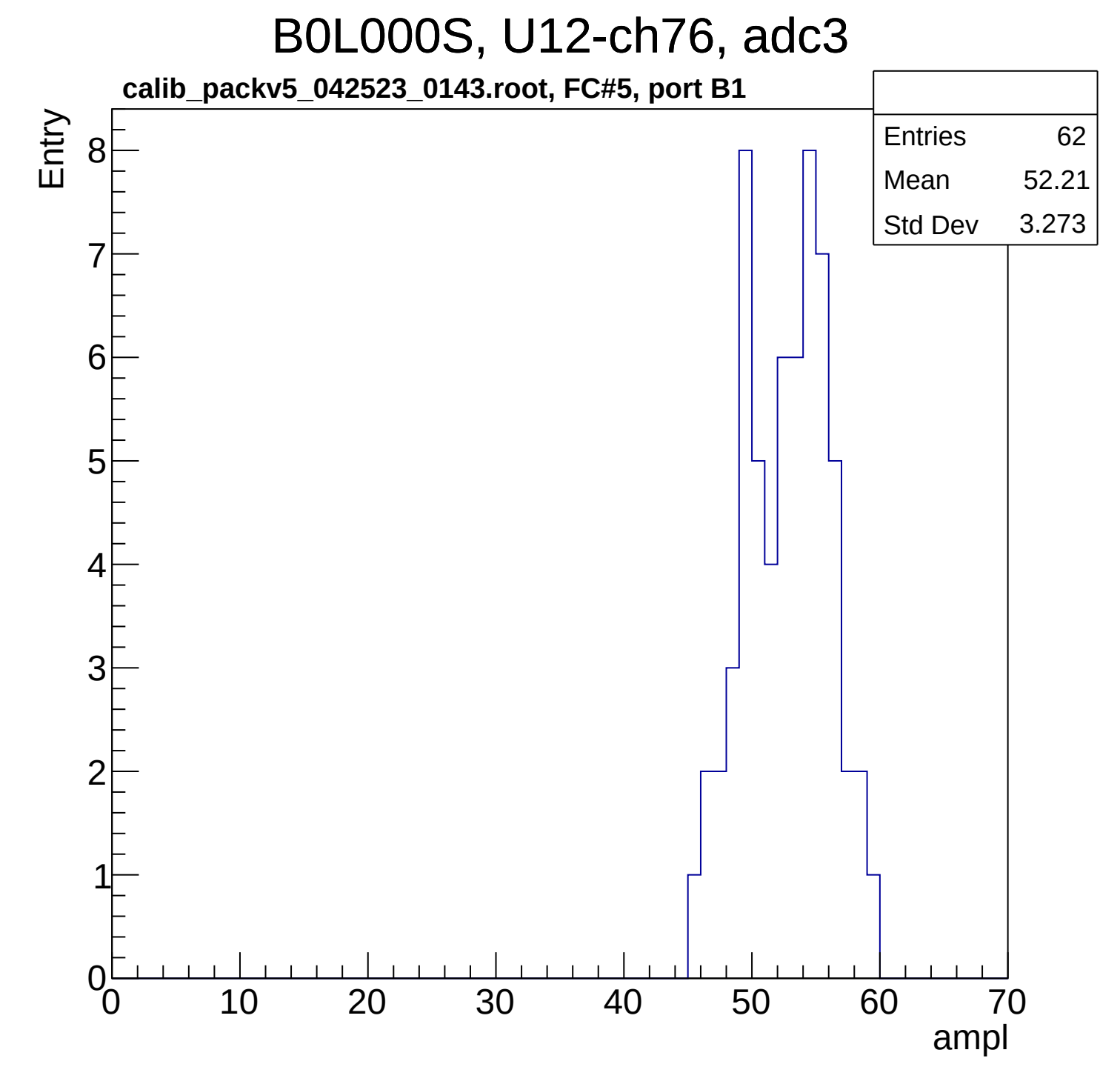

B0L000S, U12-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 45.65
Std Dev 3.621
Gaus mean : 45.9929
Gaus Width: 3.4351
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 52.21
Std Dev 3.273
Entry
ampl
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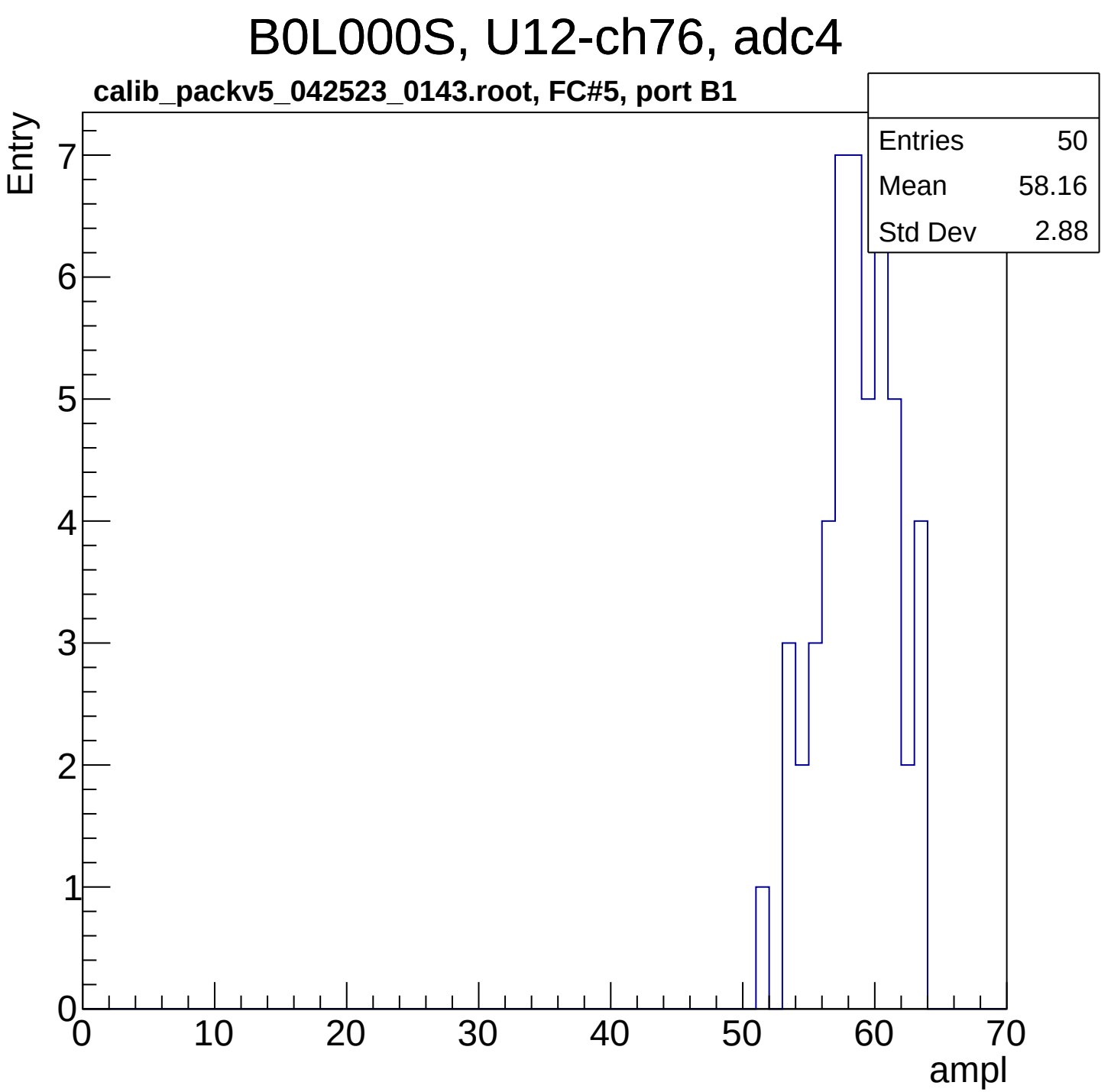
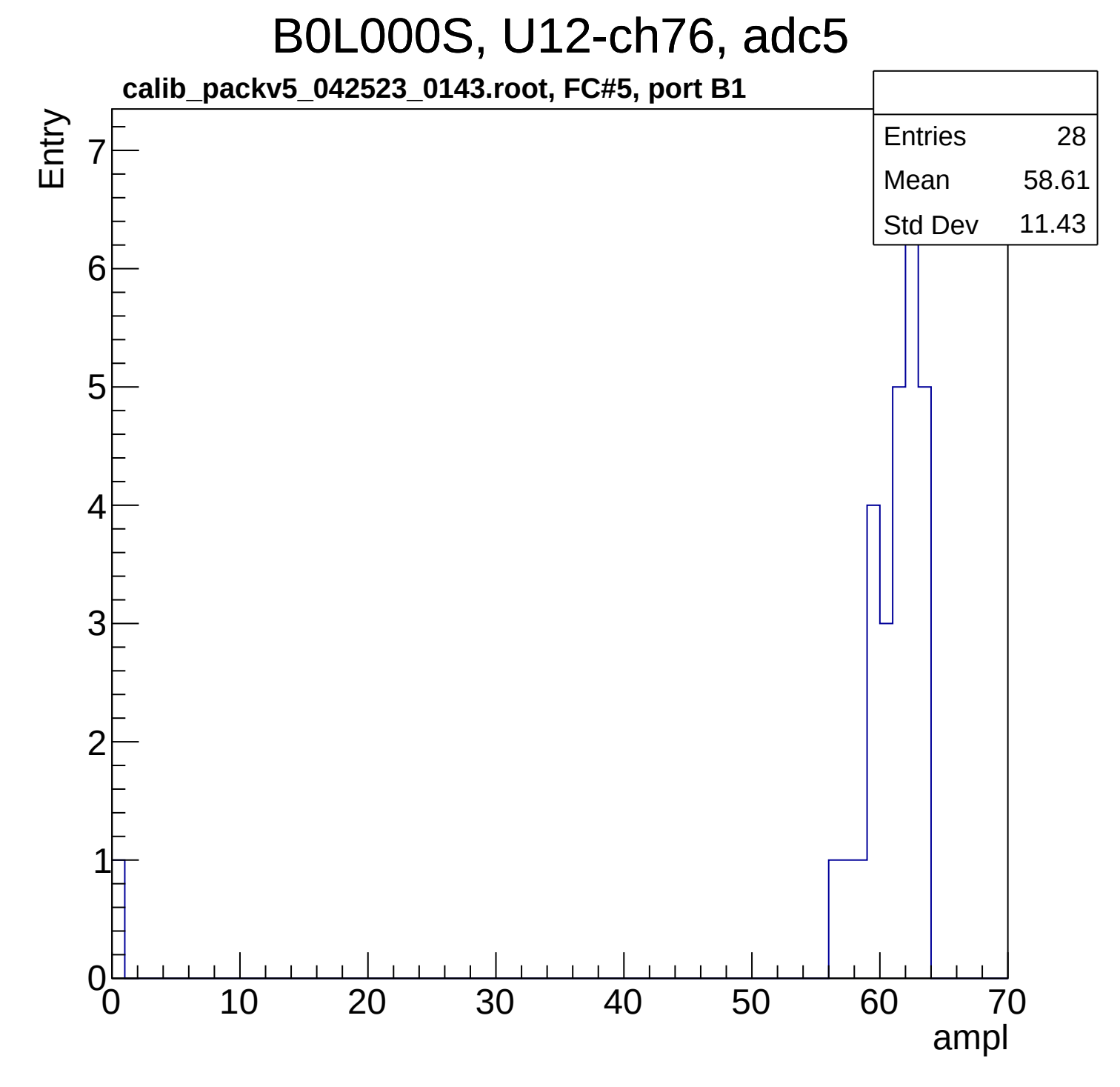
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.16
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.61
Std Dev 11.43
Entry
ampl
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2
3
4
5
6
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0
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20
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40
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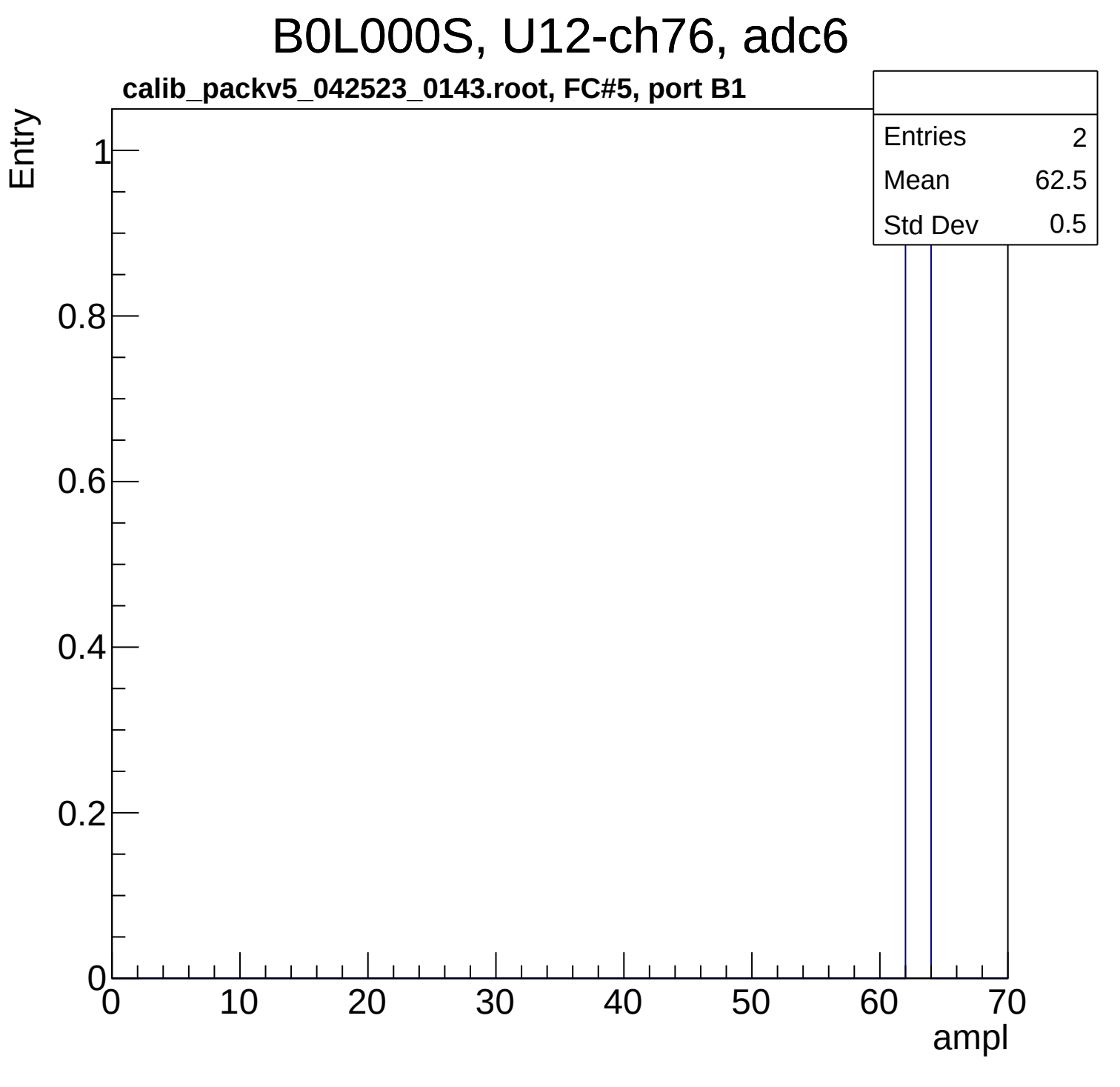
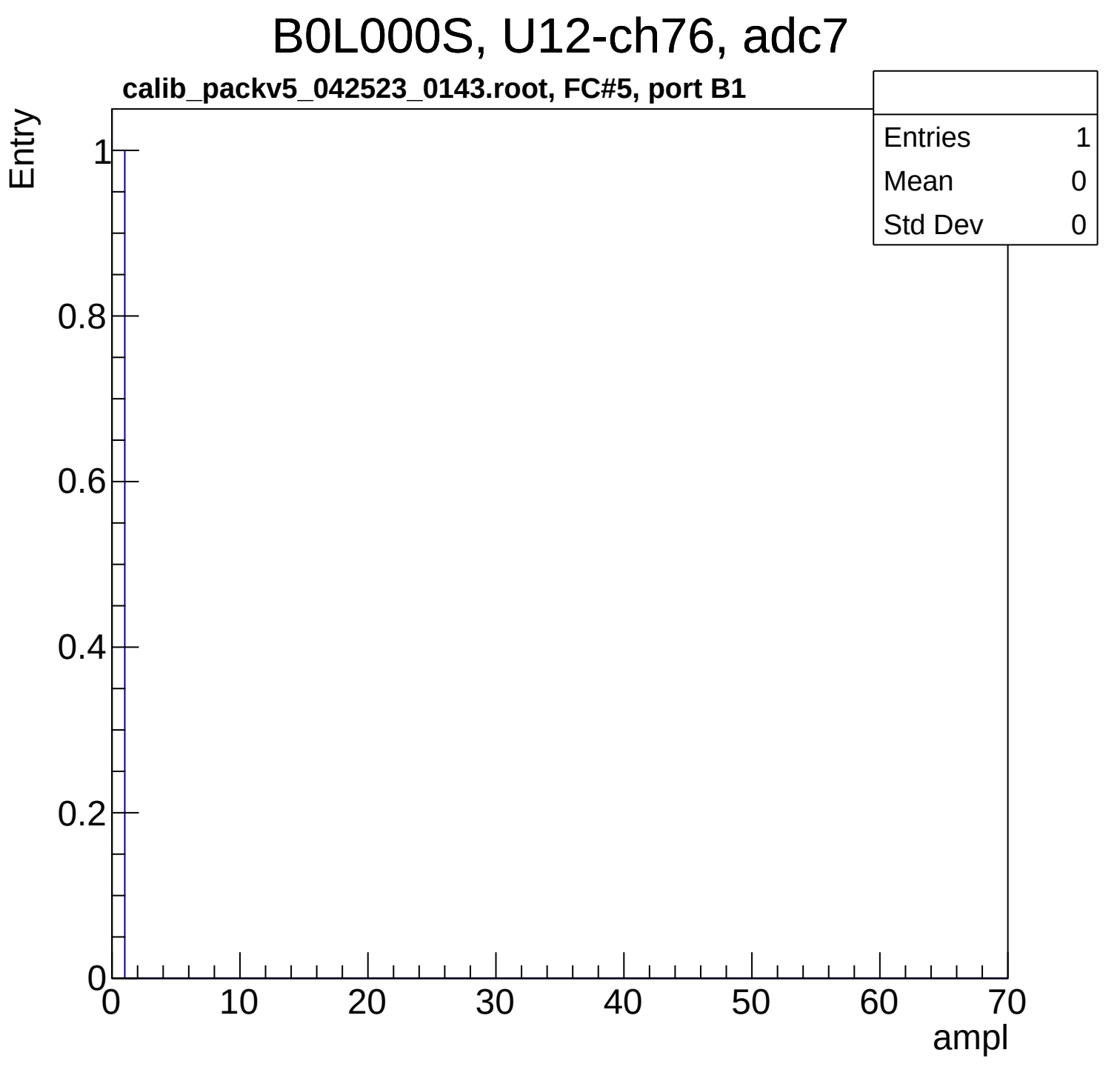
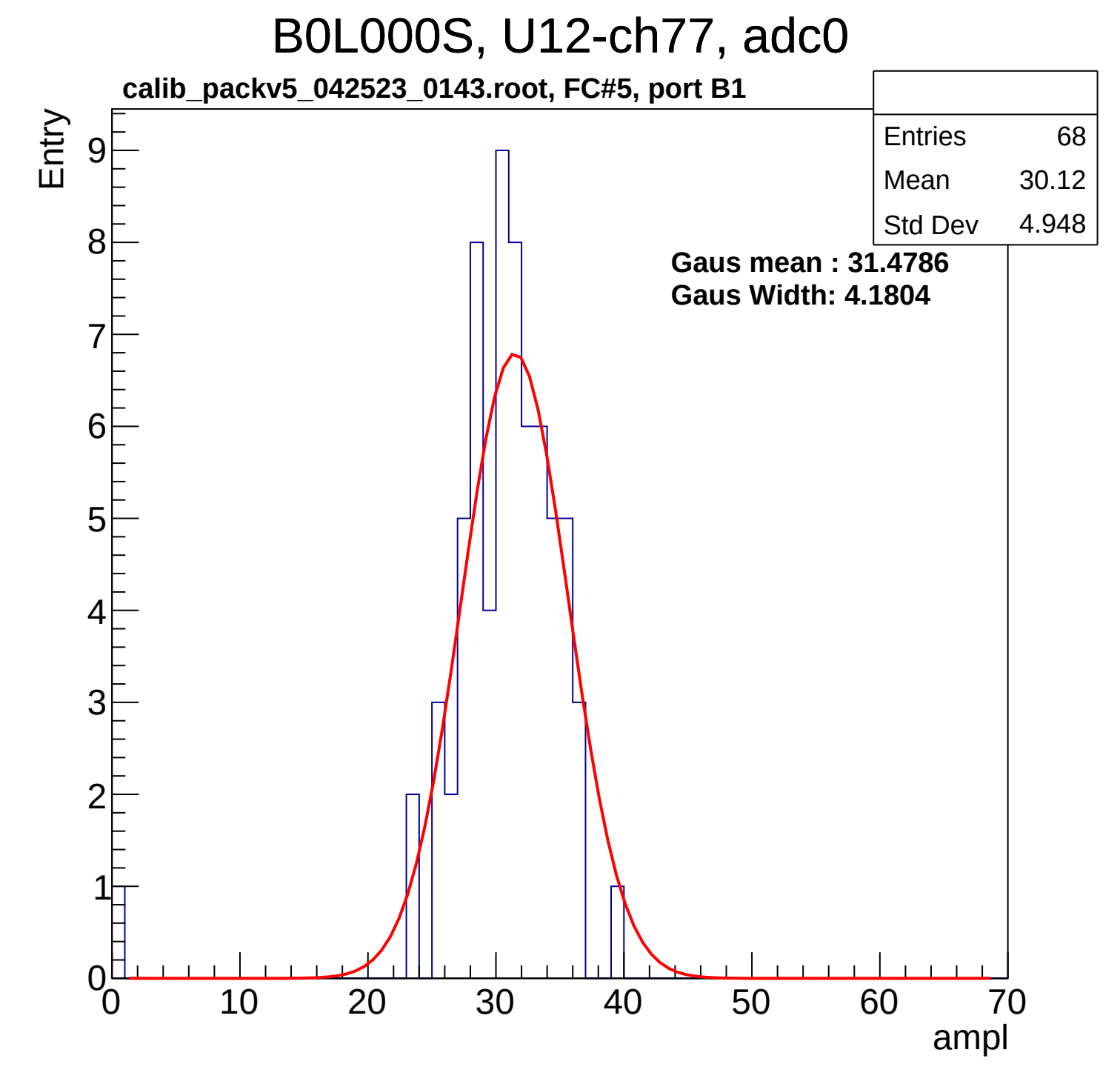

B0L000S, U12-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.12
Std Dev 4.948
Gaus mean : 31.4786
Gaus Width: 4.1804
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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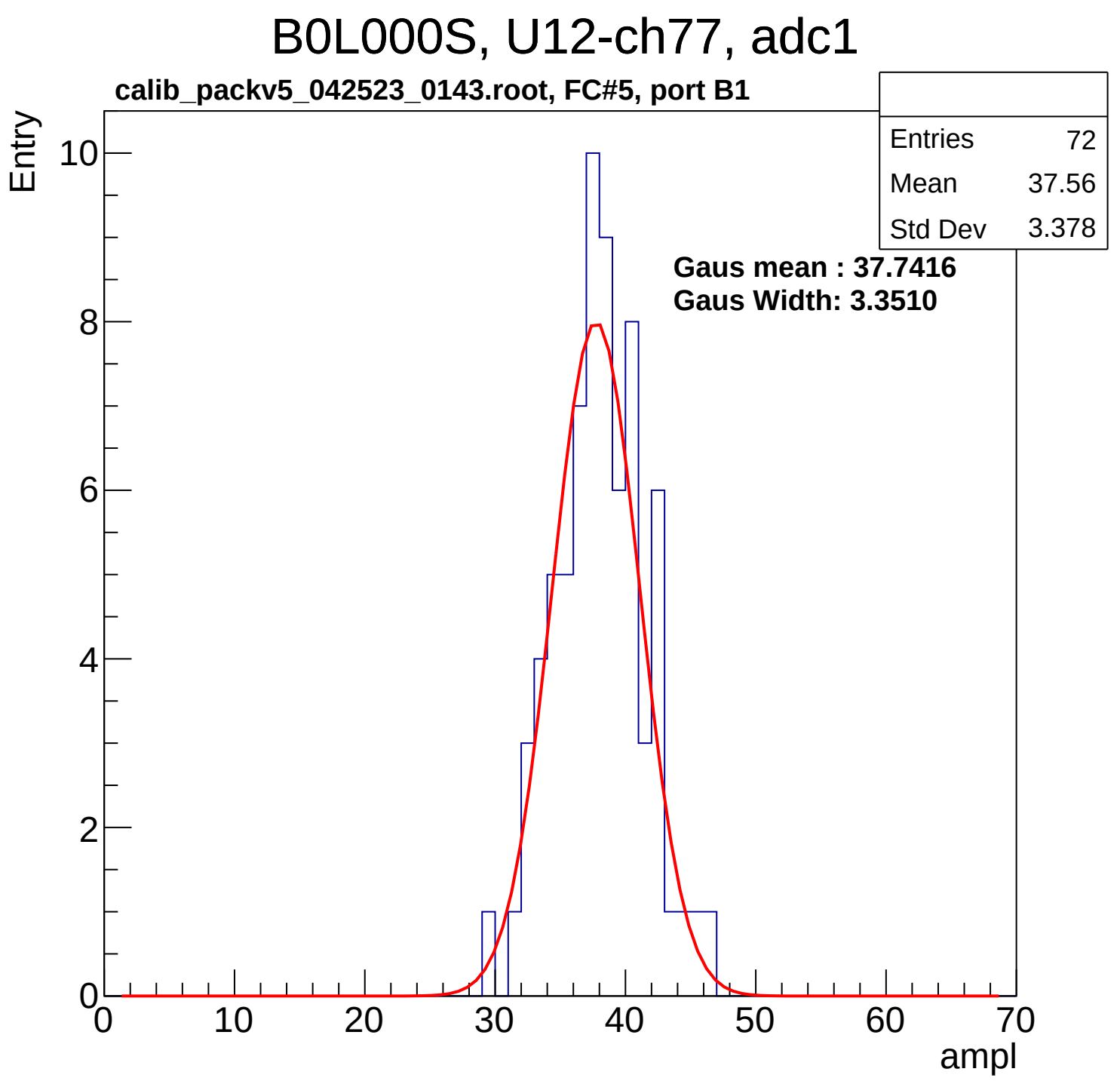
B0L000S, U12-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.56
Std Dev 3.378
Gaus mean : 37.7416
Gaus Width: 3.3510
Entry
ampl
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4
6
8
10
0
10
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40
50
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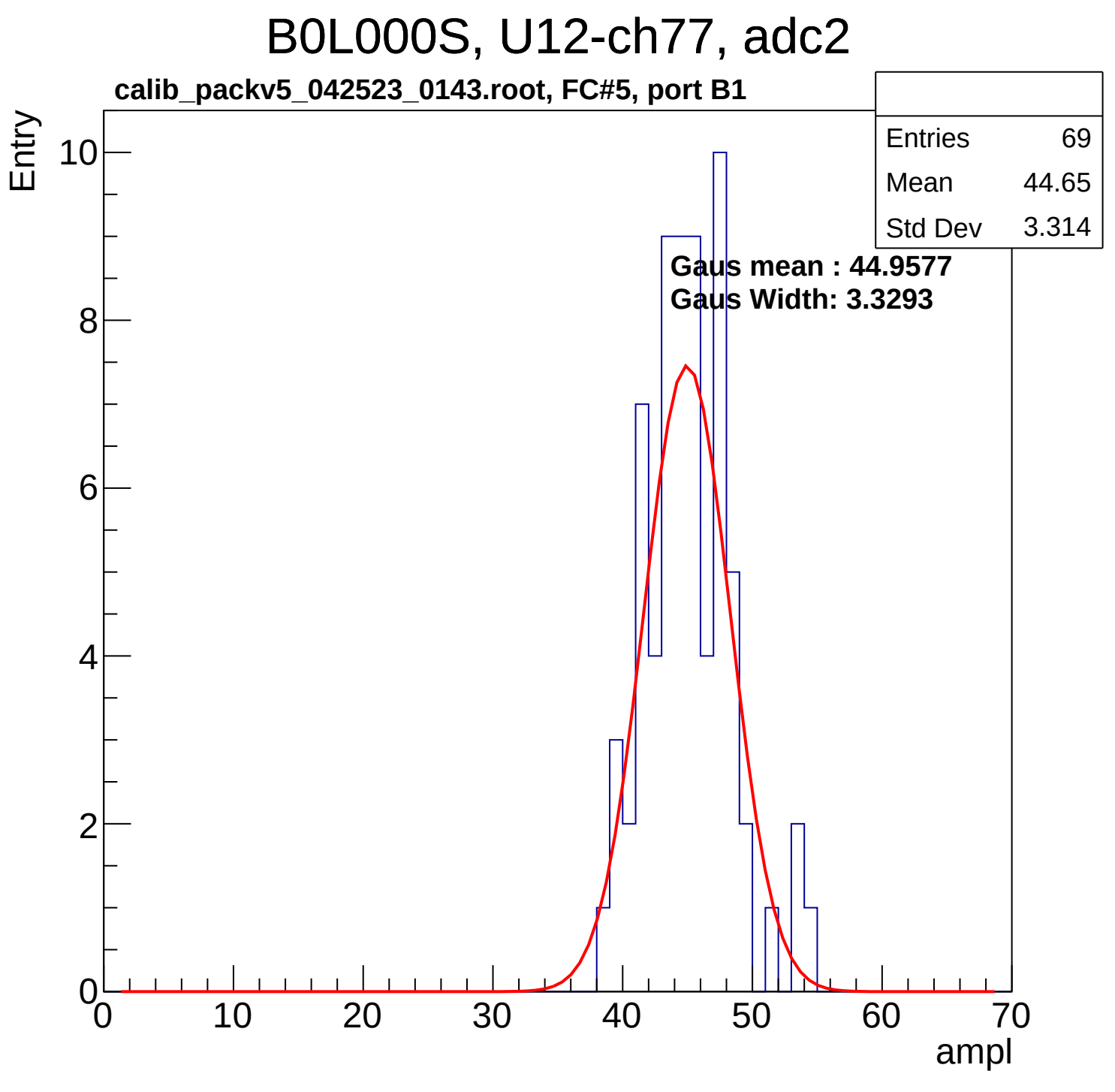
B0L000S, U12-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.65
Std Dev 3.314
Gaus mean : 44.9577
Gaus Width: 3.3293
Entry
ampl
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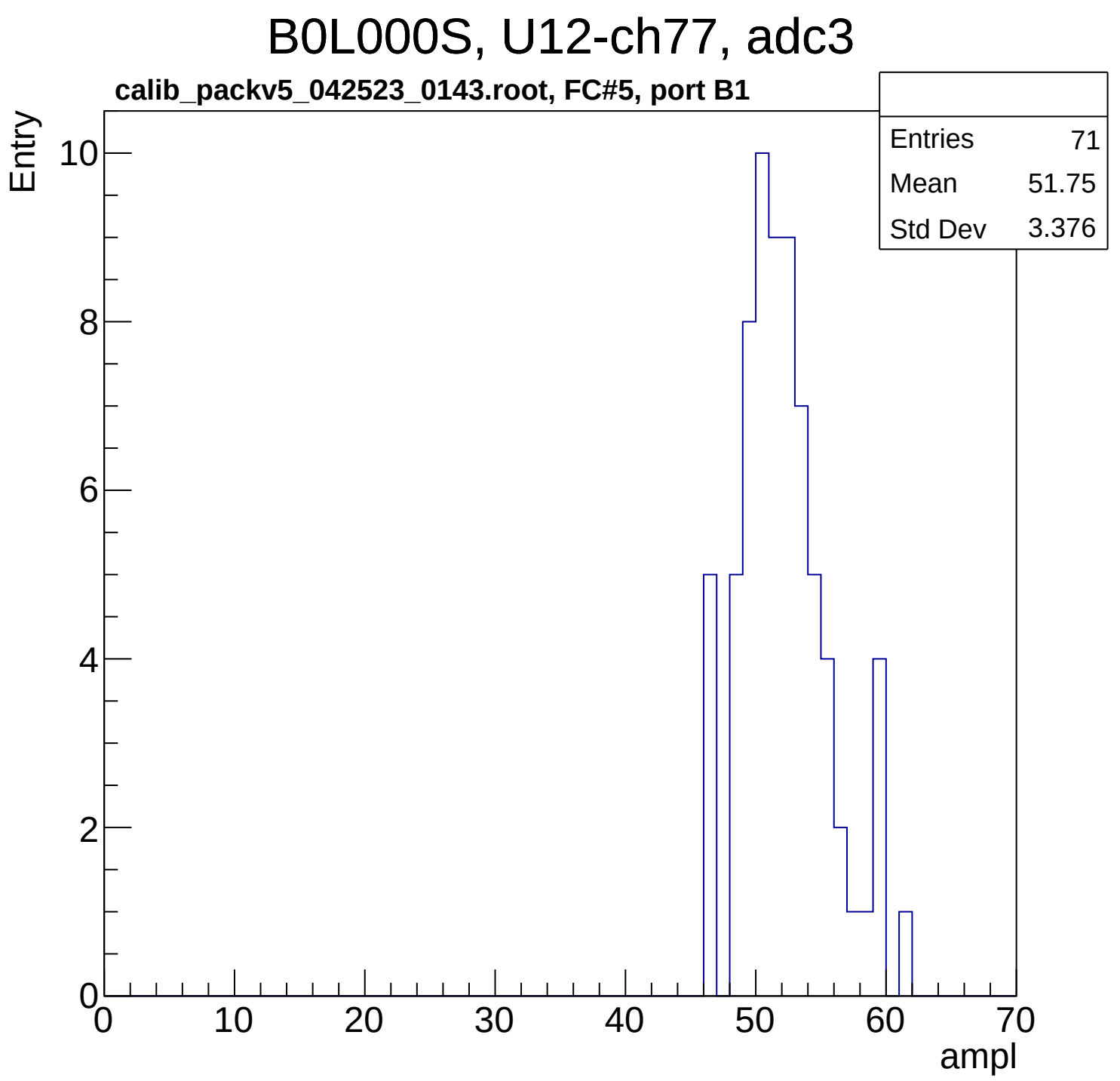
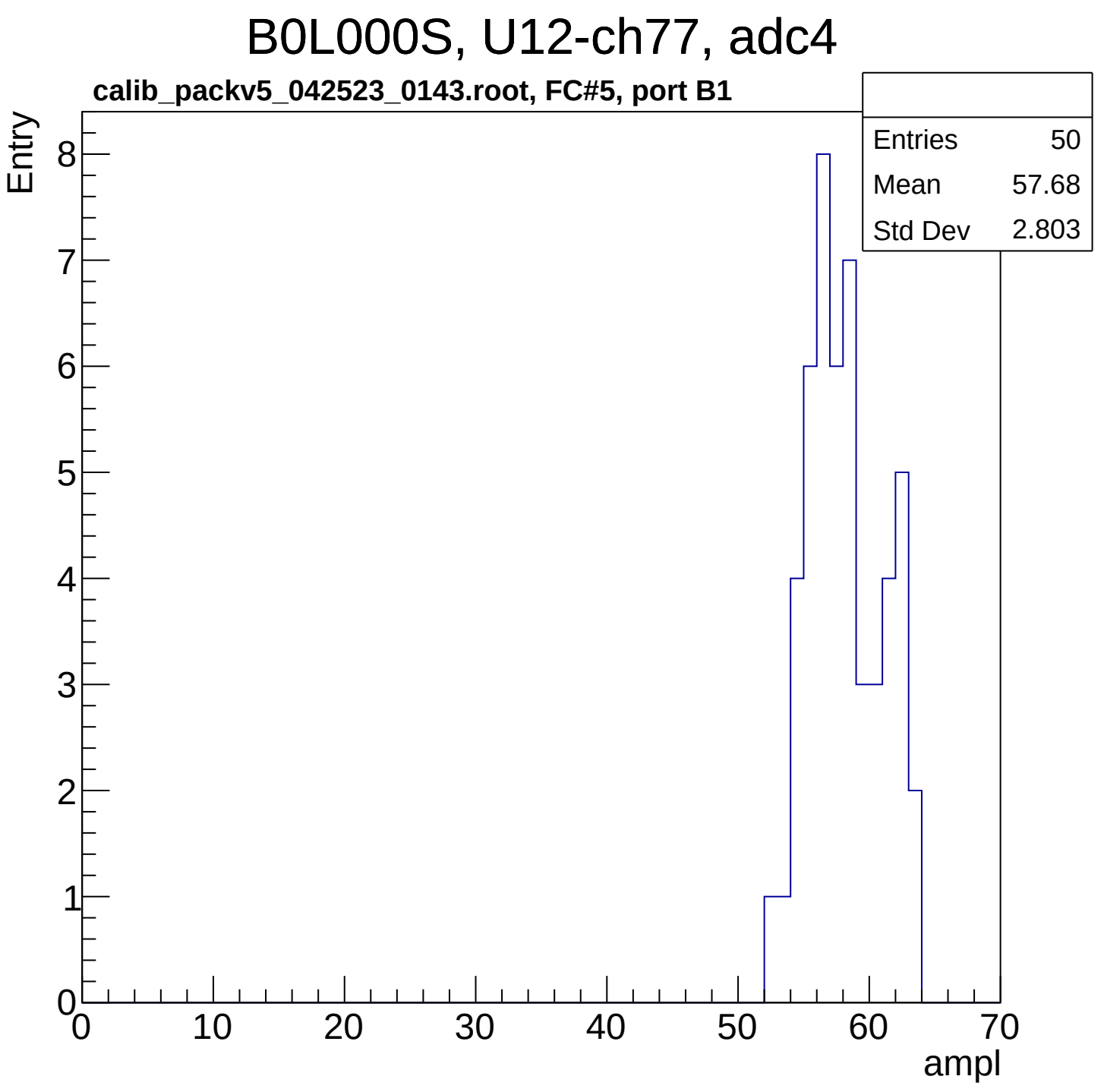
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 51.75
Std Dev 3.376
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.68
Std Dev 2.803
Entry
0
1
2
3
4
5
6
7
8
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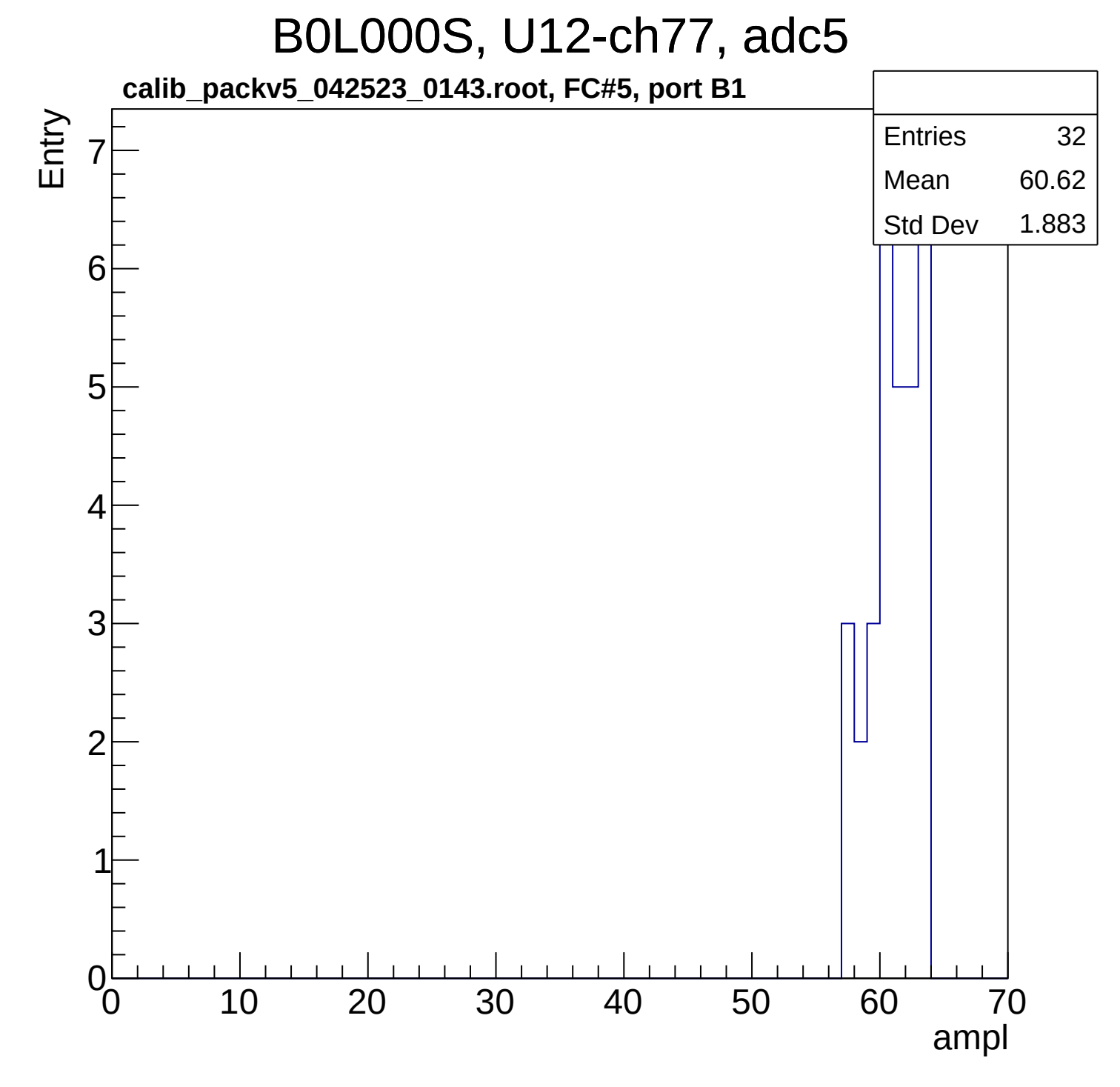
50
60
70
ampl


B0L000S, U12-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.62
Std Dev 1.883
Entry
ampl
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1
2
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60
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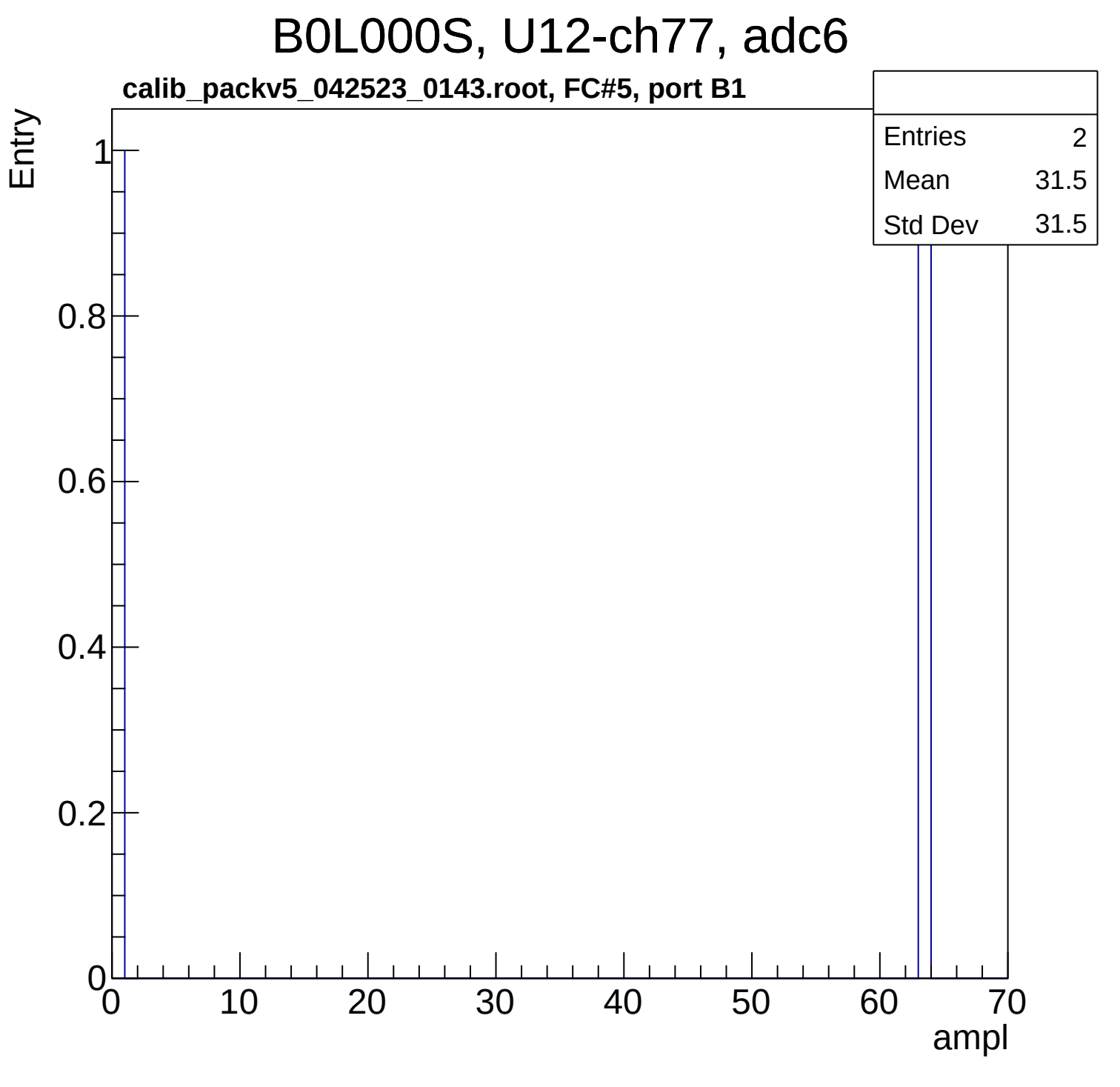
B0L000S, U12-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
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10
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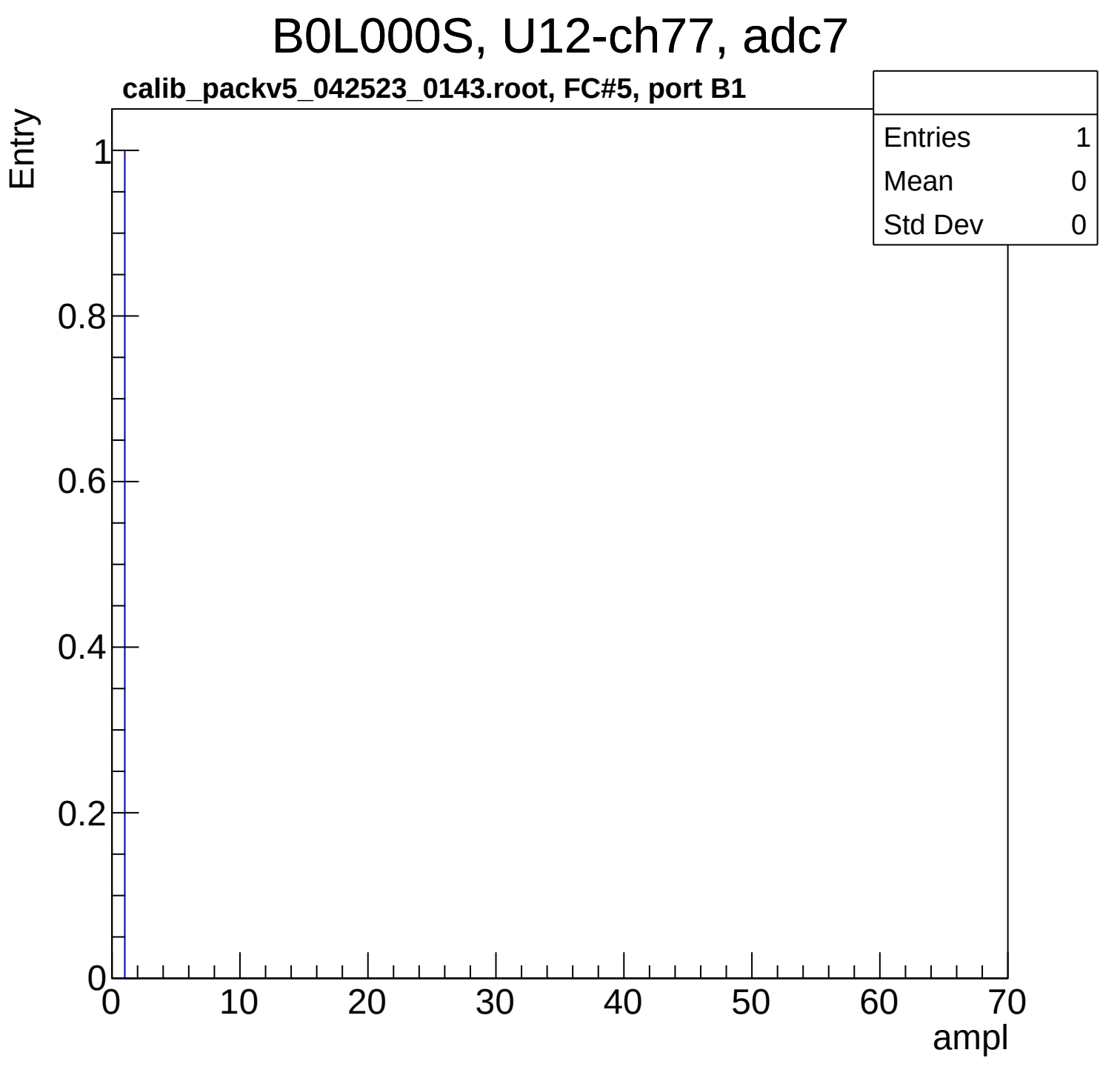
70
ampl


B0L000S, U12-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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50
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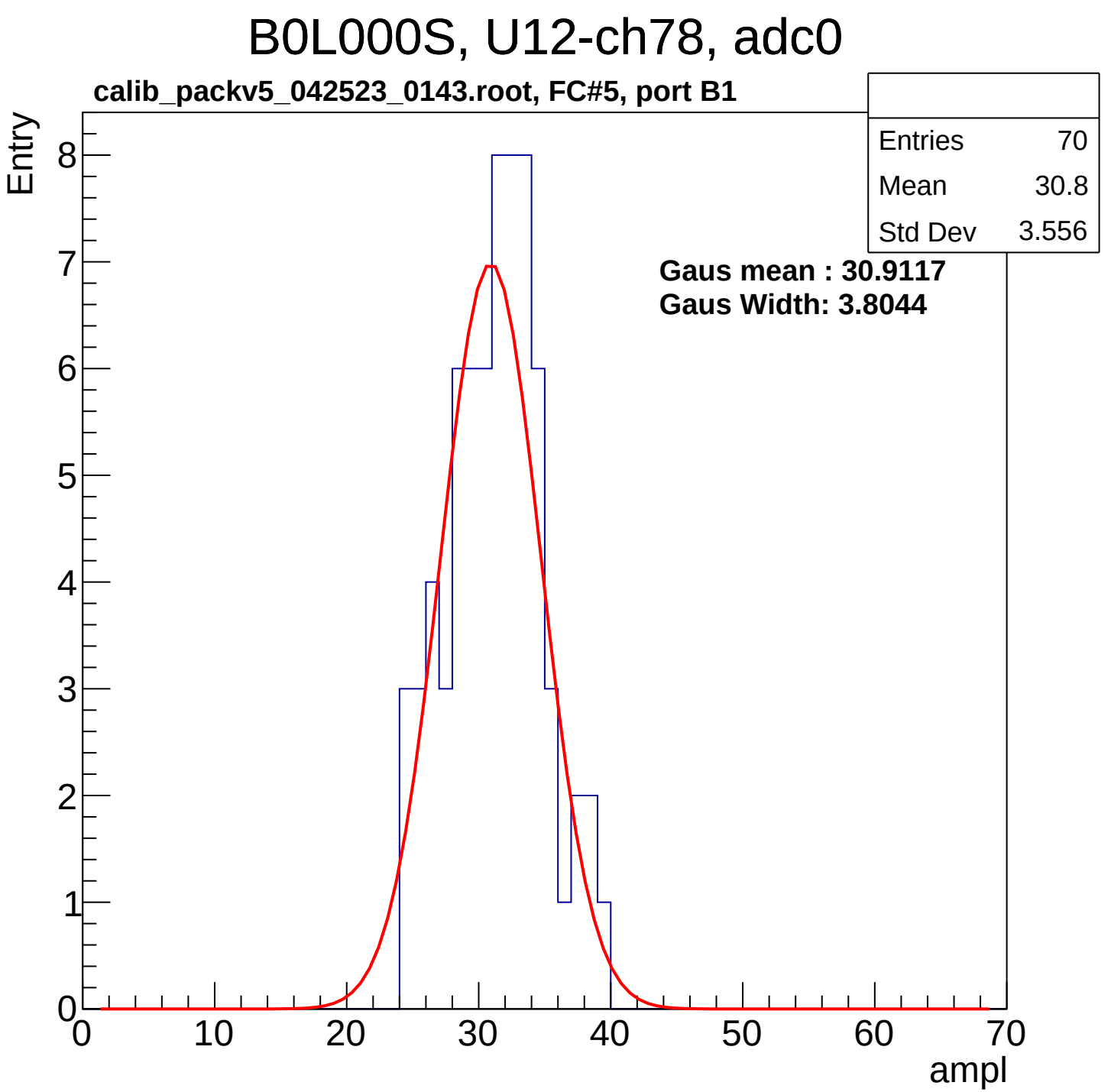
70
ampl


B0L000S, U12-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.8
Std Dev 3.556
Gaus mean : 30.9117
Gaus Width: 3.8044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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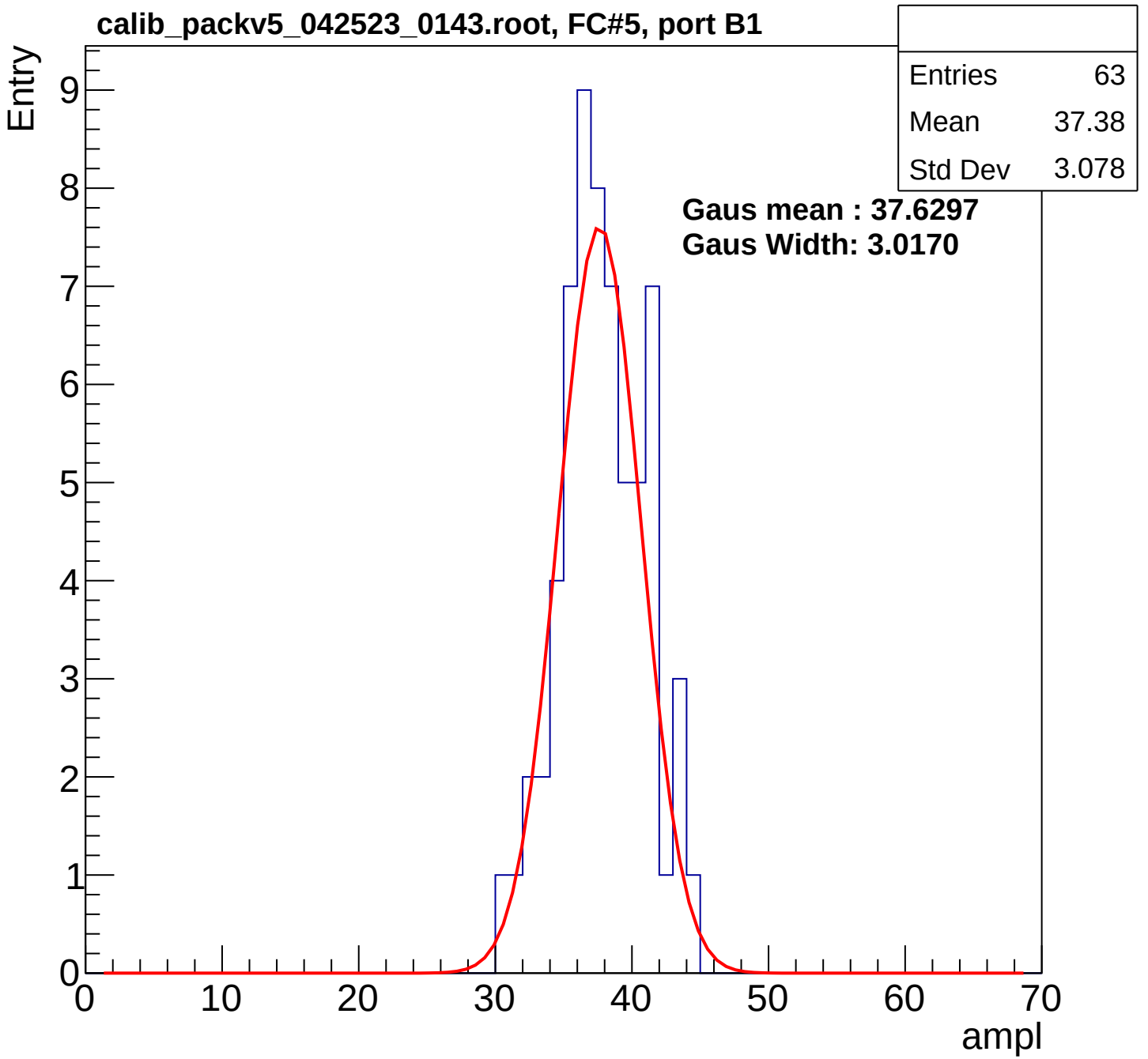
B0L000S, U12-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 37.38
Std Dev 3.078
Gaus mean : 37.6297
Gaus Width: 3.0170
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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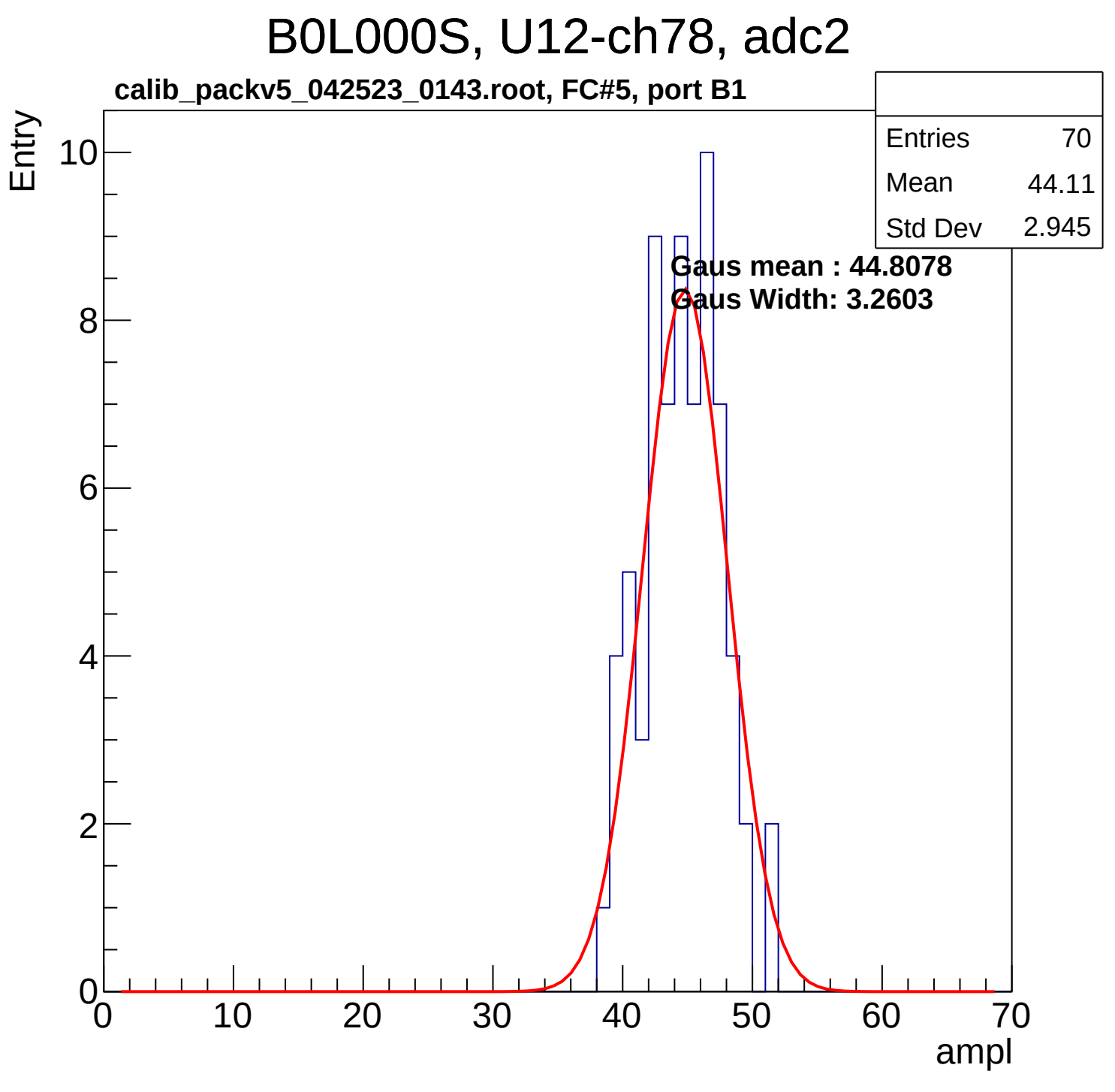
B0L000S, U12-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44.11
Std Dev 2.945
Gaus mean : 44.8078
Gaus Width: 3.2603
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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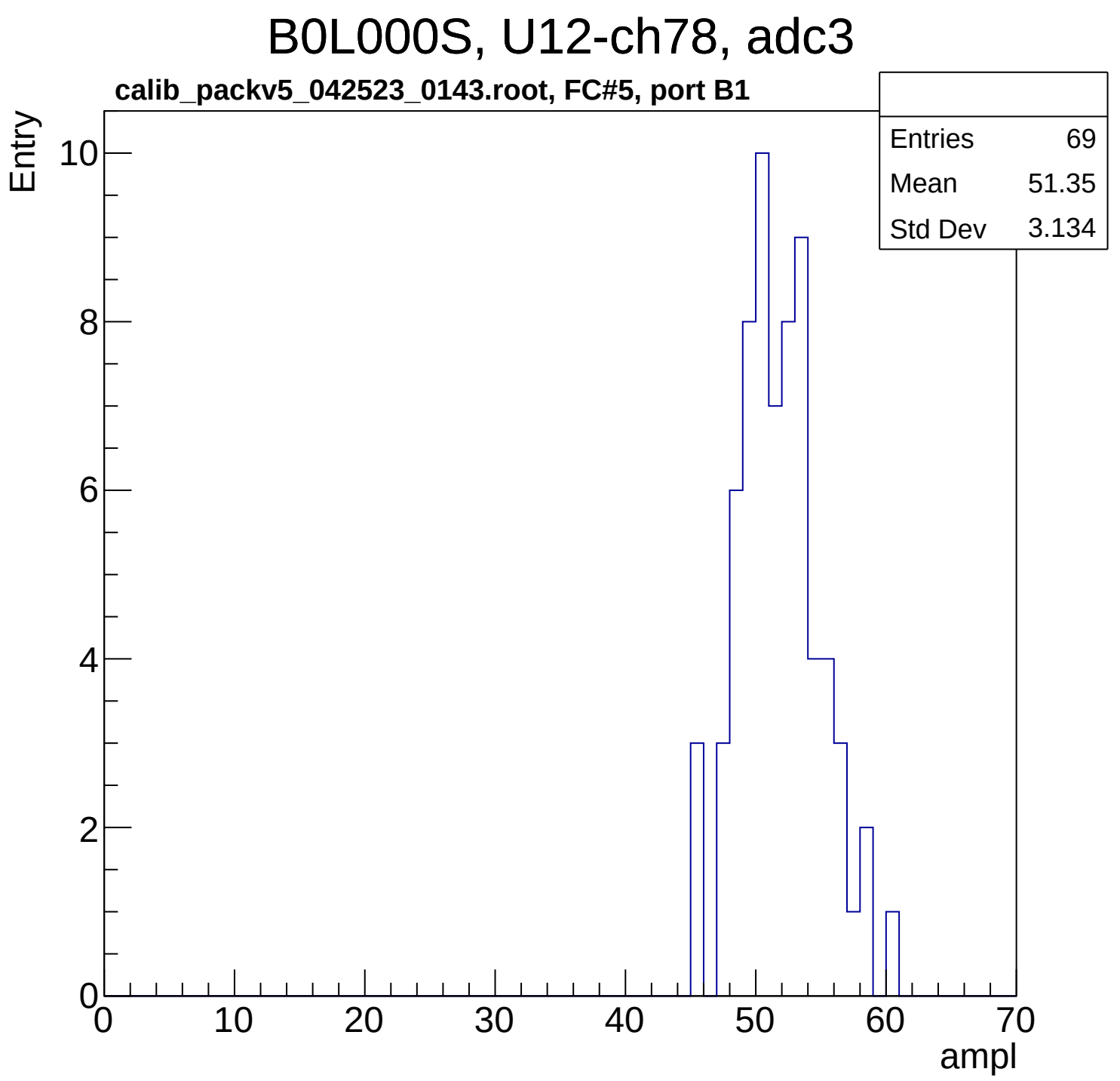
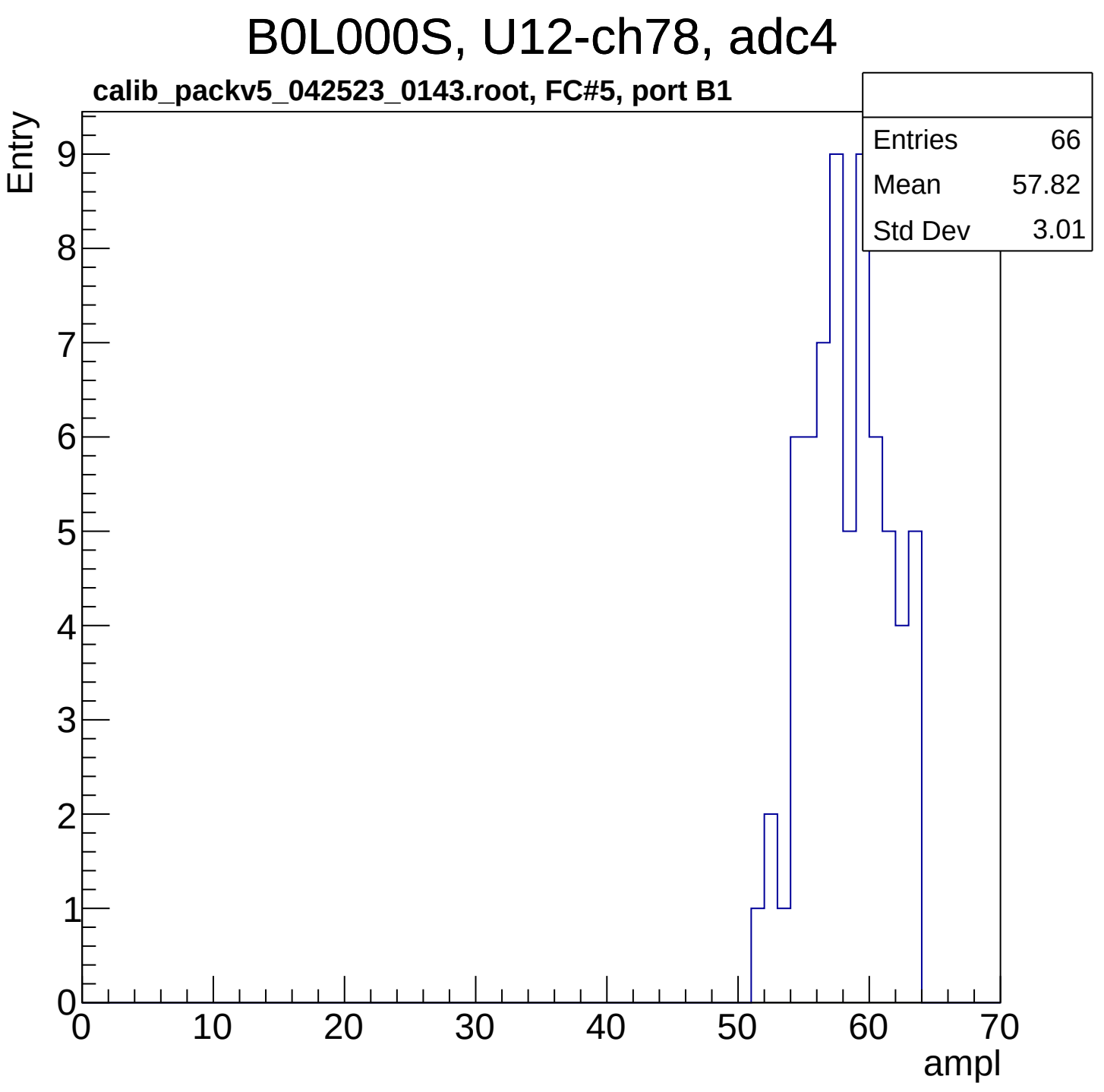
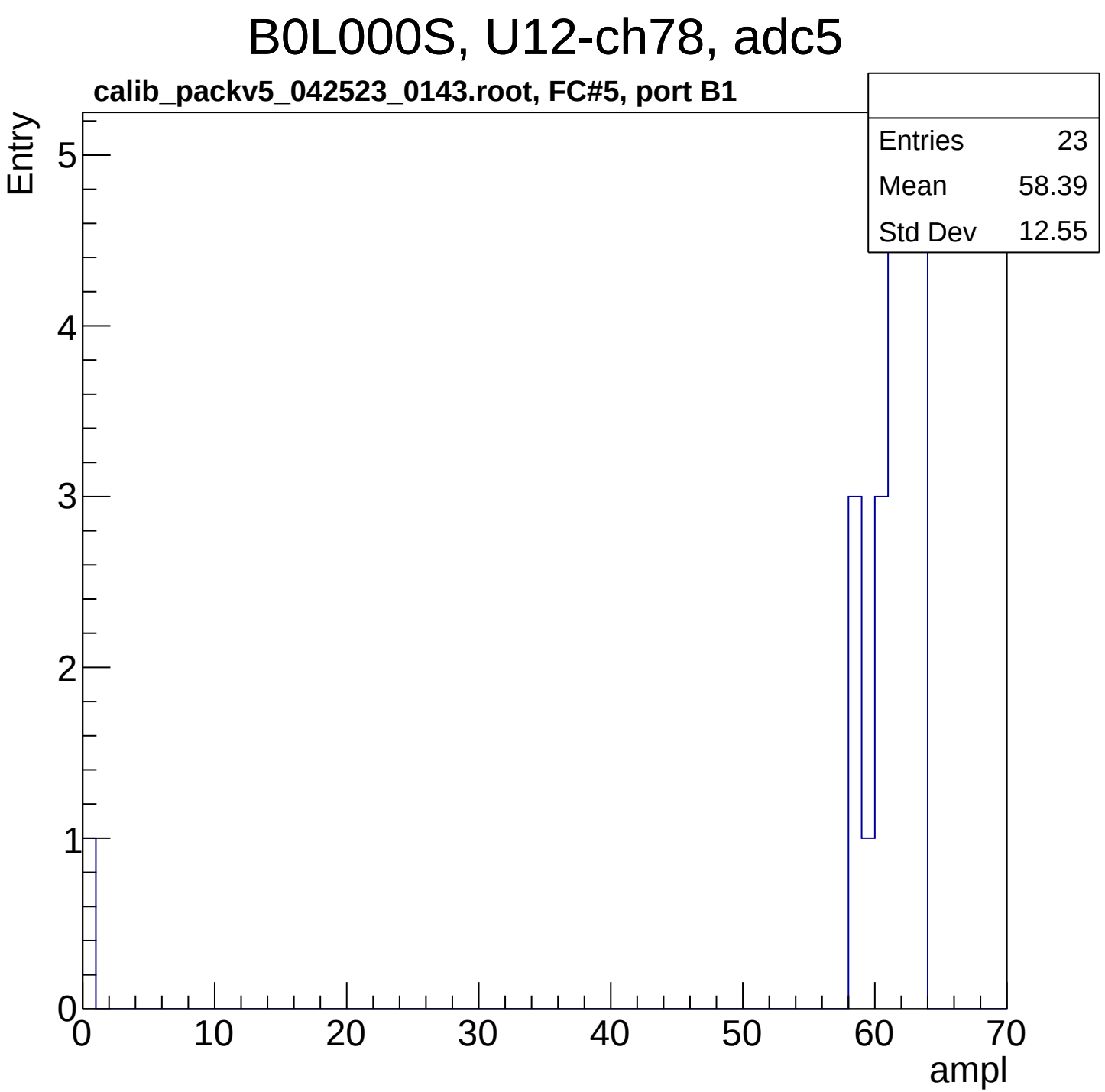
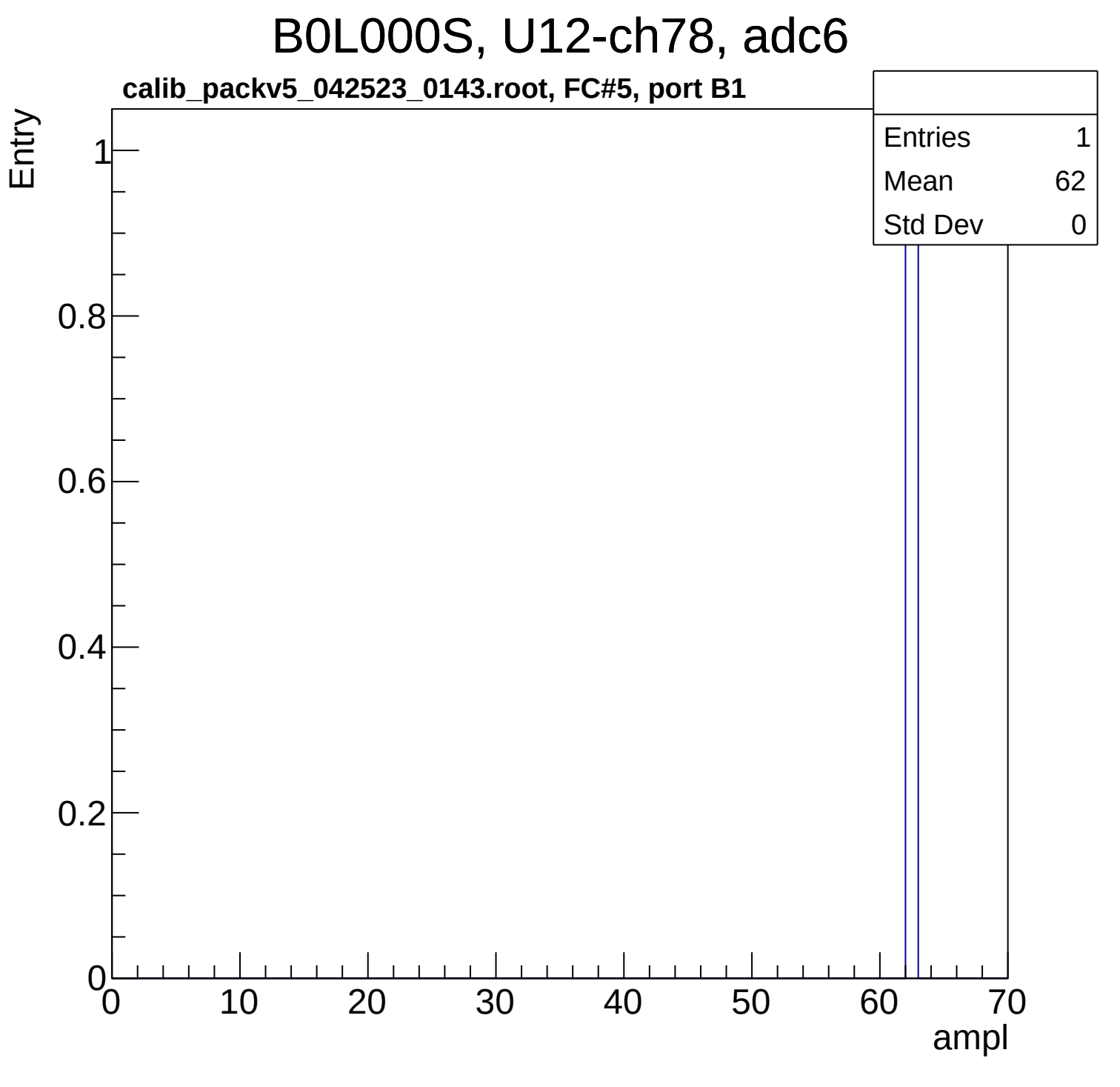
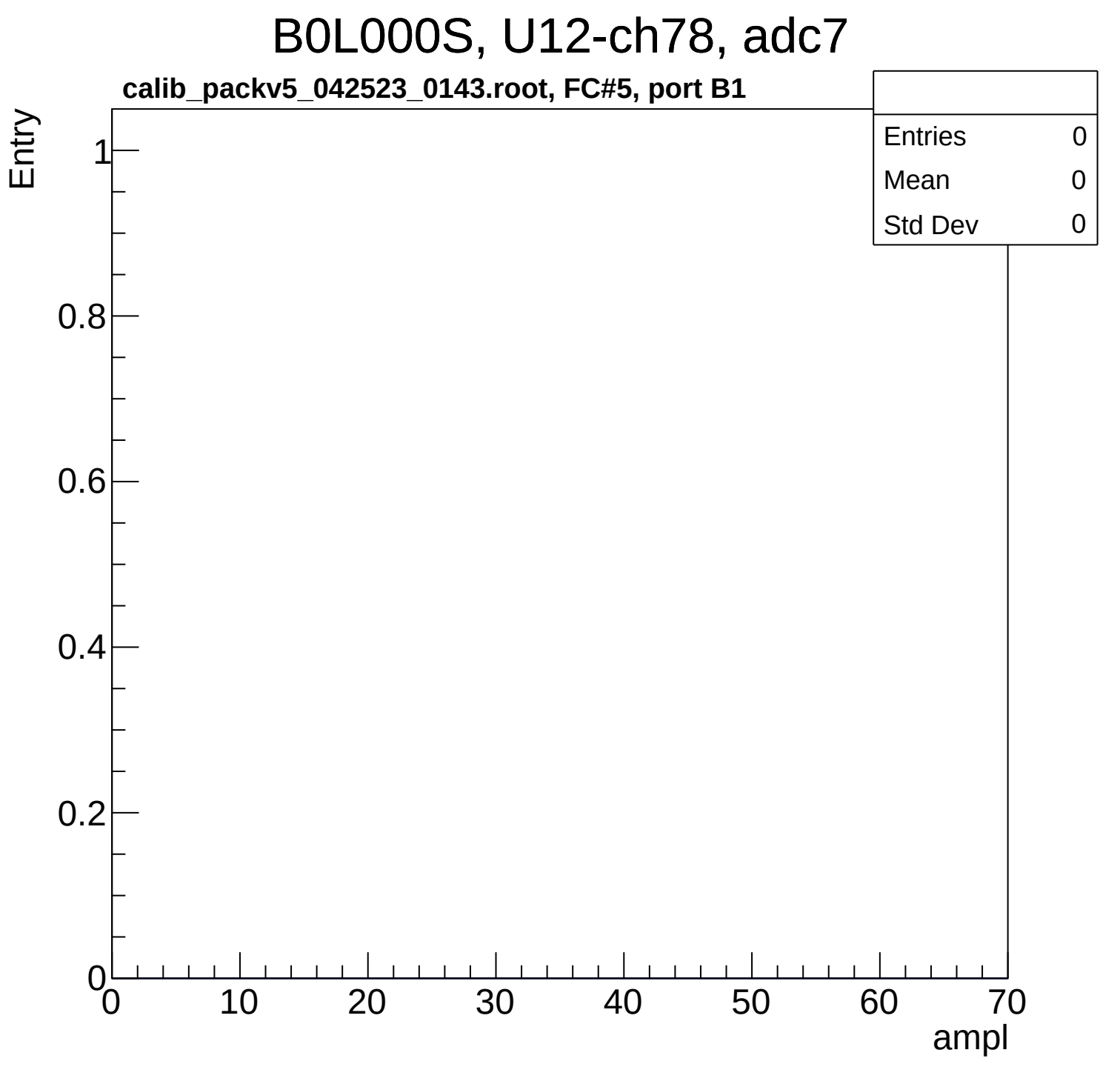
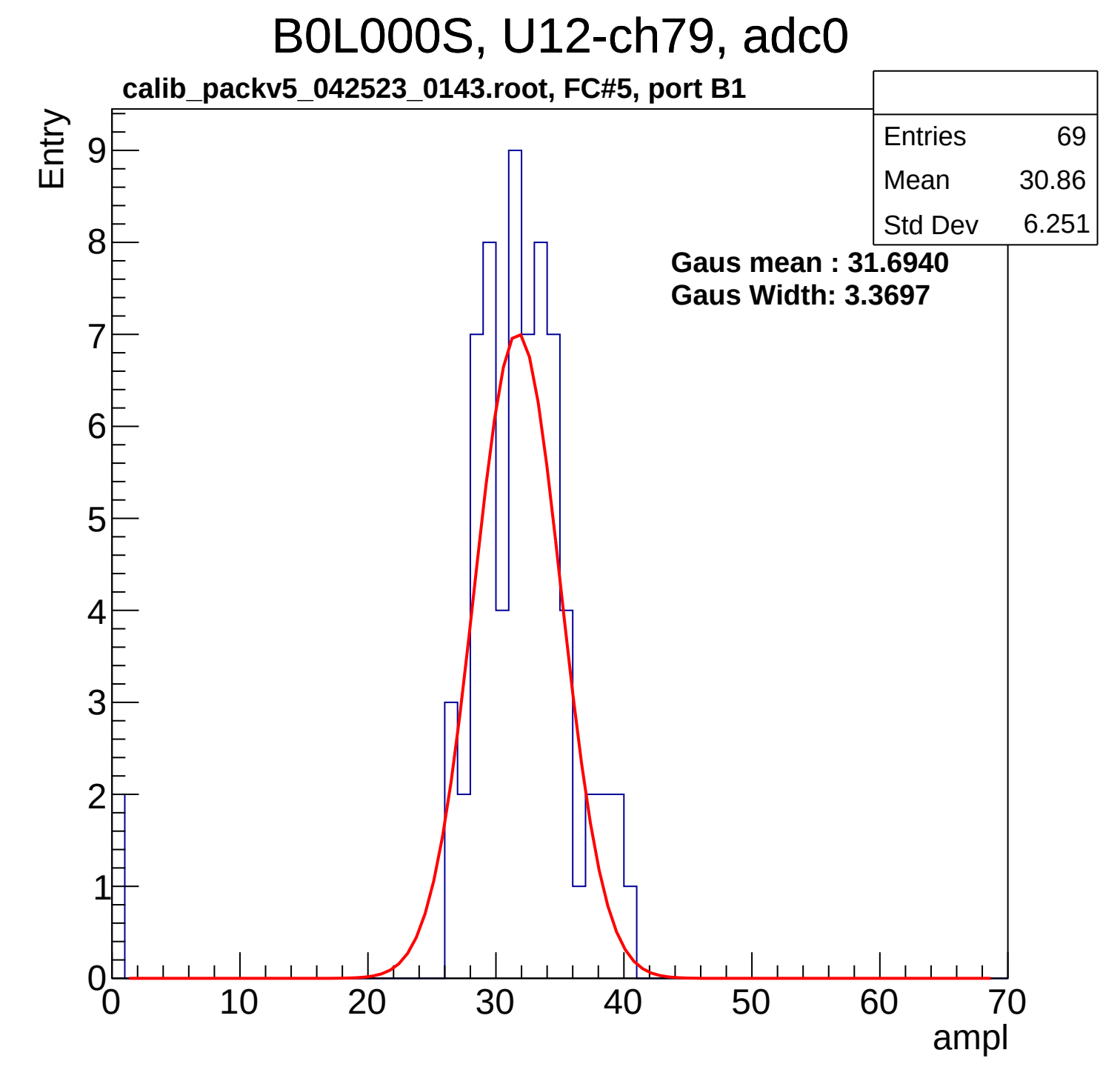

B0L000S, U12-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 51.35
Std Dev 3.134
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.82
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.39
Std Dev 12.55
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.86
Std Dev 6.251
Gaus mean : 31.6940
Gaus Width: 3.3697
Entry
ampl
0
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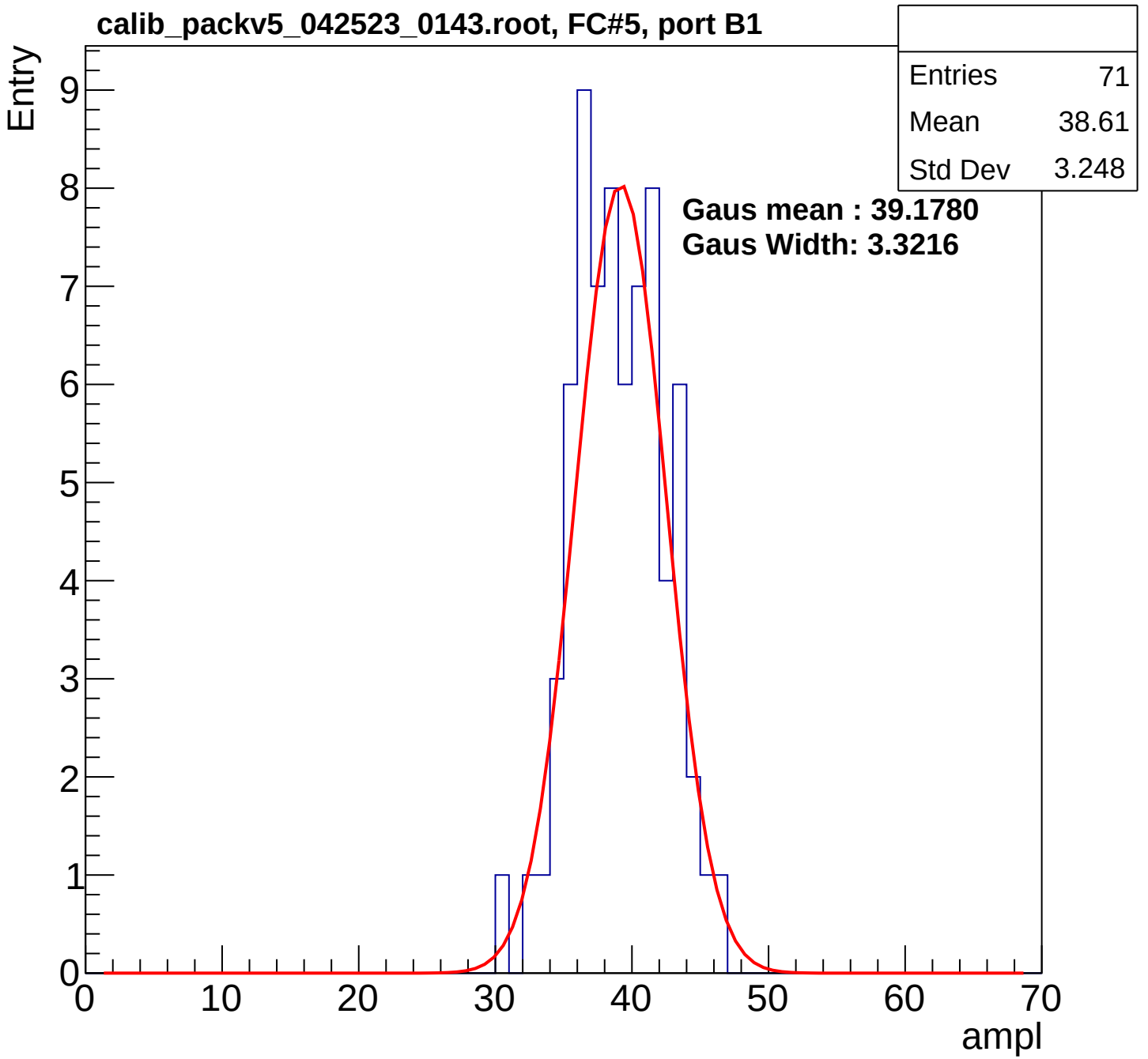
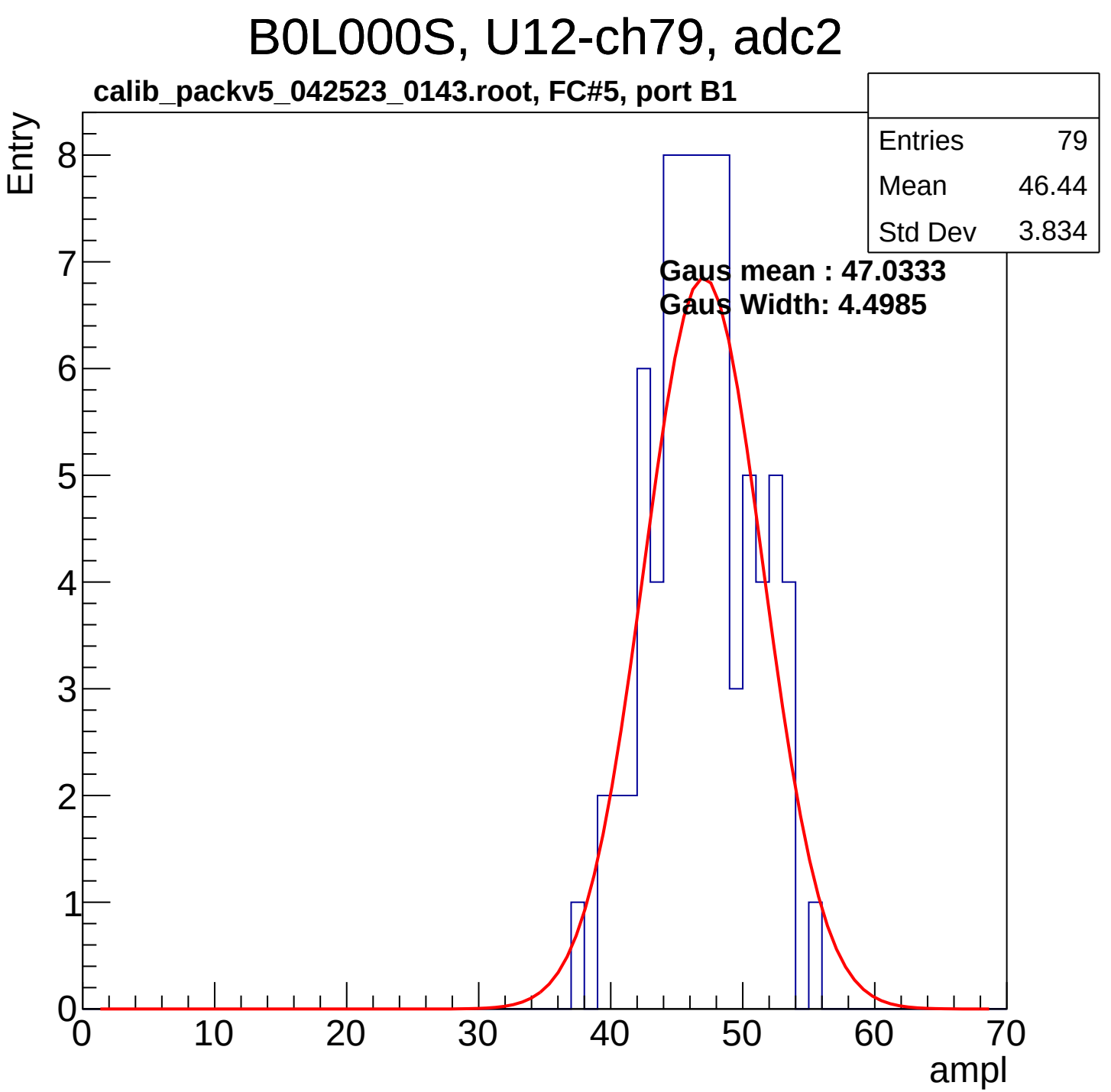
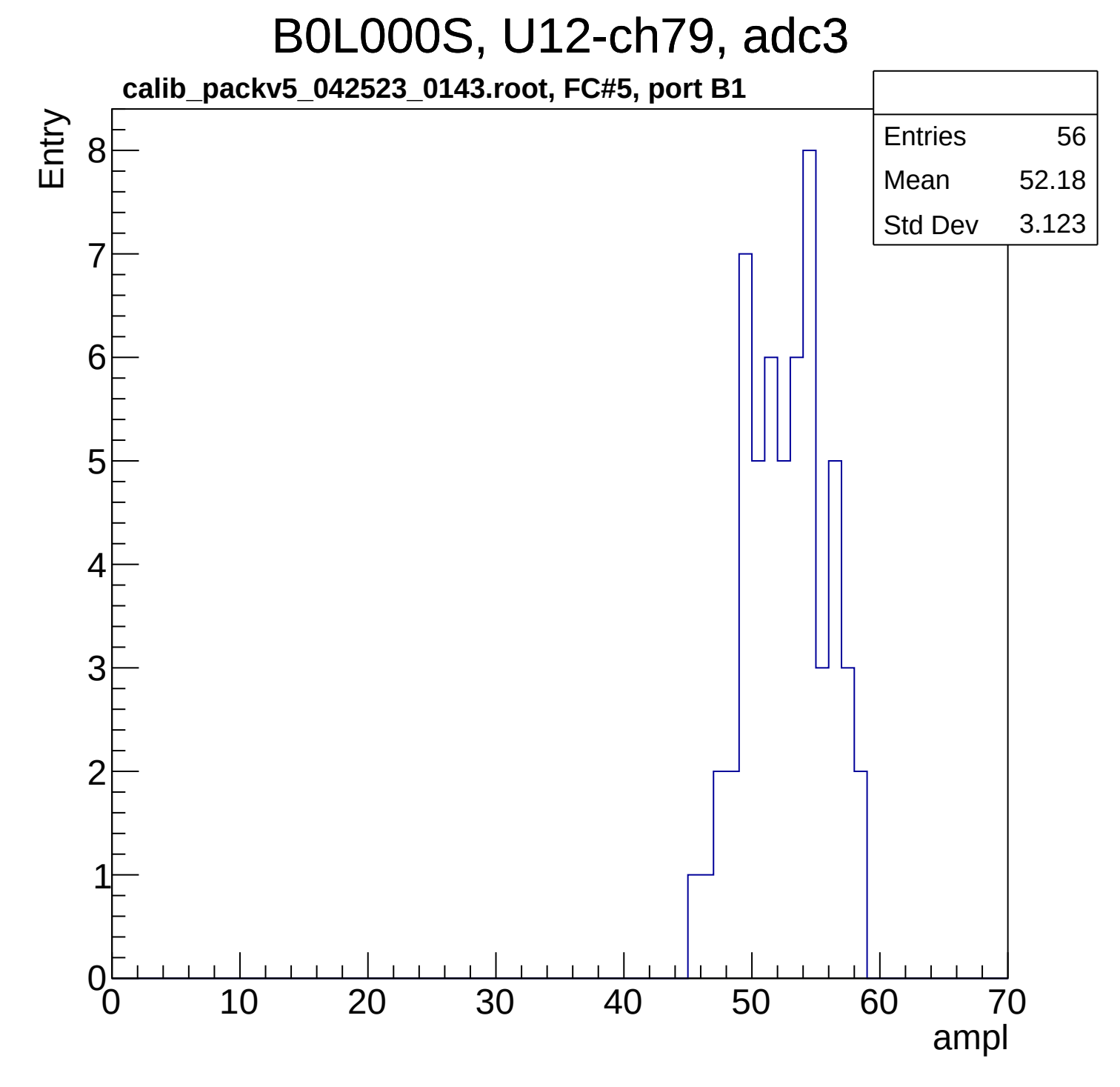
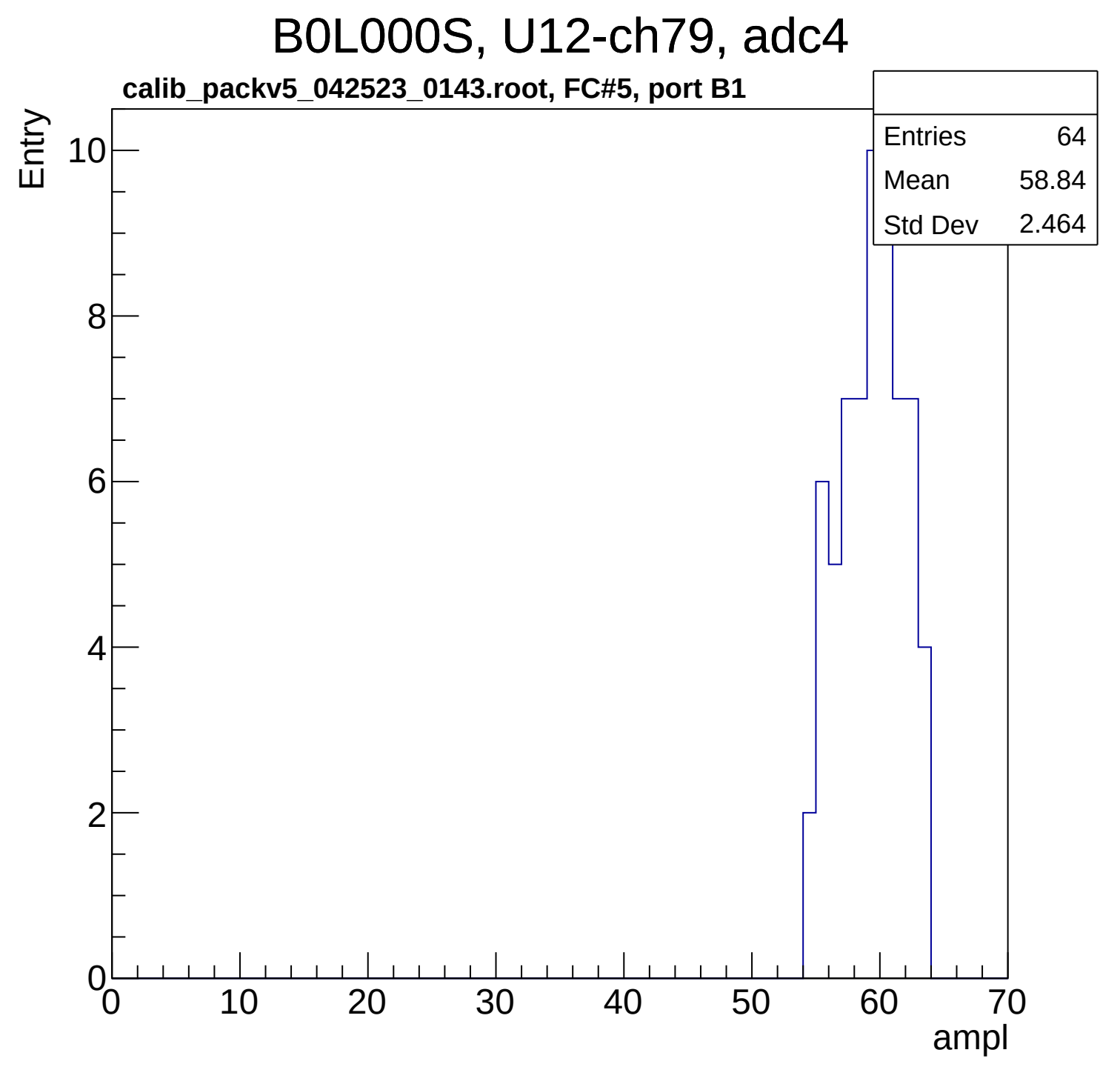
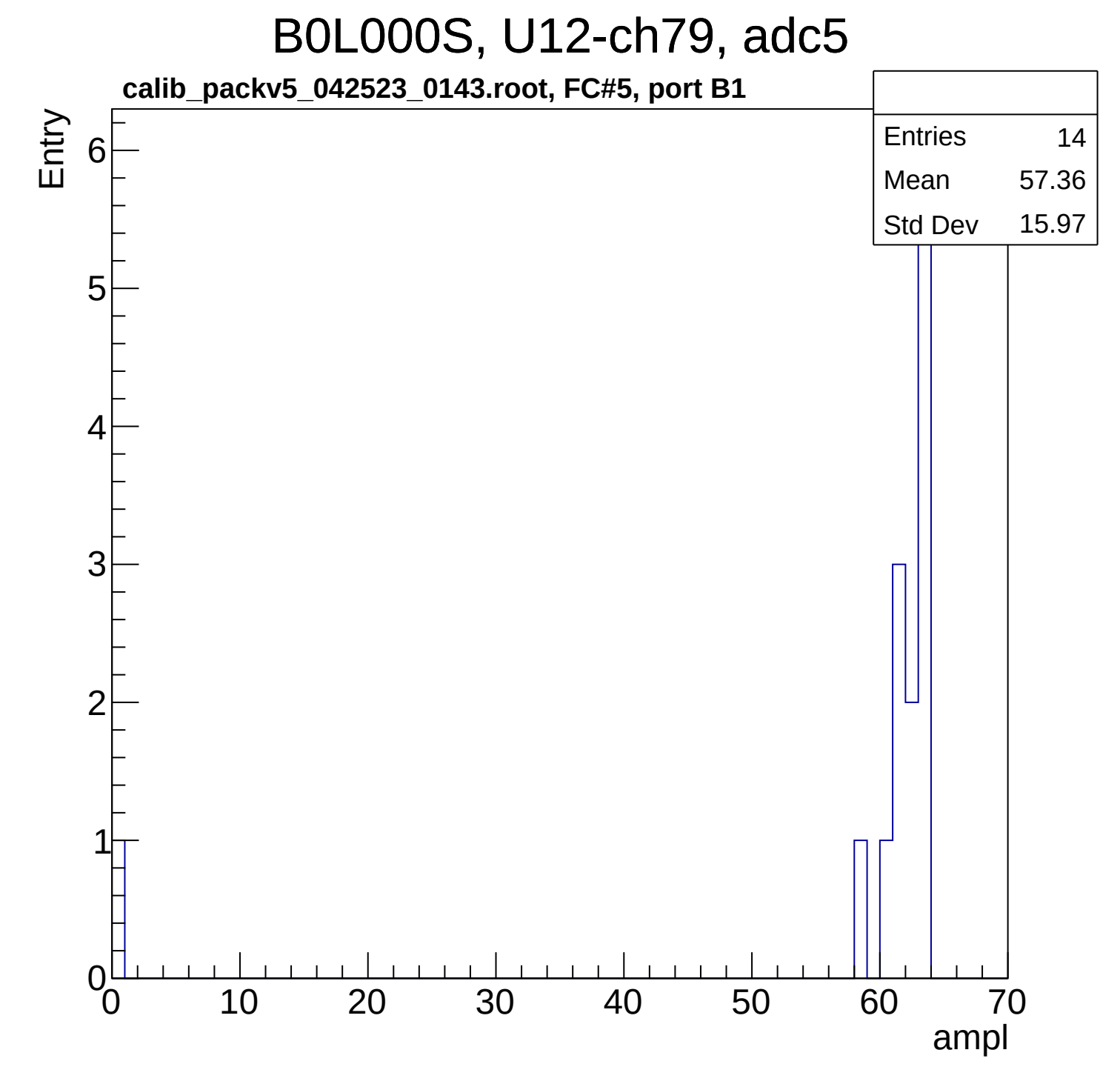
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

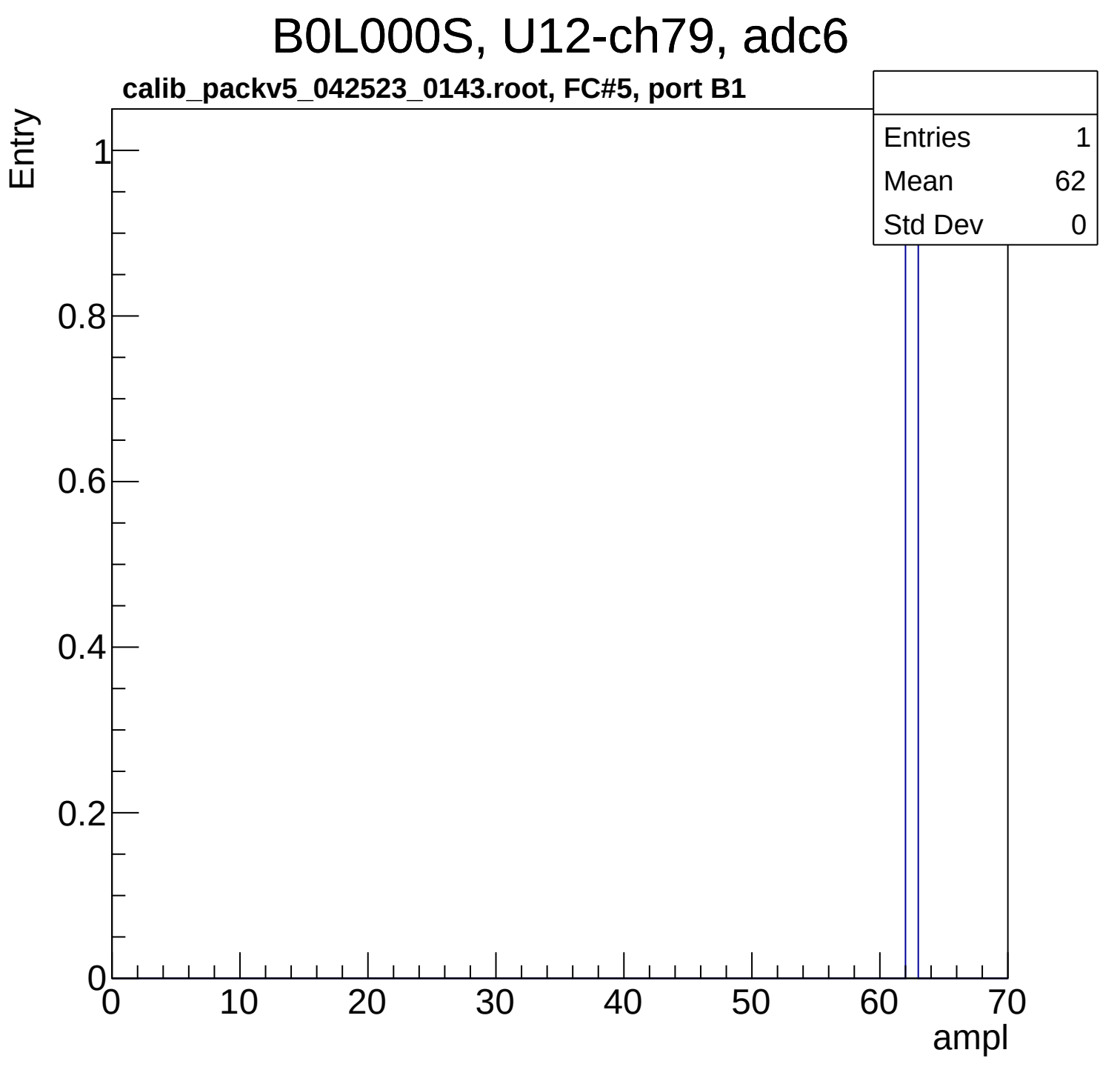

B0L000S, U12-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 38.61
Std Dev 3.248
Gaus mean : 39.1780
Gaus Width: 3.3216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

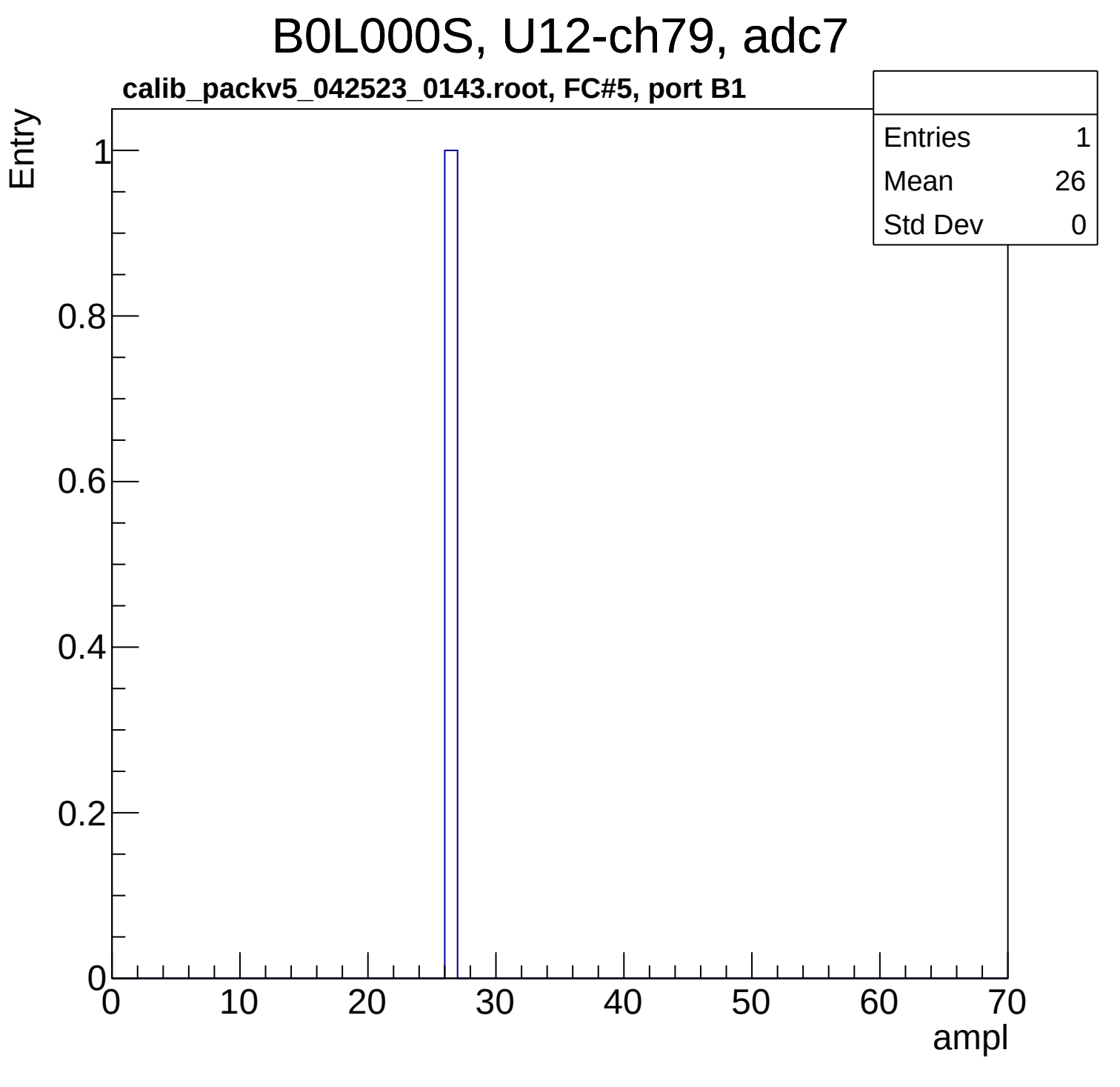

B0L000S, U12-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 46.44
Std Dev 3.834
Gaus mean : 47.0333
Gaus Width: 4.4985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 52.18
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 58.84
Std Dev 2.464
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.36
Std Dev 15.97
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 26
Std Dev 0
Entry
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0.6
0.4
0.2
0
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30
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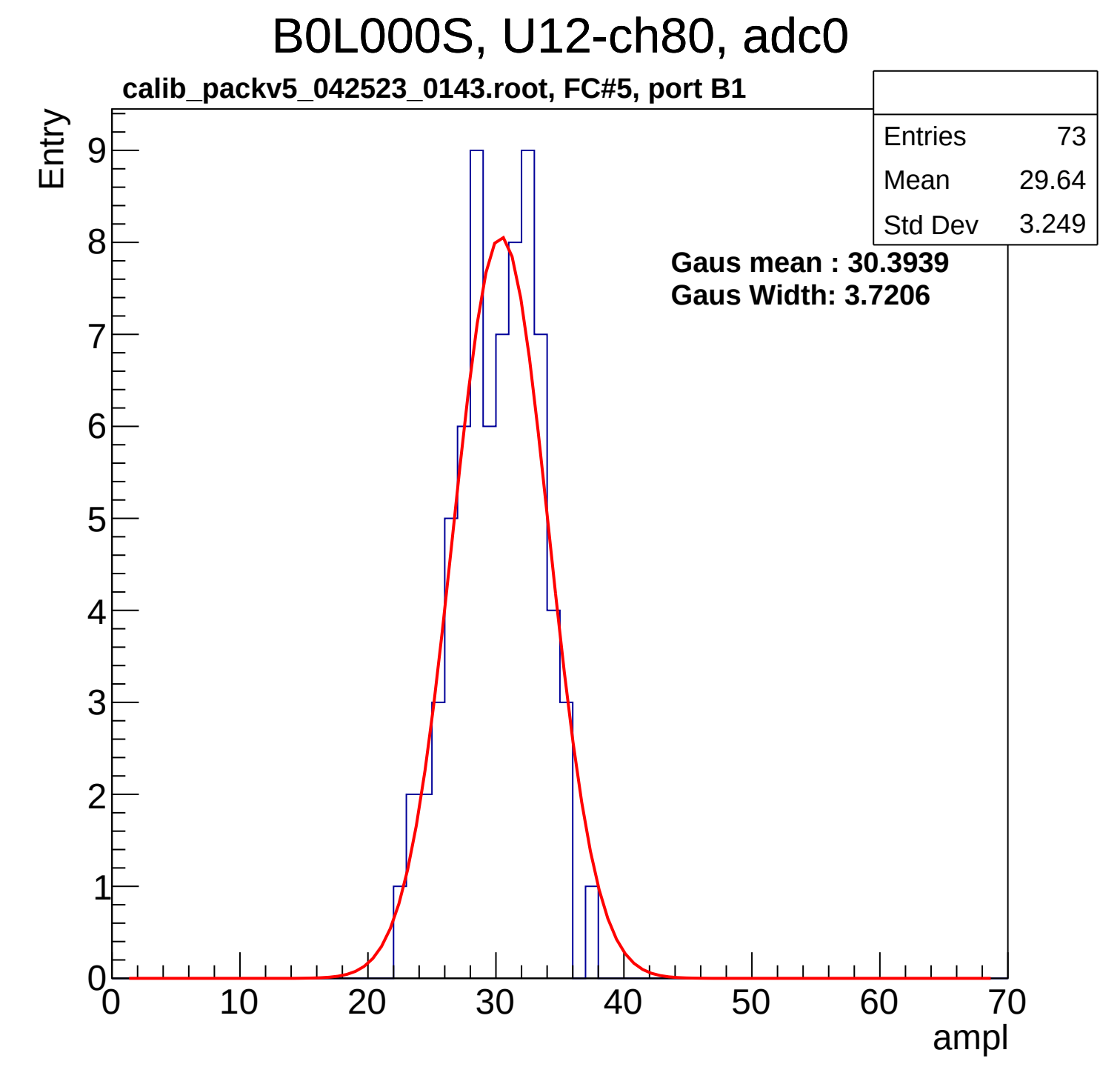
50
60
70
ampl


B0L000S, U12-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.64
Std Dev 3.249
Gaus mean : 30.3939
Gaus Width: 3.7206
Entry
ampl
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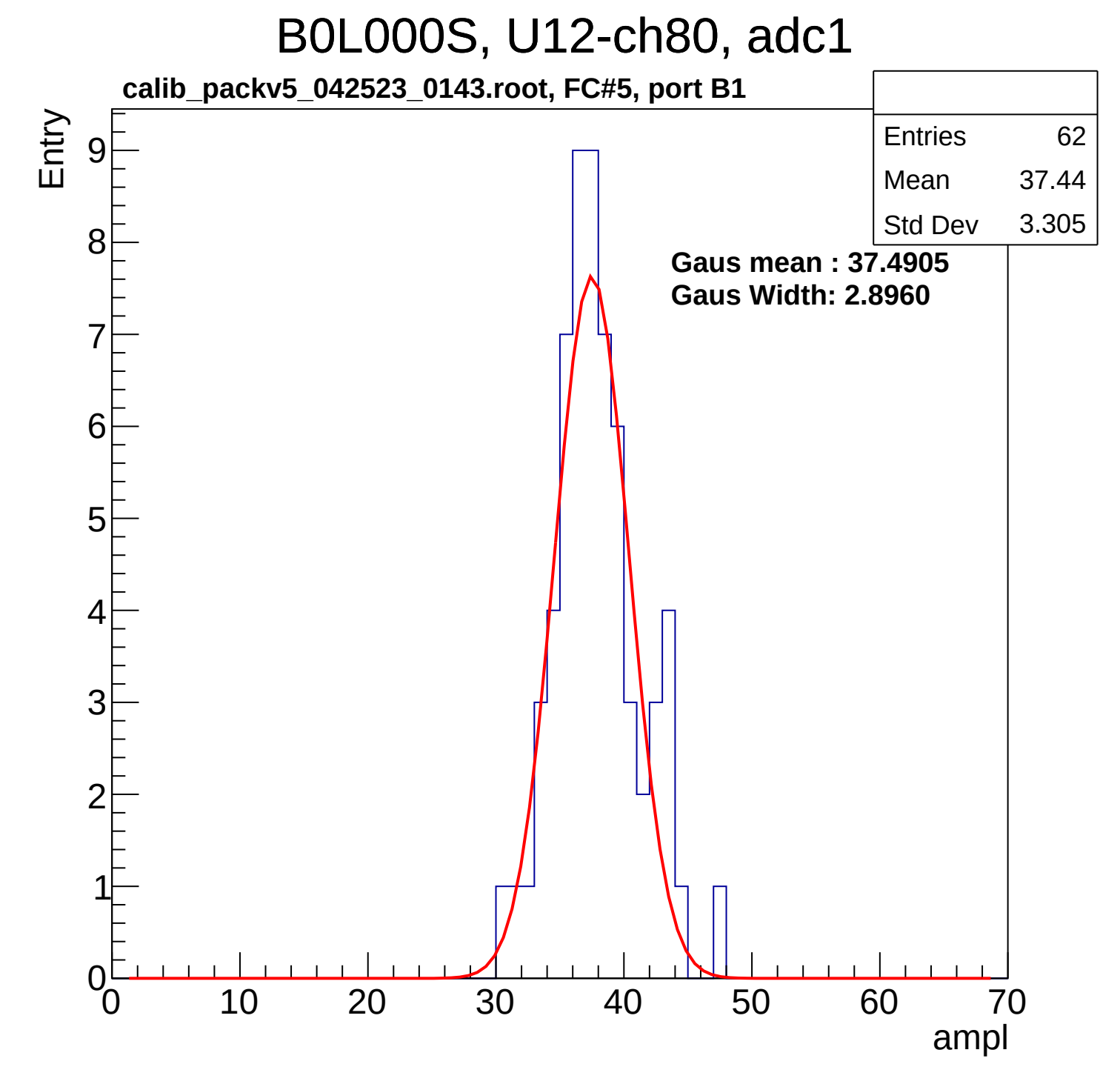
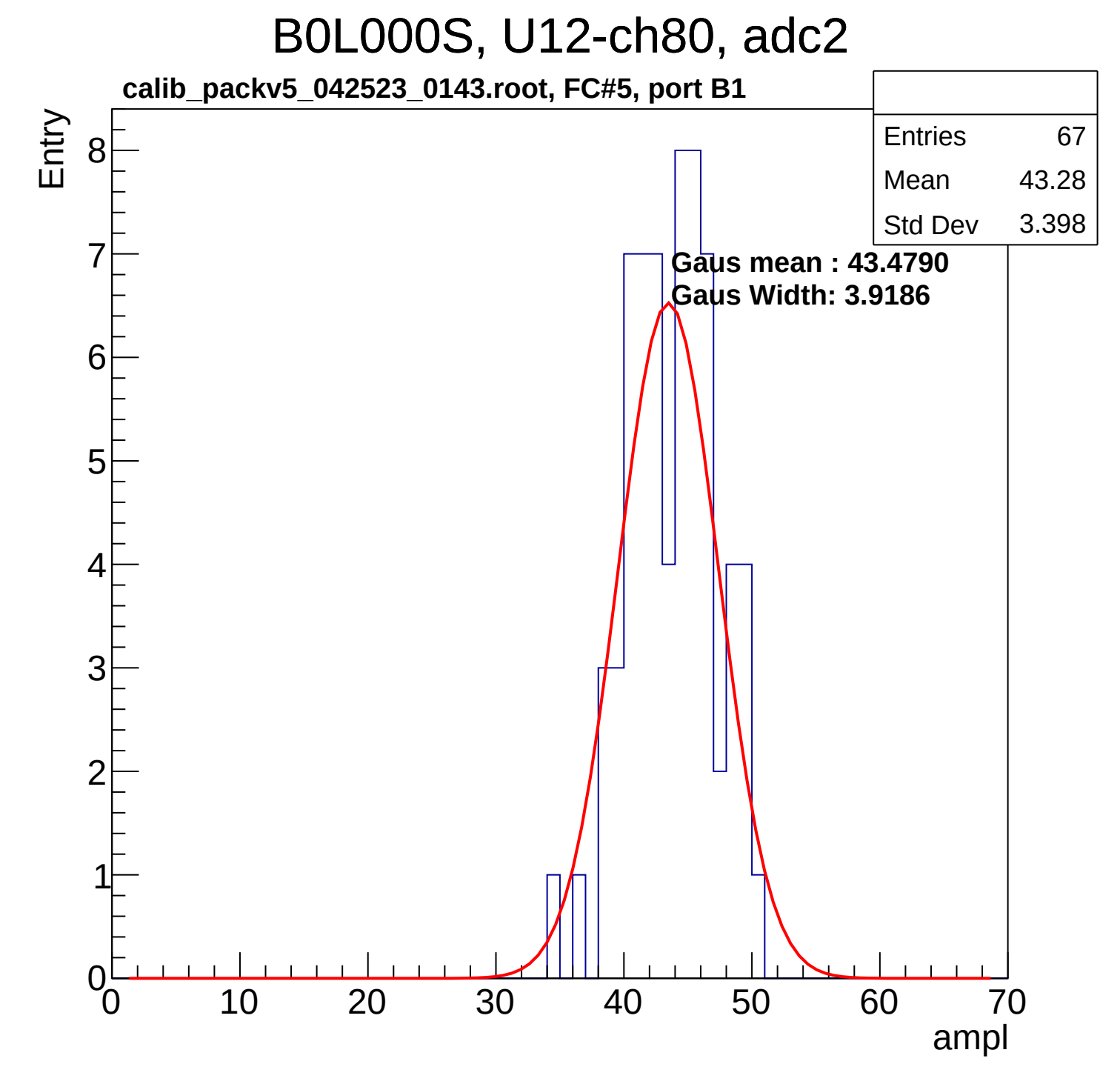
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.44
Std Dev 3.305
Gaus mean : 37.4905
Gaus Width: 2.8960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.28
Std Dev 3.398
Gaus mean : 43.4790
Gaus Width: 3.9186
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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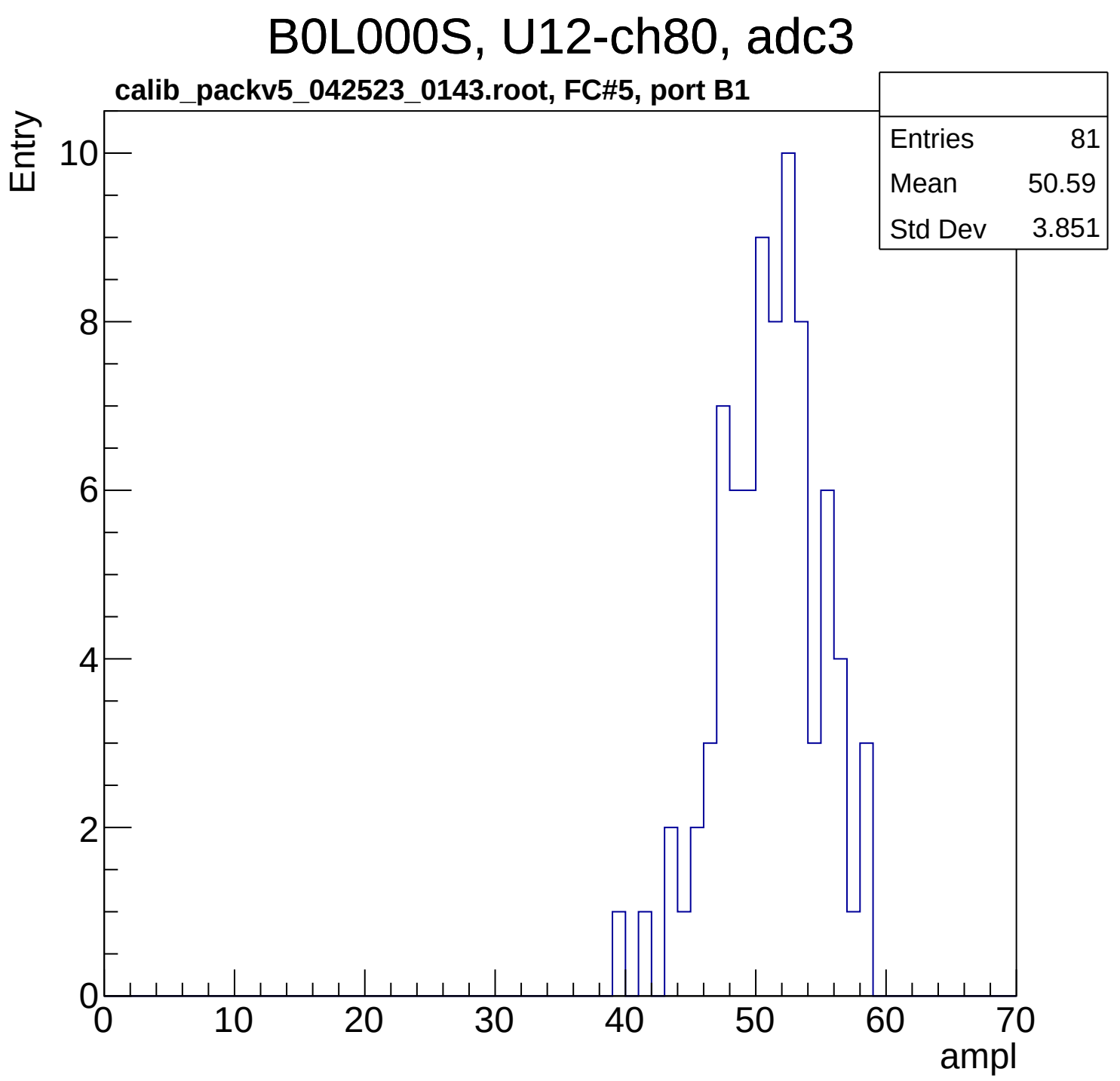
B0L000S, U12-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 50.59
Std Dev 3.851
Entry
ampl
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2
4
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8
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0
10
20
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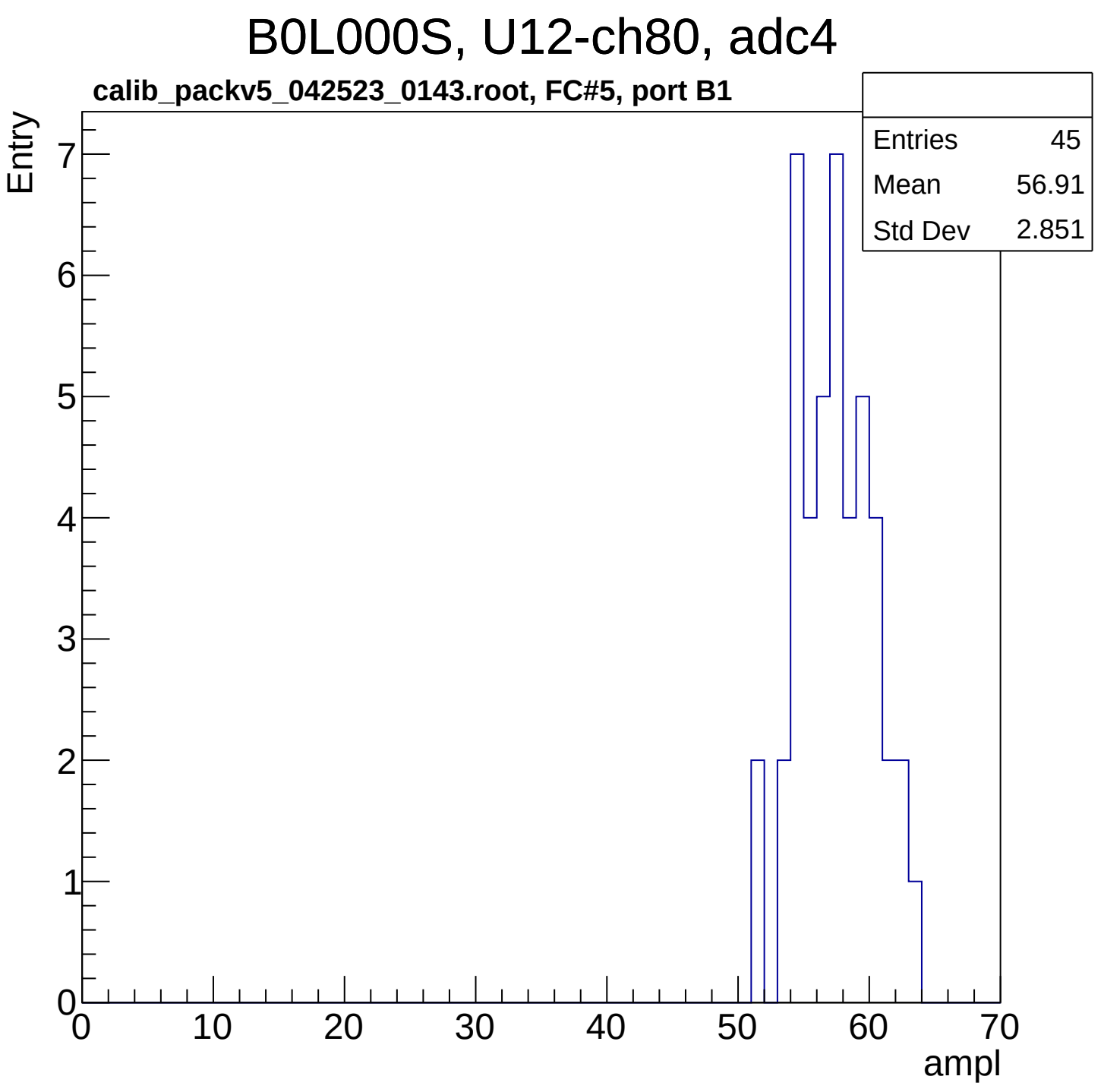
40
50
60
70


B0L000S, U12-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.91
Std Dev 2.851
Entry
ampl
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3
4
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6
7
0
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60
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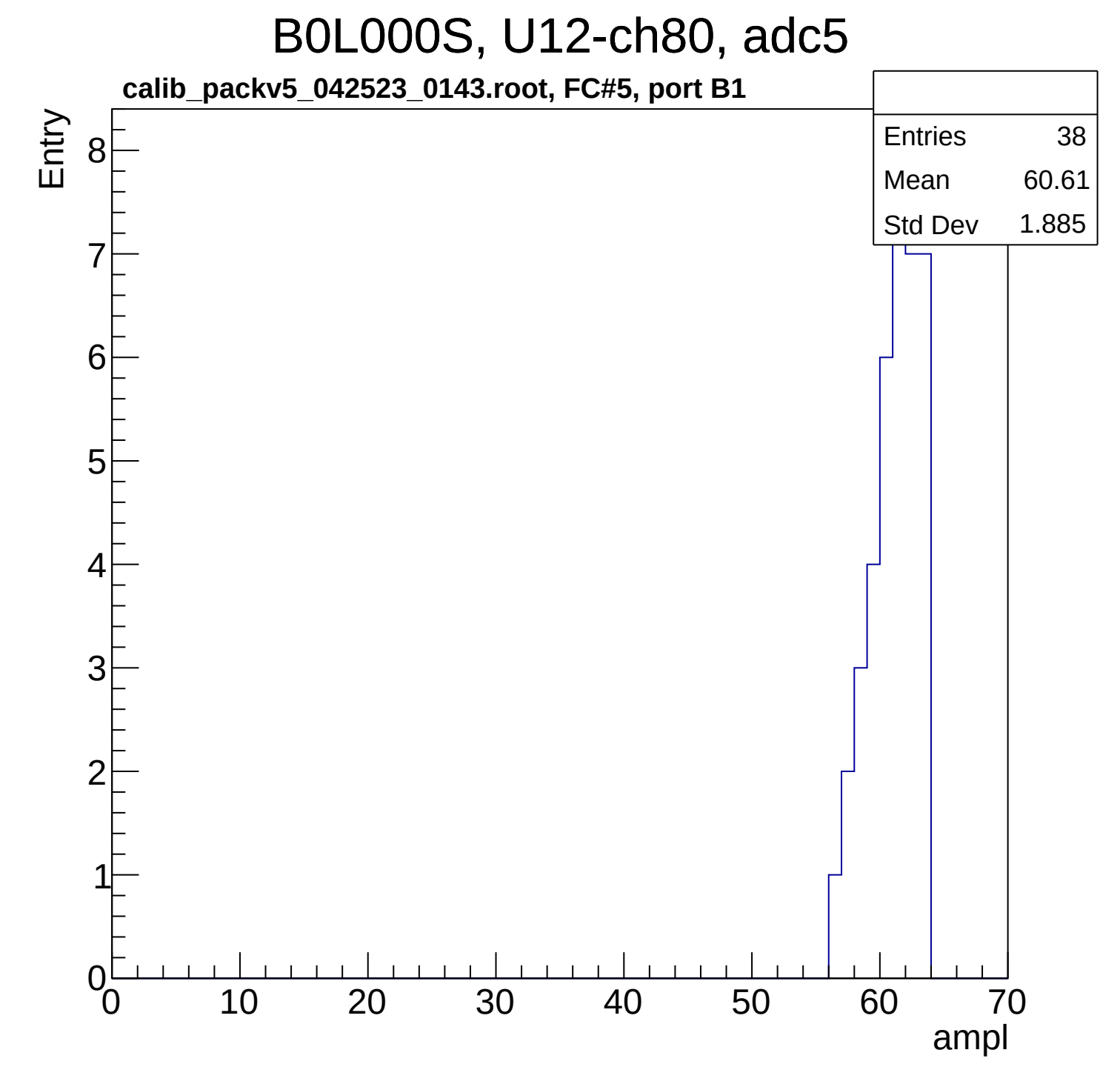
B0L000S, U12-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 60.61
Std Dev 1.885
Entry
ampl
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1
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3
4
5
6
7
8
0
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30
40
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60
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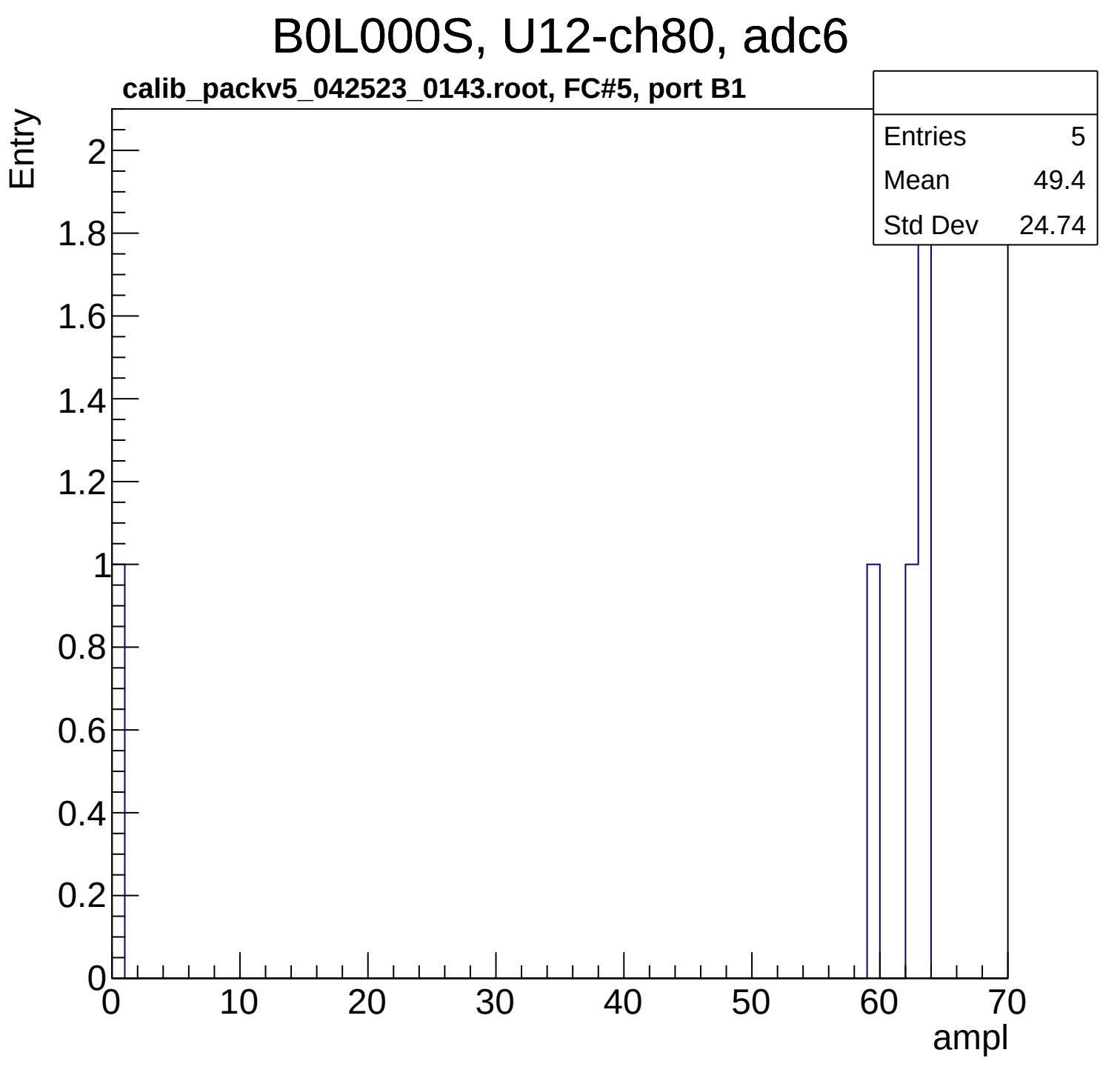
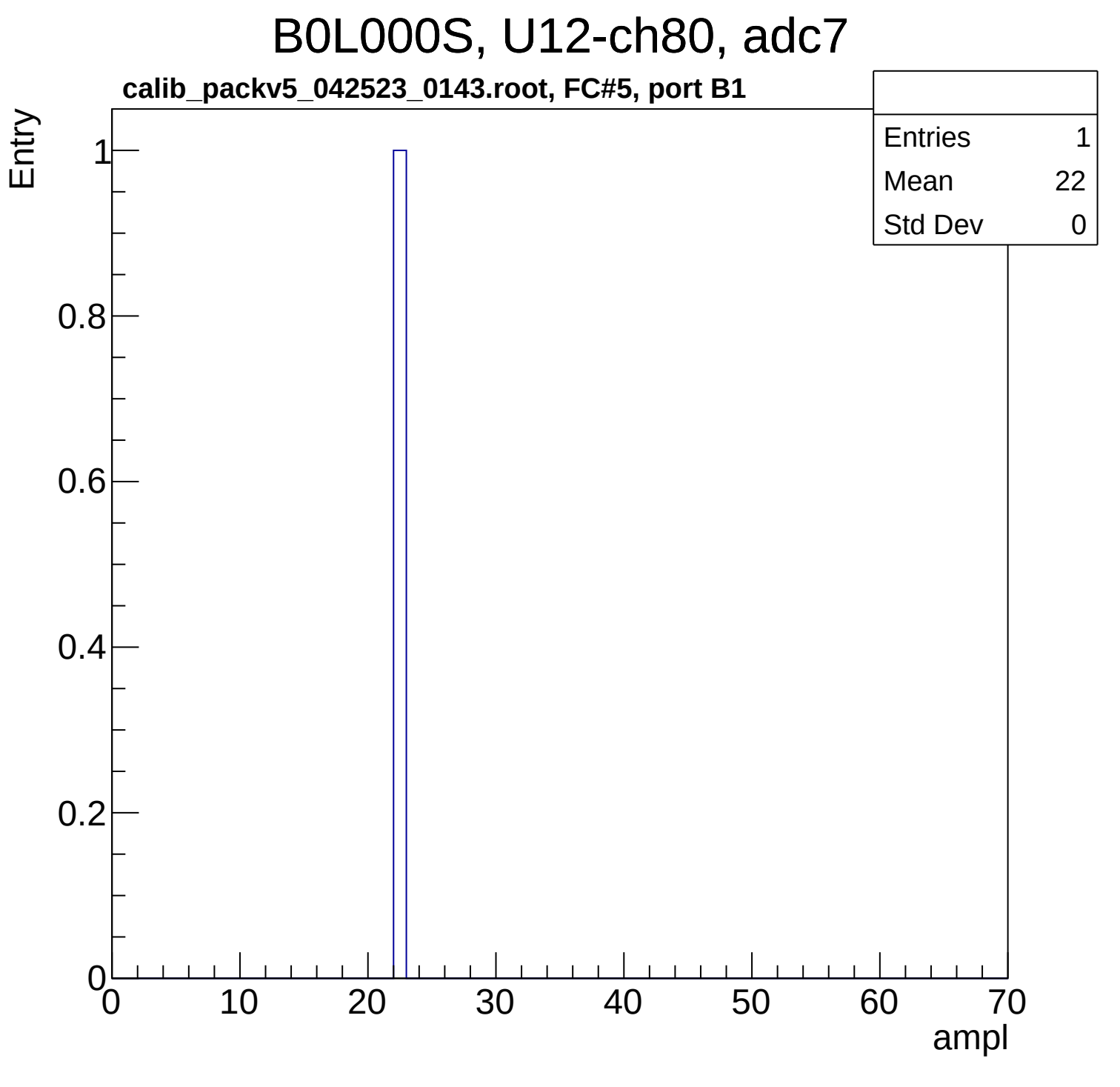
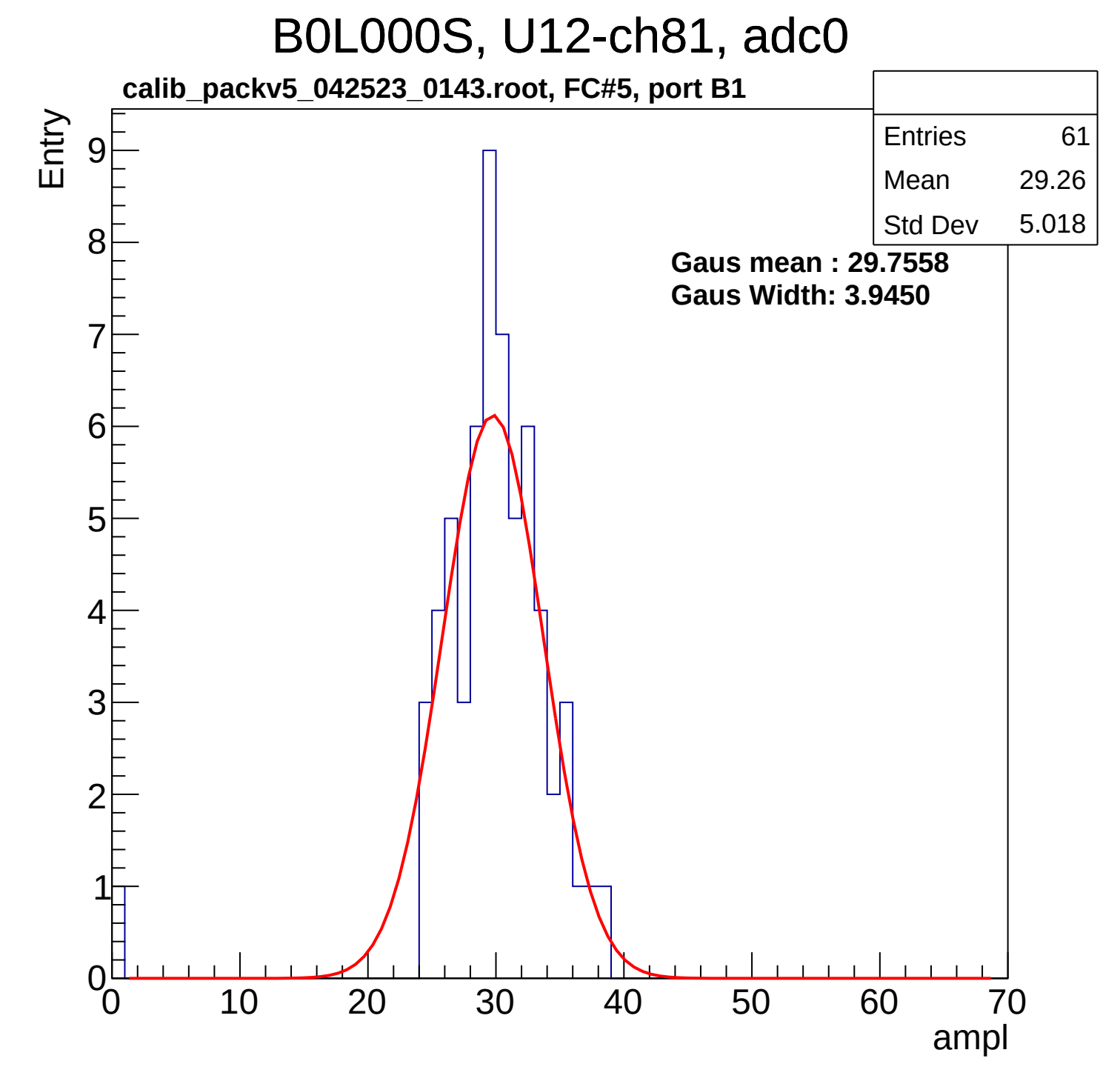
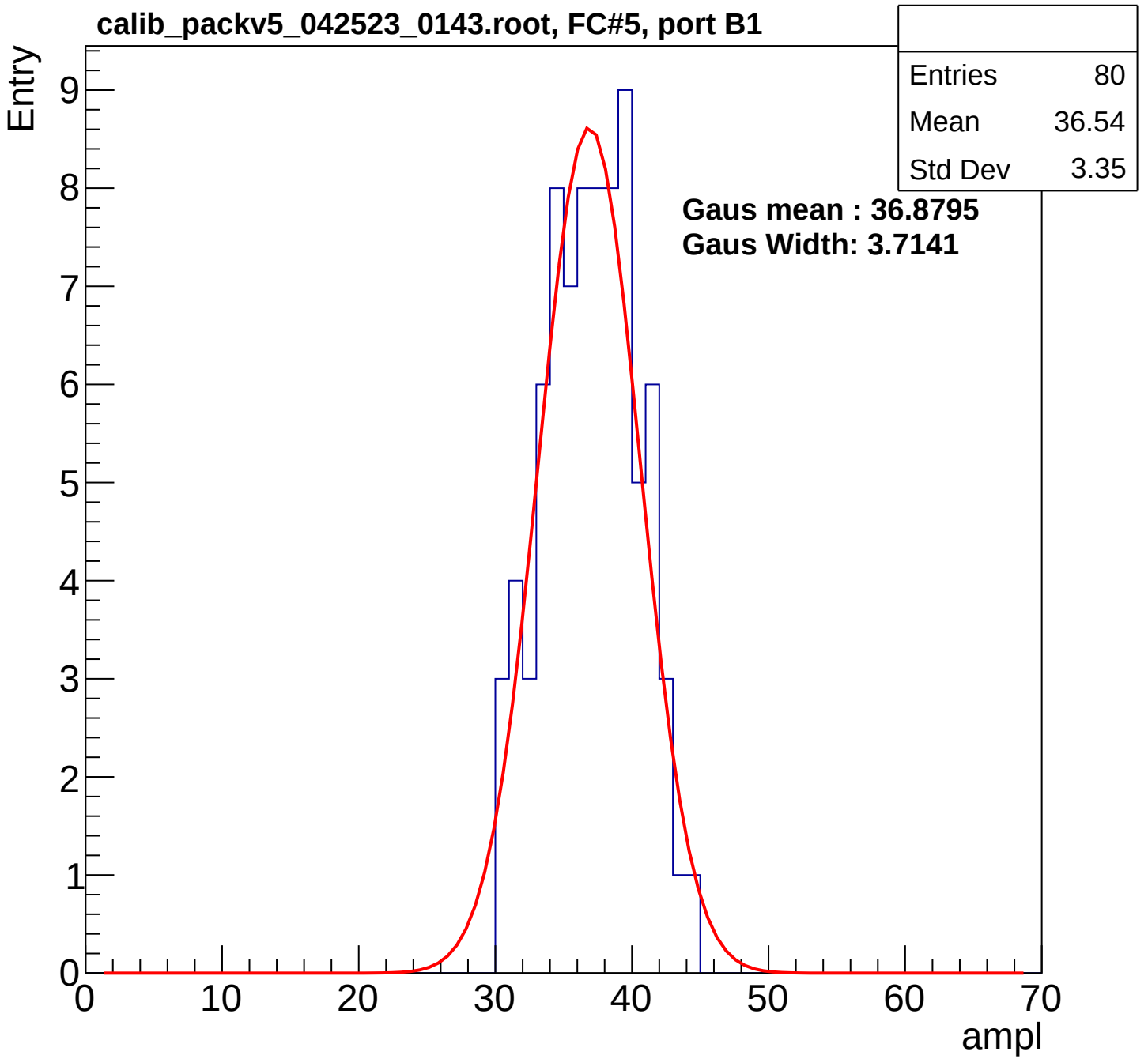

B0L000S, U12-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.4
Std Dev 24.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.26
Std Dev 5.018
Gaus mean : 29.7558
Gaus Width: 3.9450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.54
Std Dev 3.35
Gaus mean : 36.8795
Gaus Width: 3.7141
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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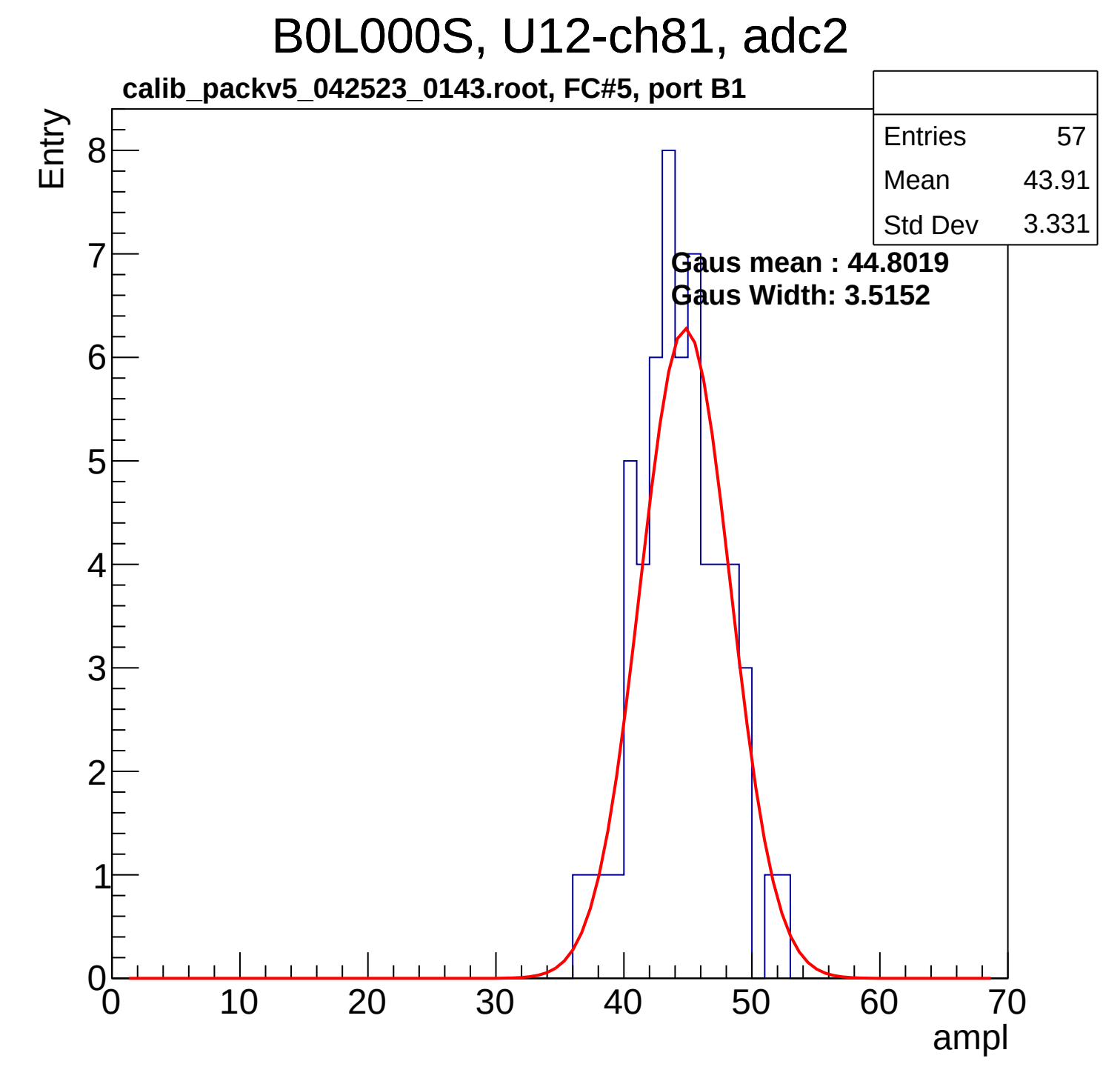
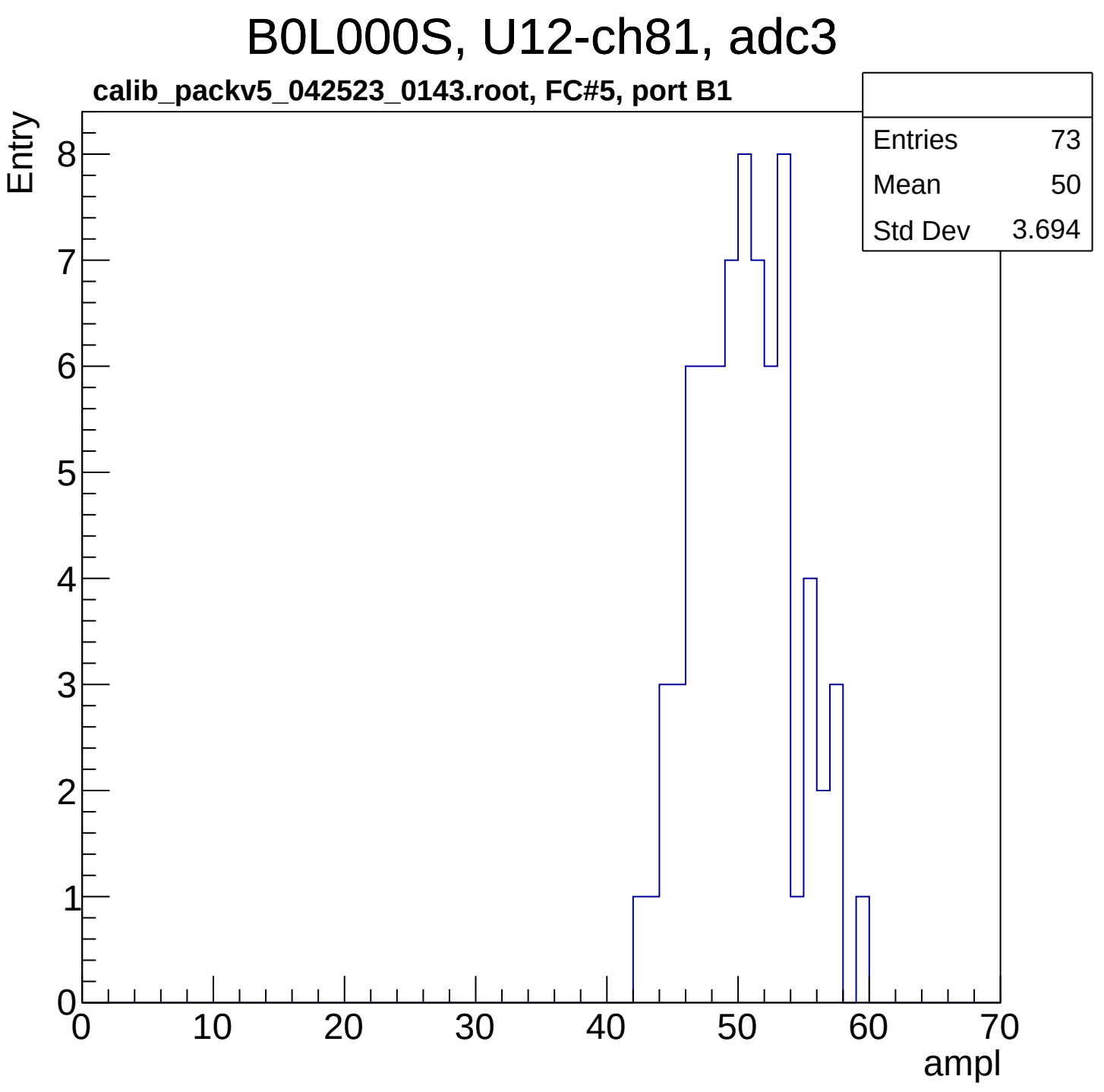

B0L000S, U12-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.91
Std Dev 3.331
Gaus mean : 44.8019
Gaus Width: 3.5152
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50
Std Dev 3.694
Entry
0
1
2
3
4
5
6
7
8
0
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20
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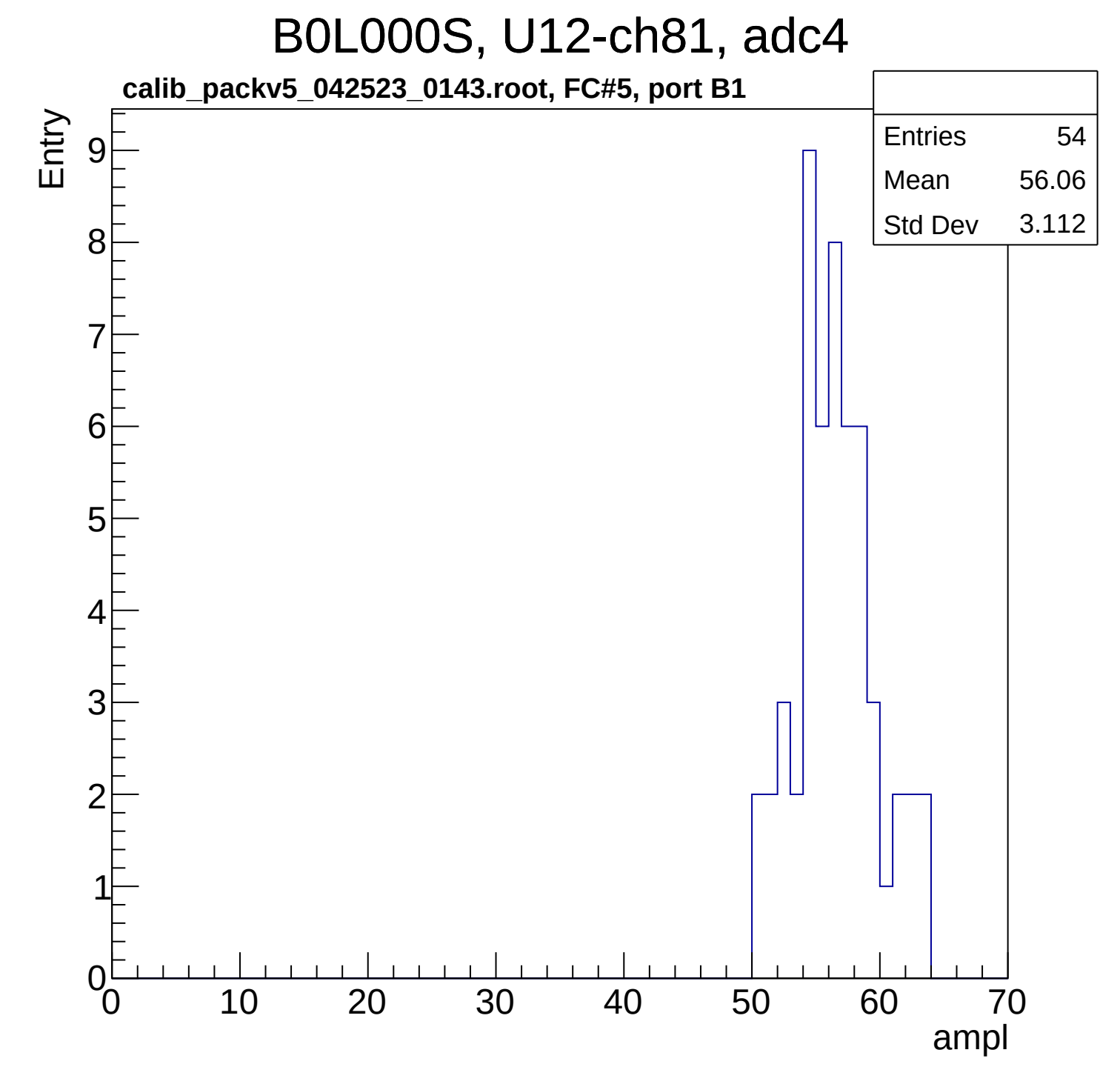
50
60
70
ampl


B0L000S, U12-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.06
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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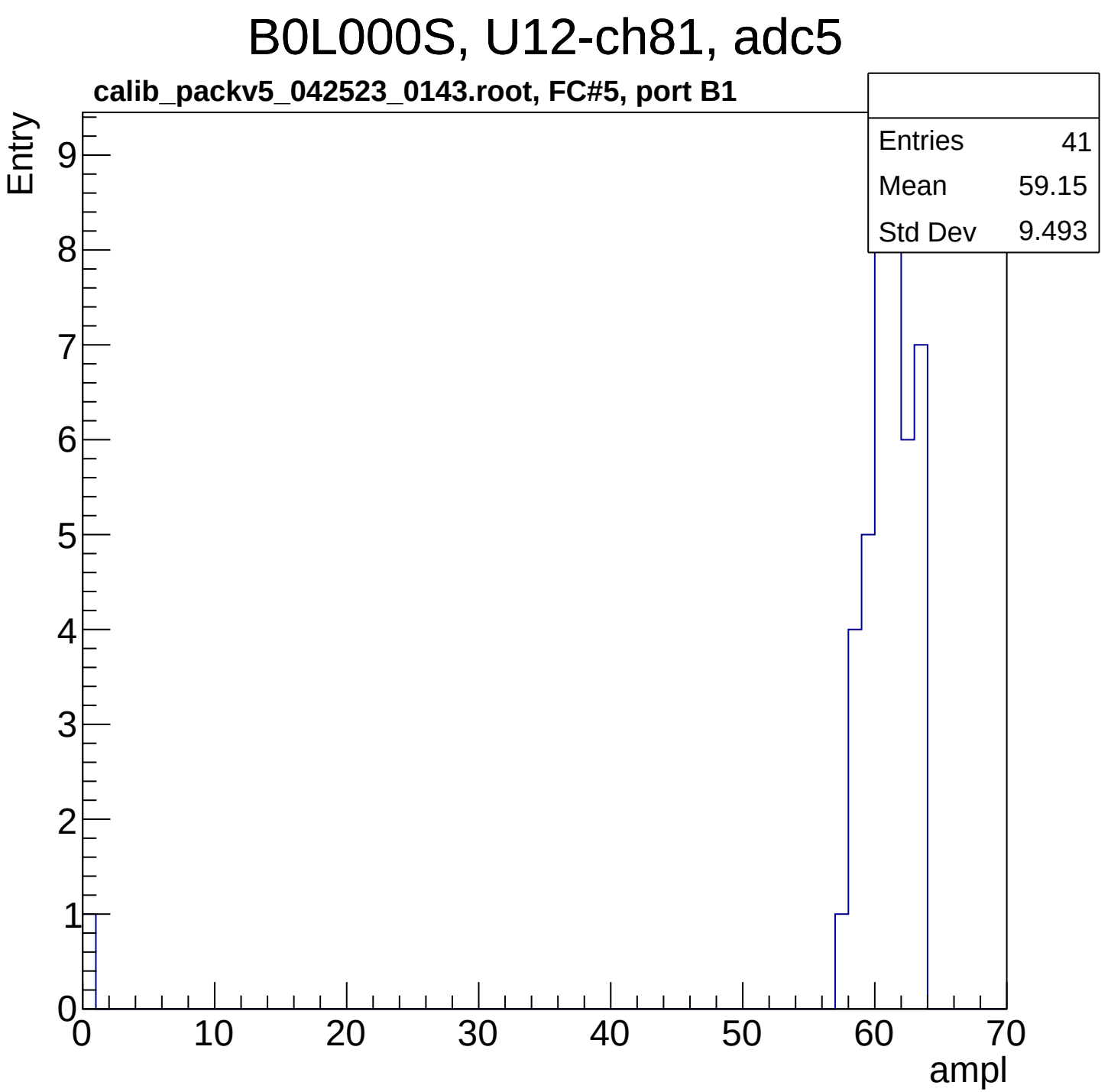
40
50
60
70


B0L000S, U12-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.15
Std Dev 9.493
Entry
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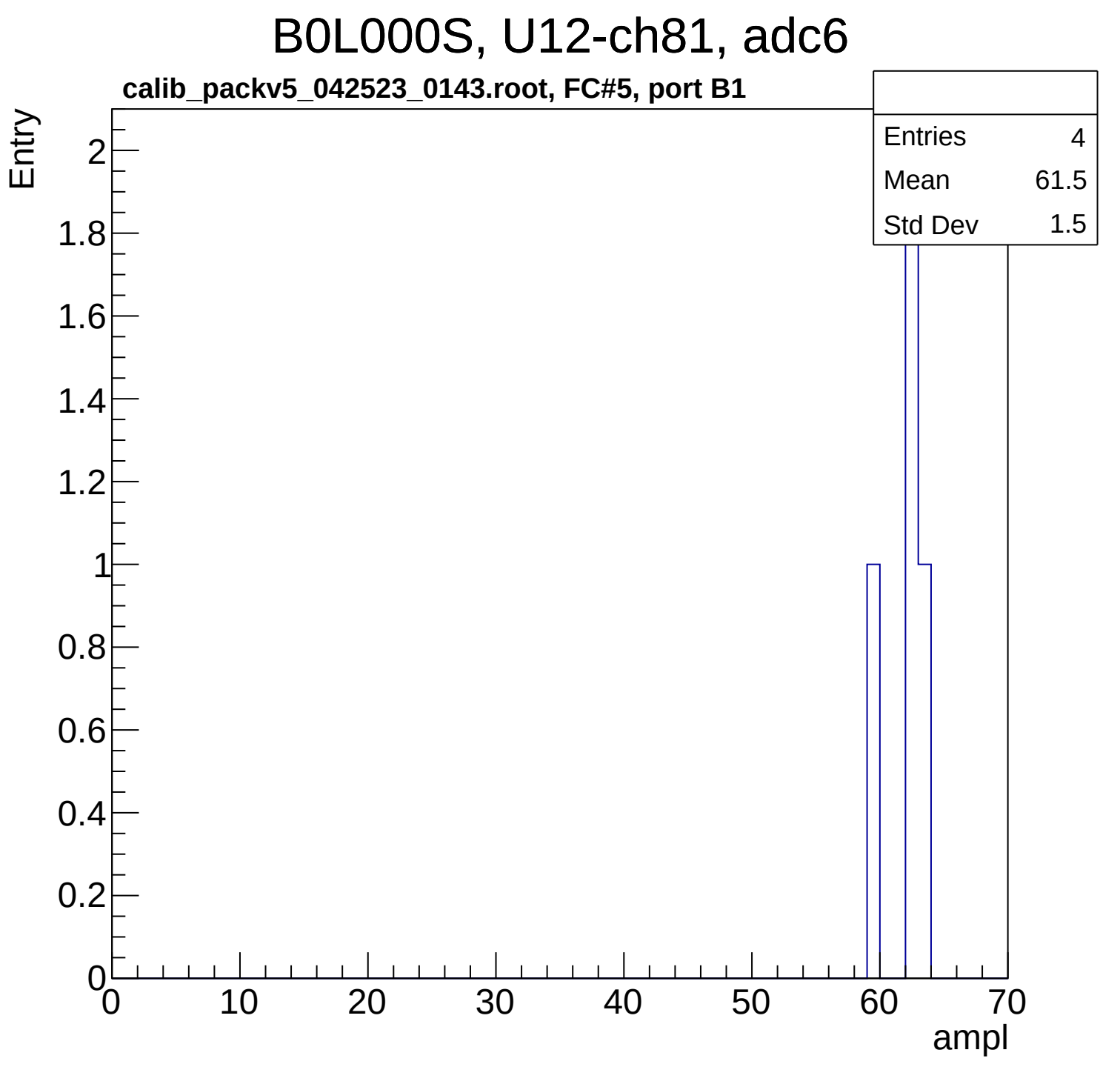
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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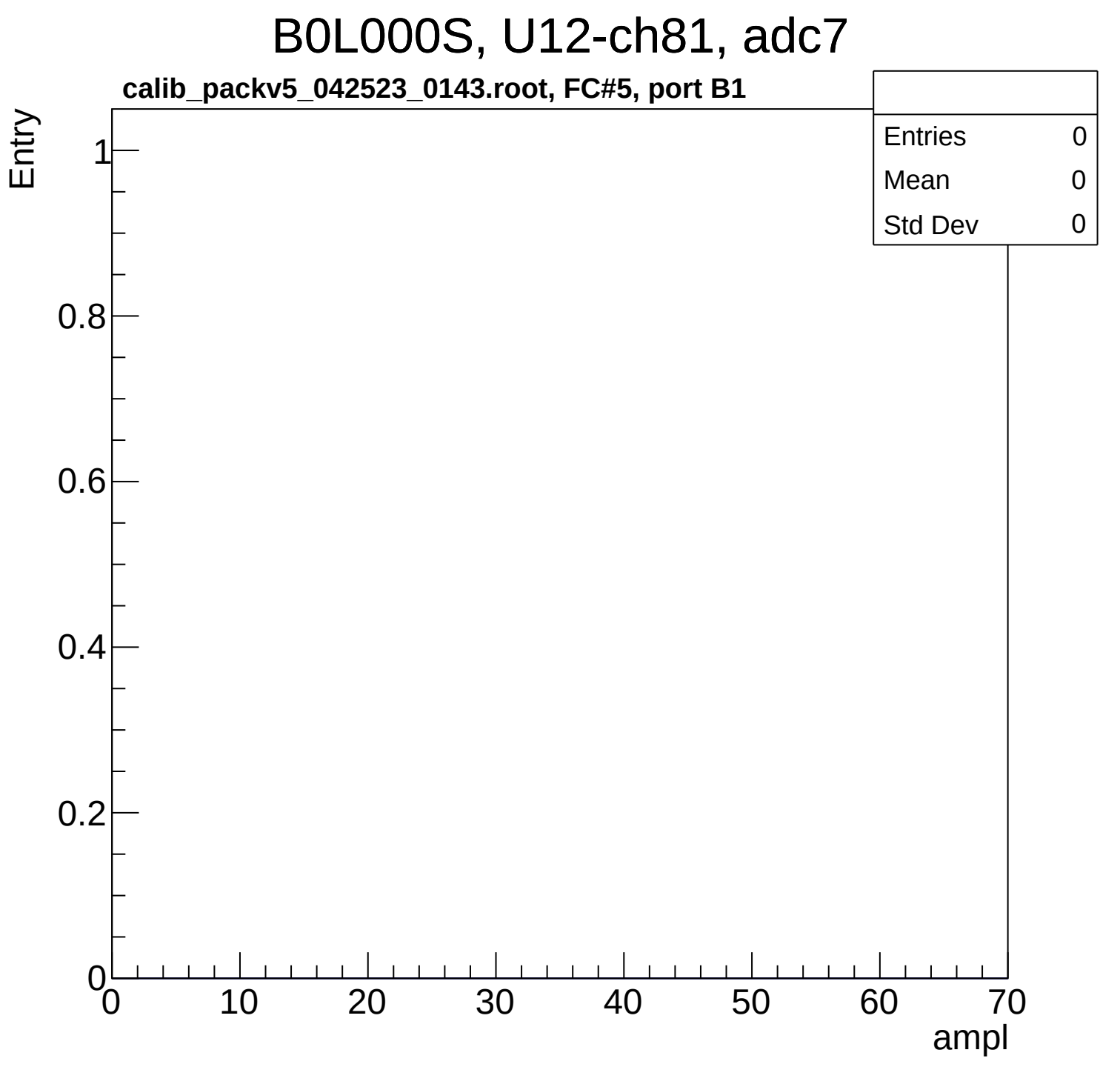
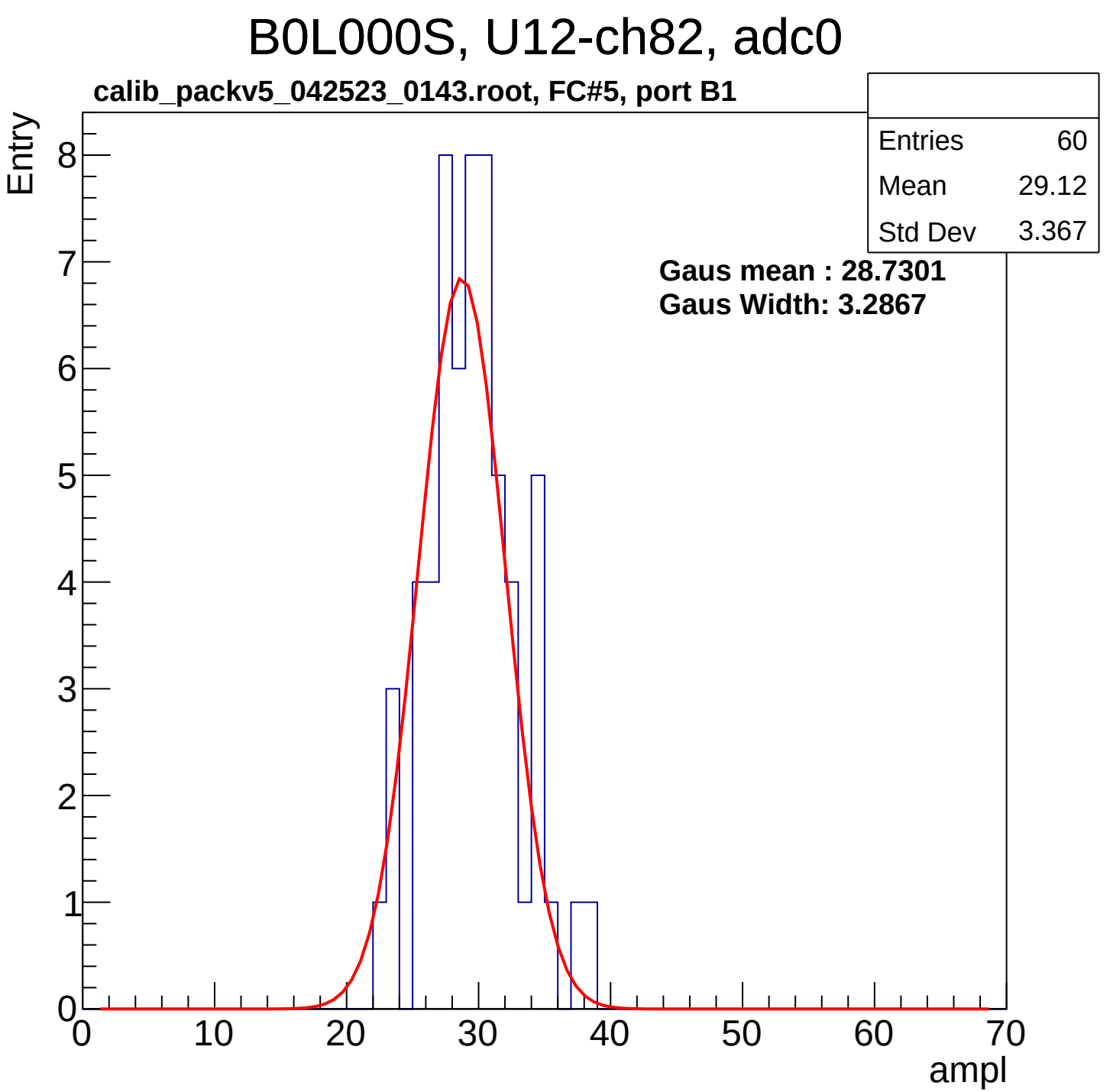
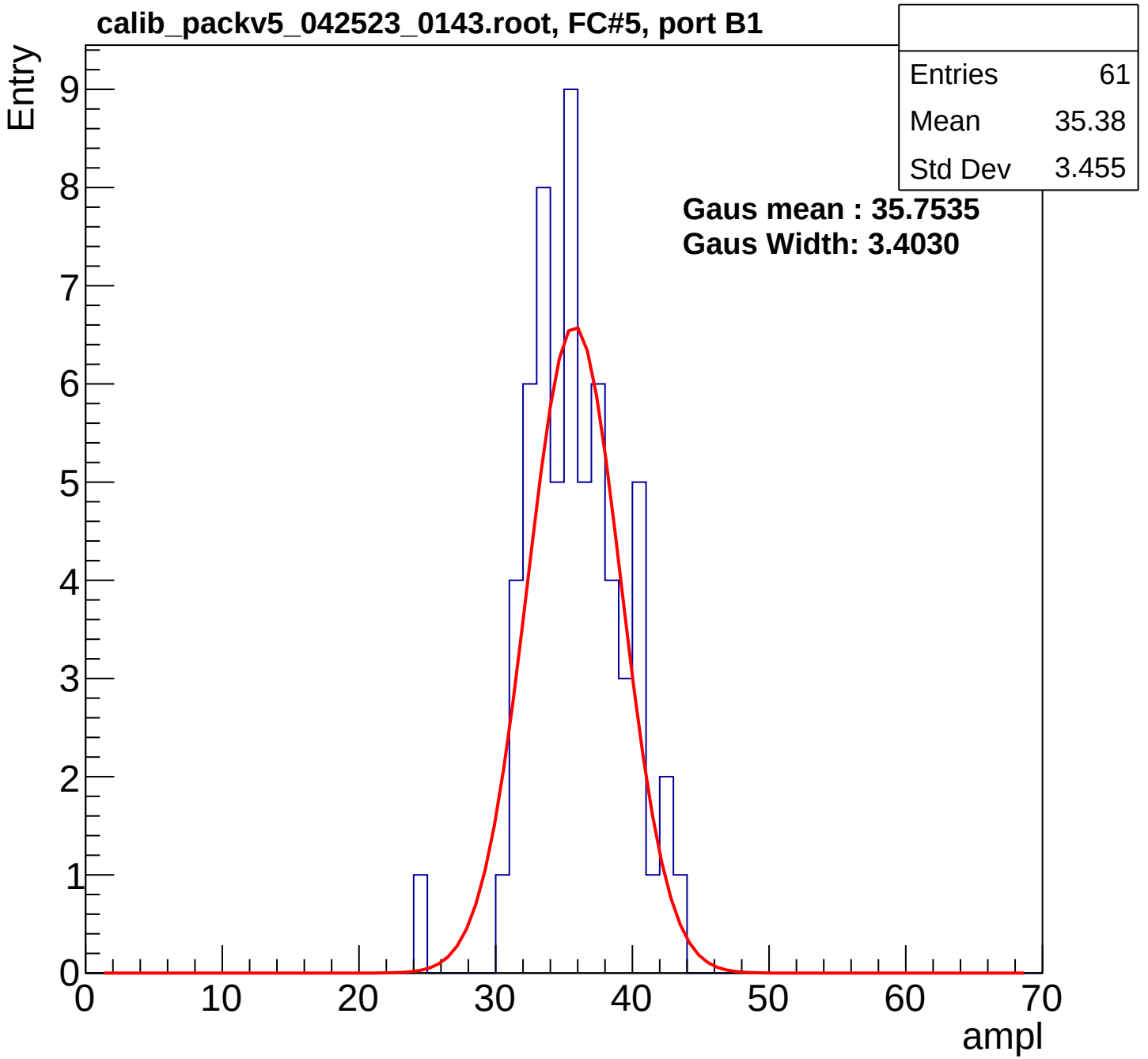

B0L000S, U12-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.12
Std Dev 3.367
Gaus mean : 28.7301
Gaus Width: 3.2867
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.38
Std Dev 3.455
Gaus mean : 35.7535
Gaus Width: 3.4030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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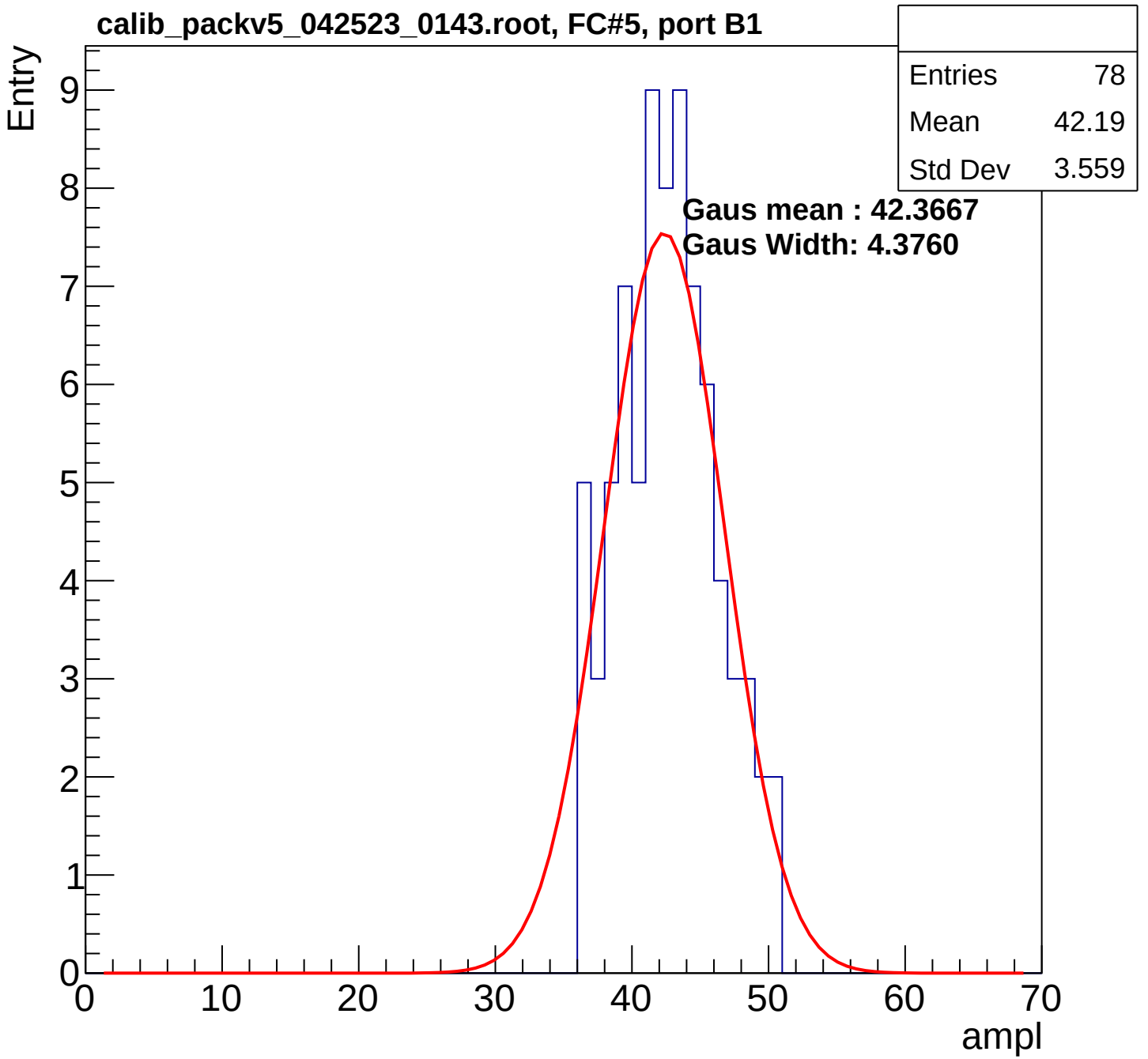
B0L000S, U12-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42.19
Std Dev 3.559
Gaus mean : 42.3667
Gaus Width: 4.3760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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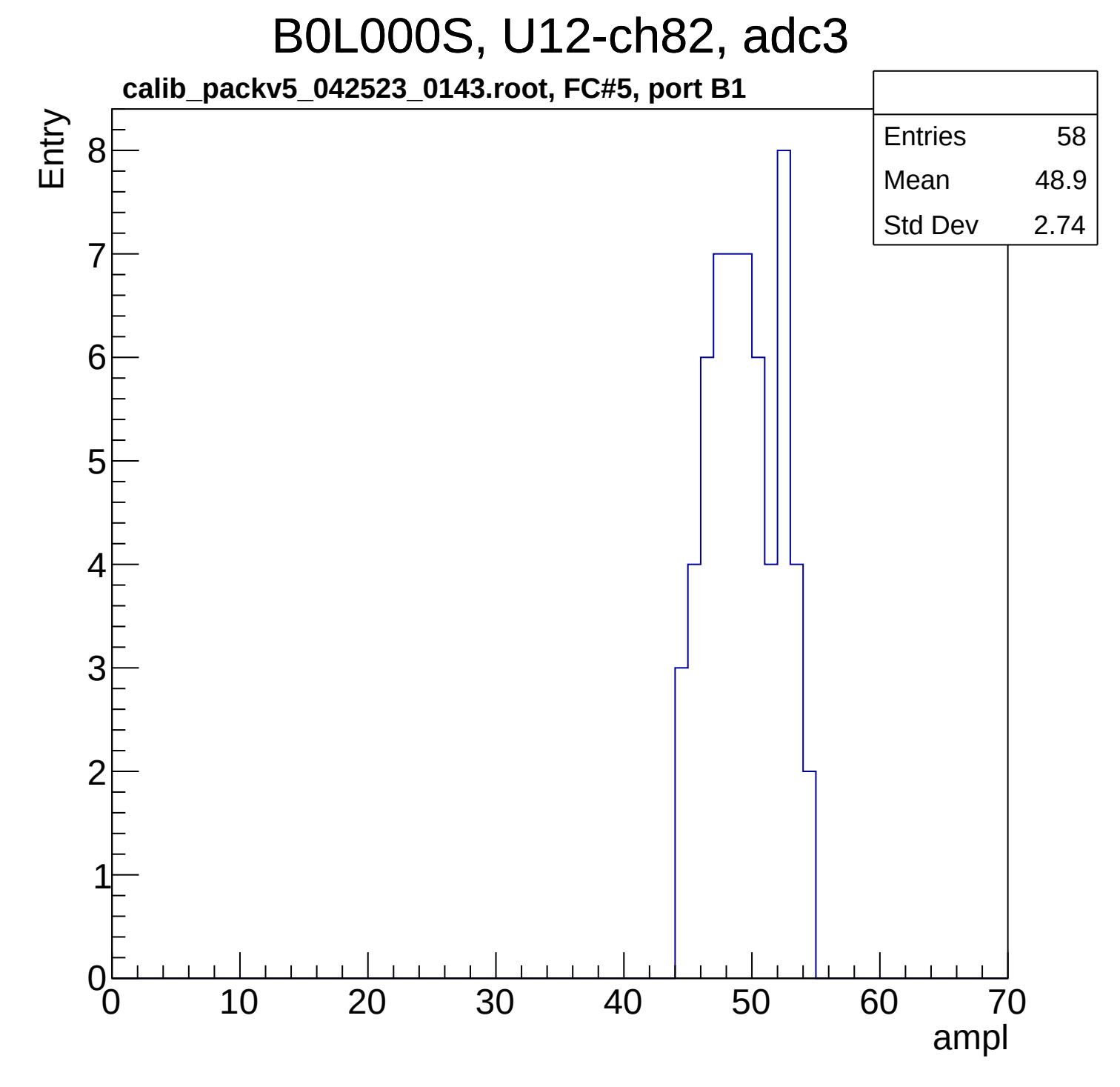
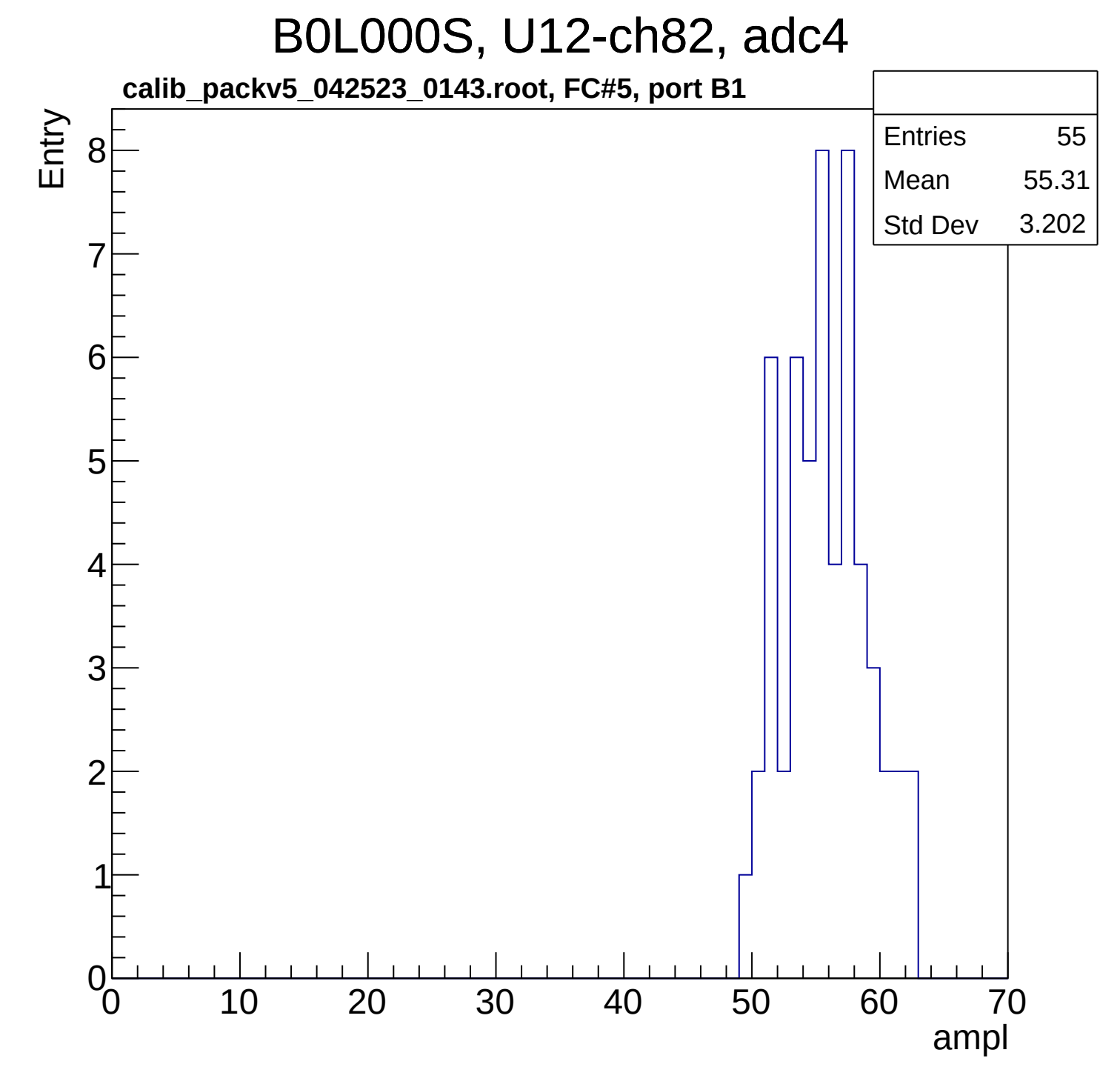
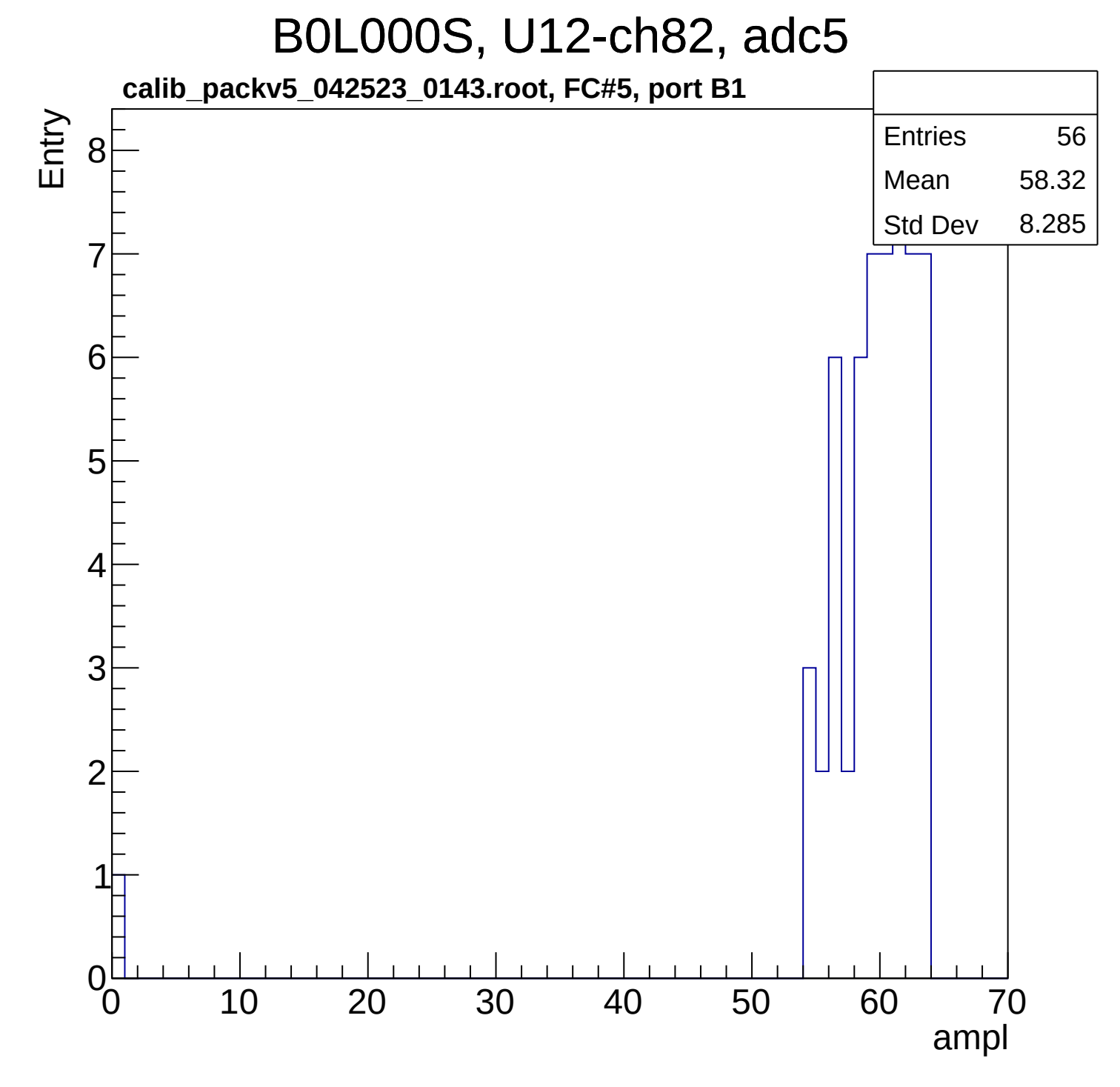

B0L000S, U12-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.9
Std Dev 2.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.31
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.32
Std Dev 8.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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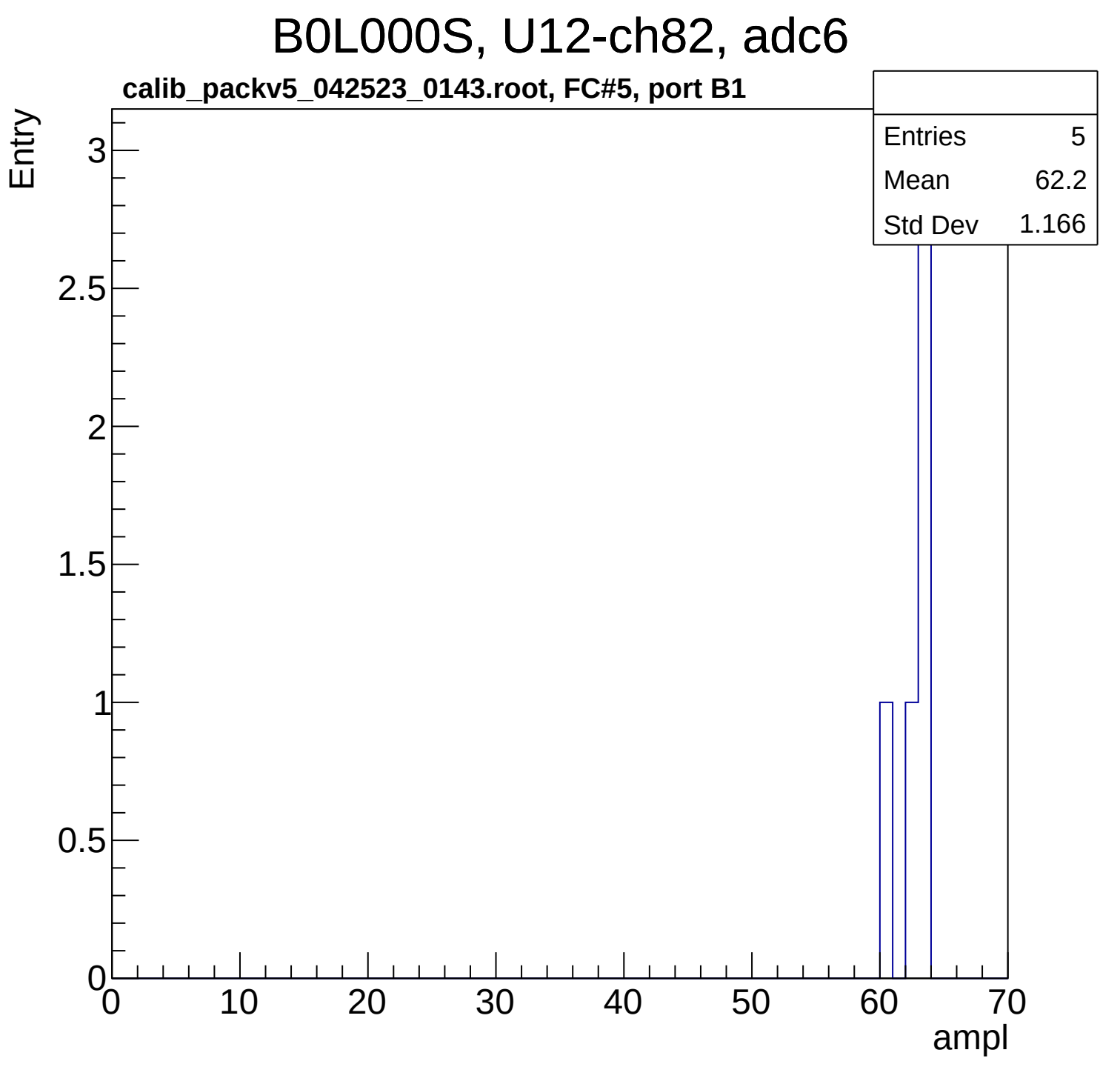
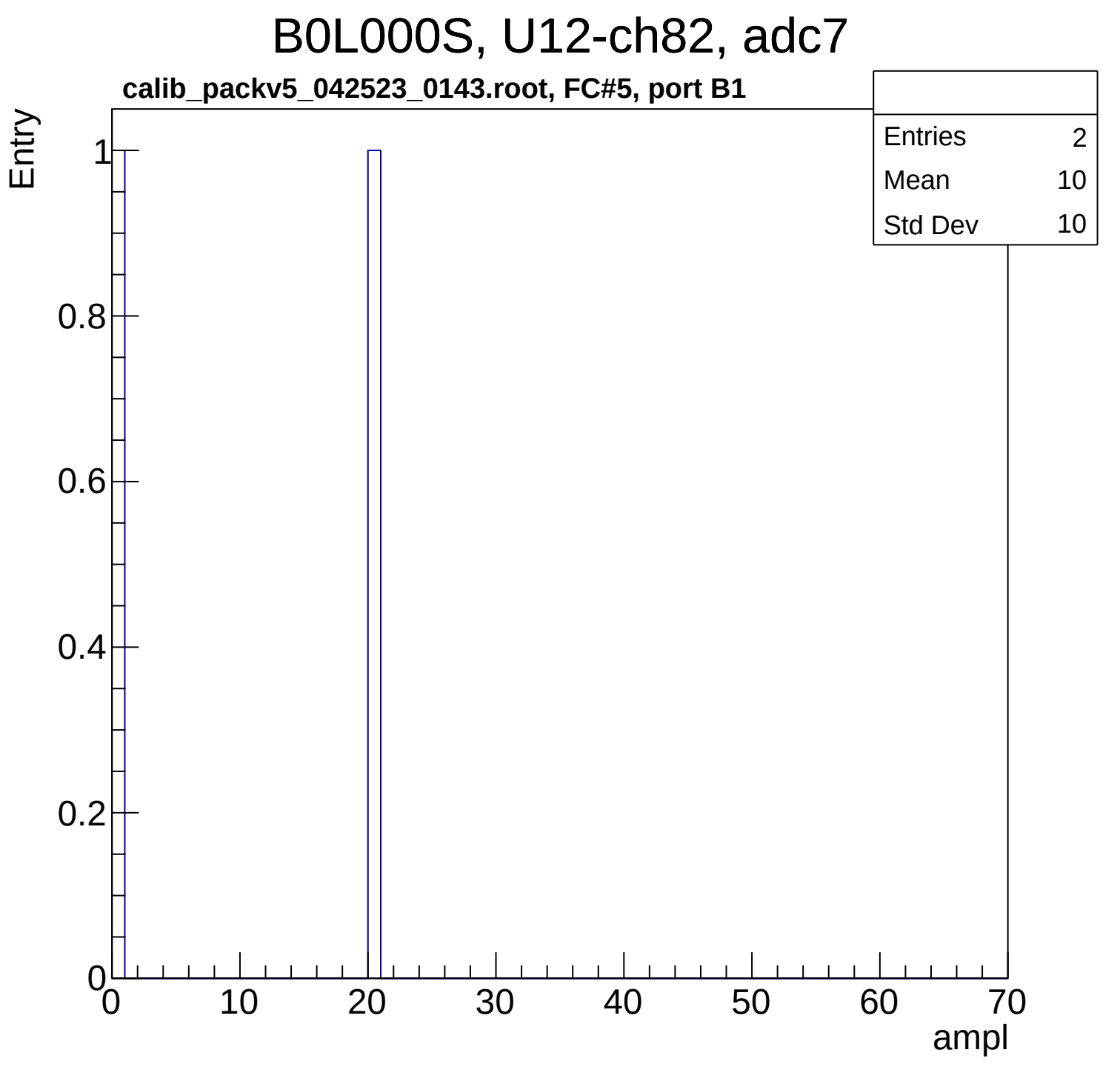
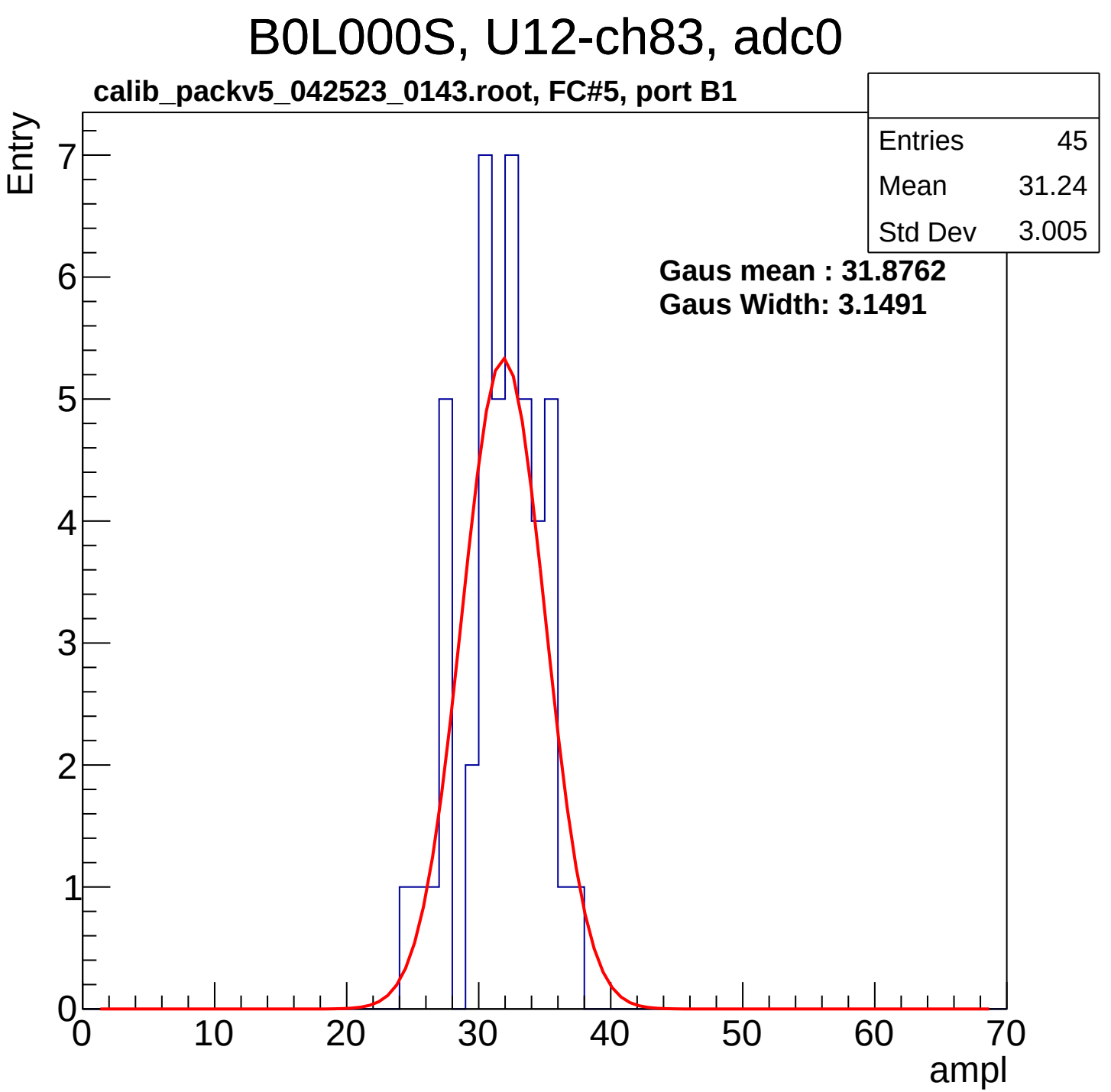
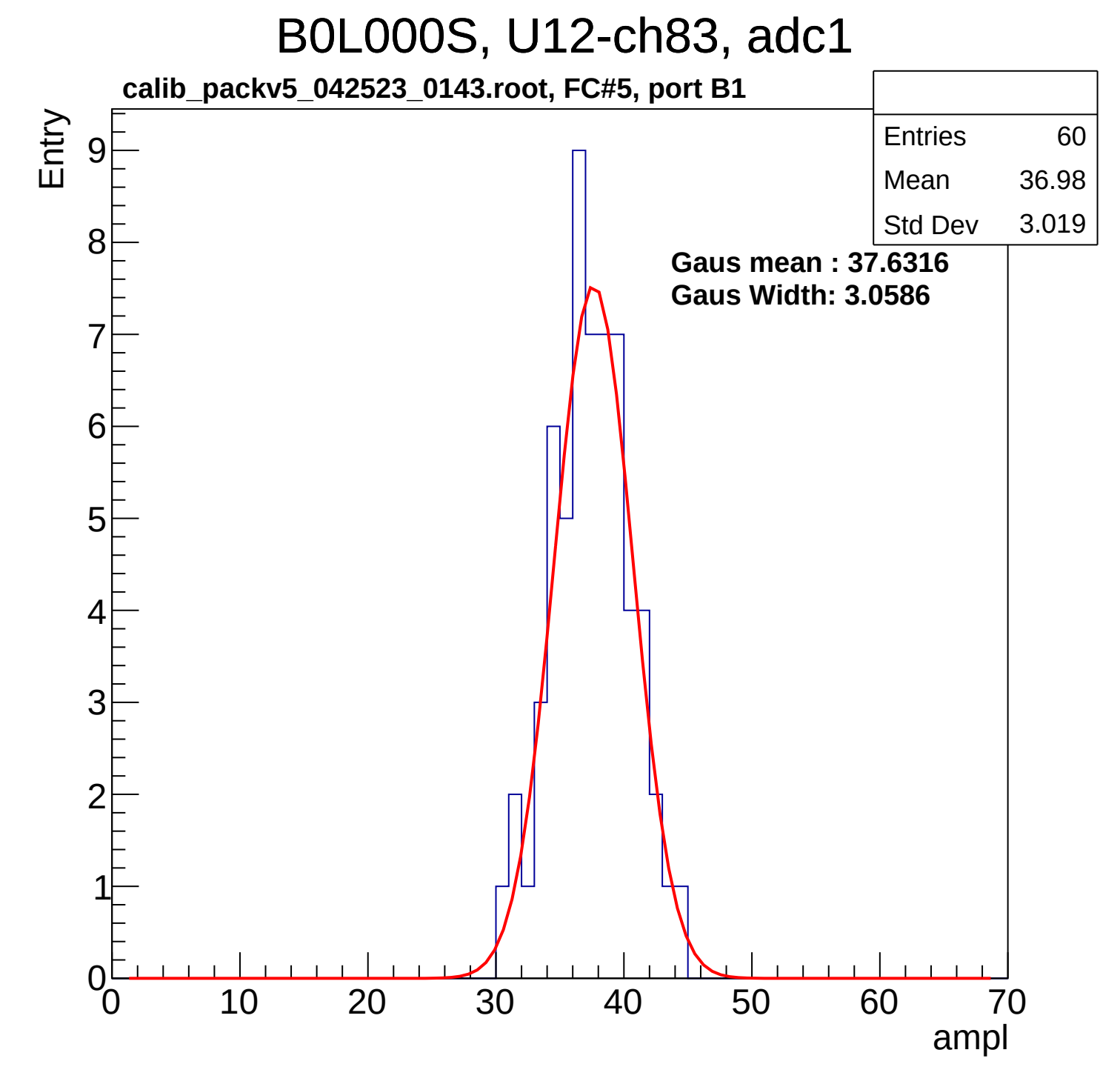
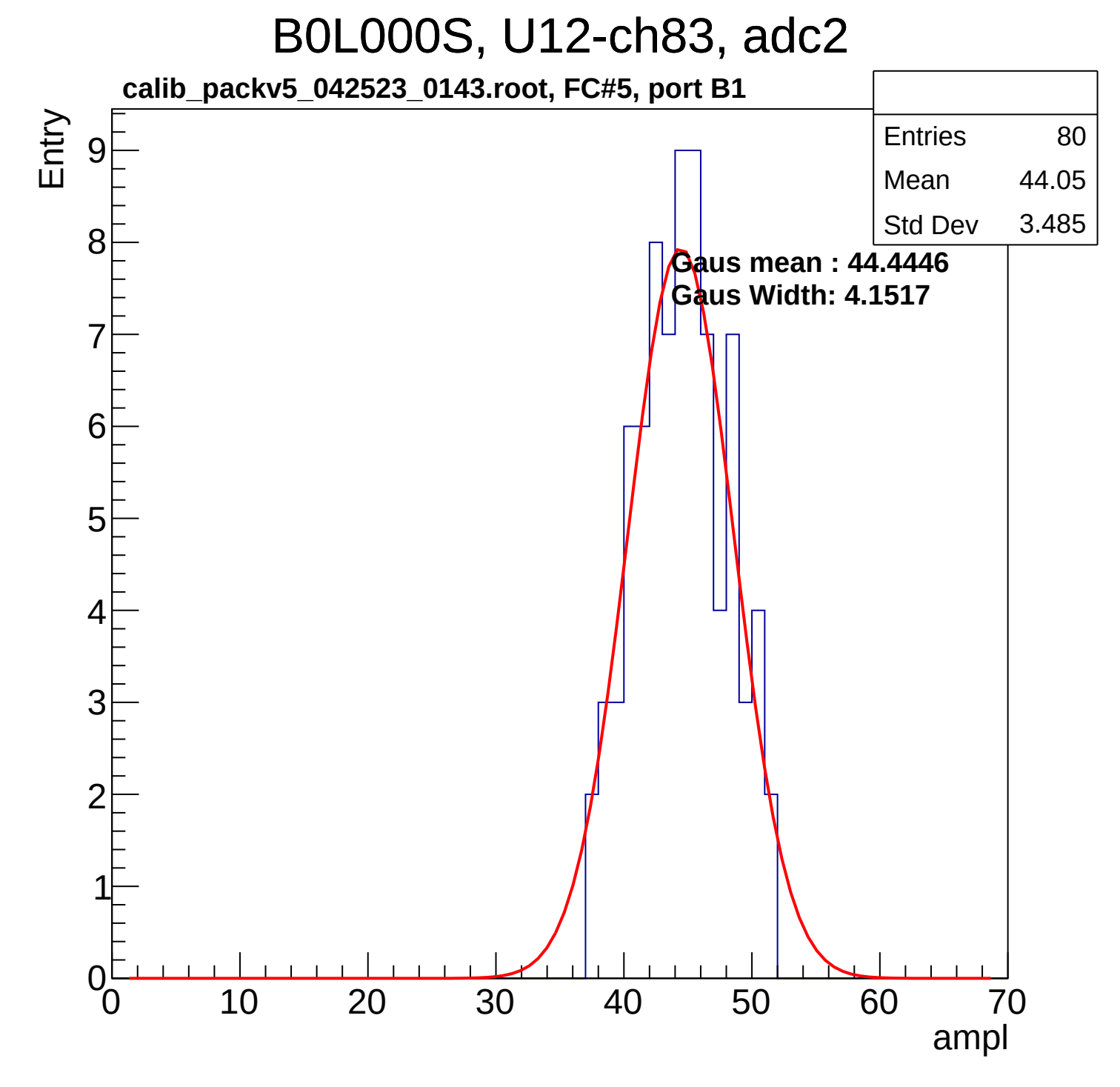
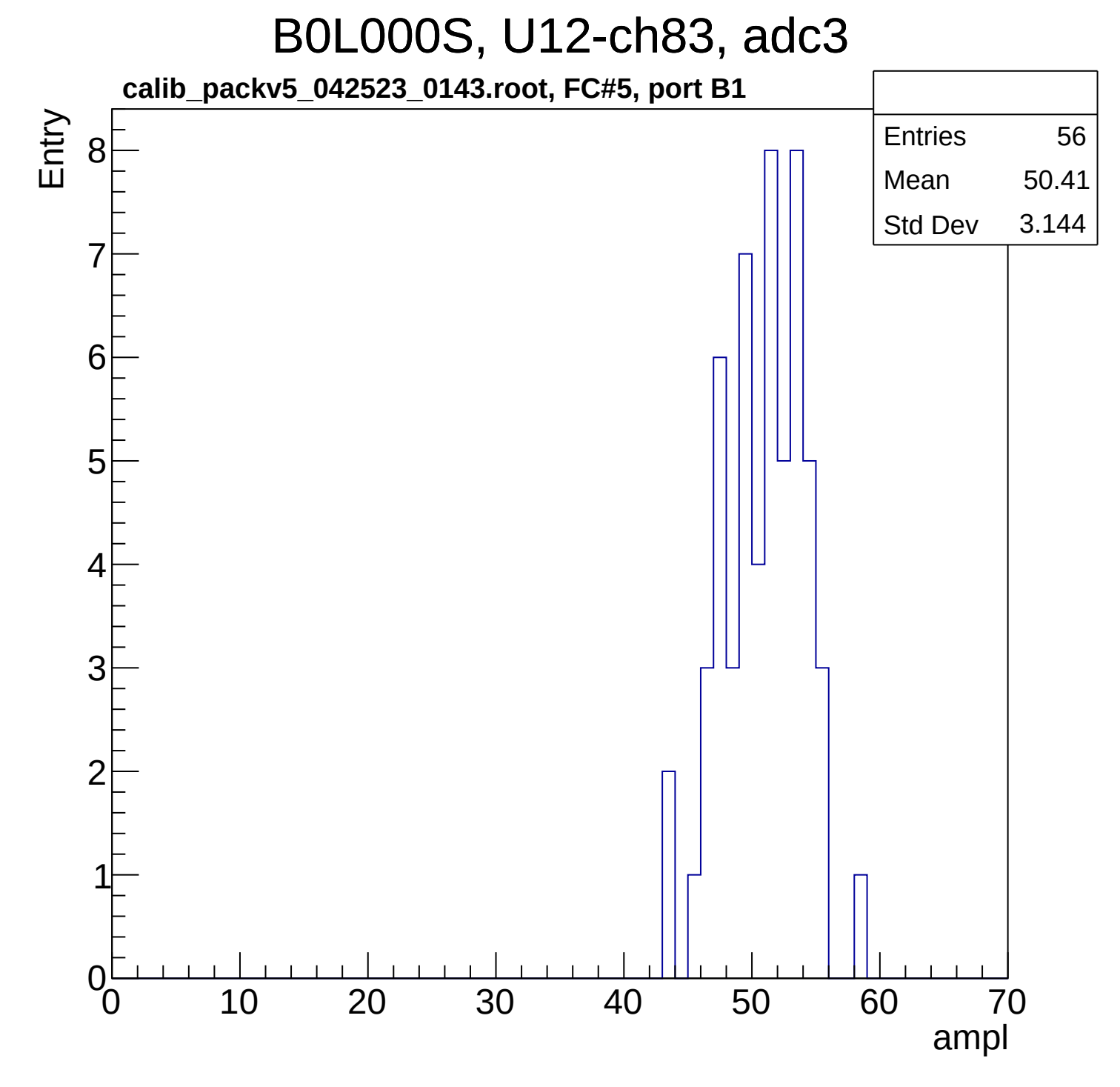

B0L000S, U12-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 31.24
Std Dev 3.005
Gaus mean : 31.8762
Gaus Width: 3.1491
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.98
Std Dev 3.019
Gaus mean : 37.6316
Gaus Width: 3.0586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 44.05
Std Dev 3.485
Gaus mean : 44.4446
Gaus Width: 4.1517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 50.41
Std Dev 3.144
Entry
ampl
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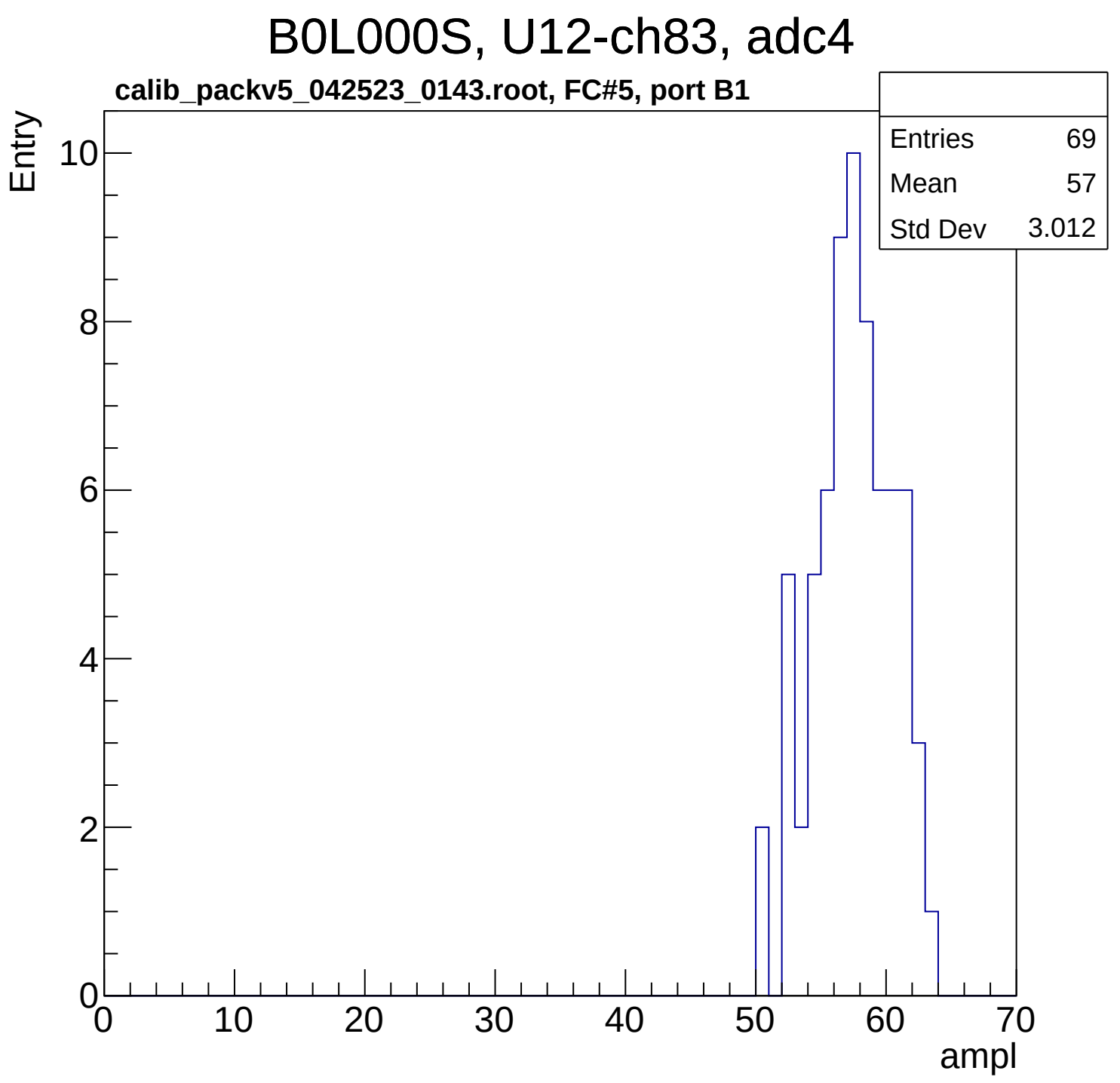
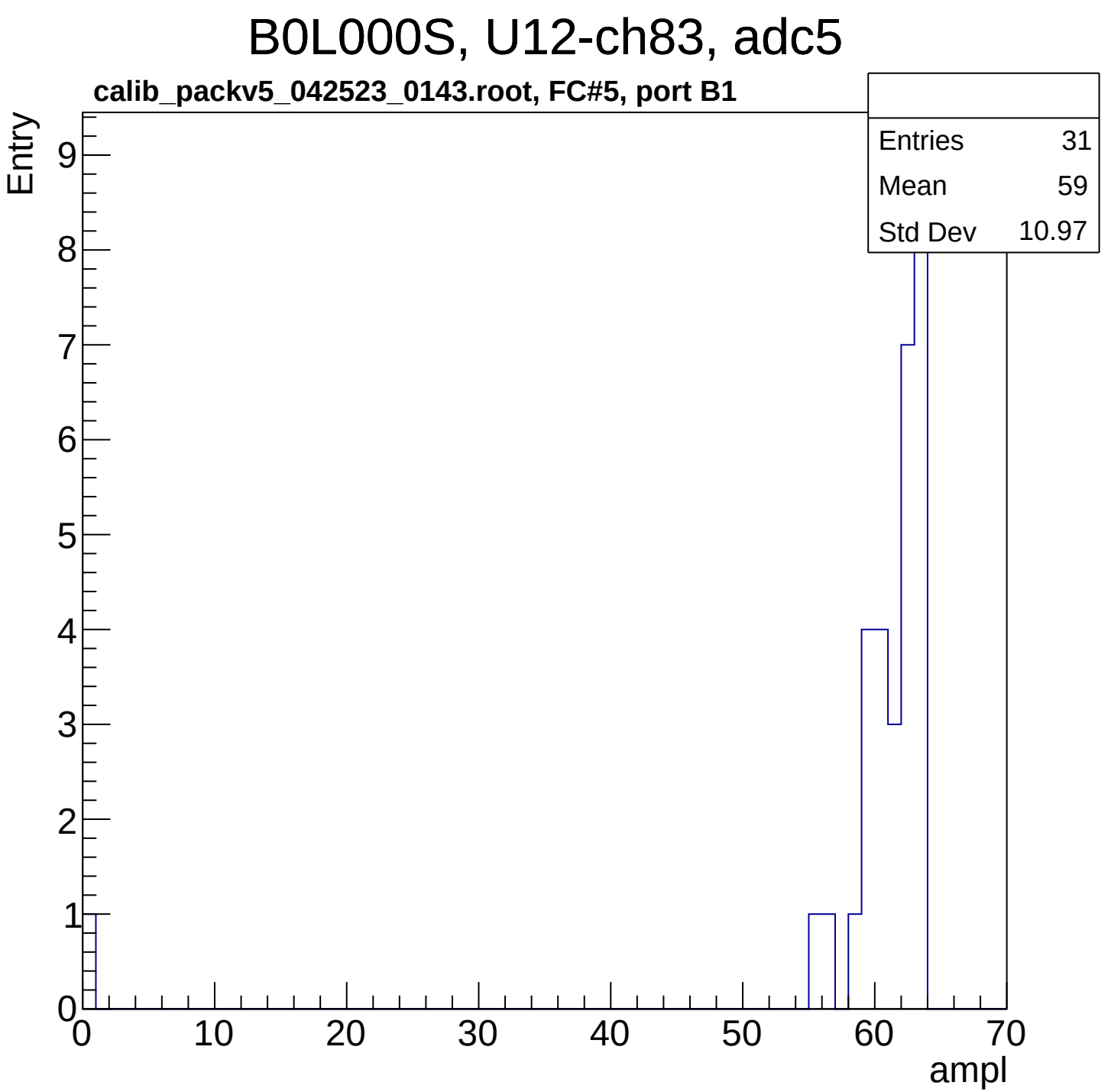
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 57
Std Dev 3.012
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59
Std Dev 10.97
Entry
ampl
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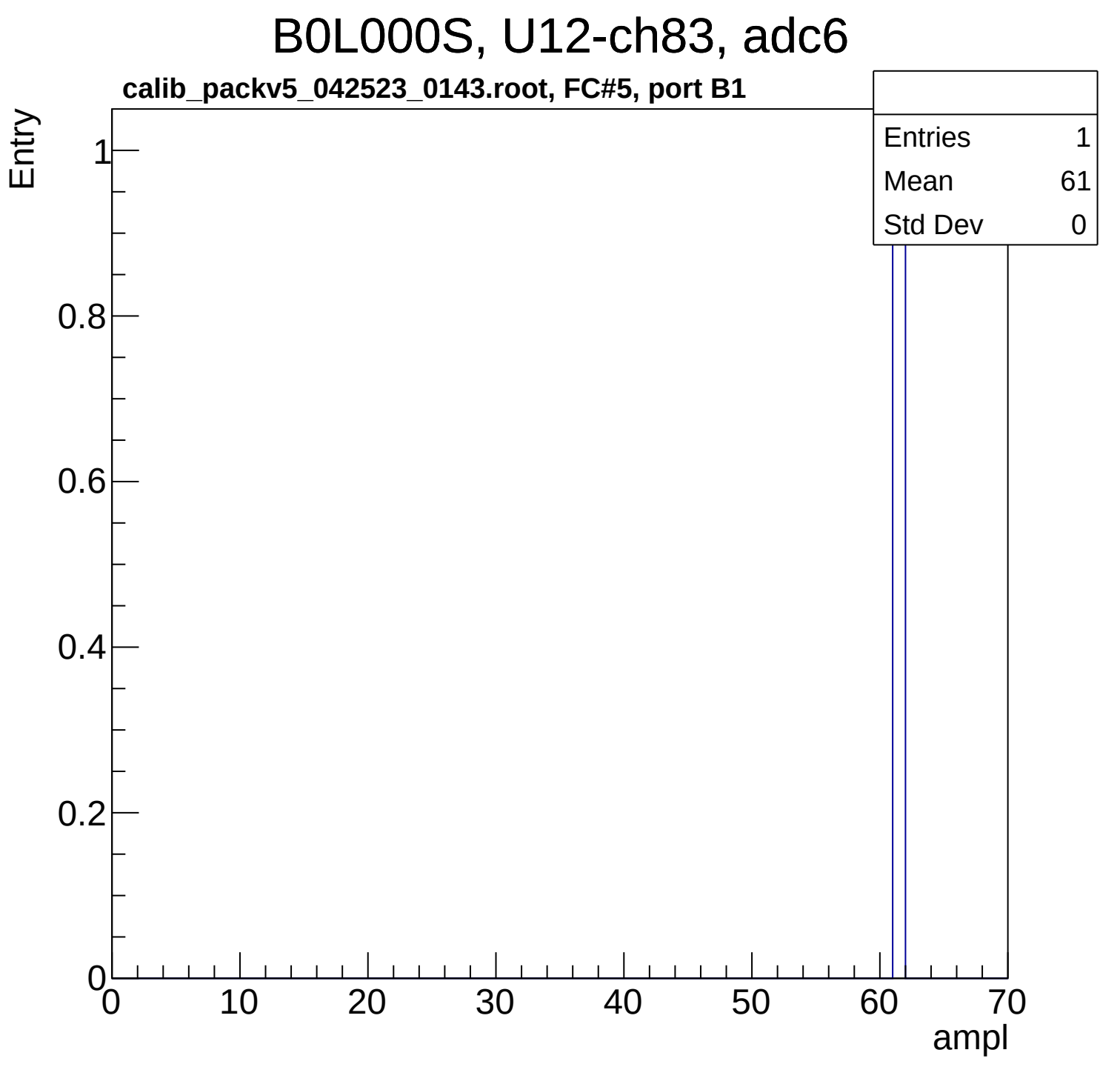
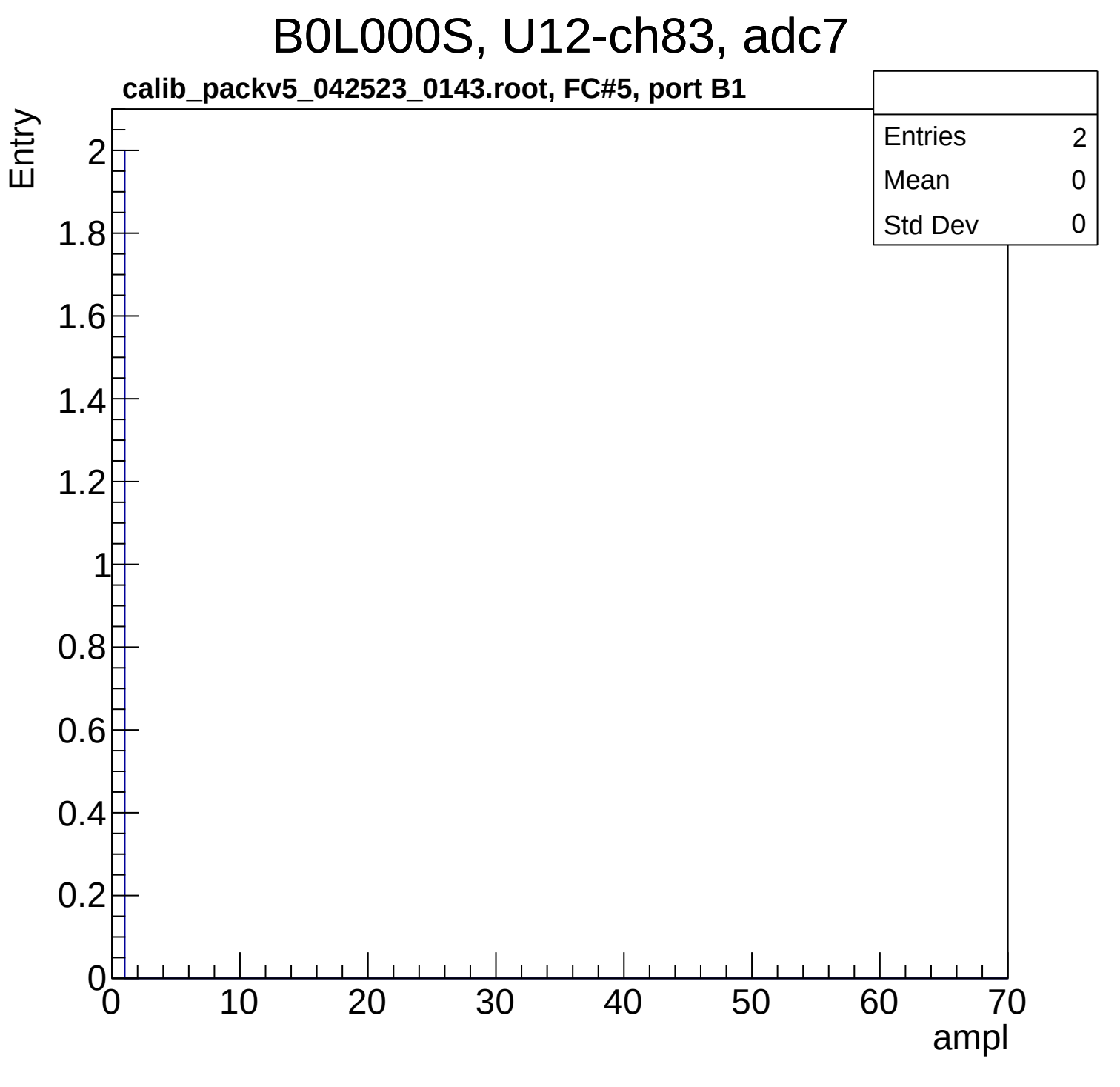
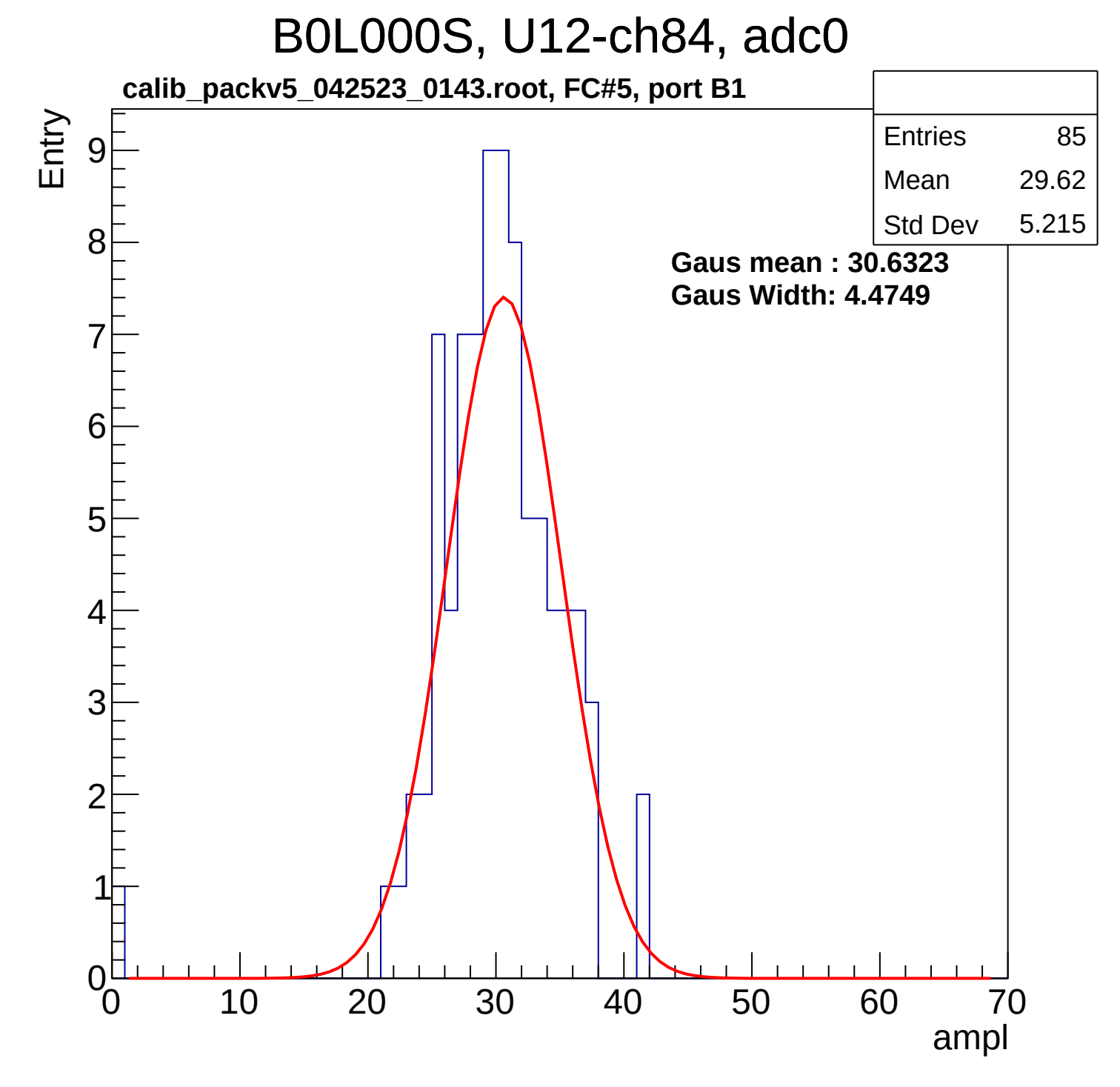
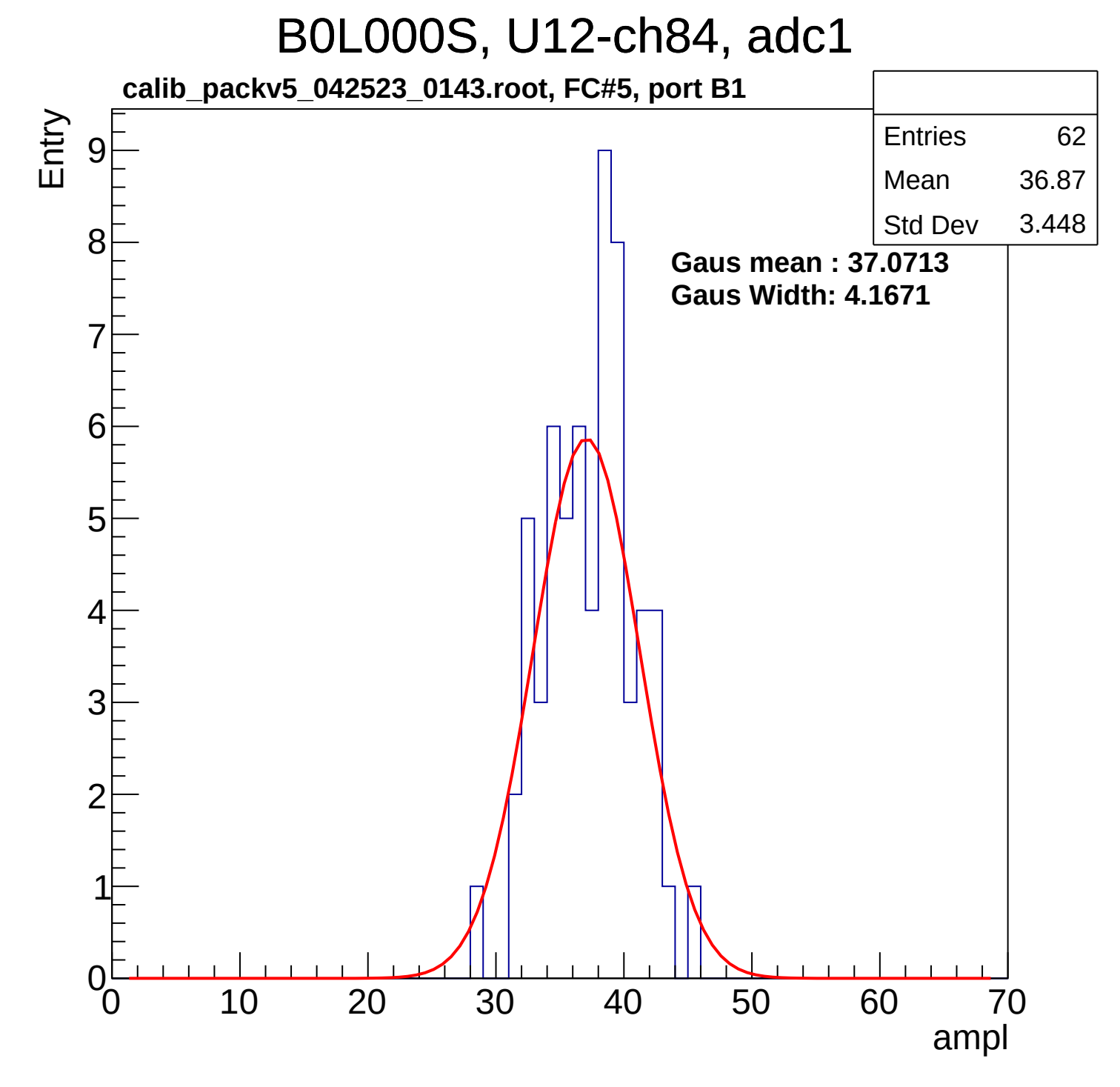
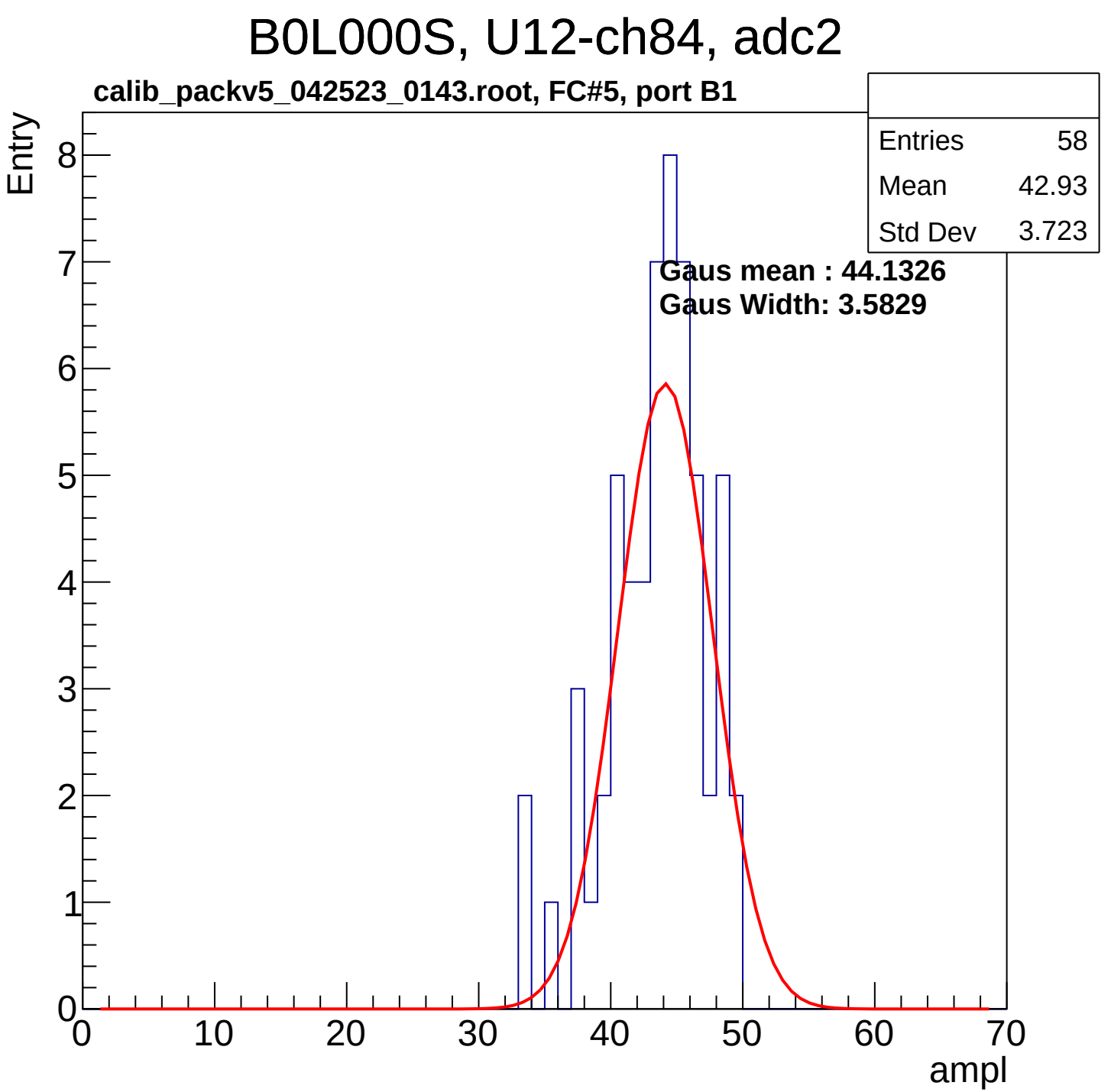
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 29.62
Std Dev 5.215
Gaus mean : 30.6323
Gaus Width: 4.4749
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.87
Std Dev 3.448
Gaus mean : 37.0713
Gaus Width: 4.1671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.93
Std Dev 3.723
Gaus mean : 44.1326
Gaus Width: 3.5829
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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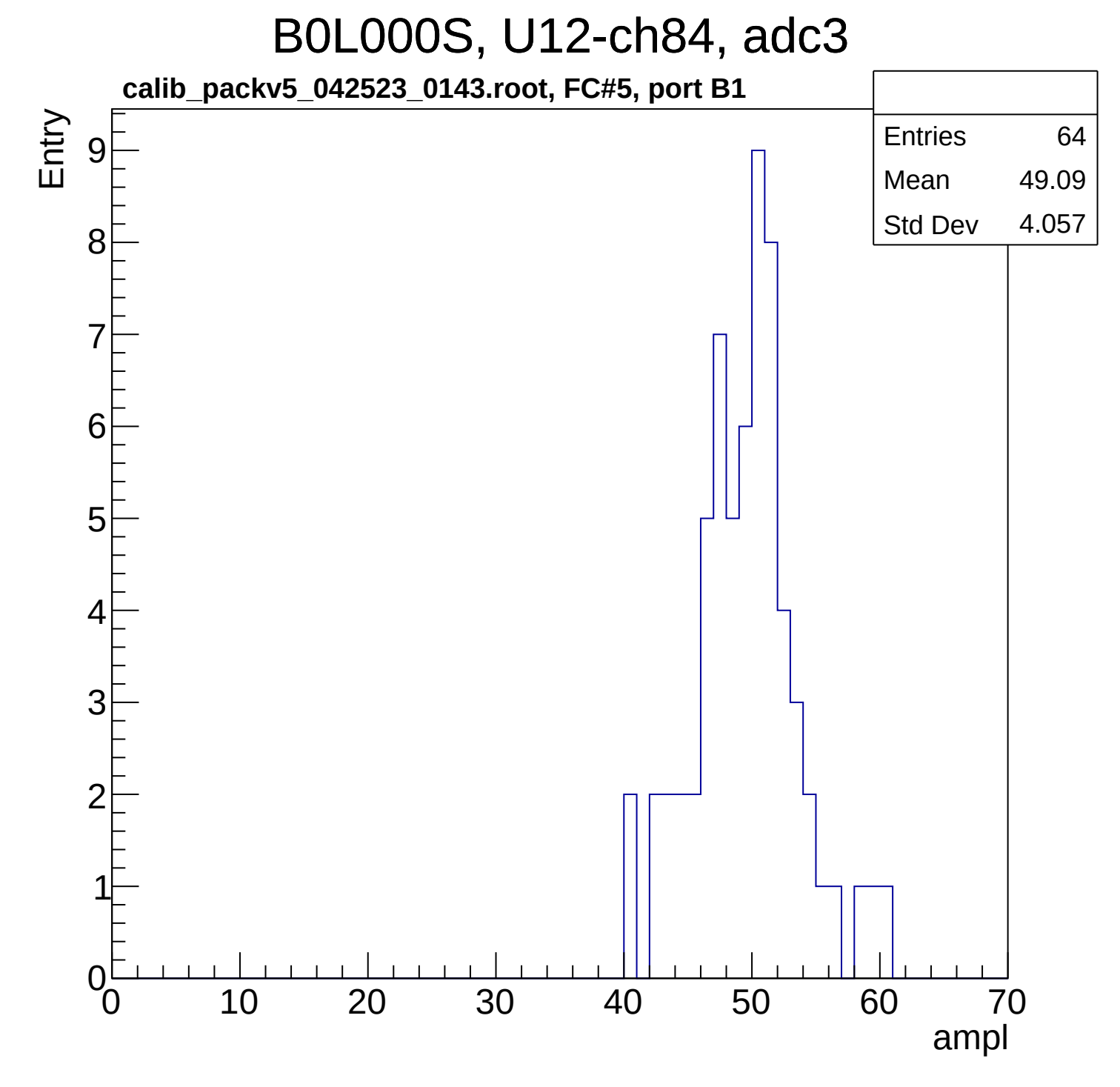
B0L000S, U12-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.09
Std Dev 4.057
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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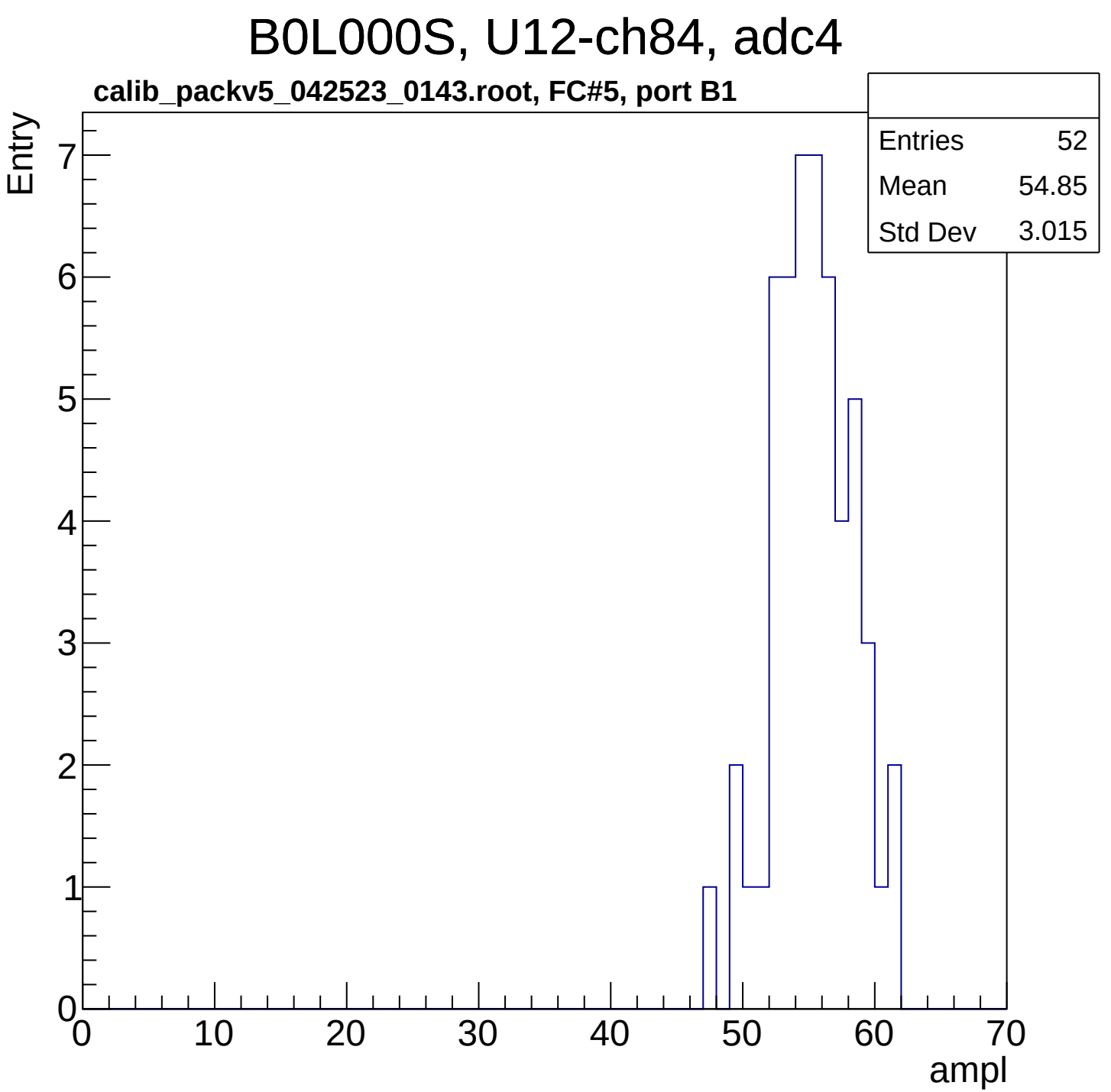
B0L000S, U12-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.85
Std Dev 3.015
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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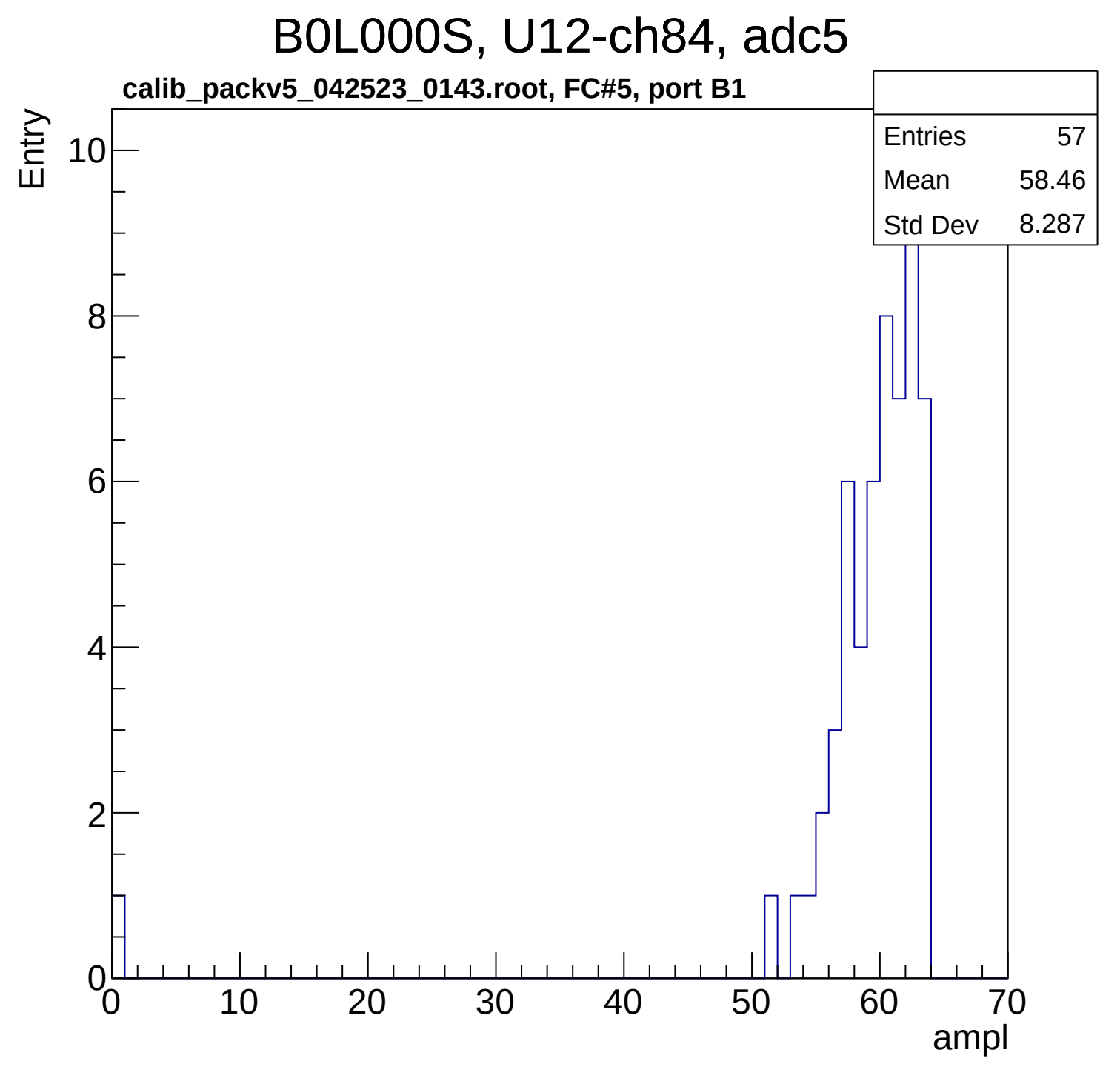
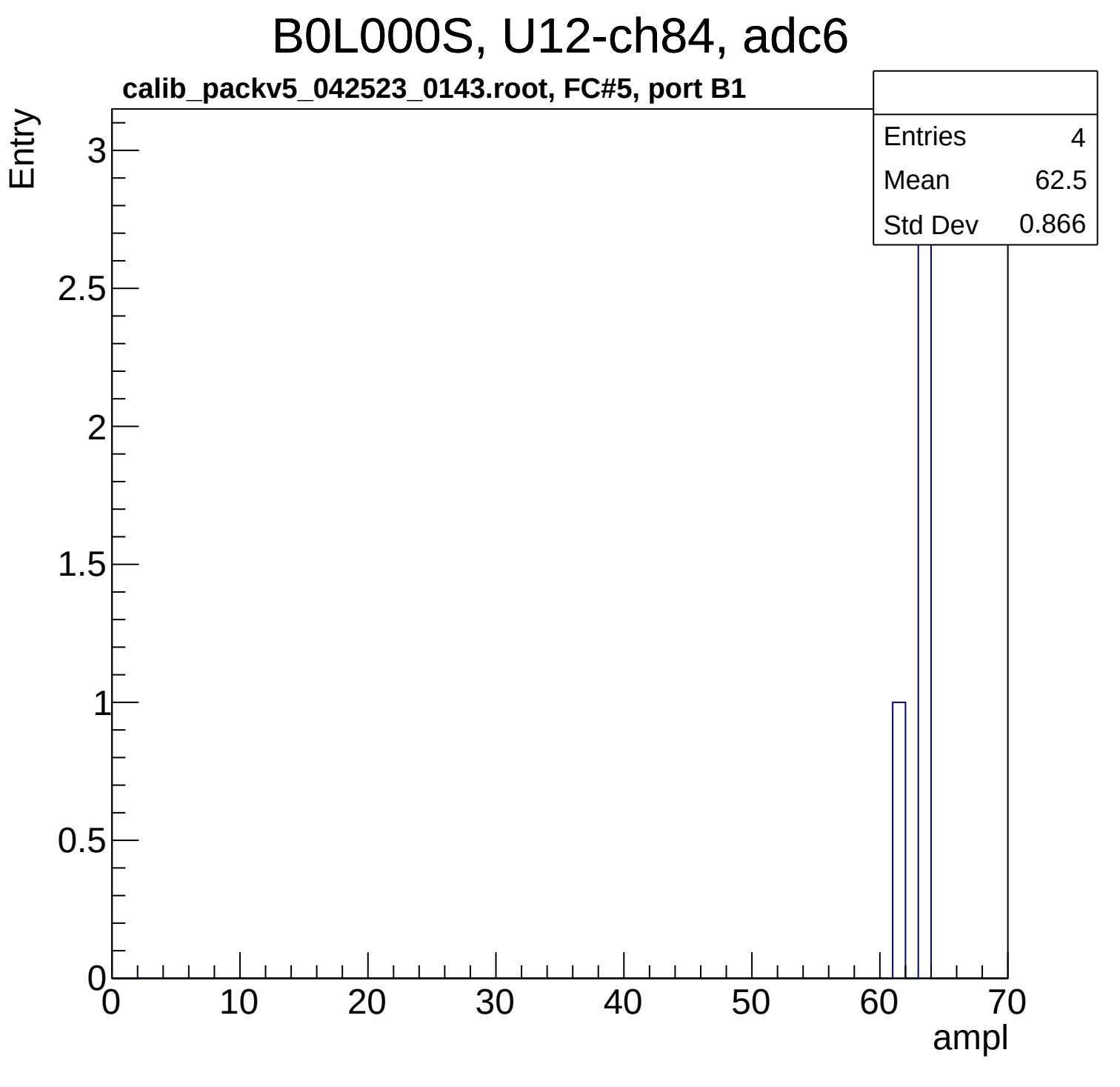
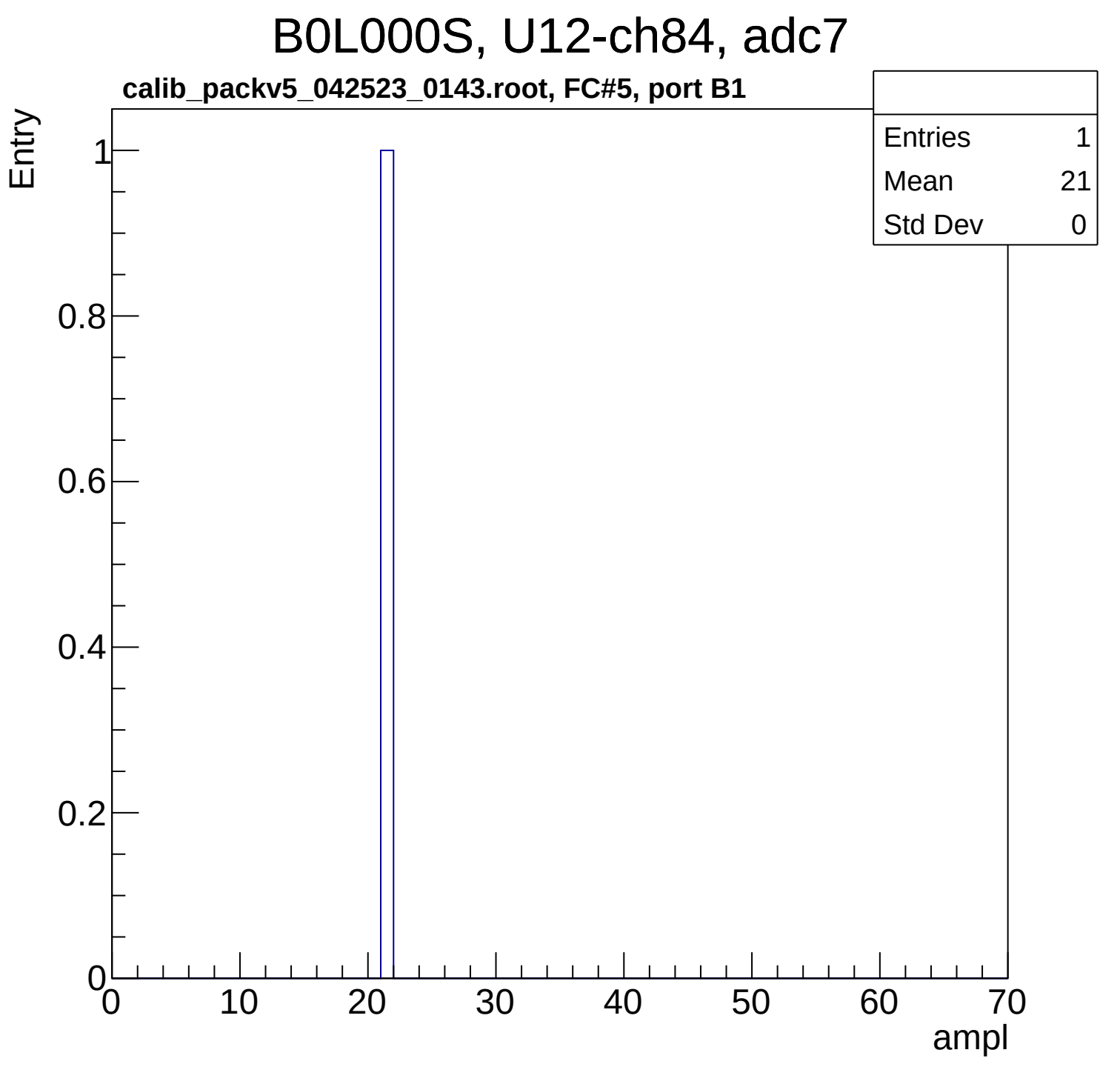

B0L000S, U12-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.46
Std Dev 8.287
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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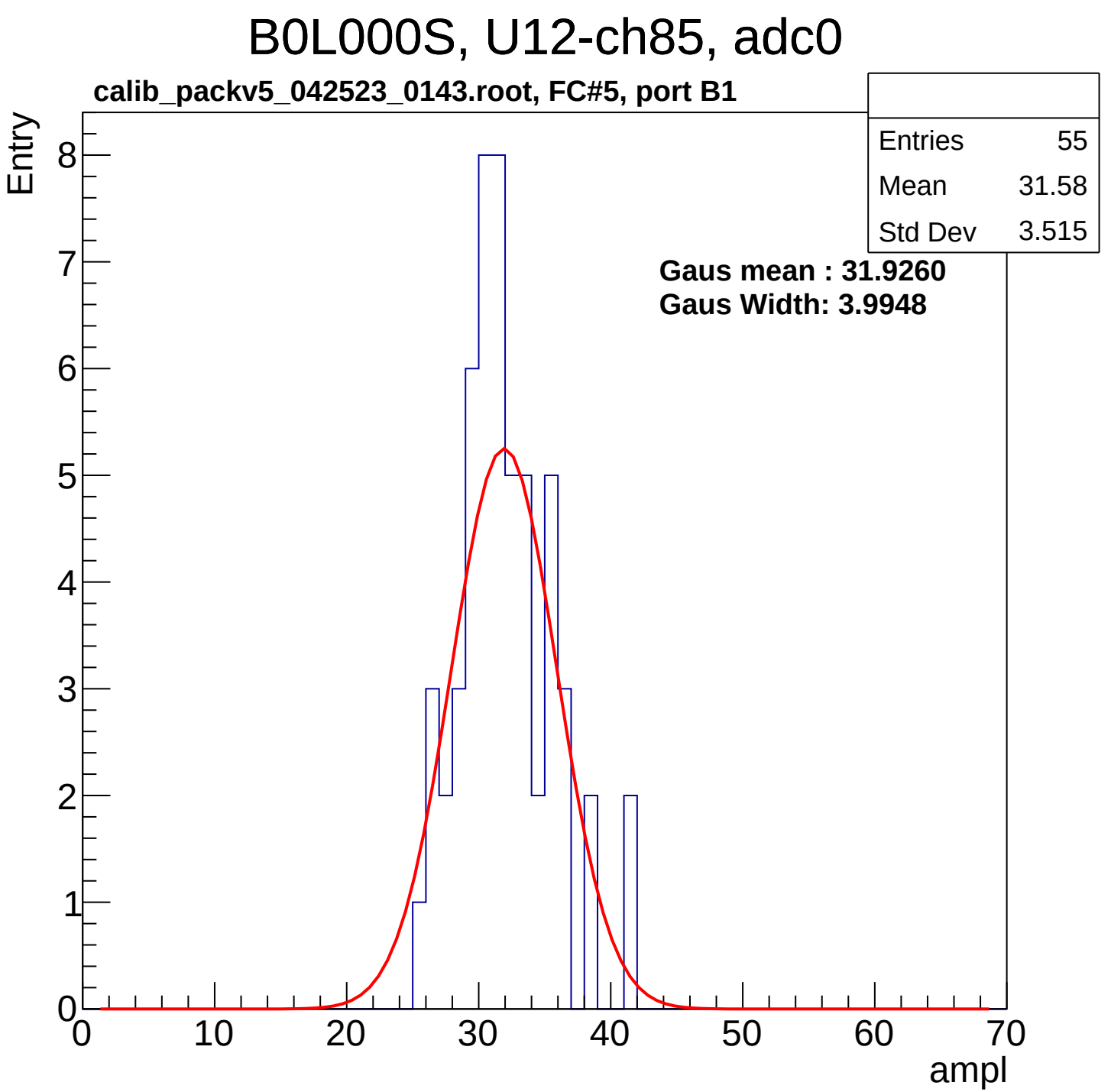
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 31.58
Std Dev 3.515
Gaus mean : 31.9260
Gaus Width: 3.9948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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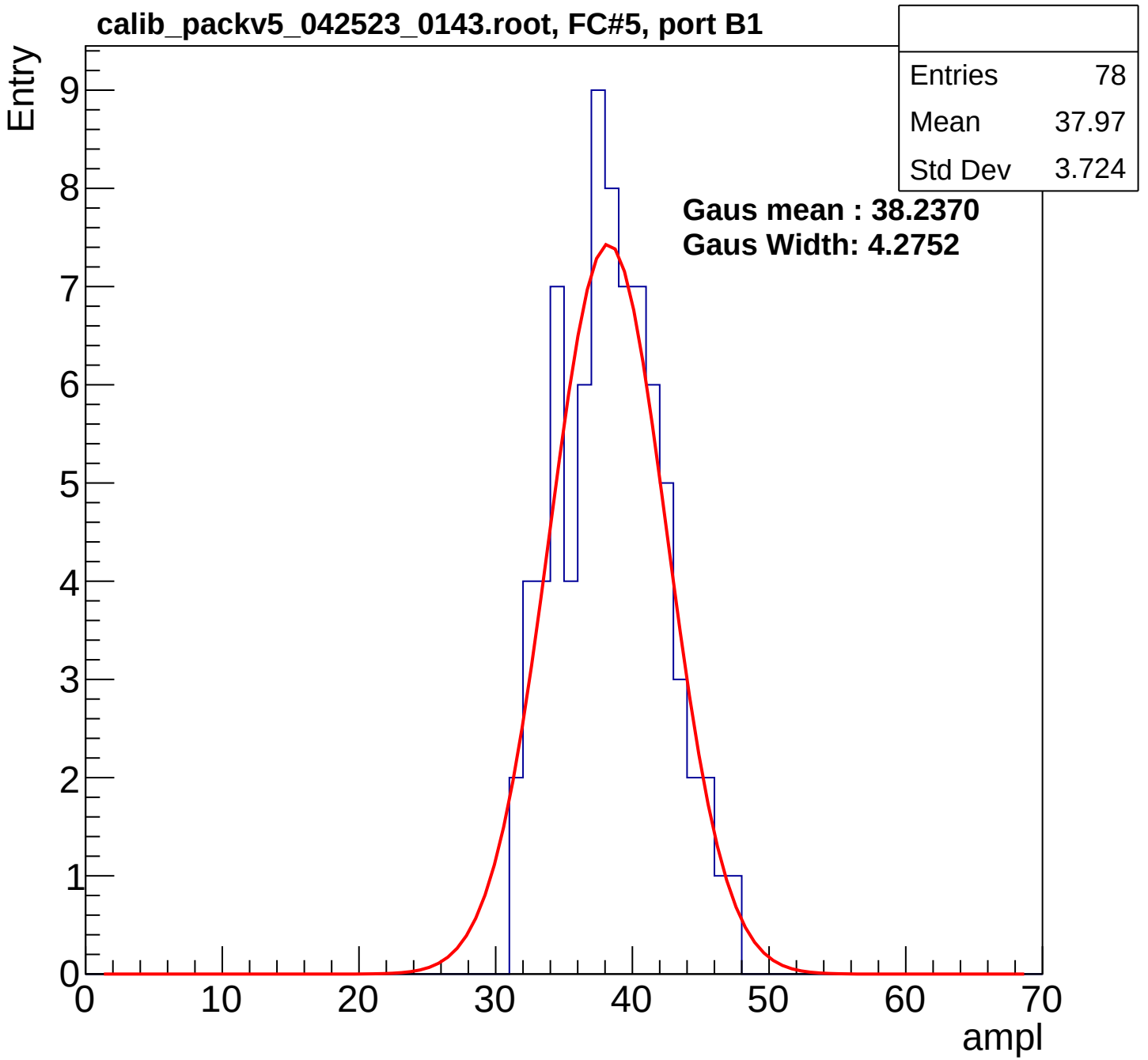
B0L000S, U12-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 37.97
Std Dev 3.724
Gaus mean : 38.2370
Gaus Width: 4.2752
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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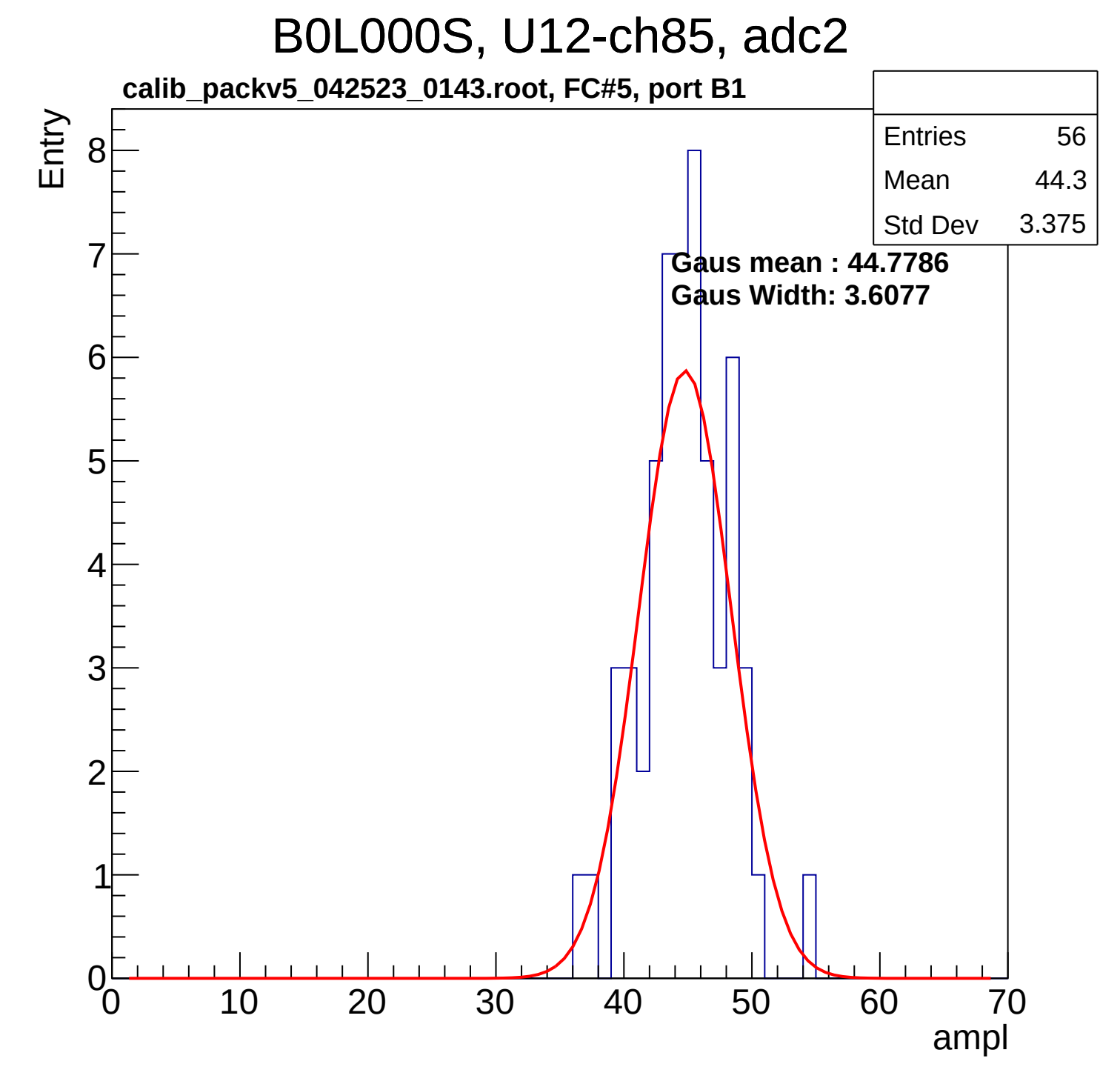
B0L000S, U12-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.3
Std Dev 3.375
Gaus mean : 44.7786
Gaus Width: 3.6077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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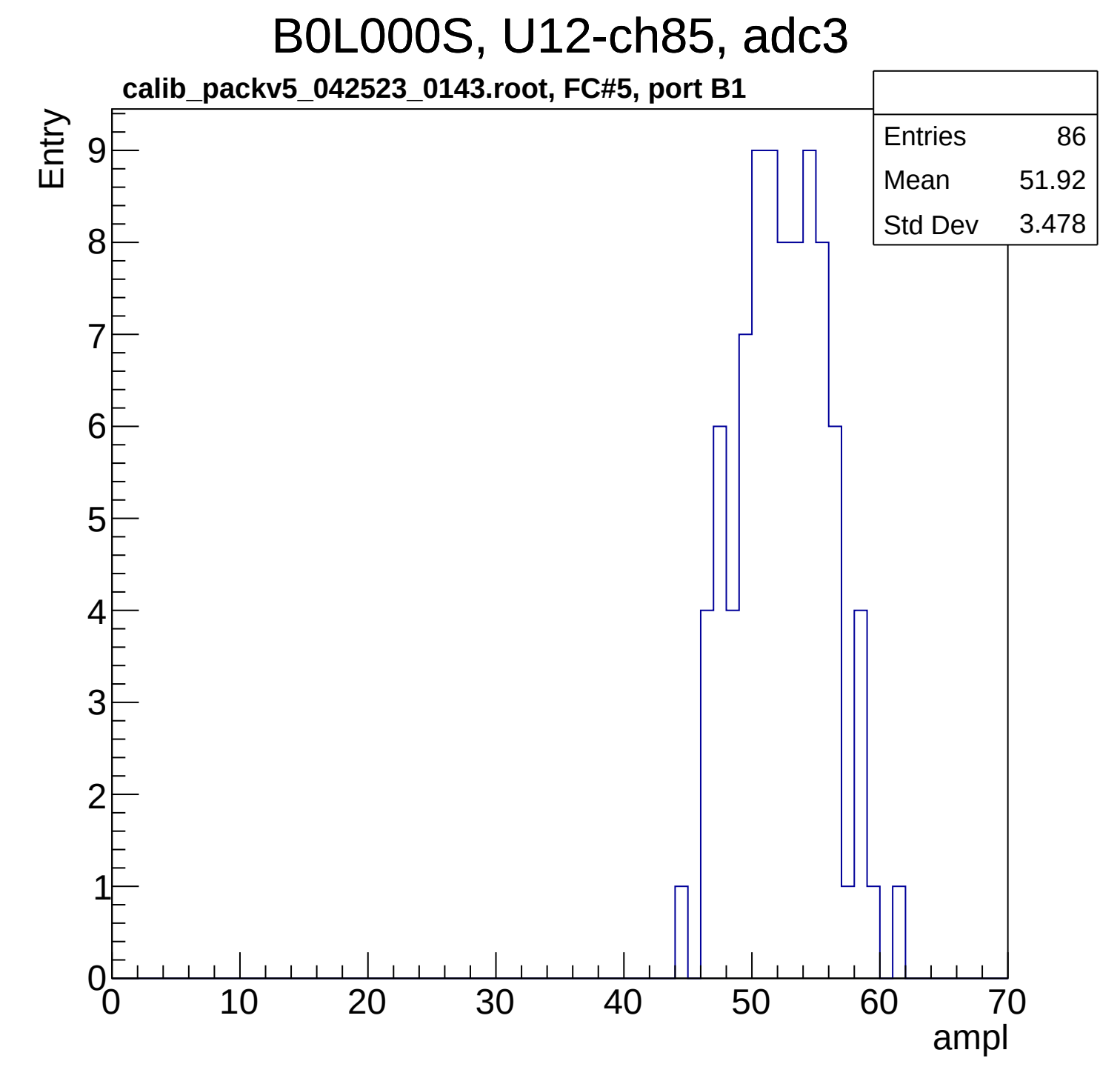
B0L000S, U12-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 51.92
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
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50
60
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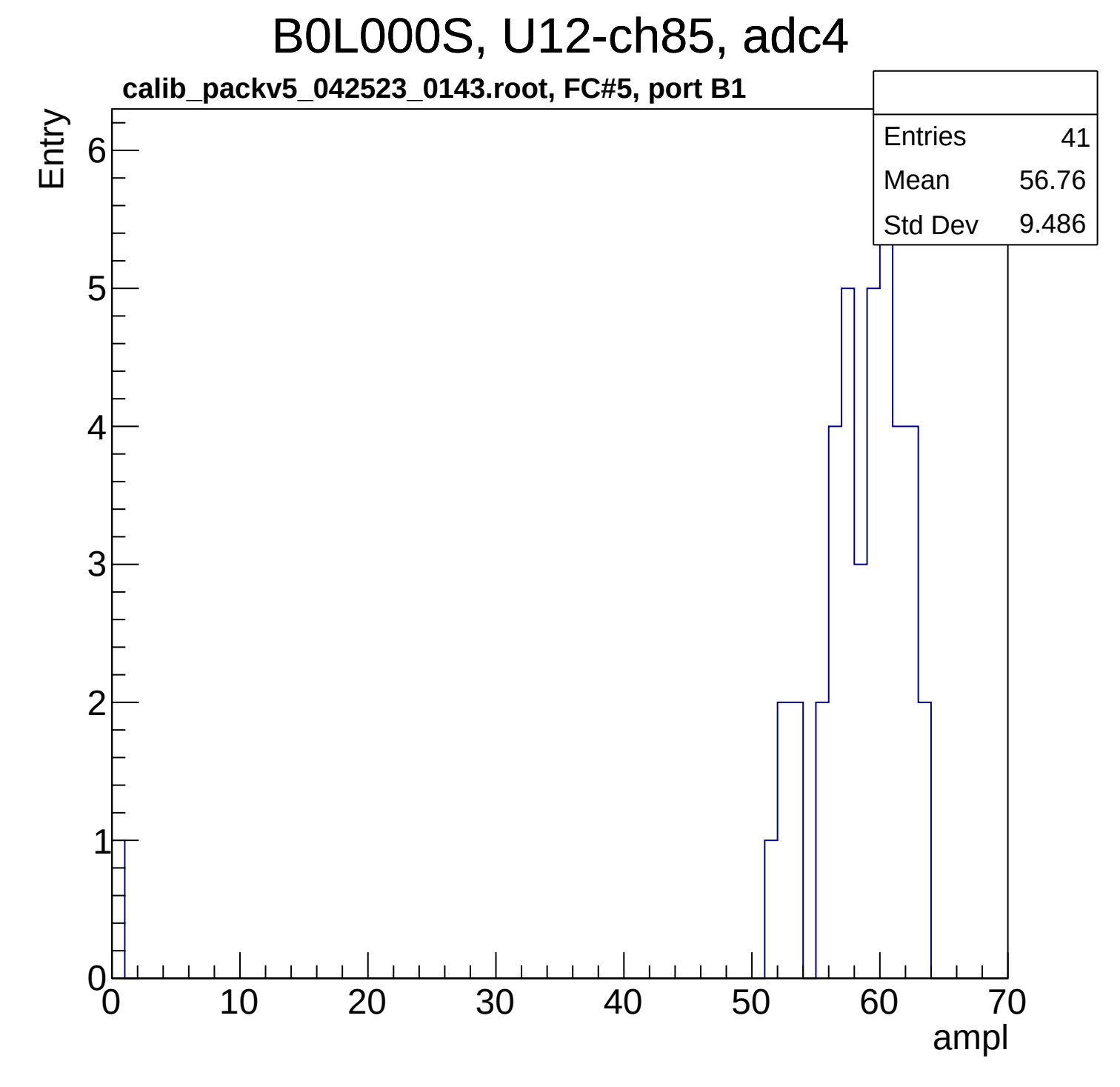
B0L000S, U12-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 56.76
Std Dev 9.486
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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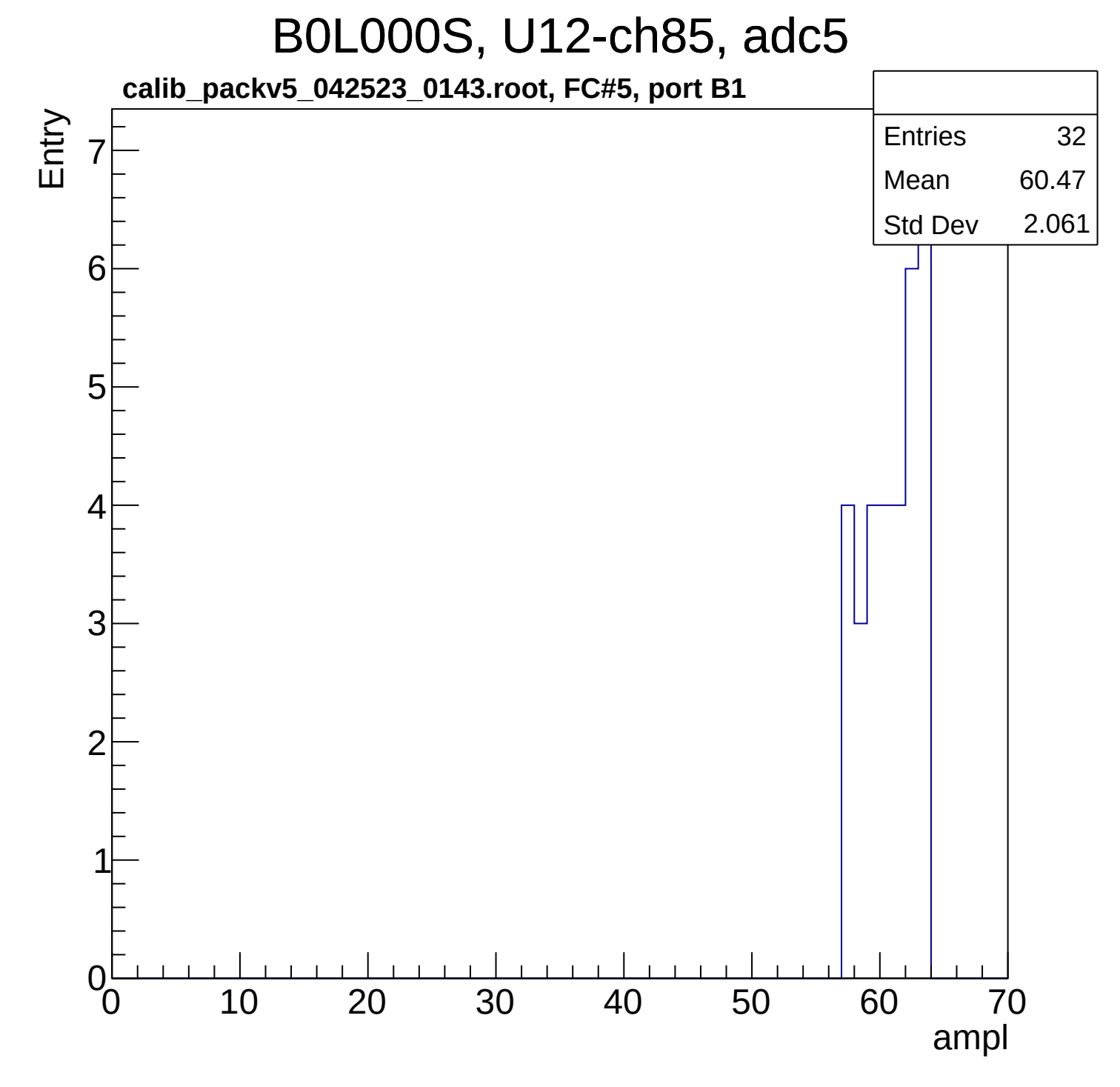
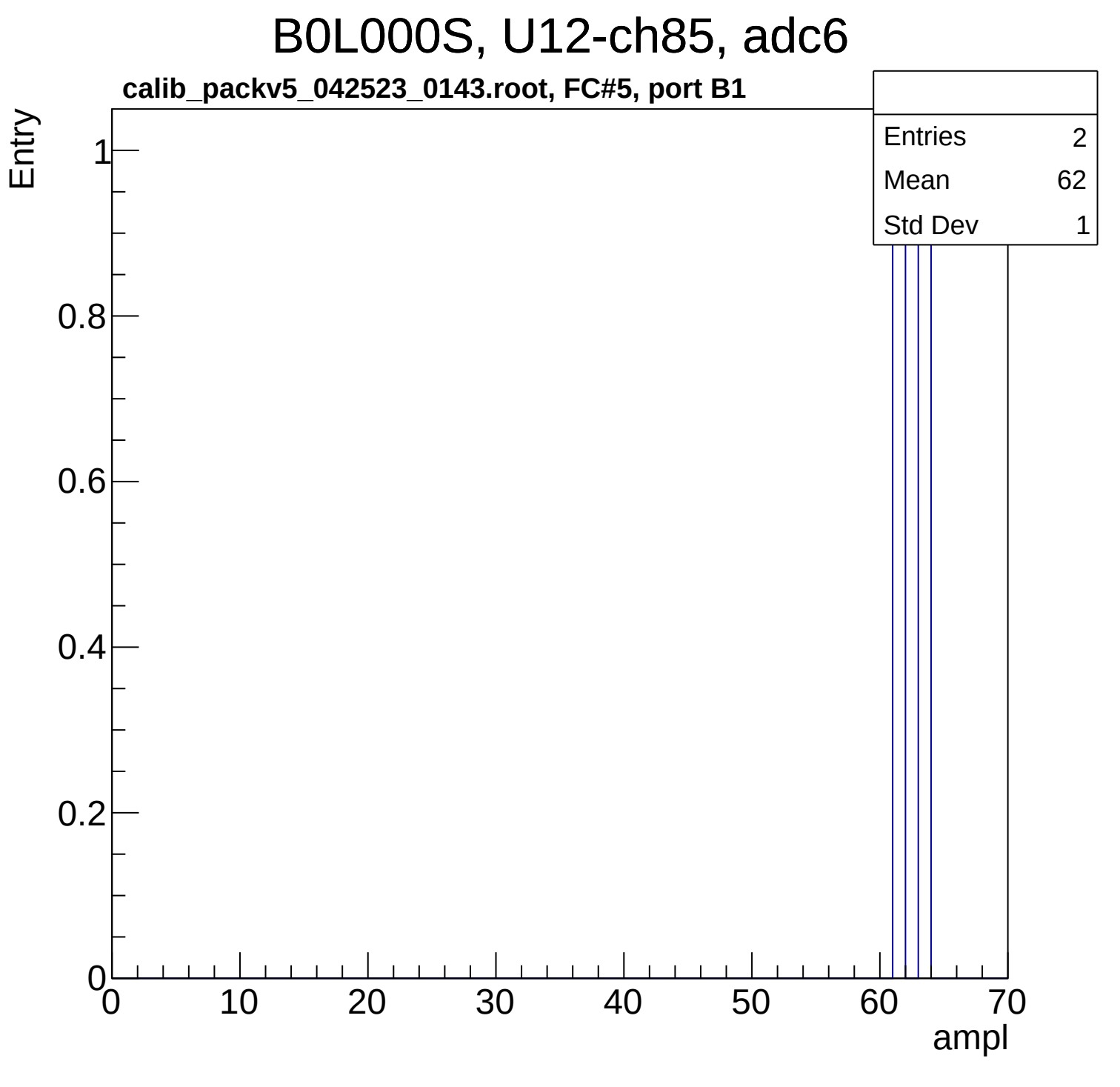
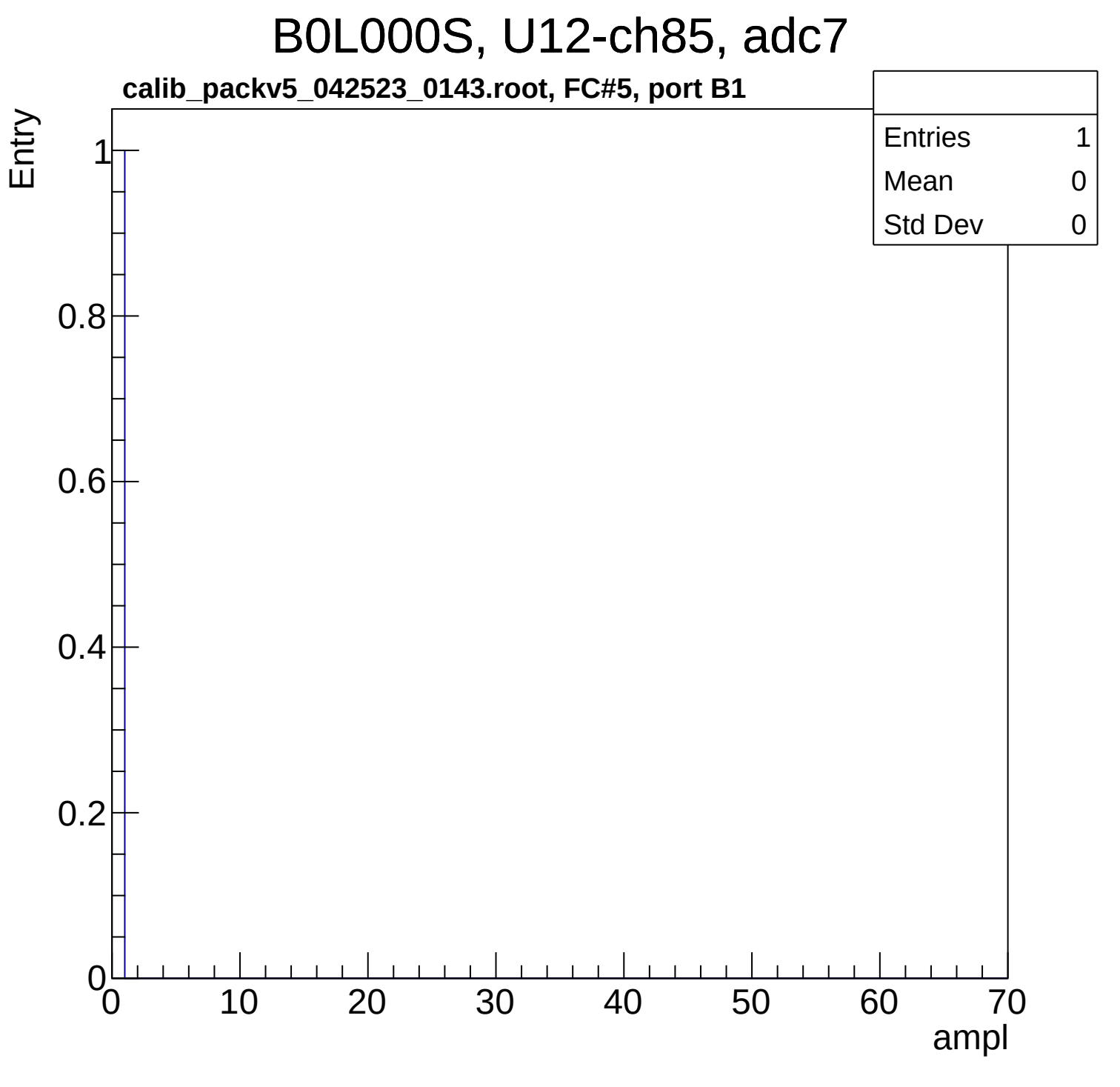
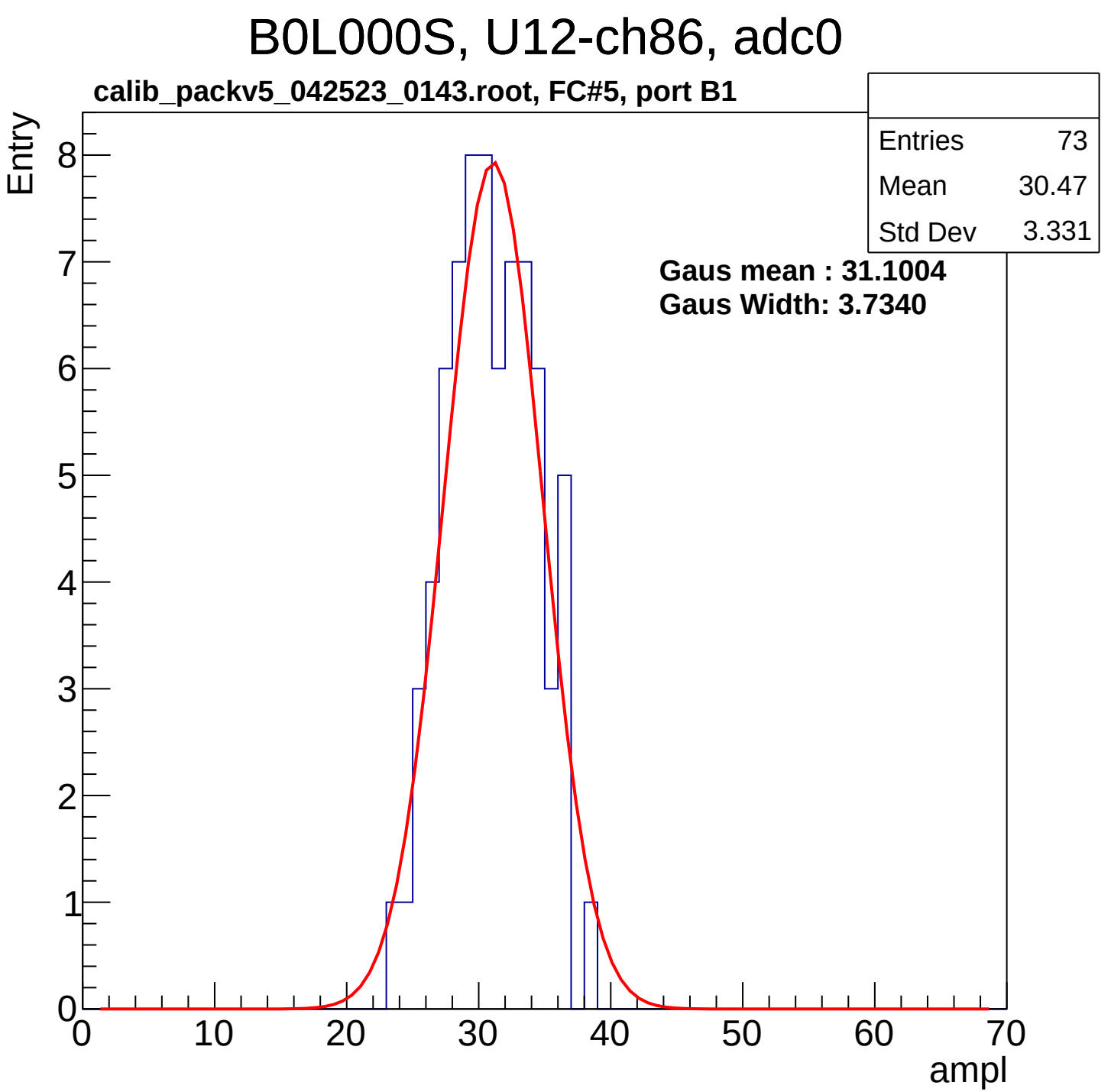
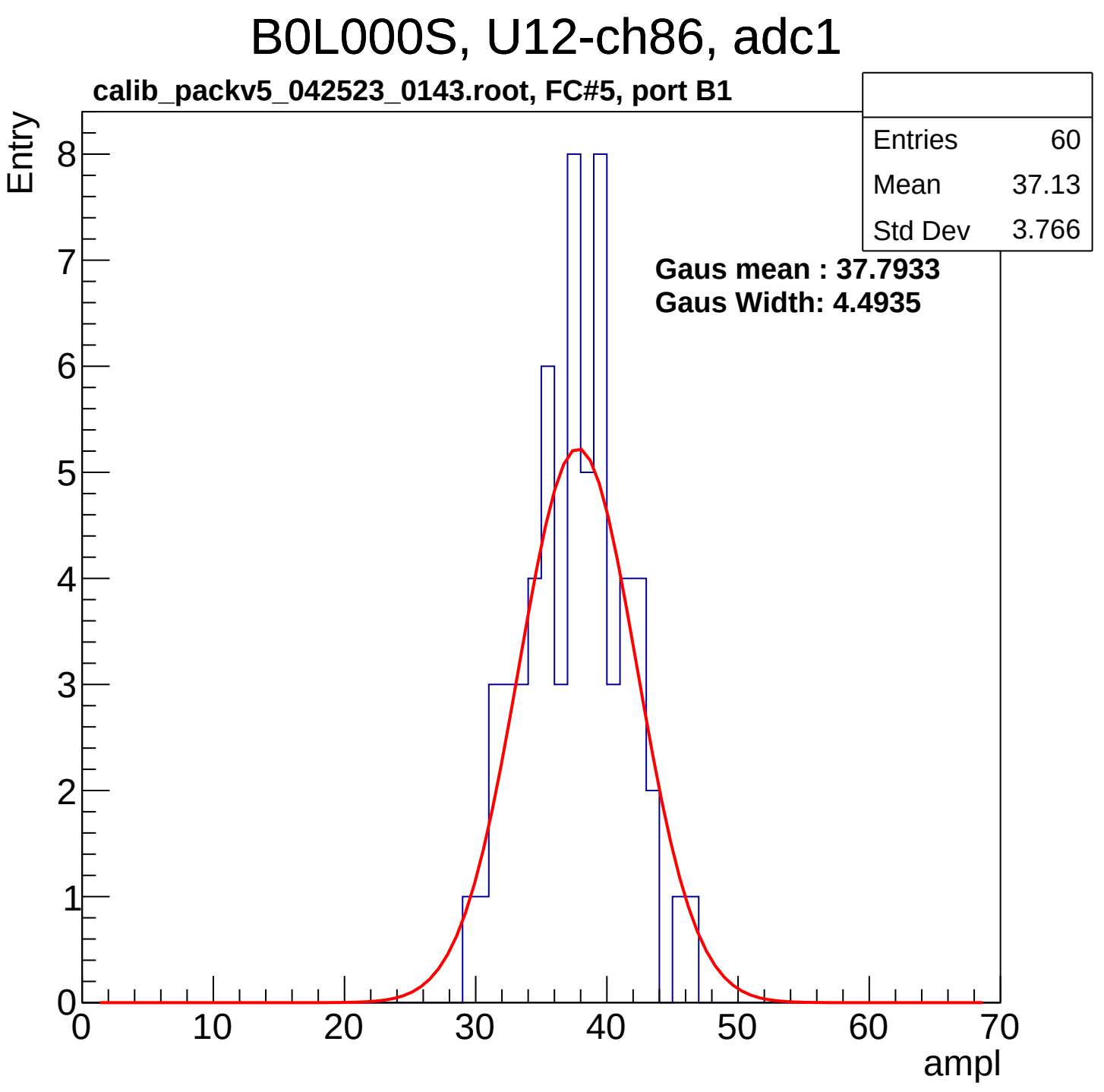
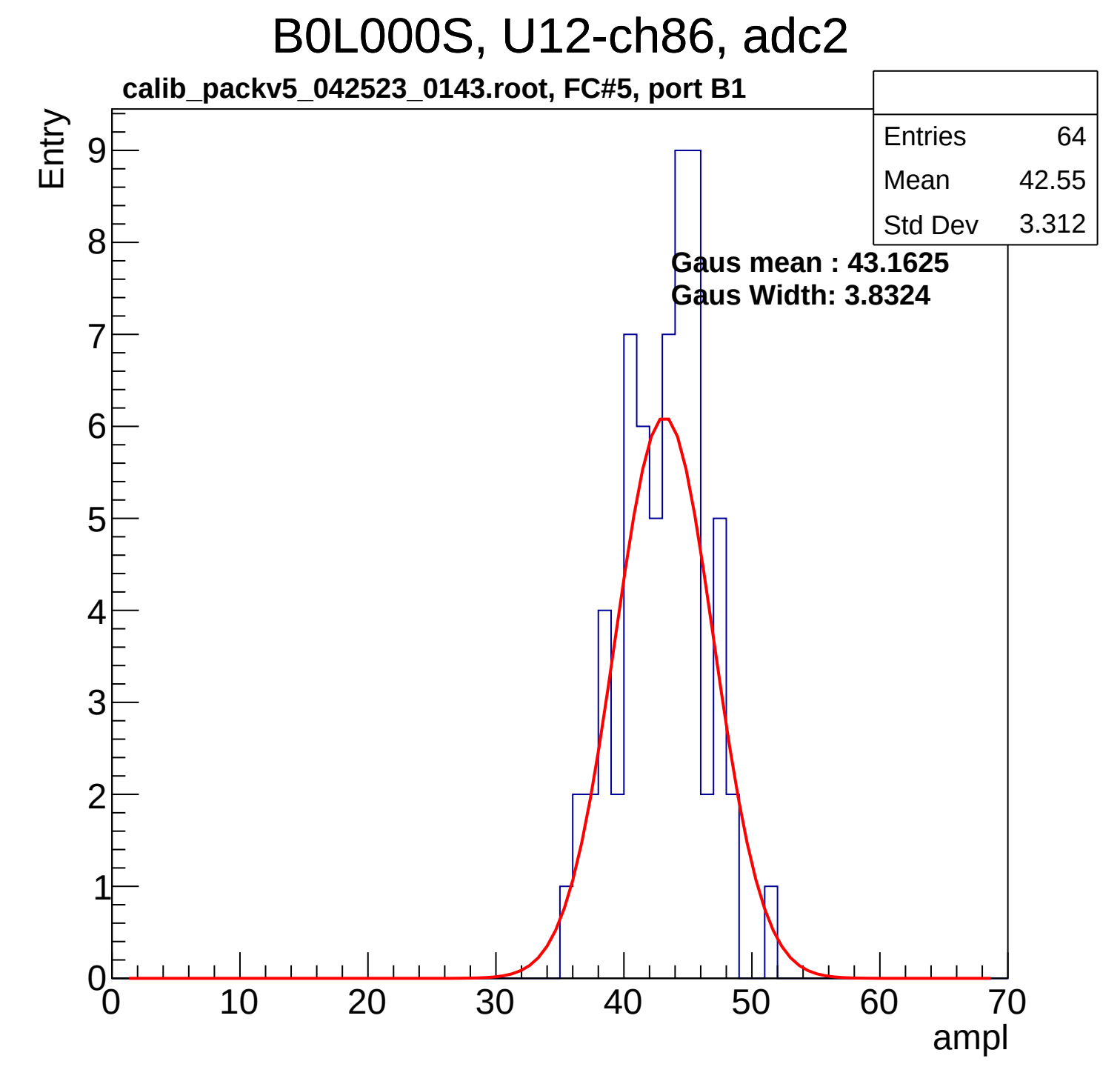

B0L000S, U12-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.47
Std Dev 2.061
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

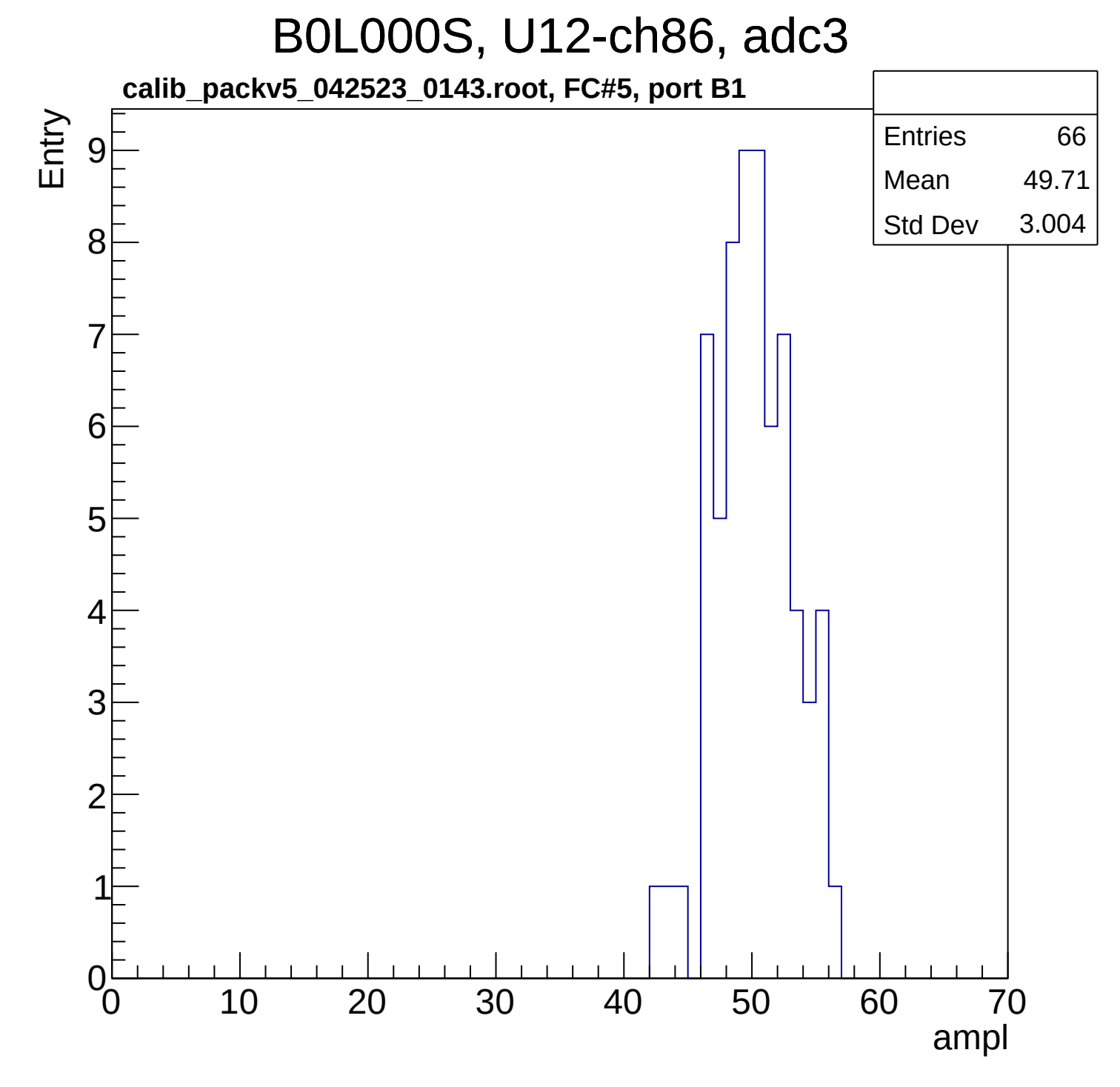

B0L000S, U12-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

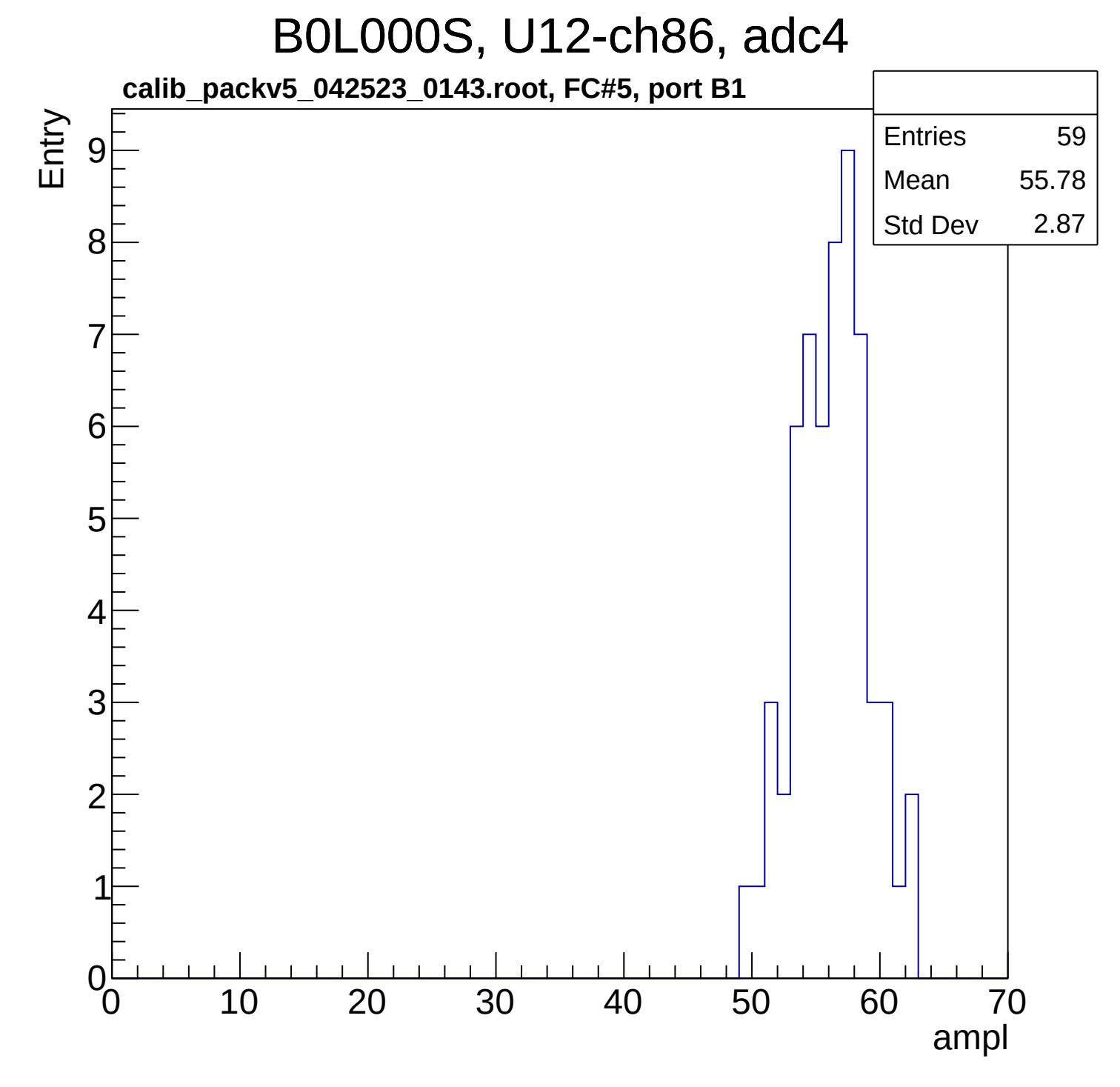

B0L000S, U12-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

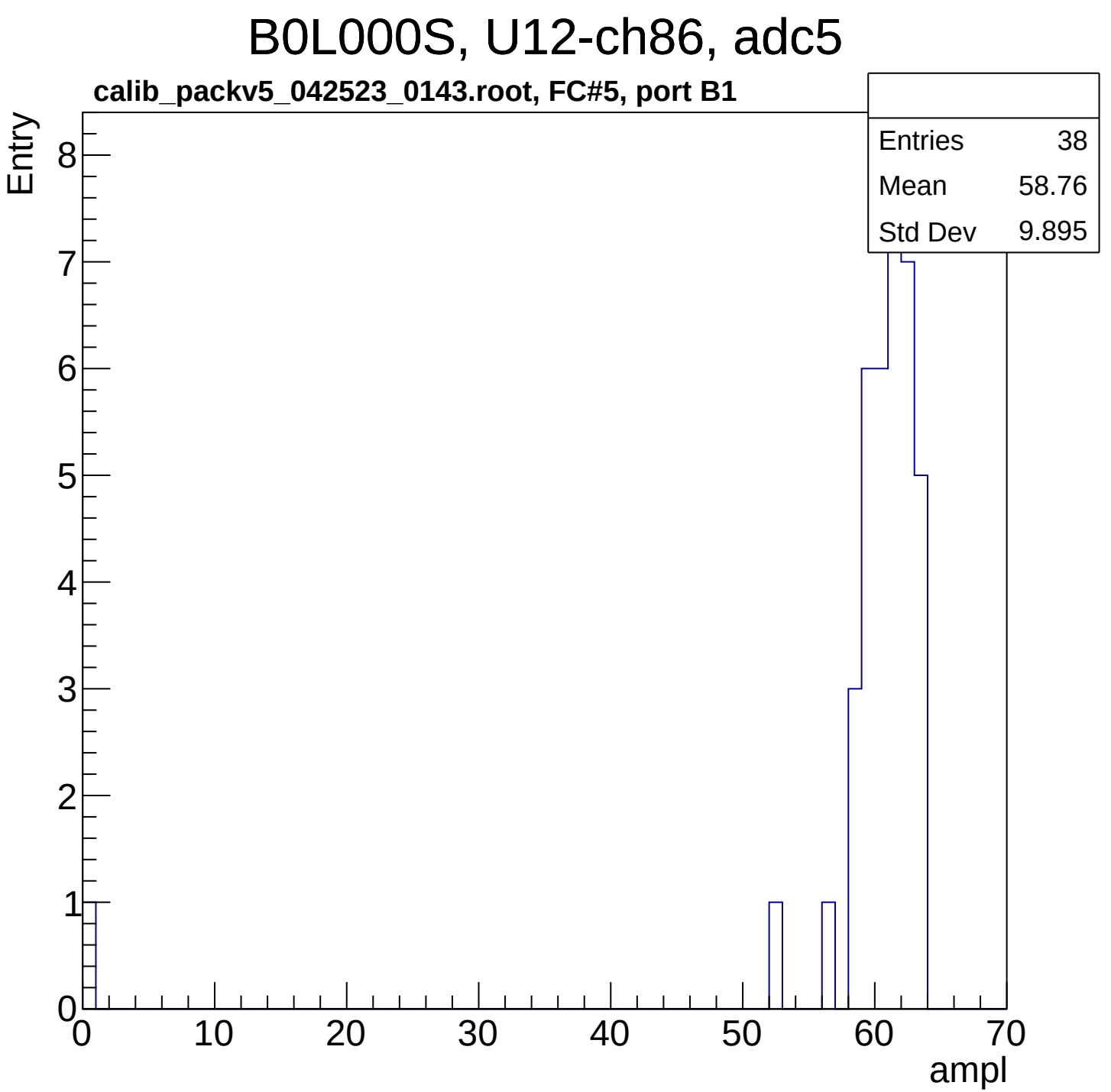

B0L000S, U12-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.47
Std Dev 3.331
Gaus mean : 31.1004
Gaus Width: 3.7340
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.13
Std Dev 3.766
Gaus mean : 37.7933
Gaus Width: 4.4935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.55
Std Dev 3.312
Gaus mean : 43.1625
Gaus Width: 3.8324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.71
Std Dev 3.004
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.78
Std Dev 2.87
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.76
Std Dev 9.895
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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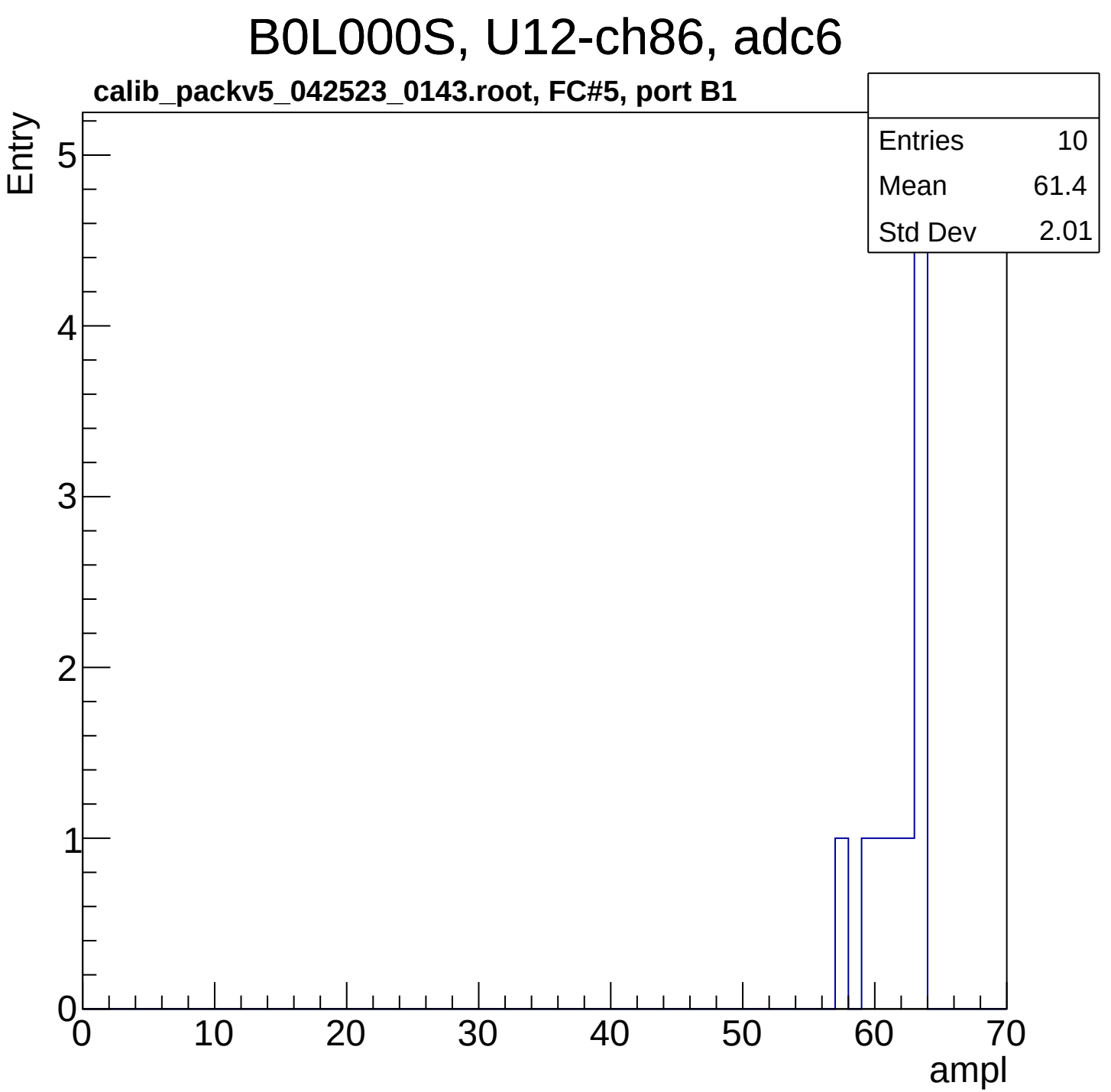
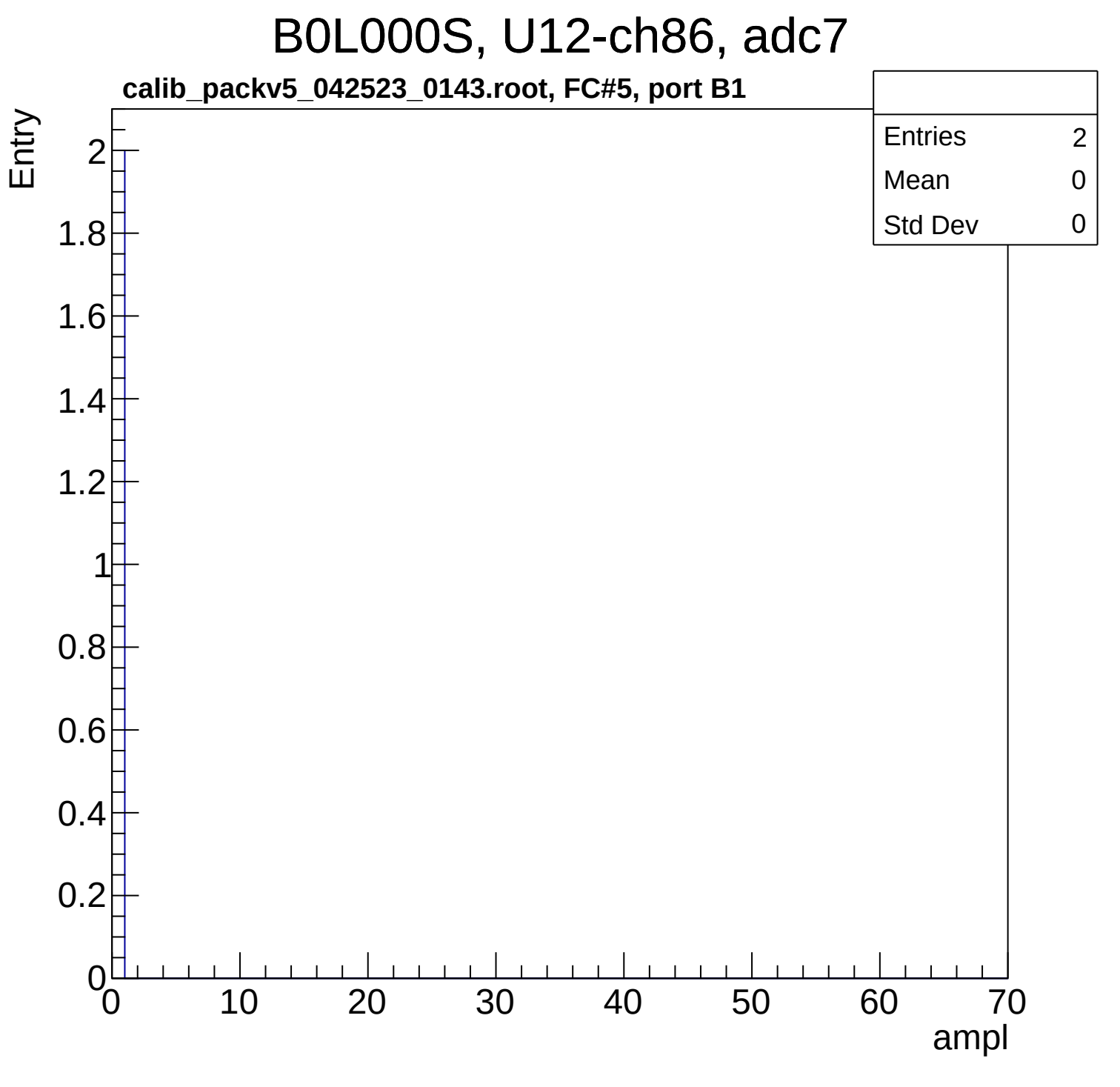
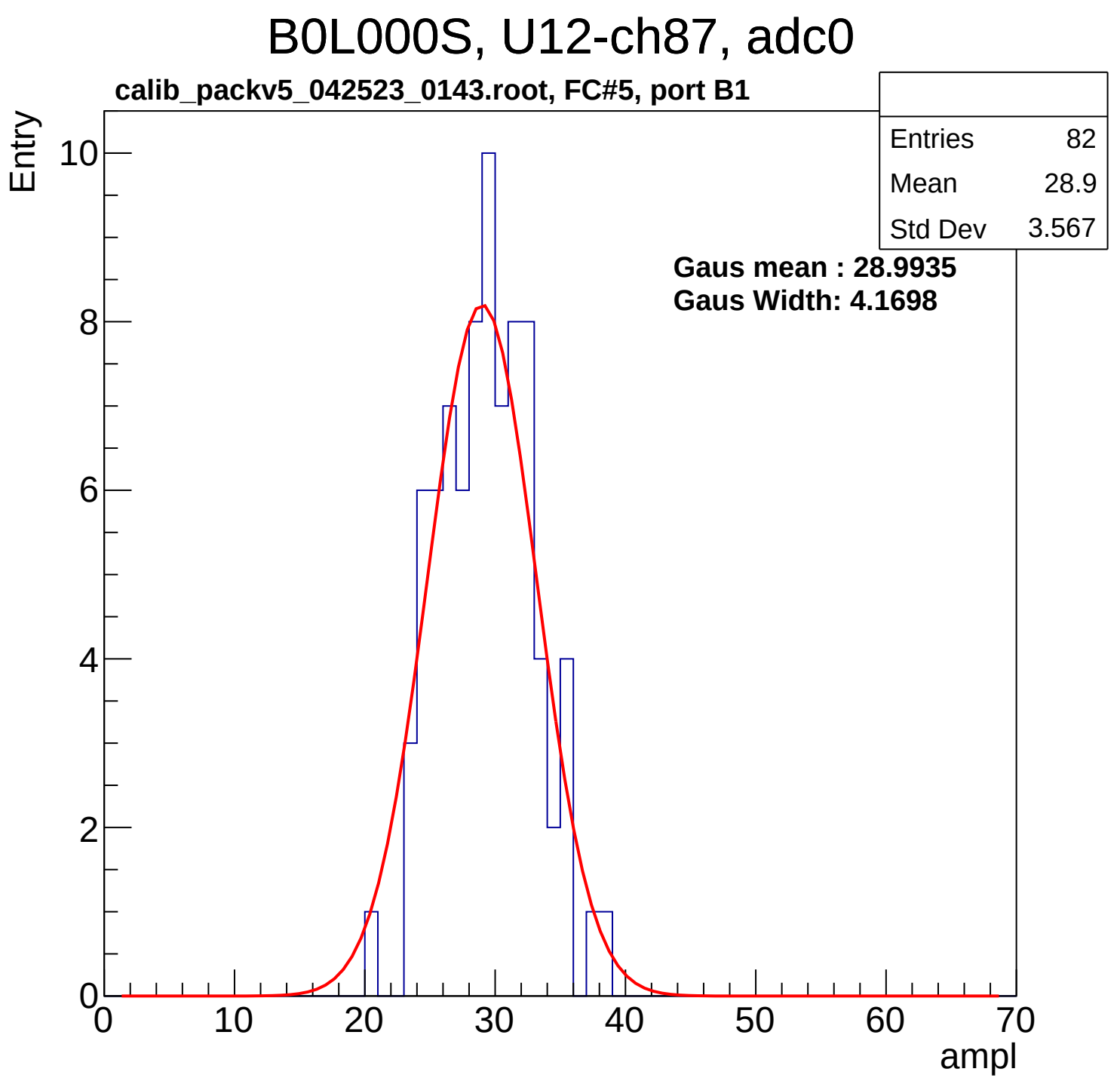
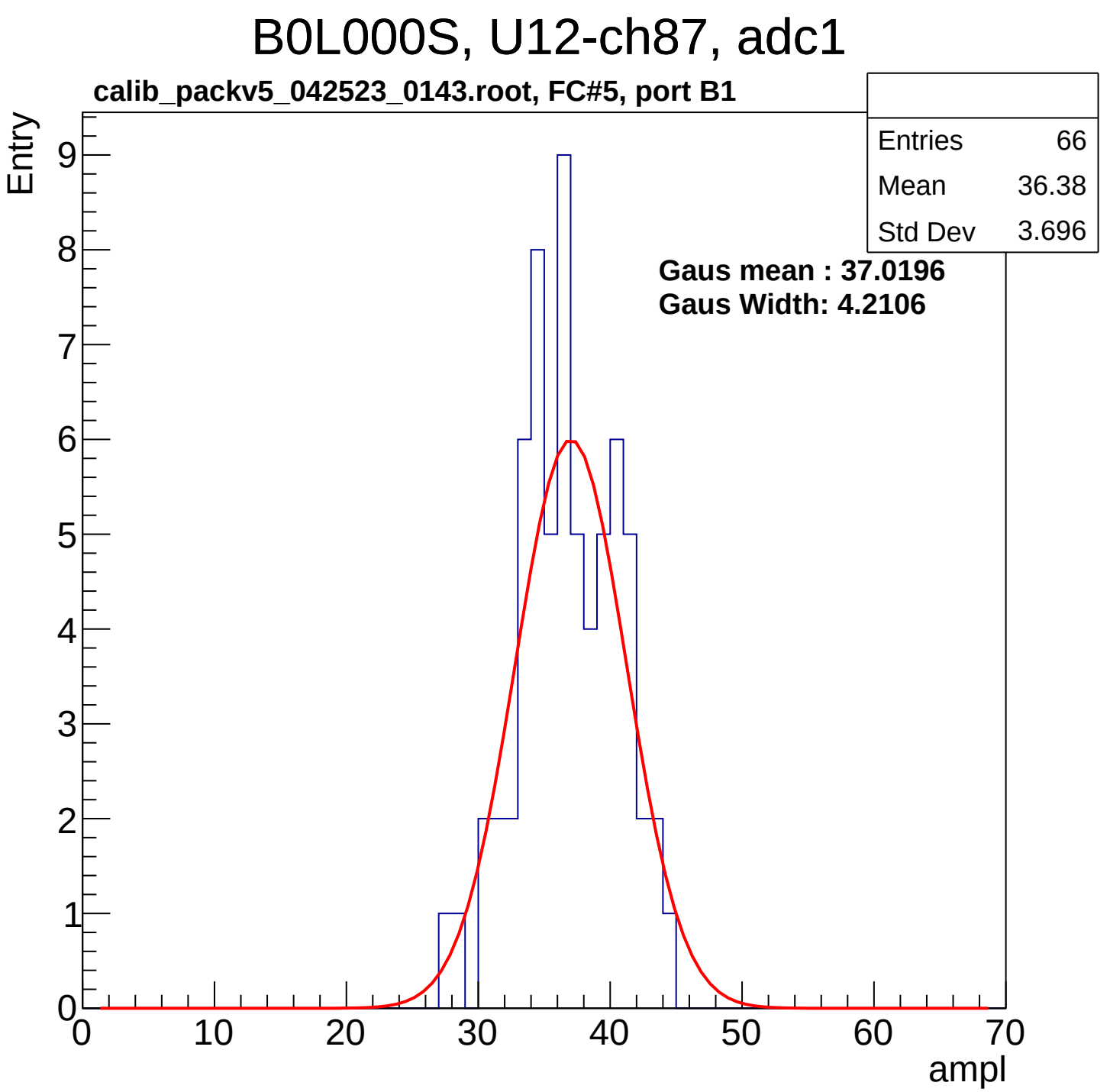
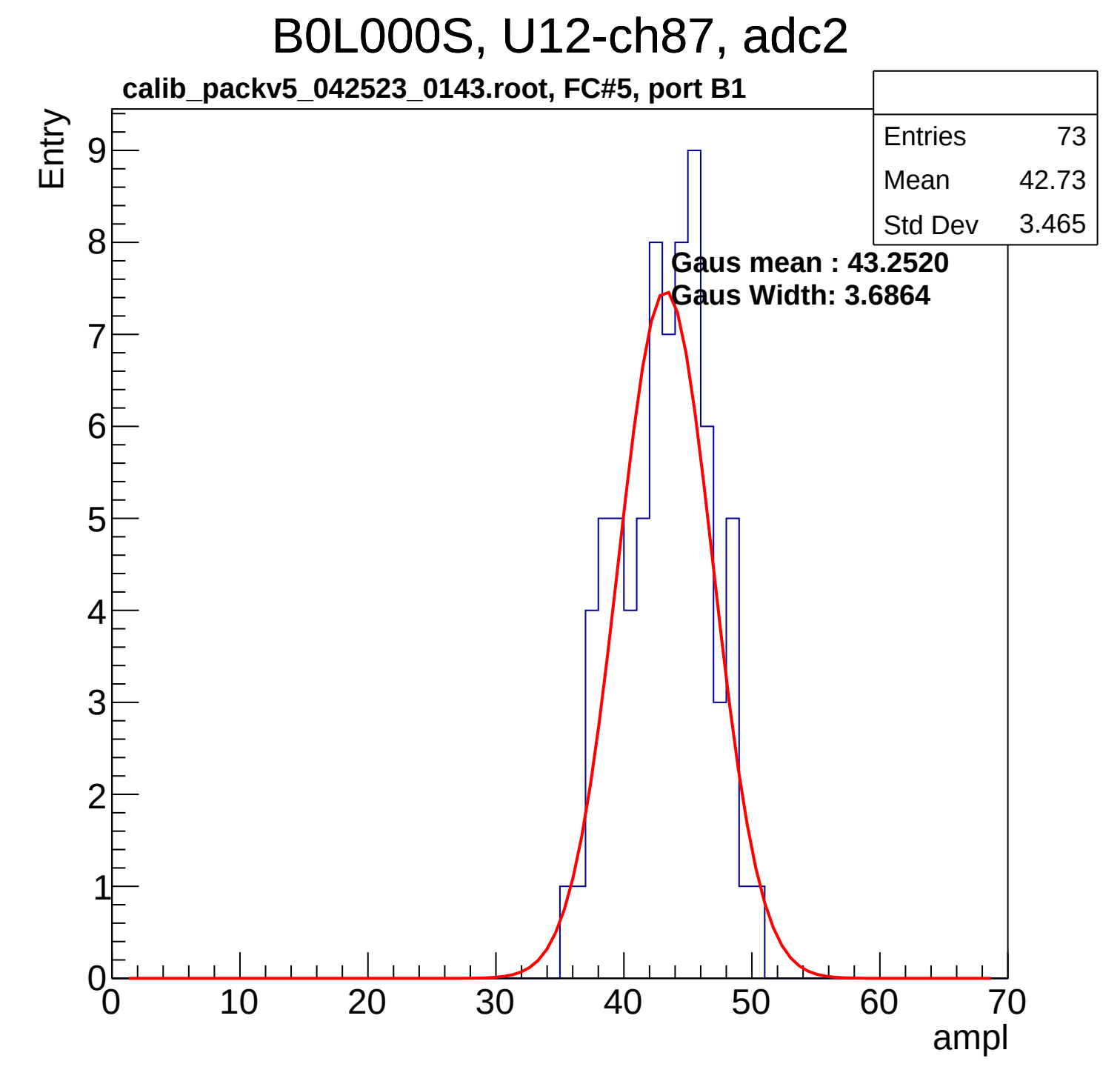
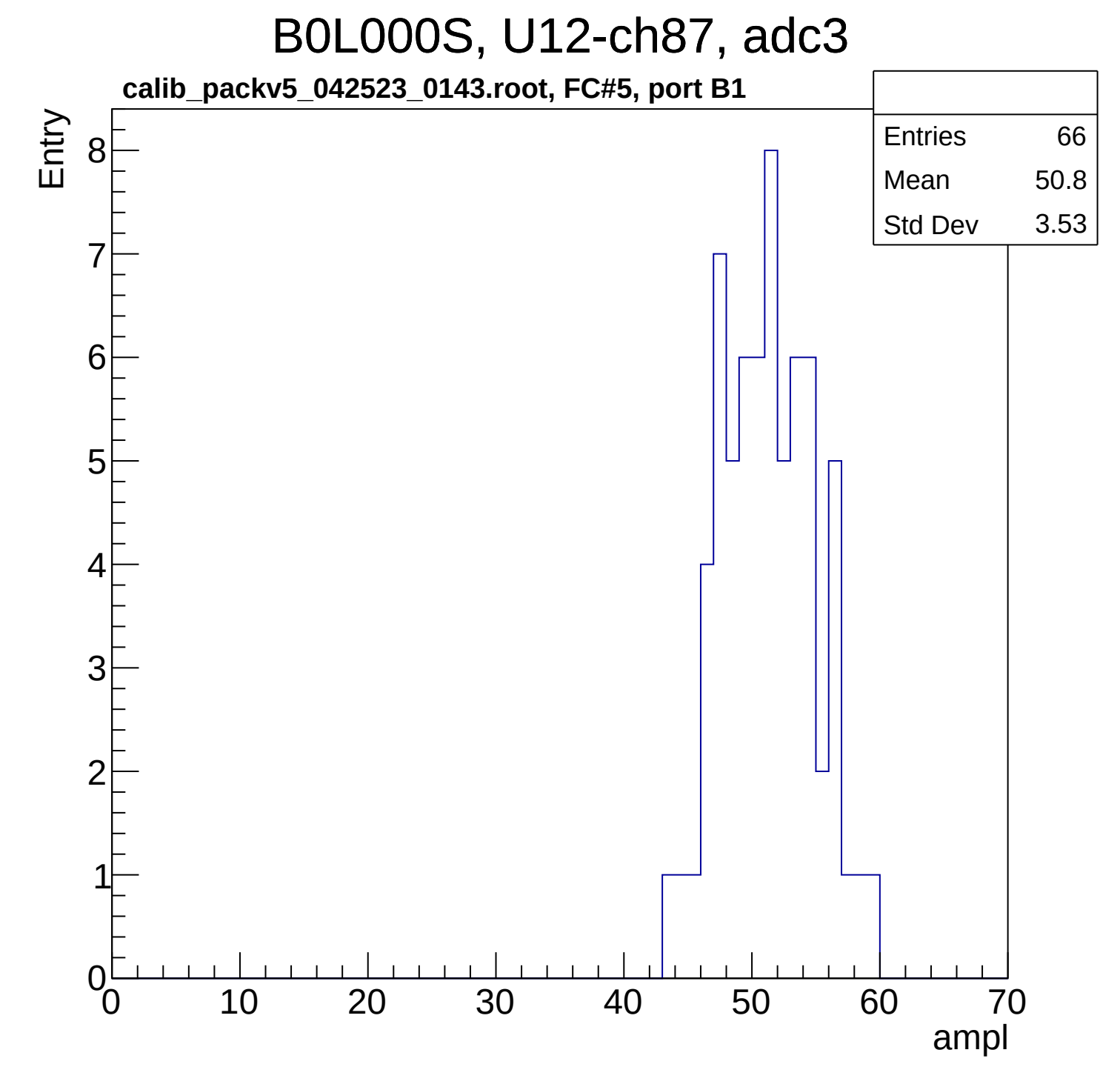

B0L000S, U12-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.4
Std Dev 2.01
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

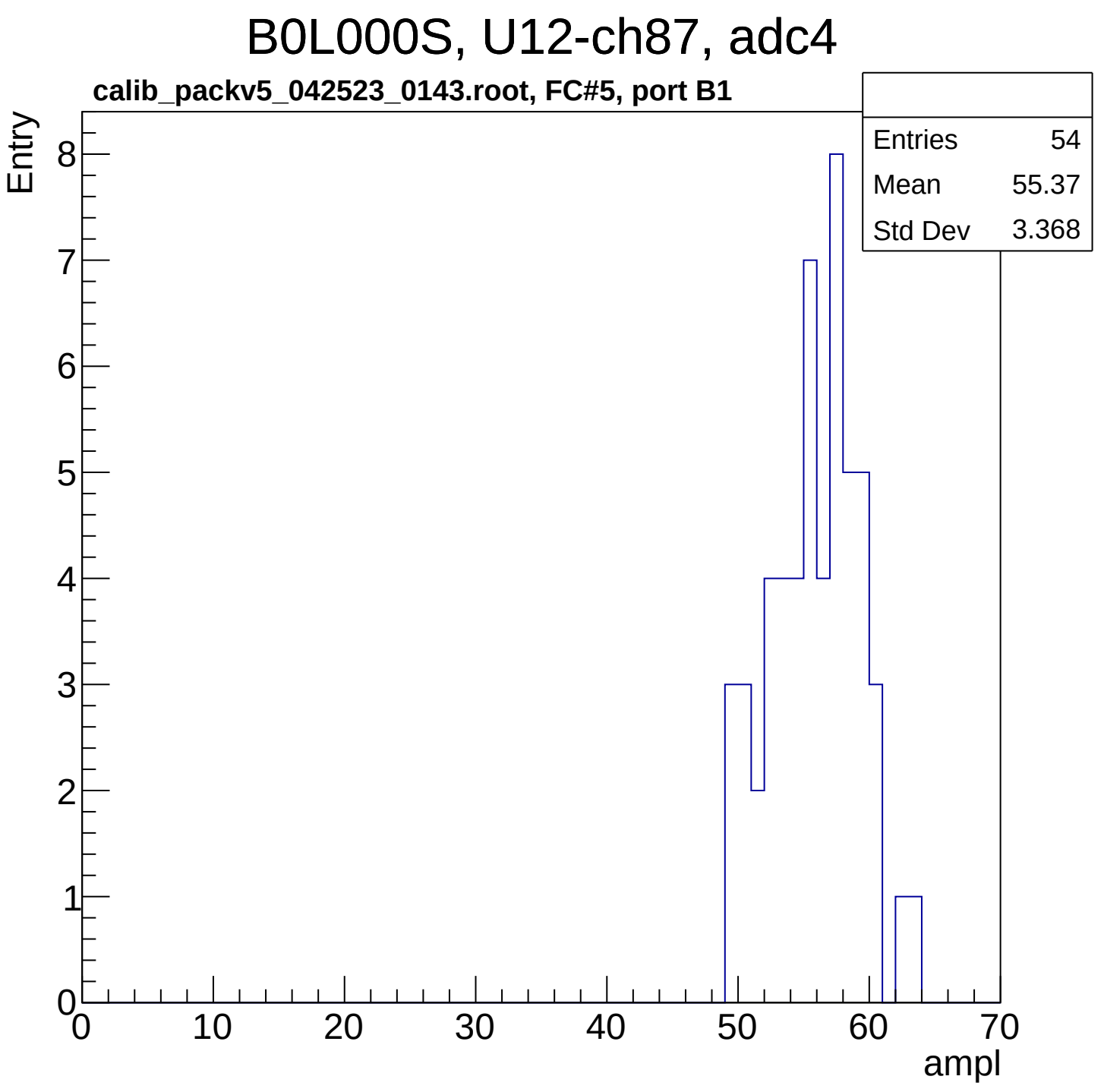

B0L000S, U12-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 28.9
Std Dev 3.567
Gaus mean : 28.9935
Gaus Width: 4.1698
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.38
Std Dev 3.696
Gaus mean : 37.0196
Gaus Width: 4.2106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.73
Std Dev 3.465
Gaus mean : 43.2520
Gaus Width: 3.6864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.8
Std Dev 3.53
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.37
Std Dev 3.368
Entry
ampl
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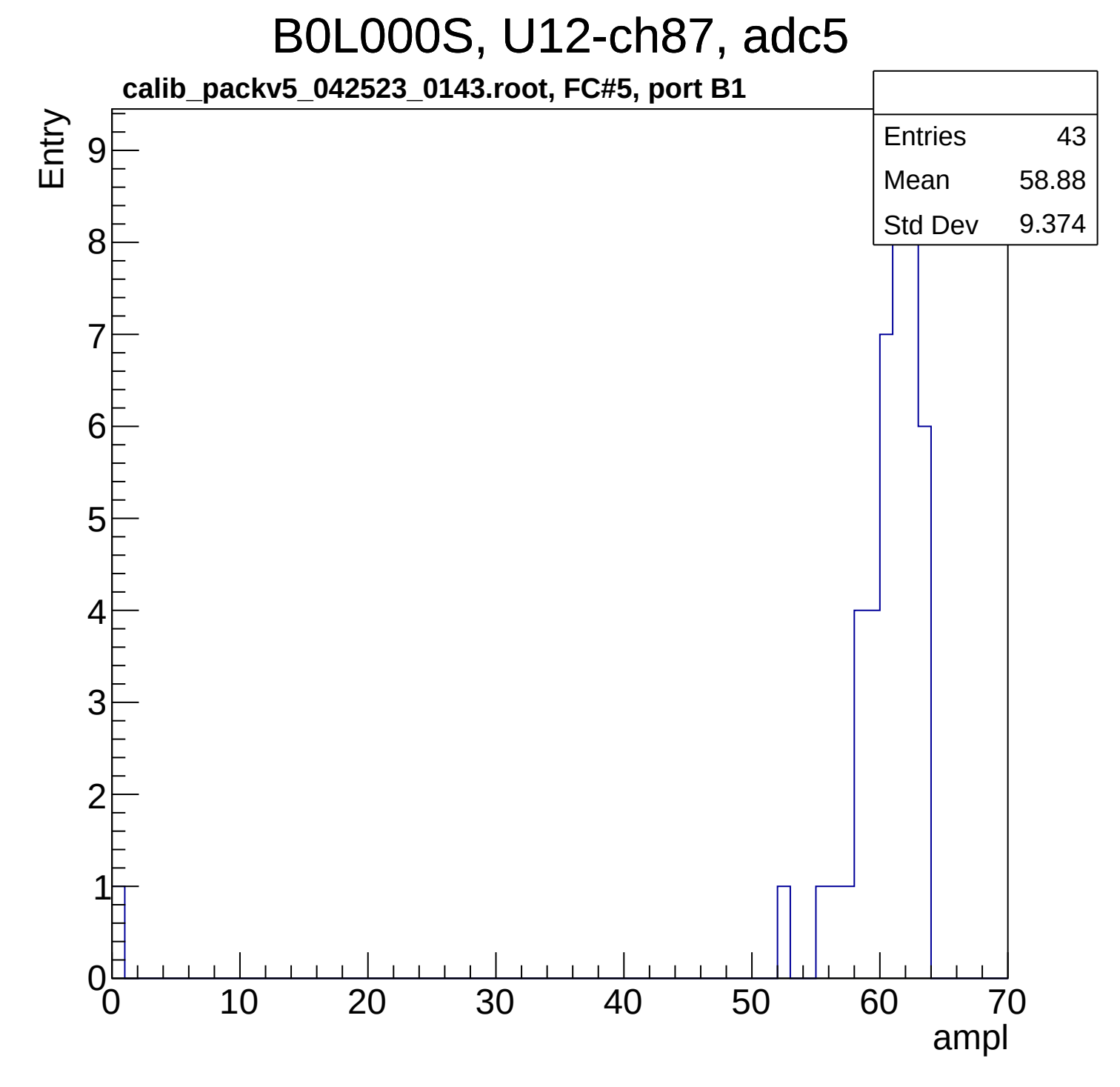
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.88
Std Dev 9.374
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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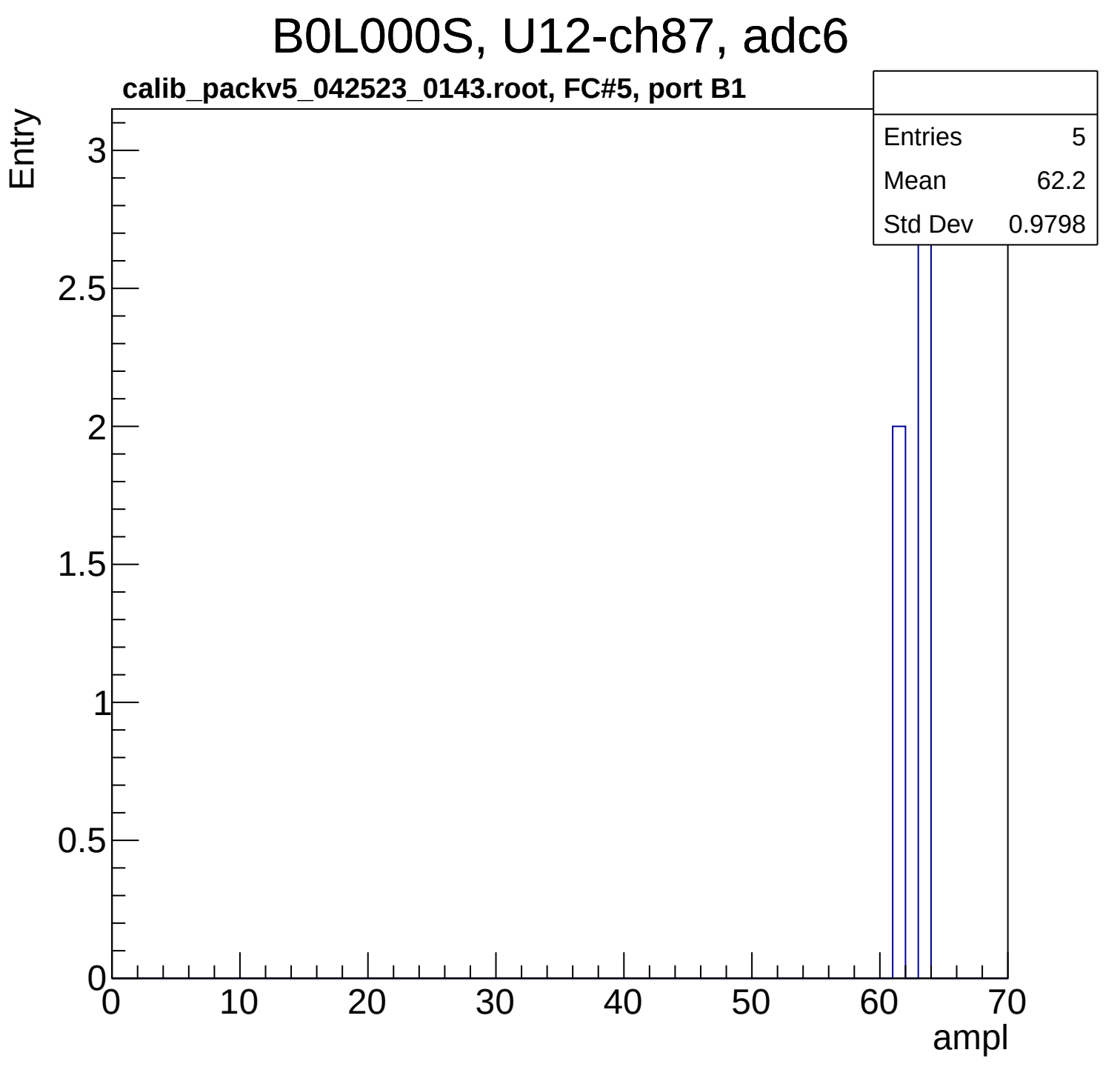
B0L000S, U12-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
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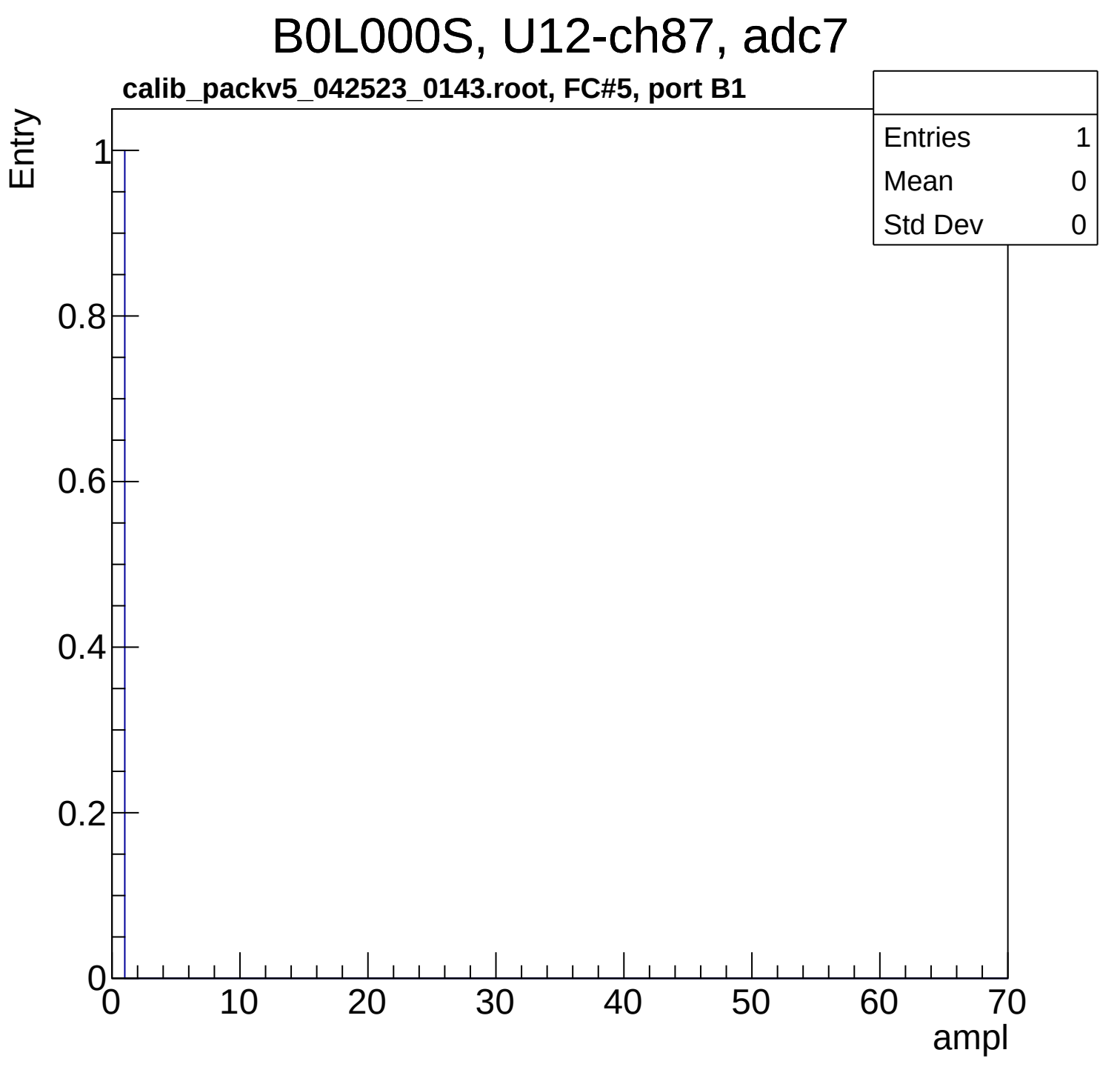
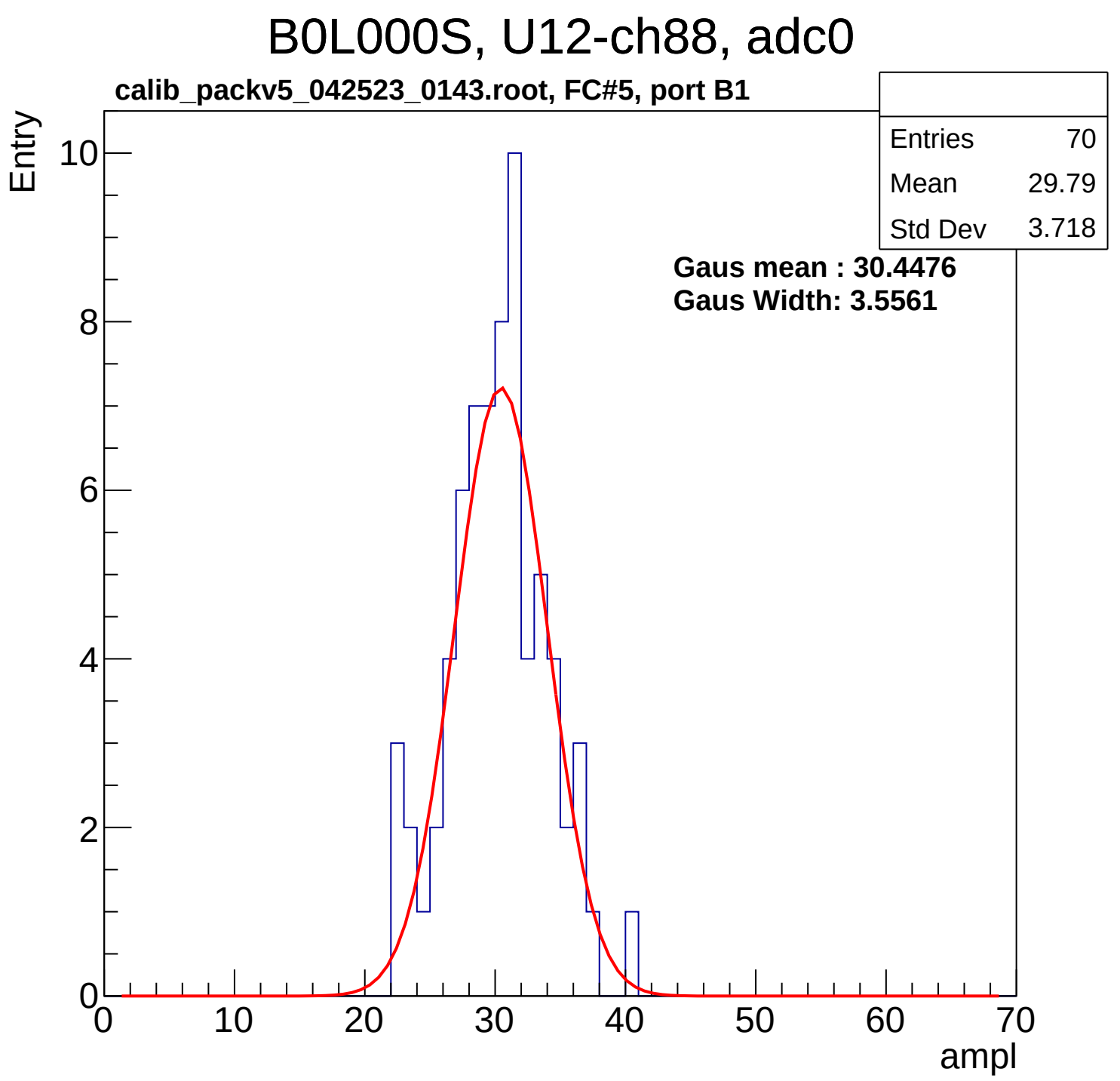
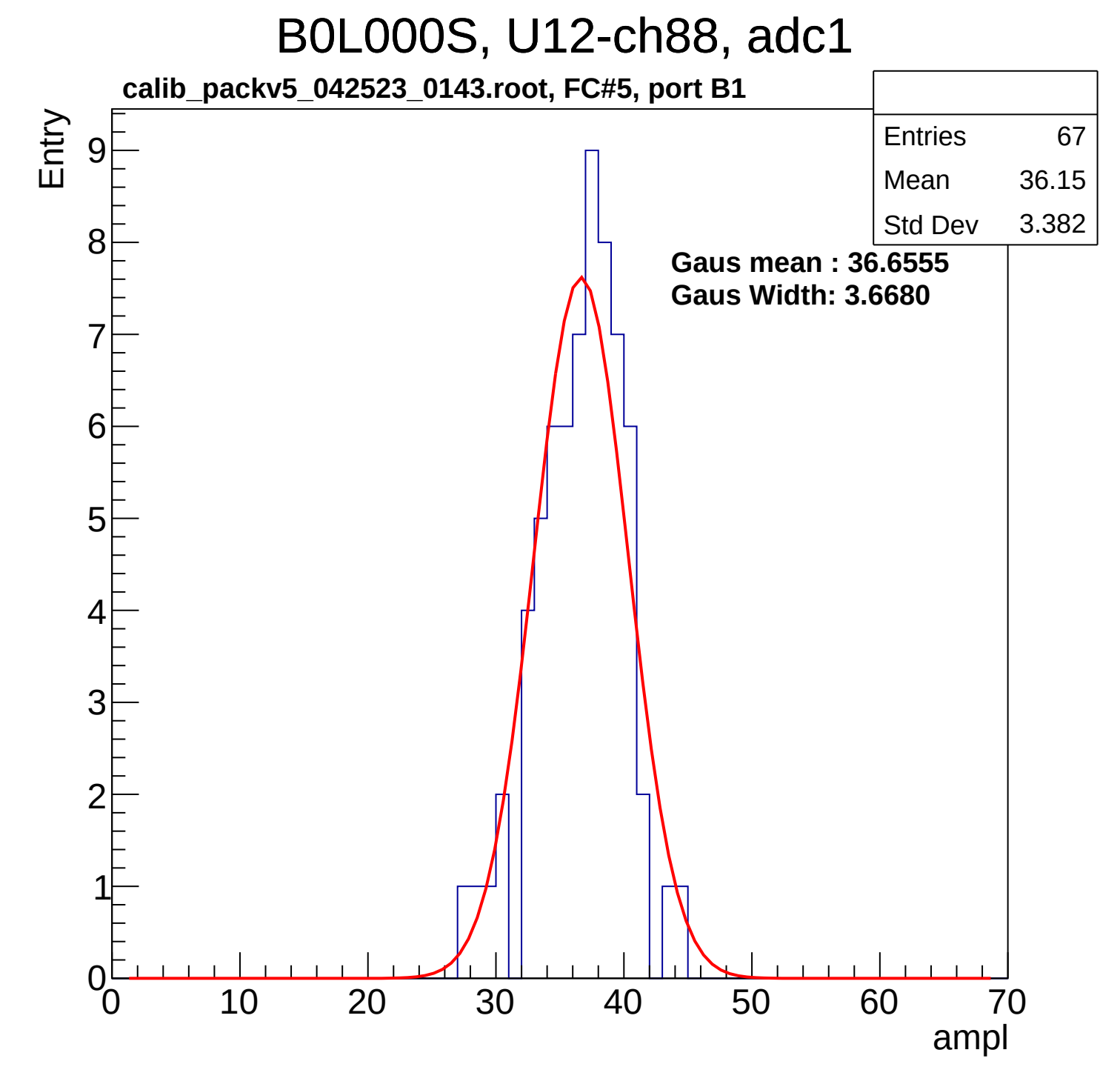
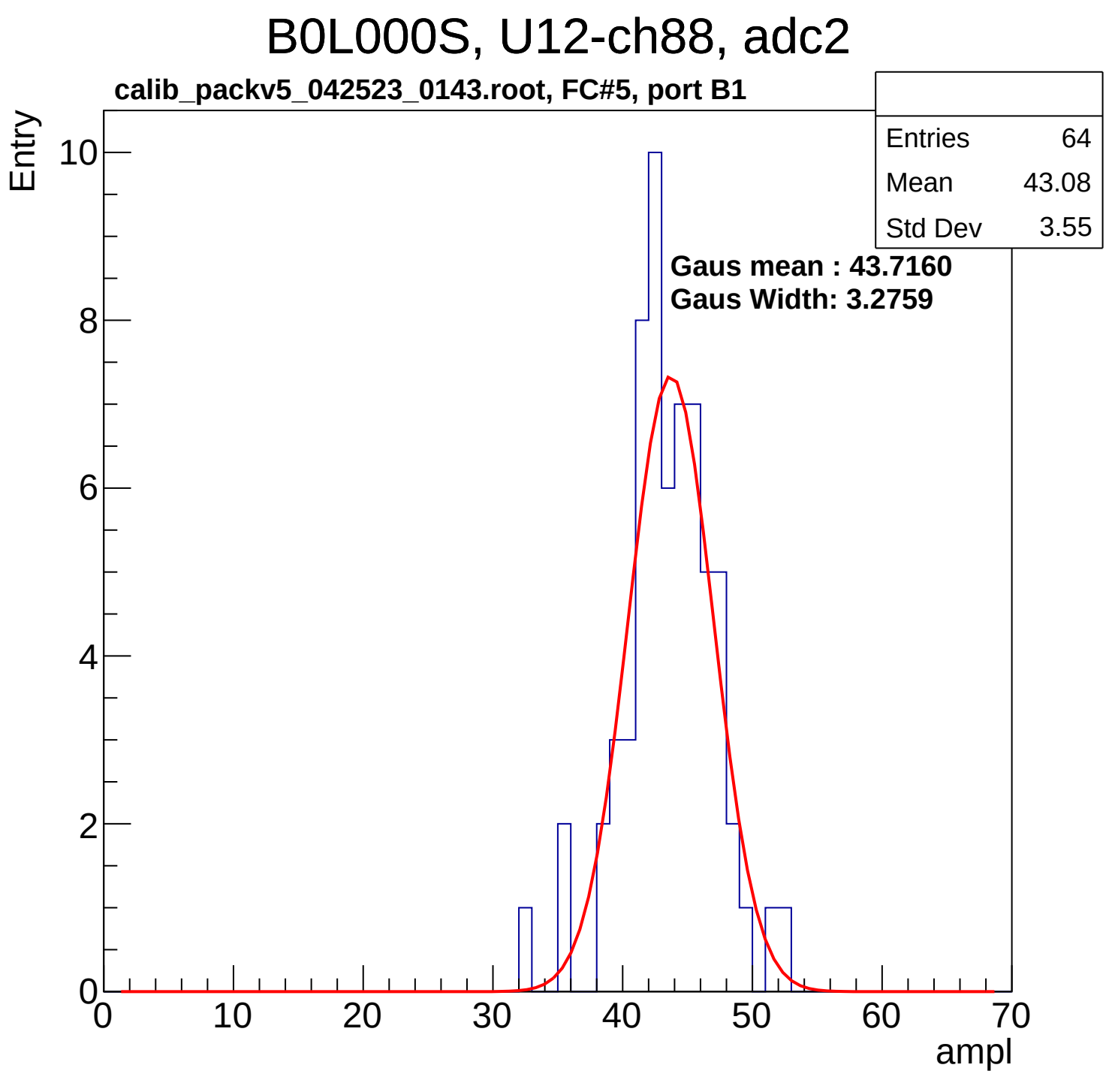
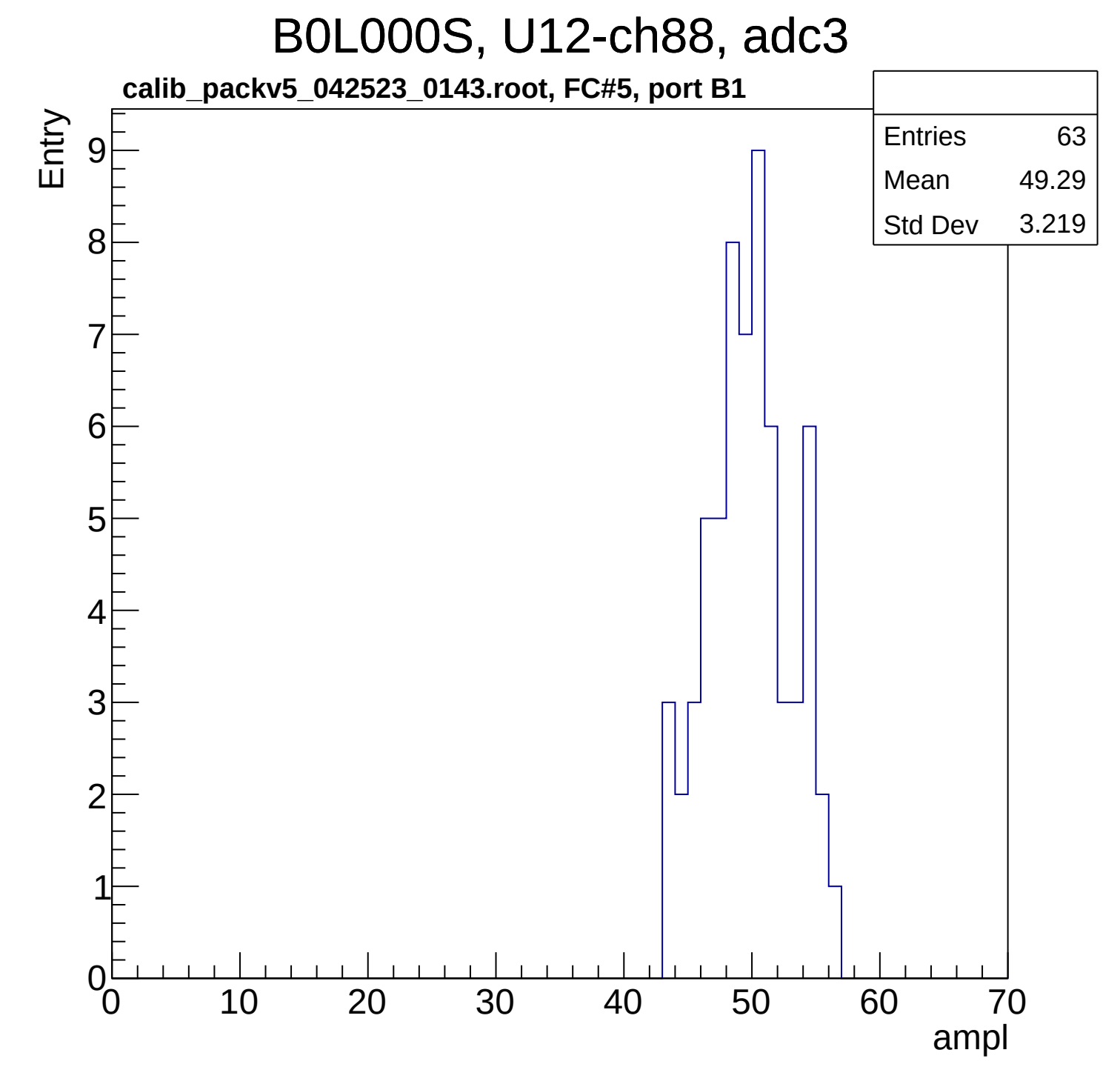
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.79
Std Dev 3.718
Gaus mean : 30.4476
Gaus Width: 3.5561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.15
Std Dev 3.382
Gaus mean : 36.6555
Gaus Width: 3.6680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.08
Std Dev 3.55
Gaus mean : 43.7160
Gaus Width: 3.2759
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.29
Std Dev 3.219
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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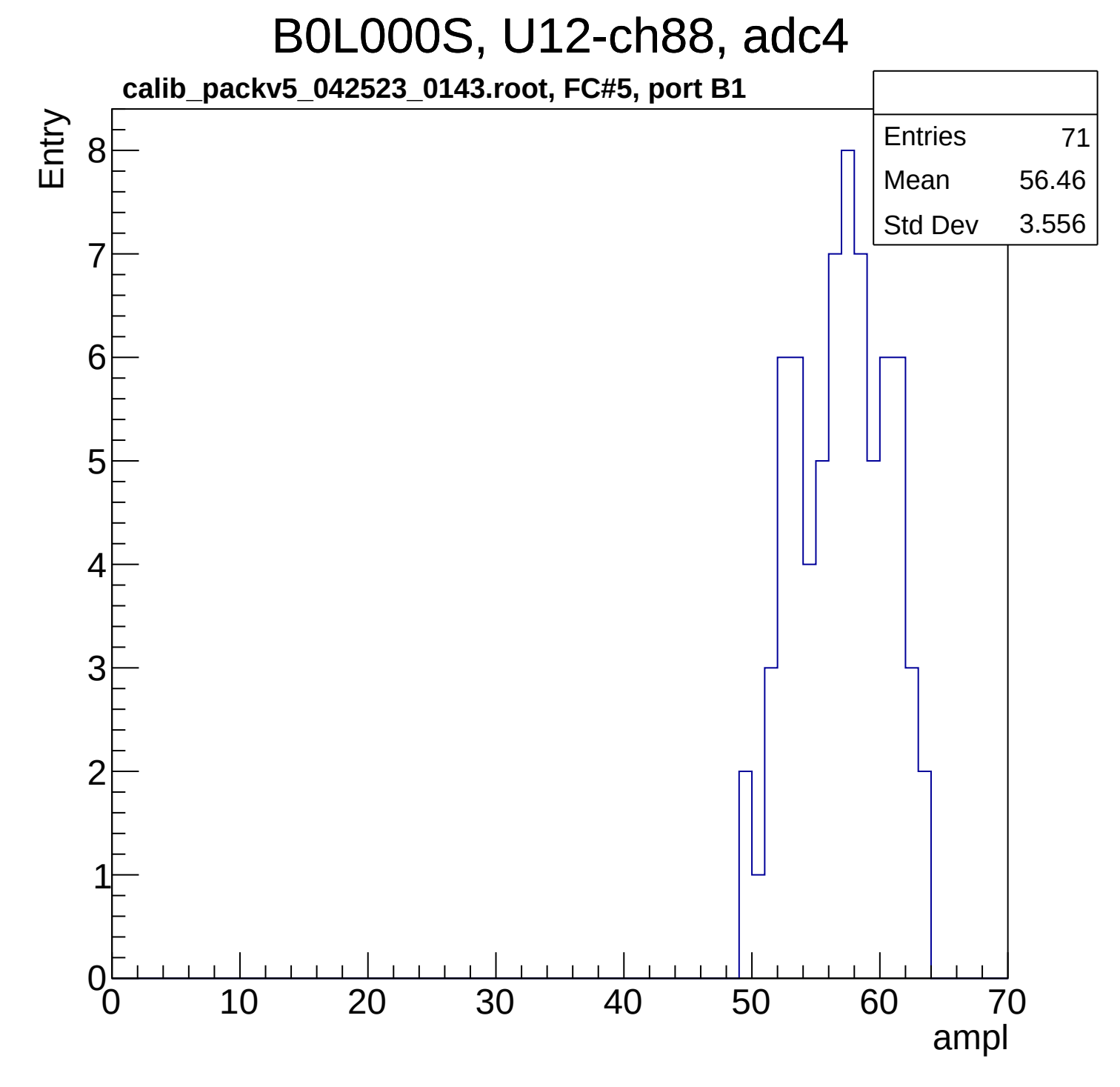
B0L000S, U12-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 56.46
Std Dev 3.556
Entry
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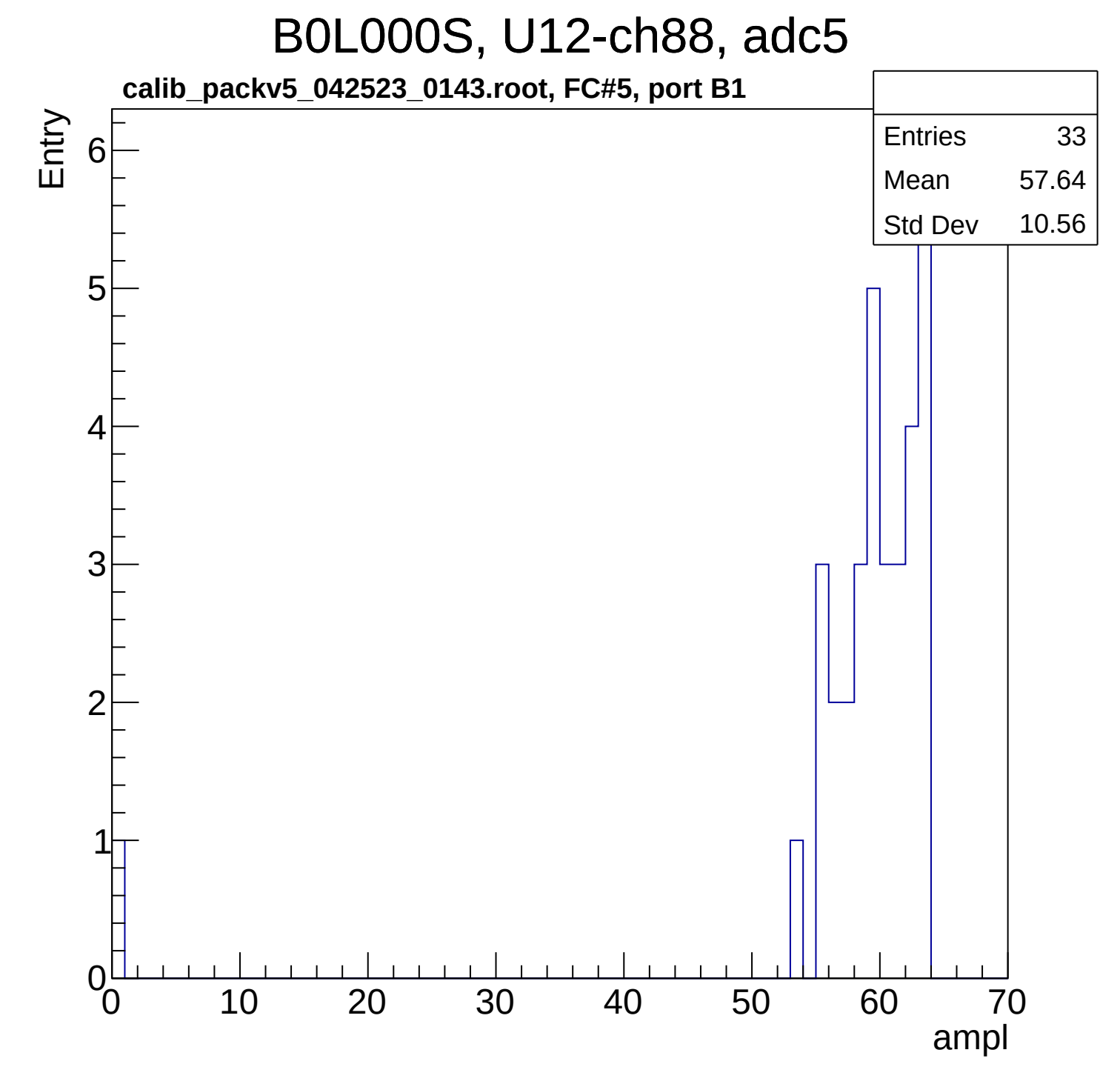
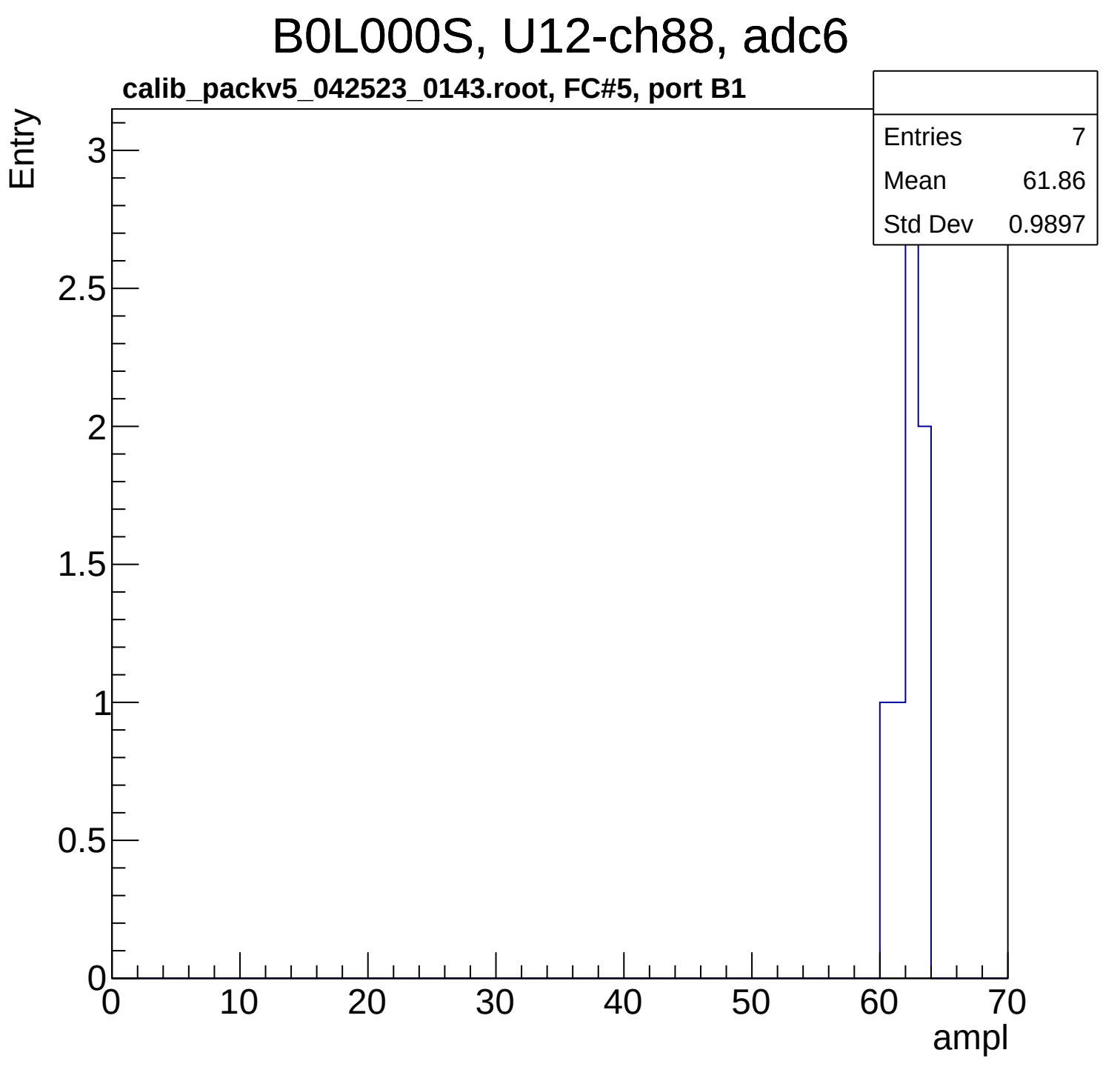
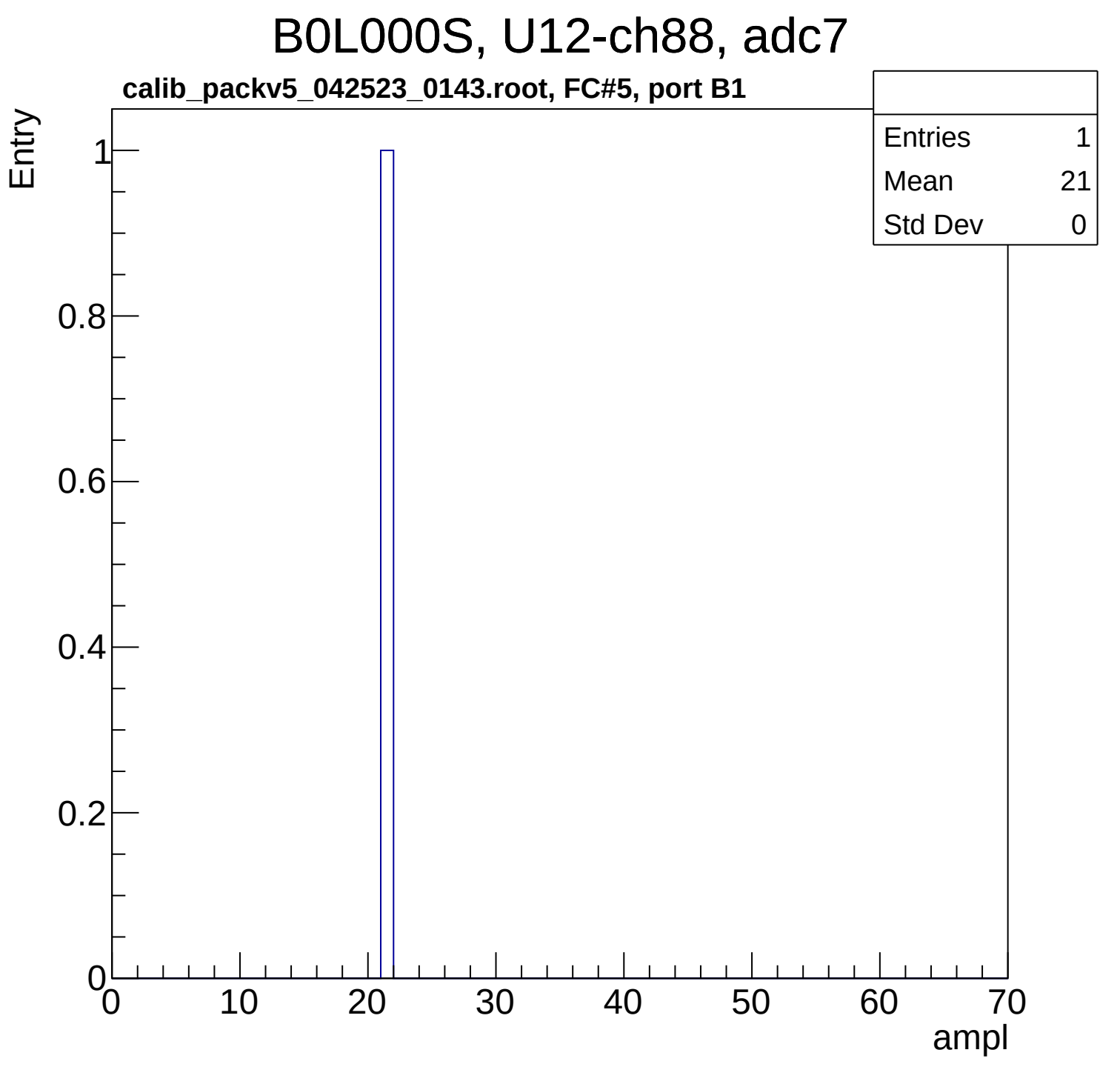
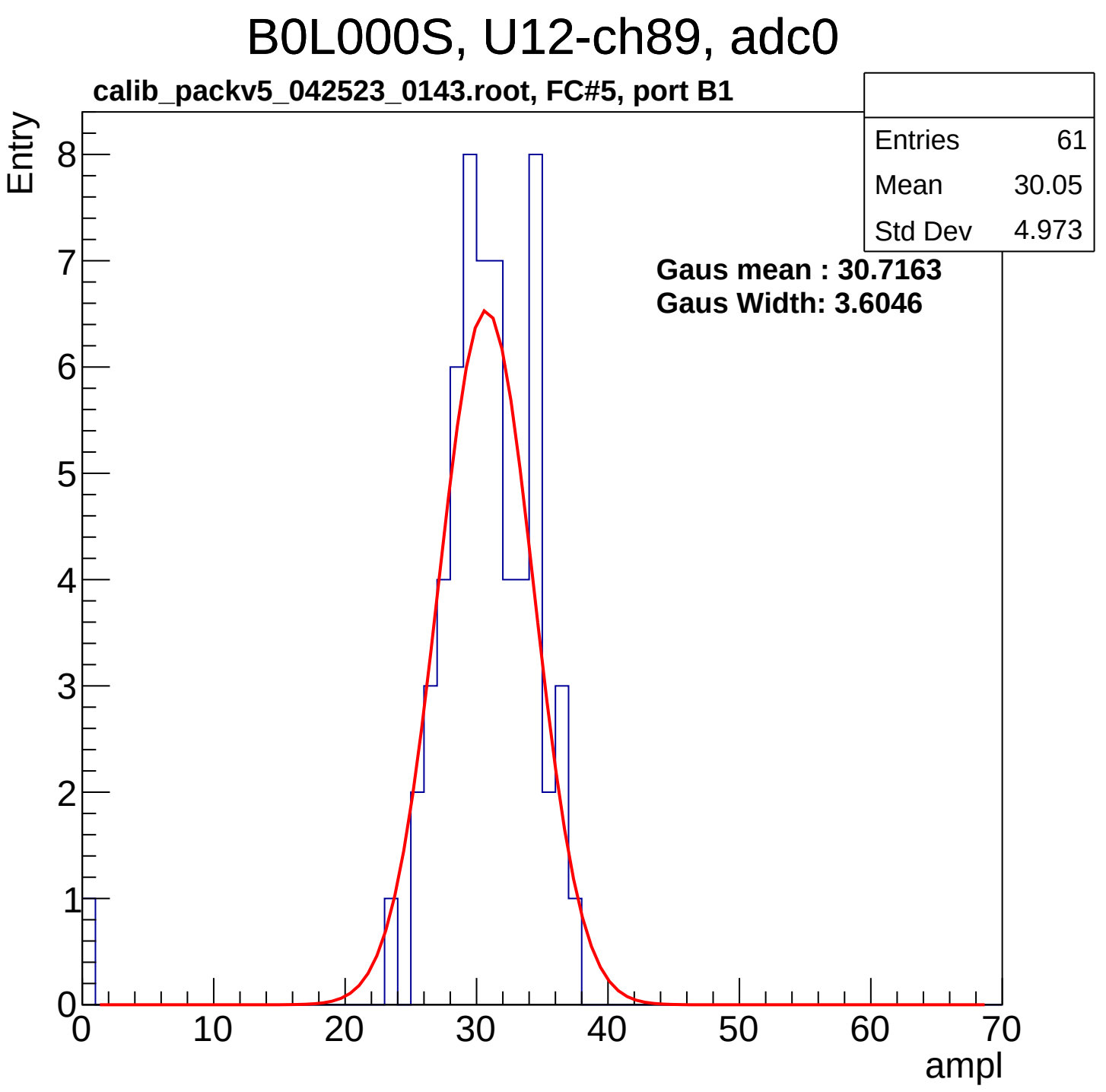
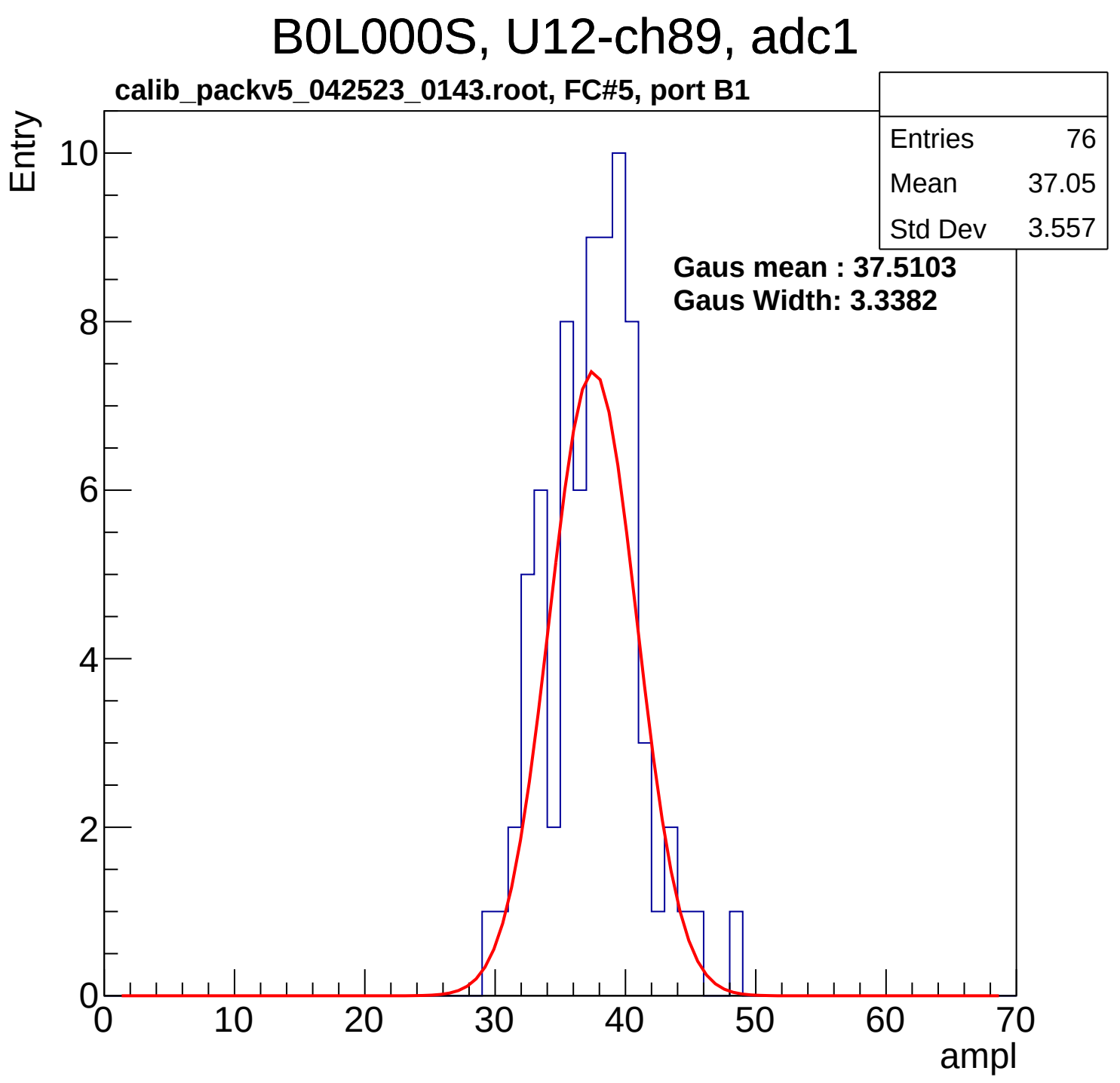
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 57.64
Std Dev 10.56
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U12-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 30.05
Std Dev 4.973
Gaus mean : 30.7163
Gaus Width: 3.6046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.05
Std Dev 3.557
Gaus mean : 37.5103
Gaus Width: 3.3382
Entry
ampl
0
2
4
6
8
10
0
10
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60
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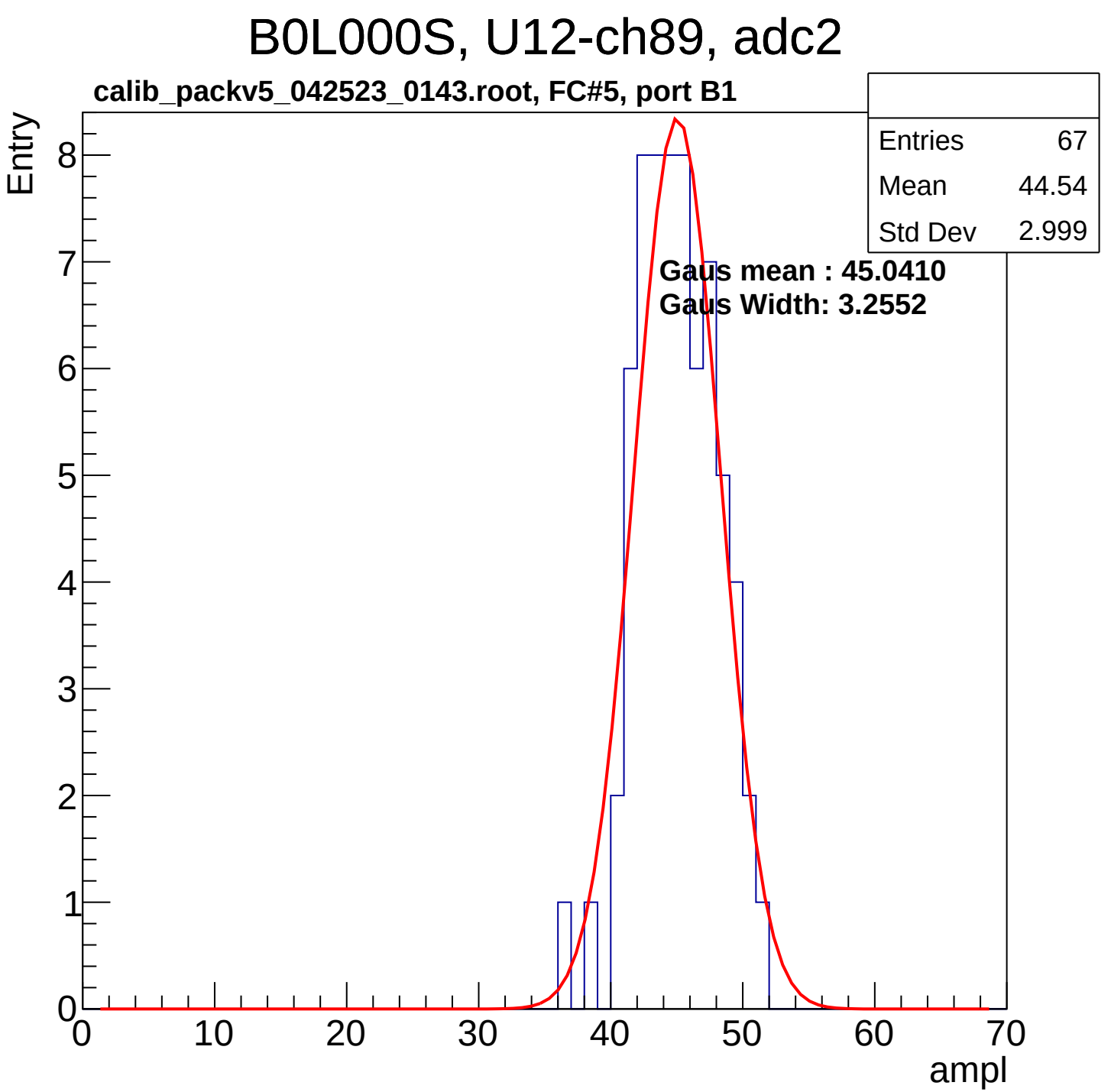
B0L000S, U12-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.54
Std Dev 2.999
Gaus mean : 45.0410
Gaus Width: 3.2552
Entry
ampl
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1
2
3
4
5
6
7
8
0
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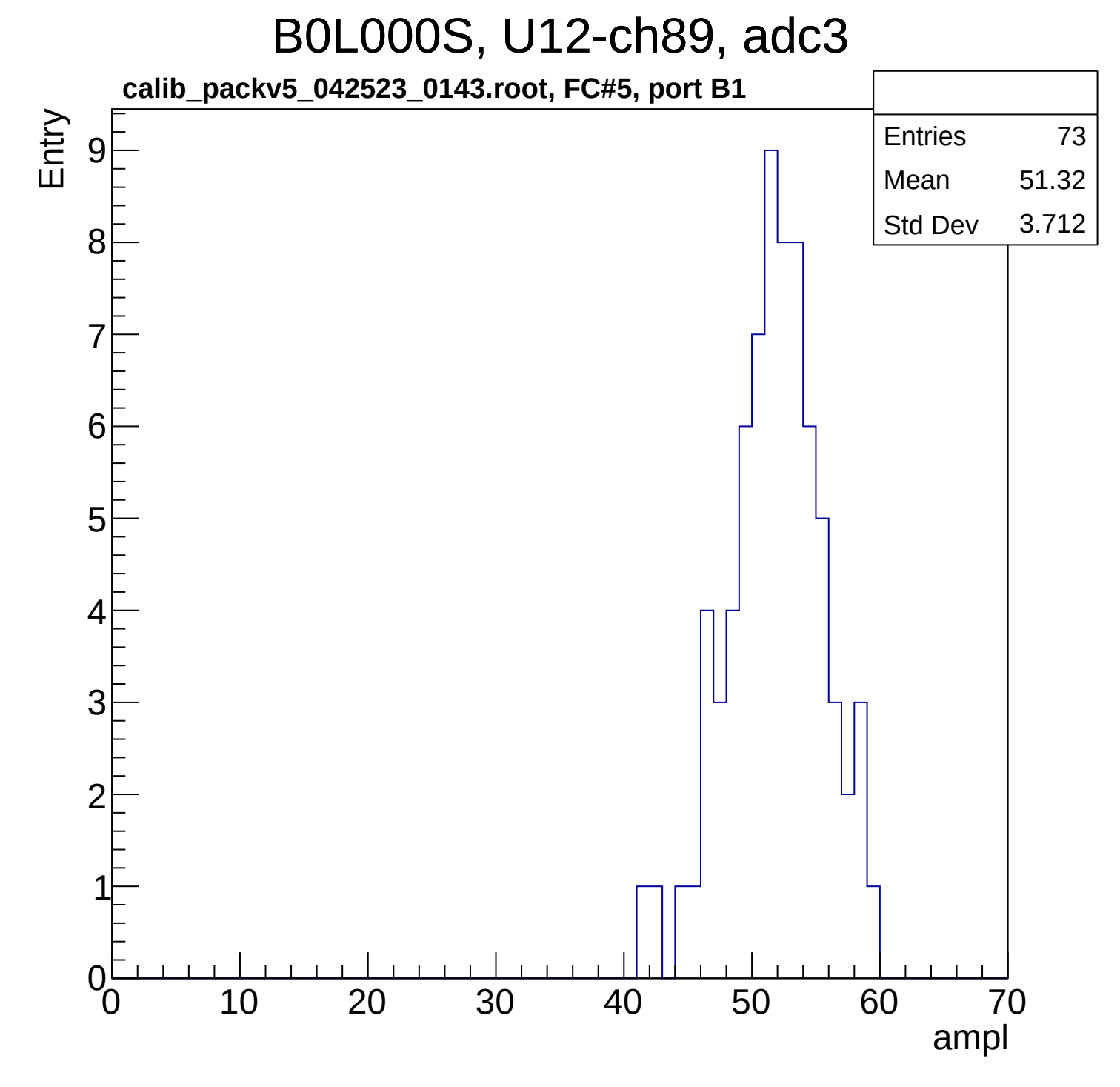
B0L000S, U12-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 51.32
Std Dev 3.712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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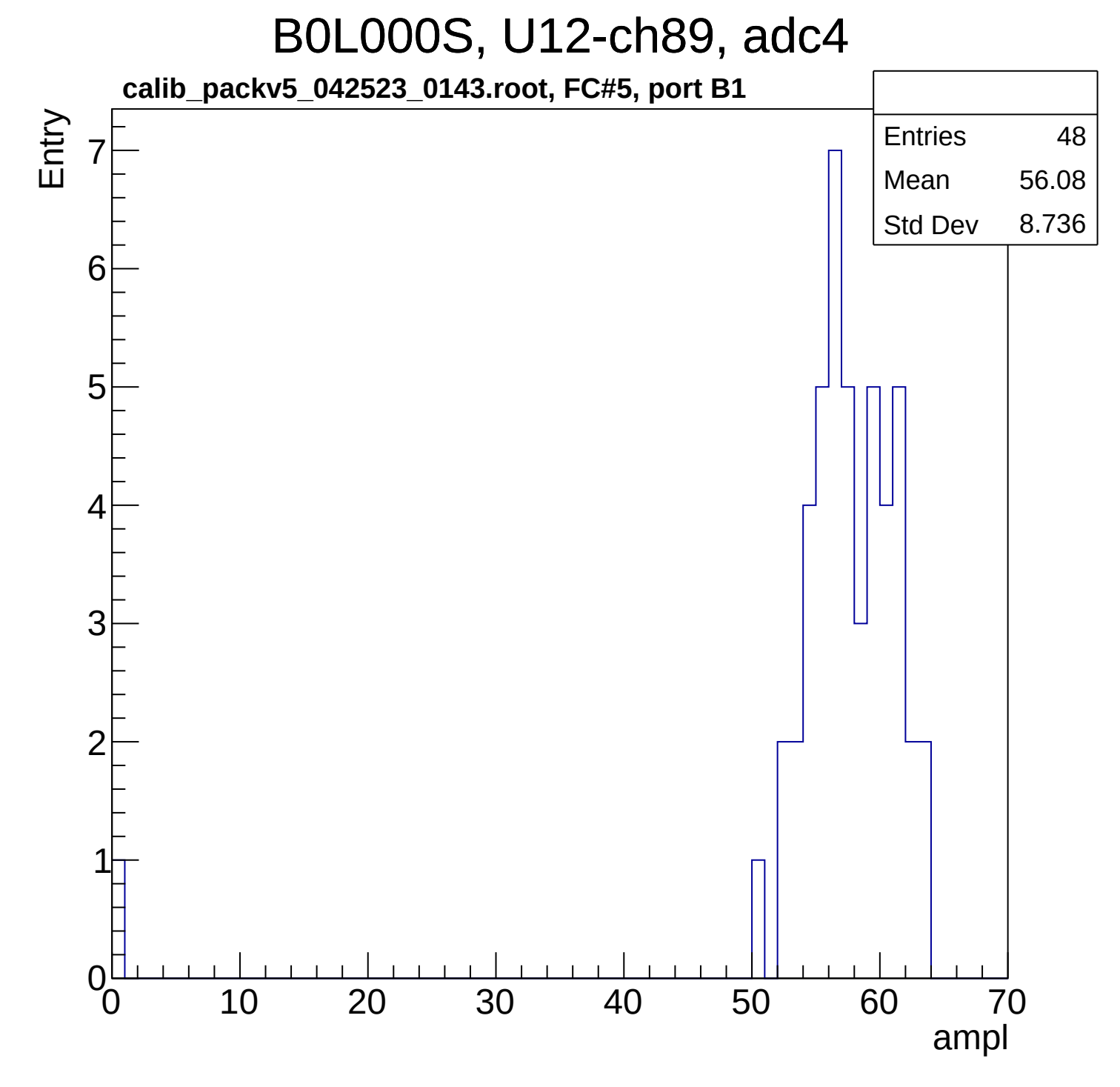
B0L000S, U12-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.08
Std Dev 8.736
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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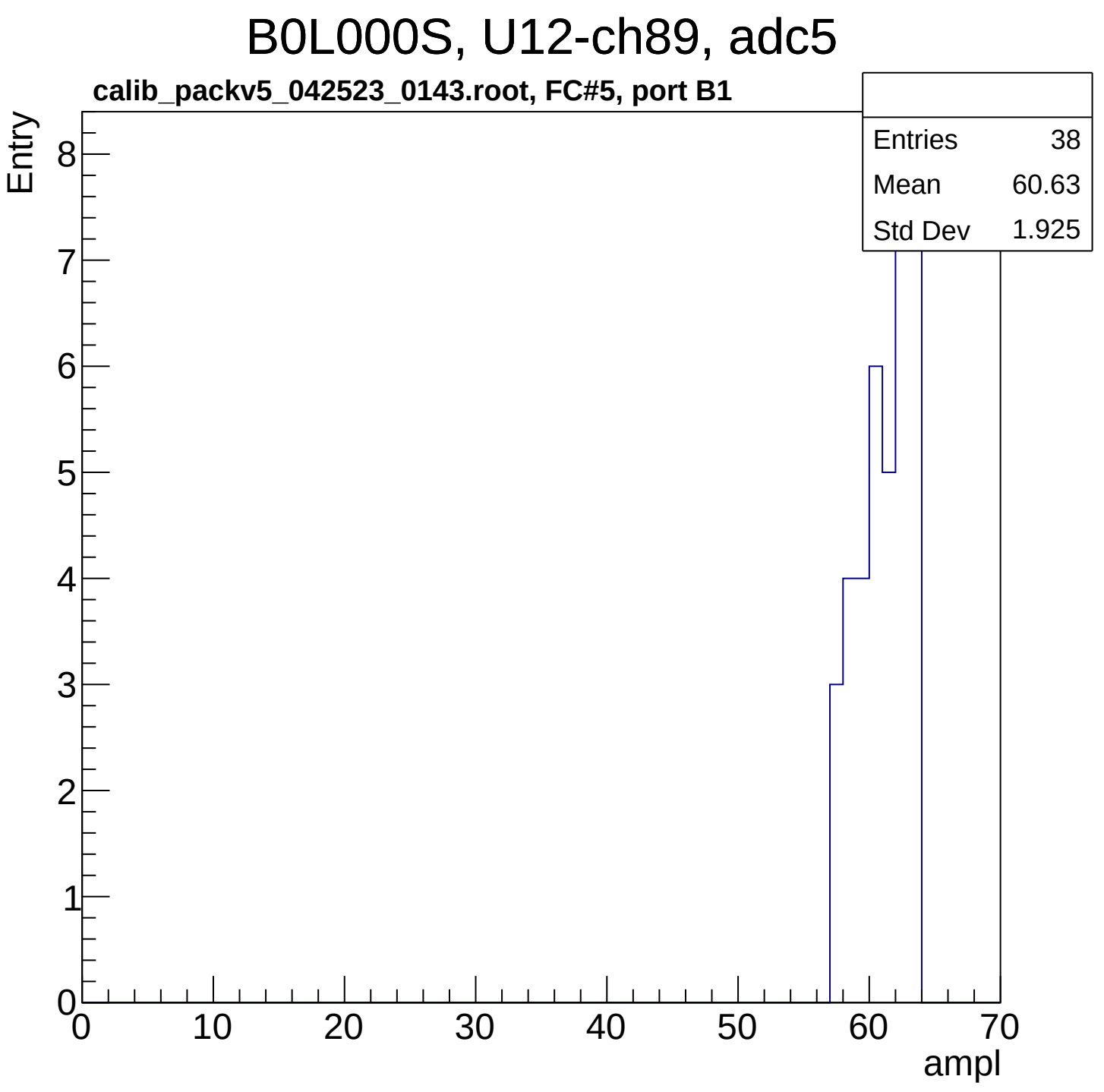
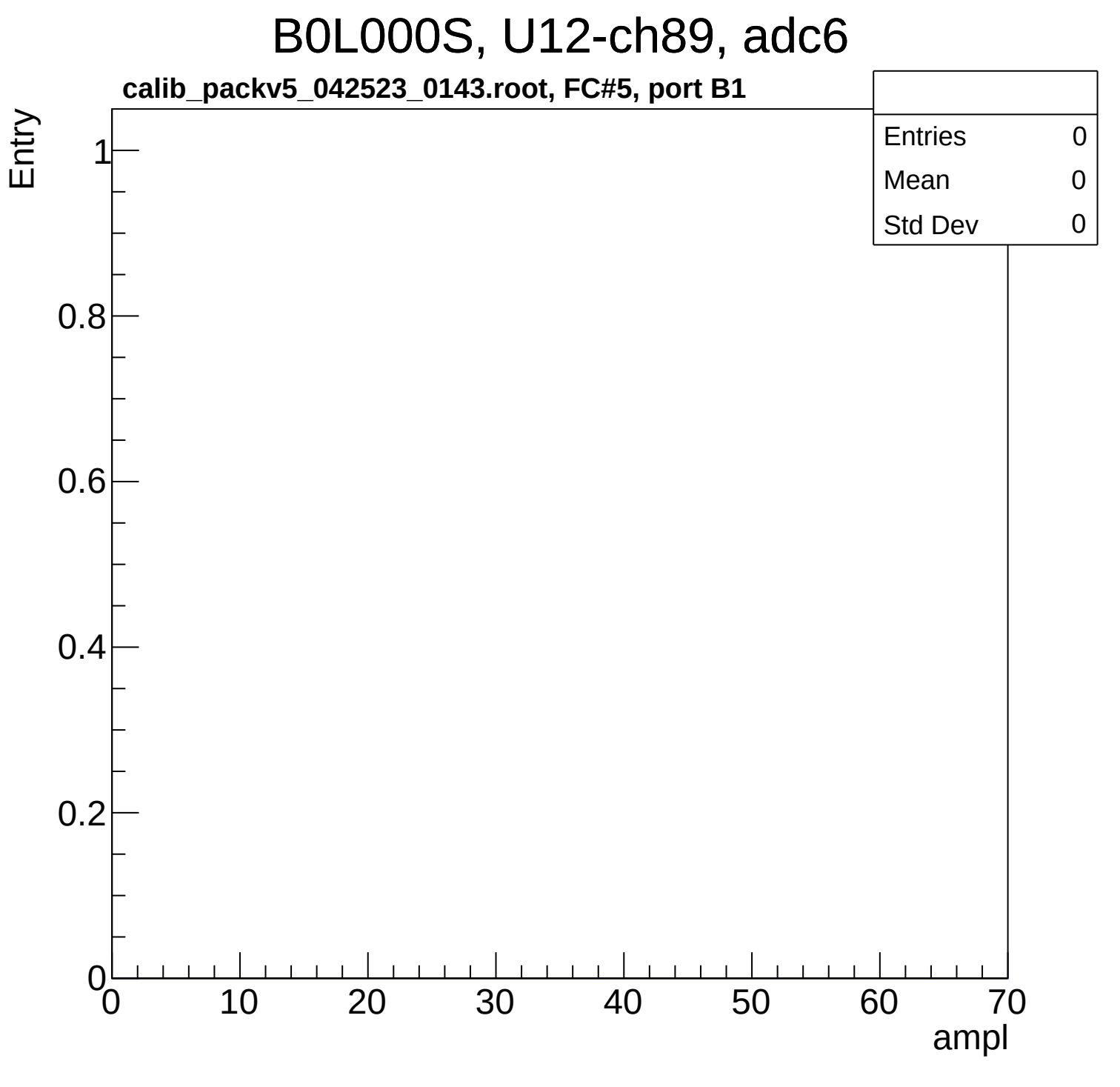
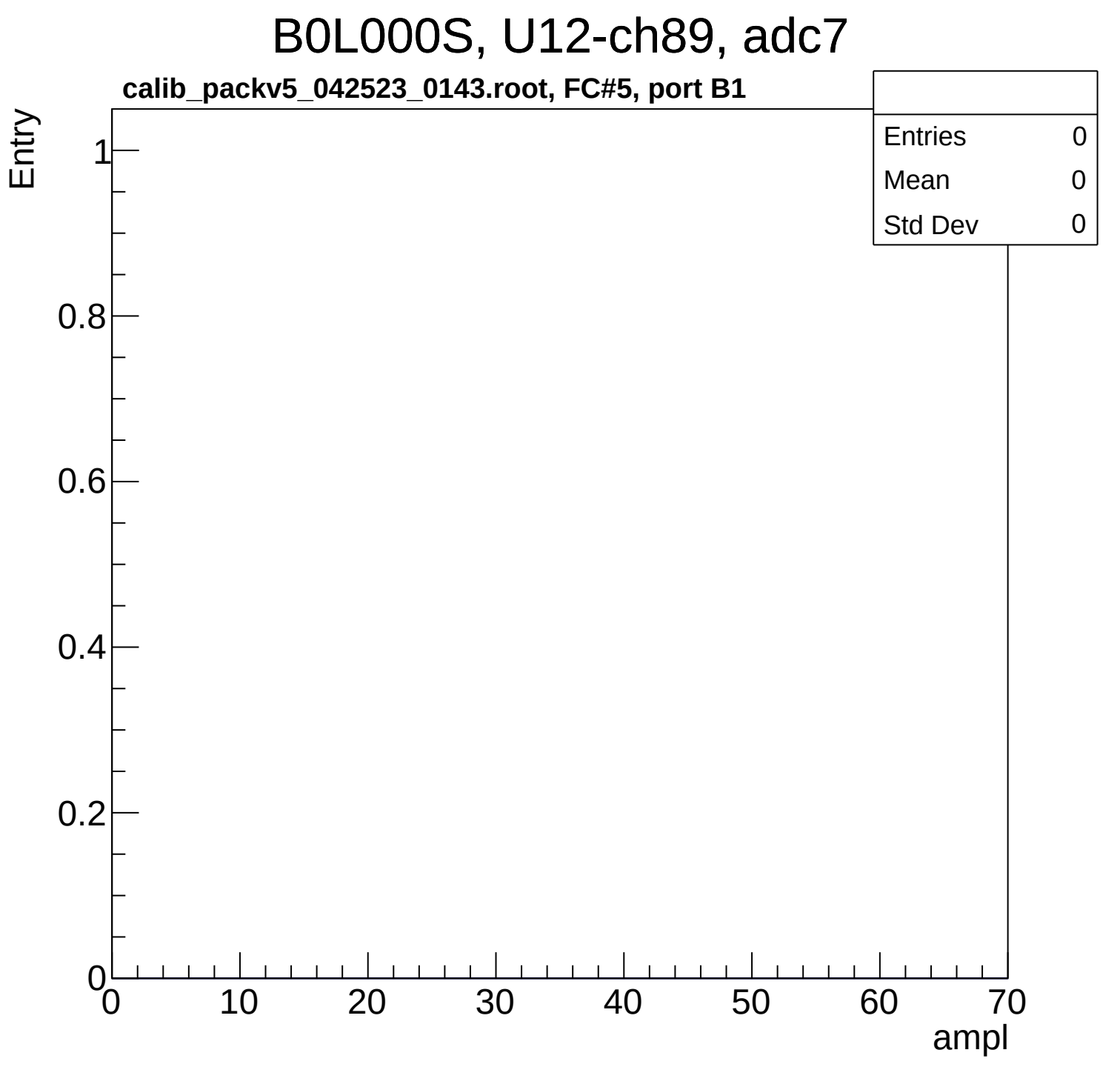

B0L000S, U12-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 60.63
Std Dev 1.925
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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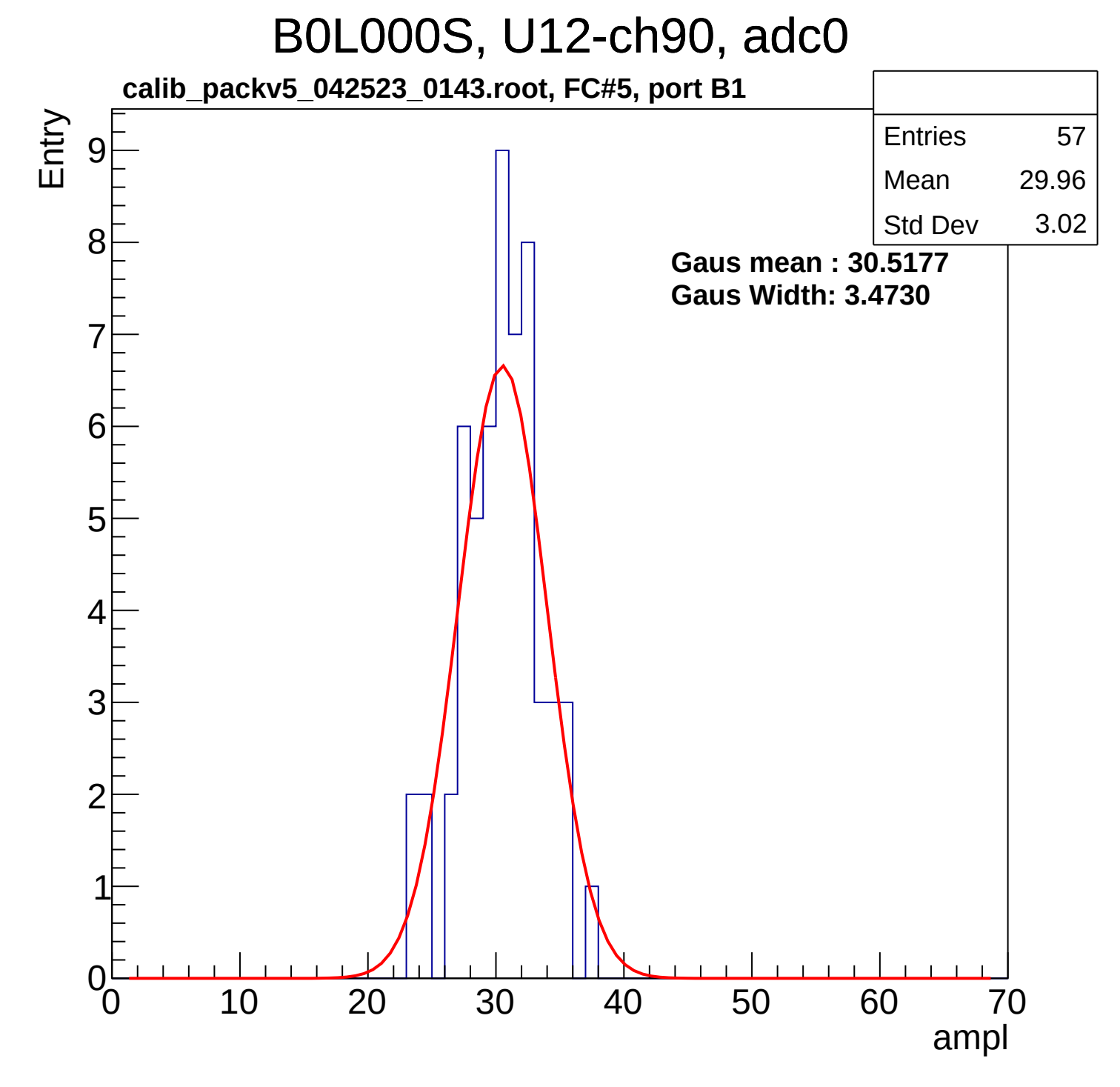
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.96
Std Dev 3.02
Gaus mean : 30.5177
Gaus Width: 3.4730
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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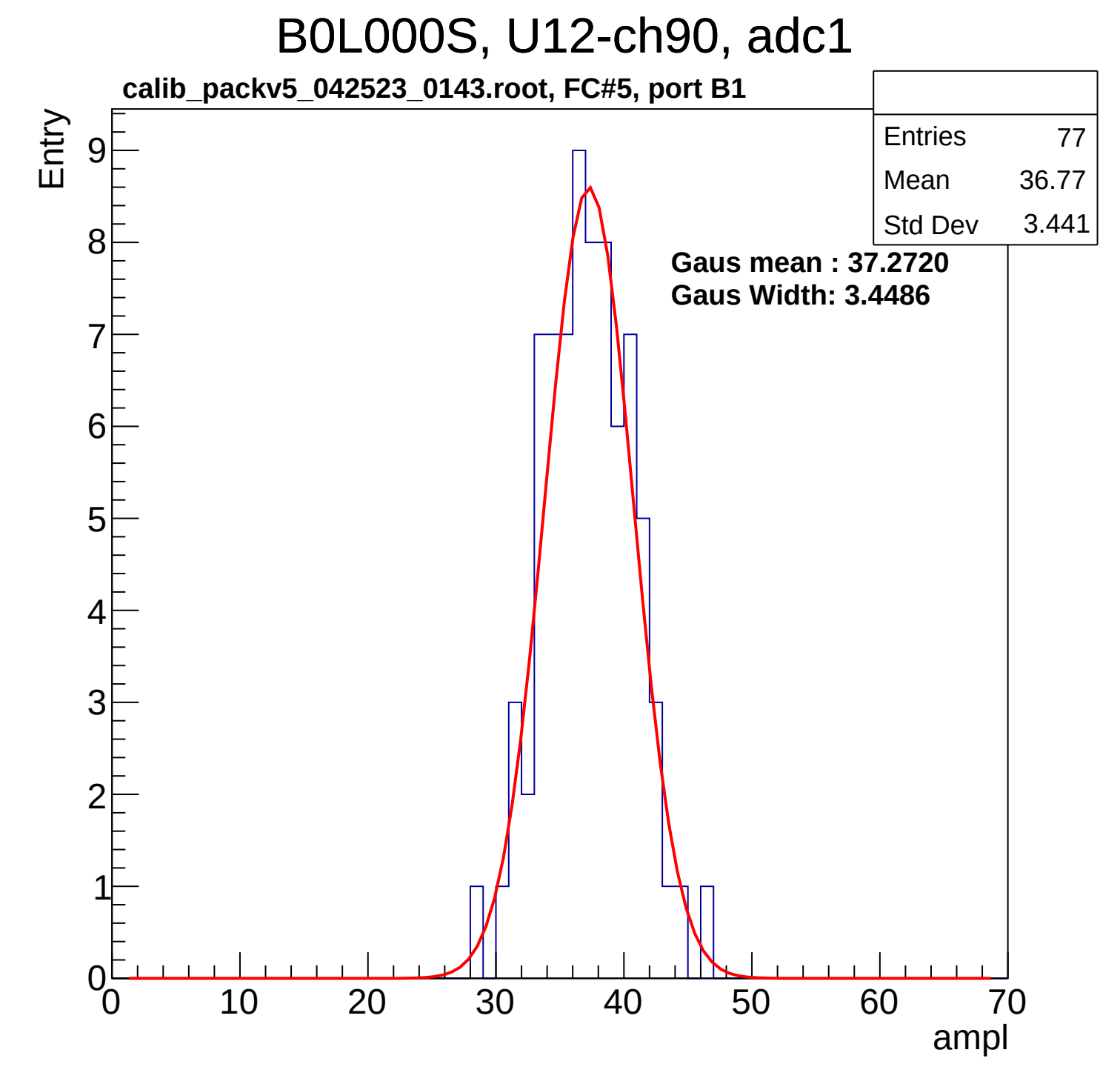
B0L000S, U12-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.77
Std Dev 3.441
Gaus mean : 37.2720
Gaus Width: 3.4486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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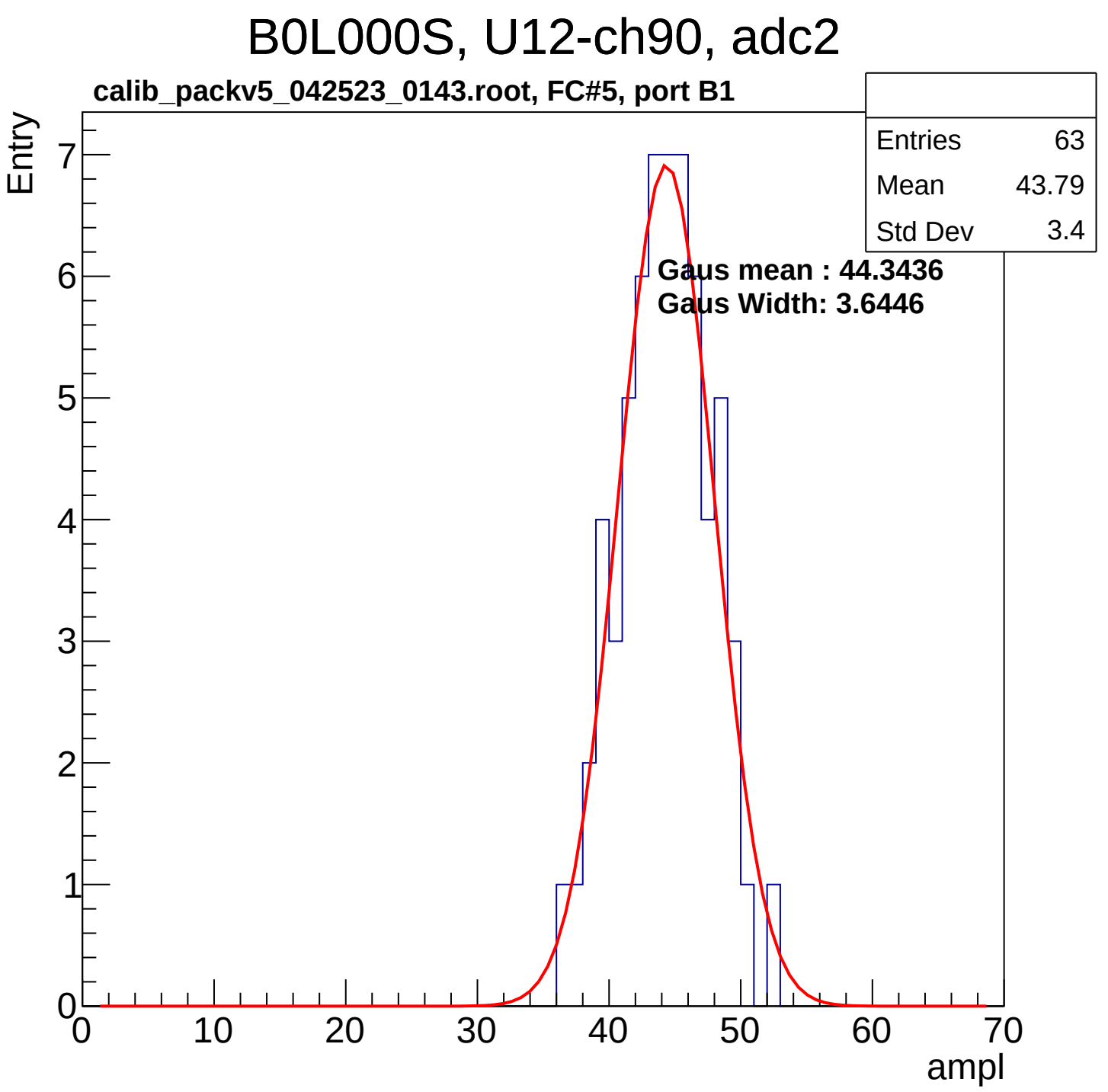
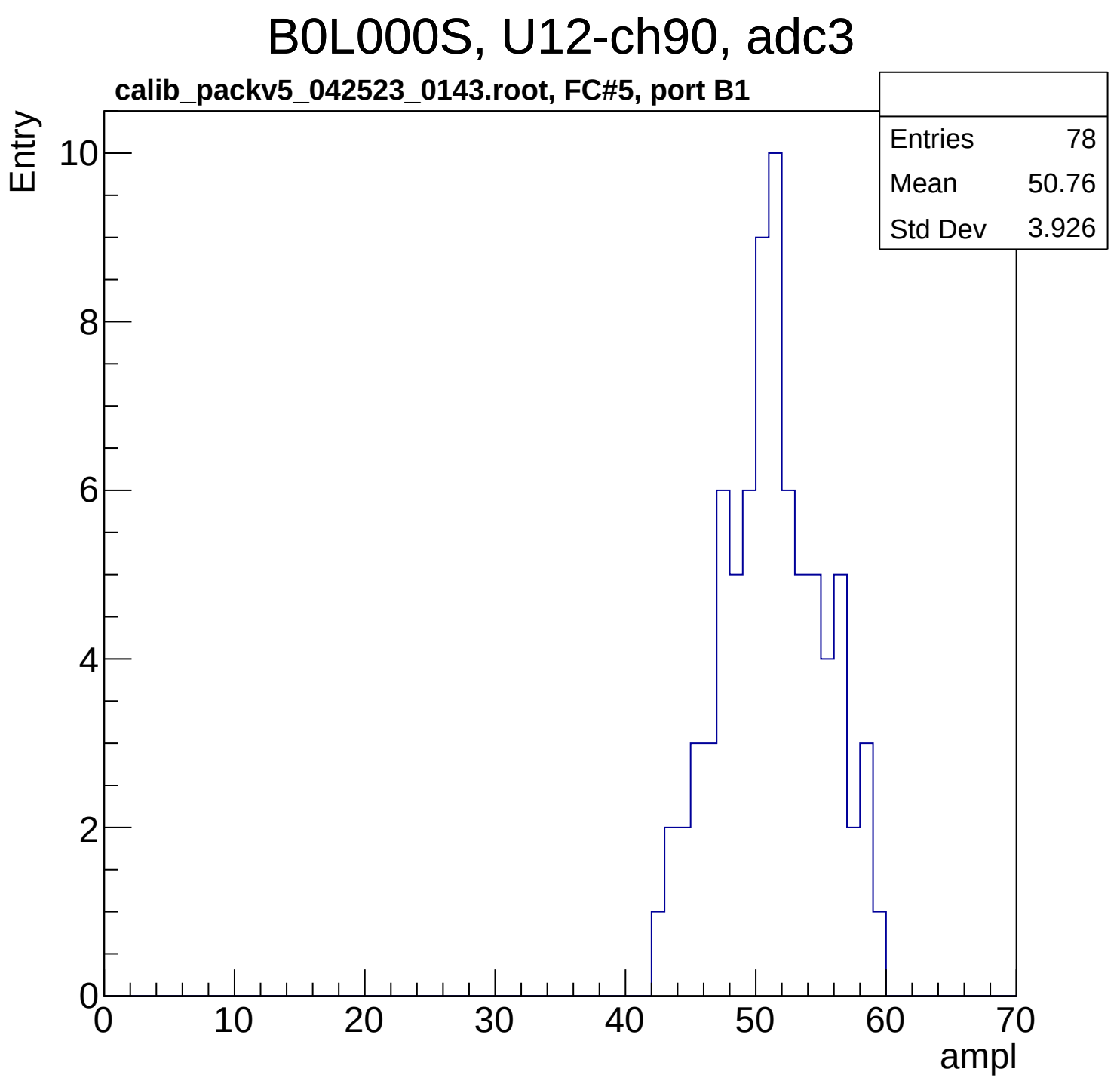
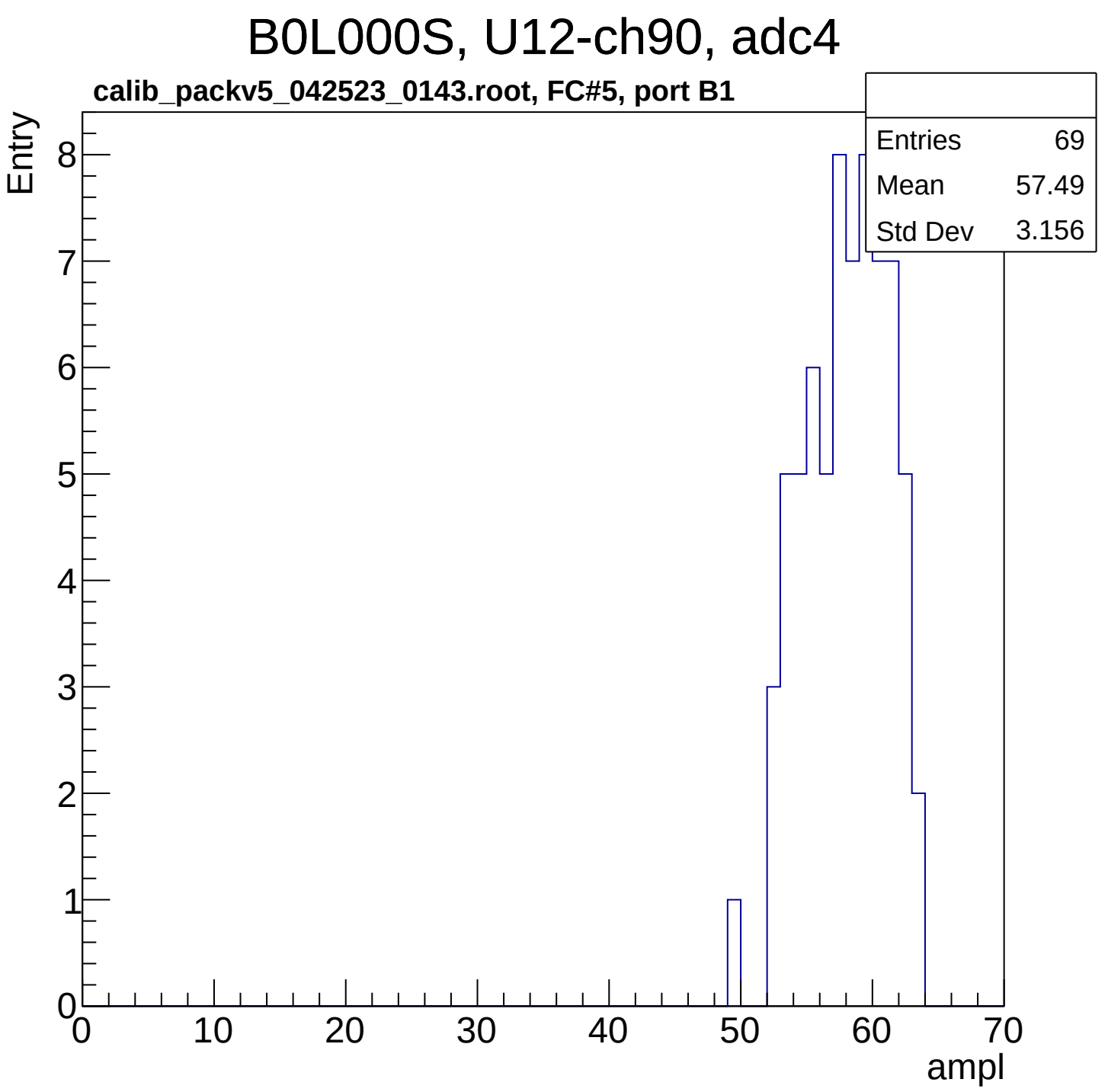

B0L000S, U12-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.79
Std Dev 3.4
Gaus mean : 44.3436
Gaus Width: 3.6446
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 50.76
Std Dev 3.926
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 57.49
Std Dev 3.156
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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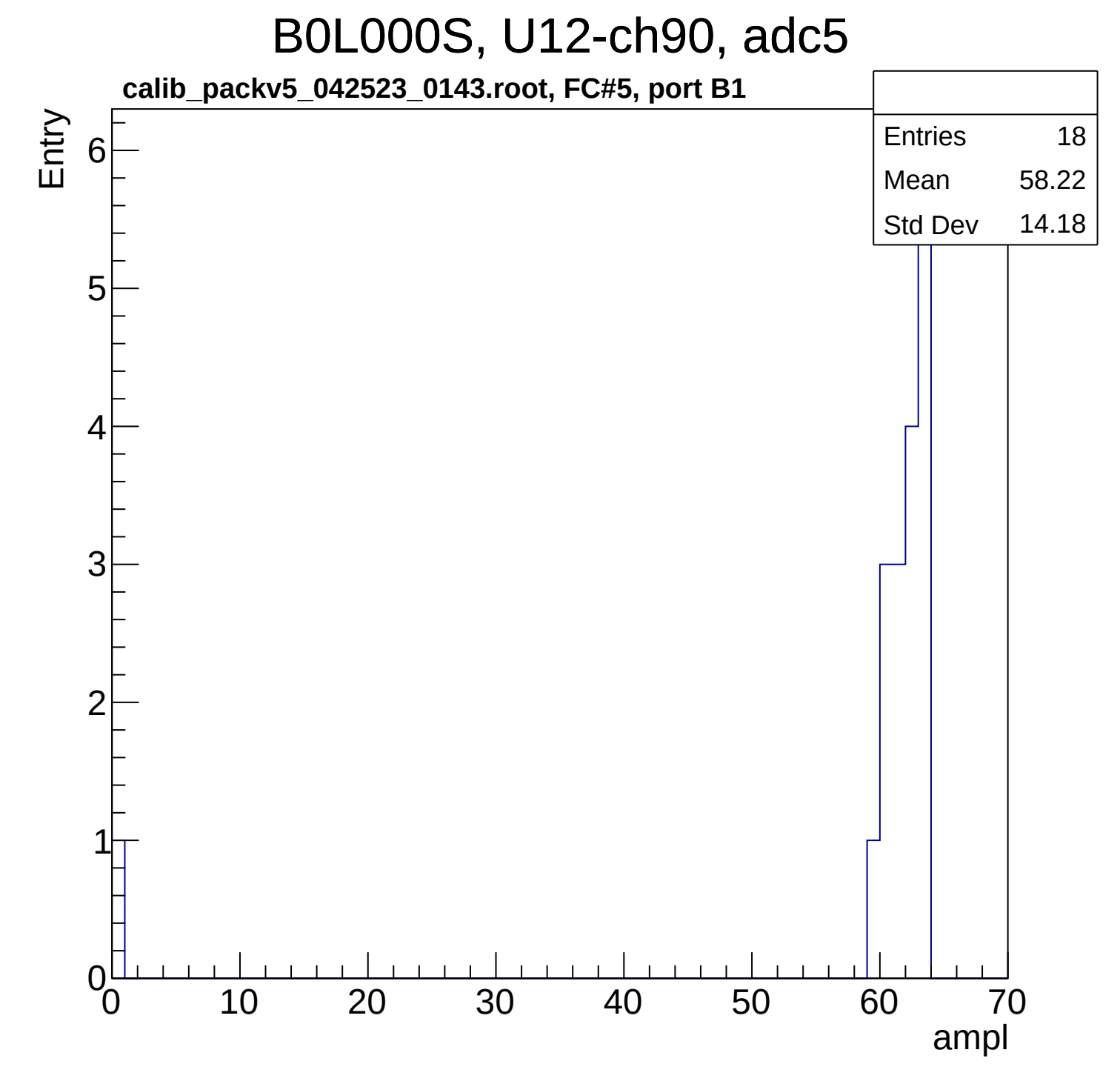
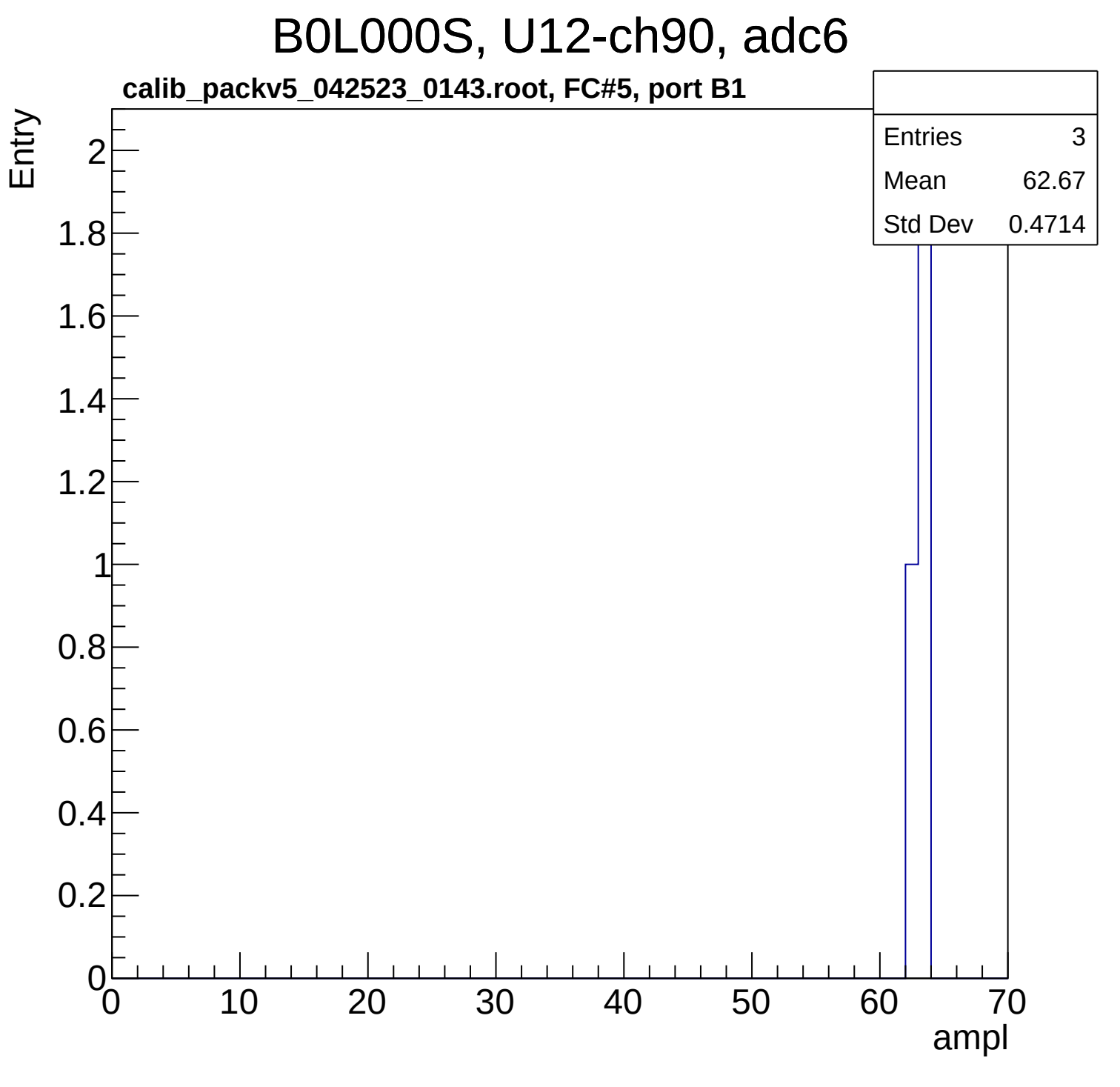
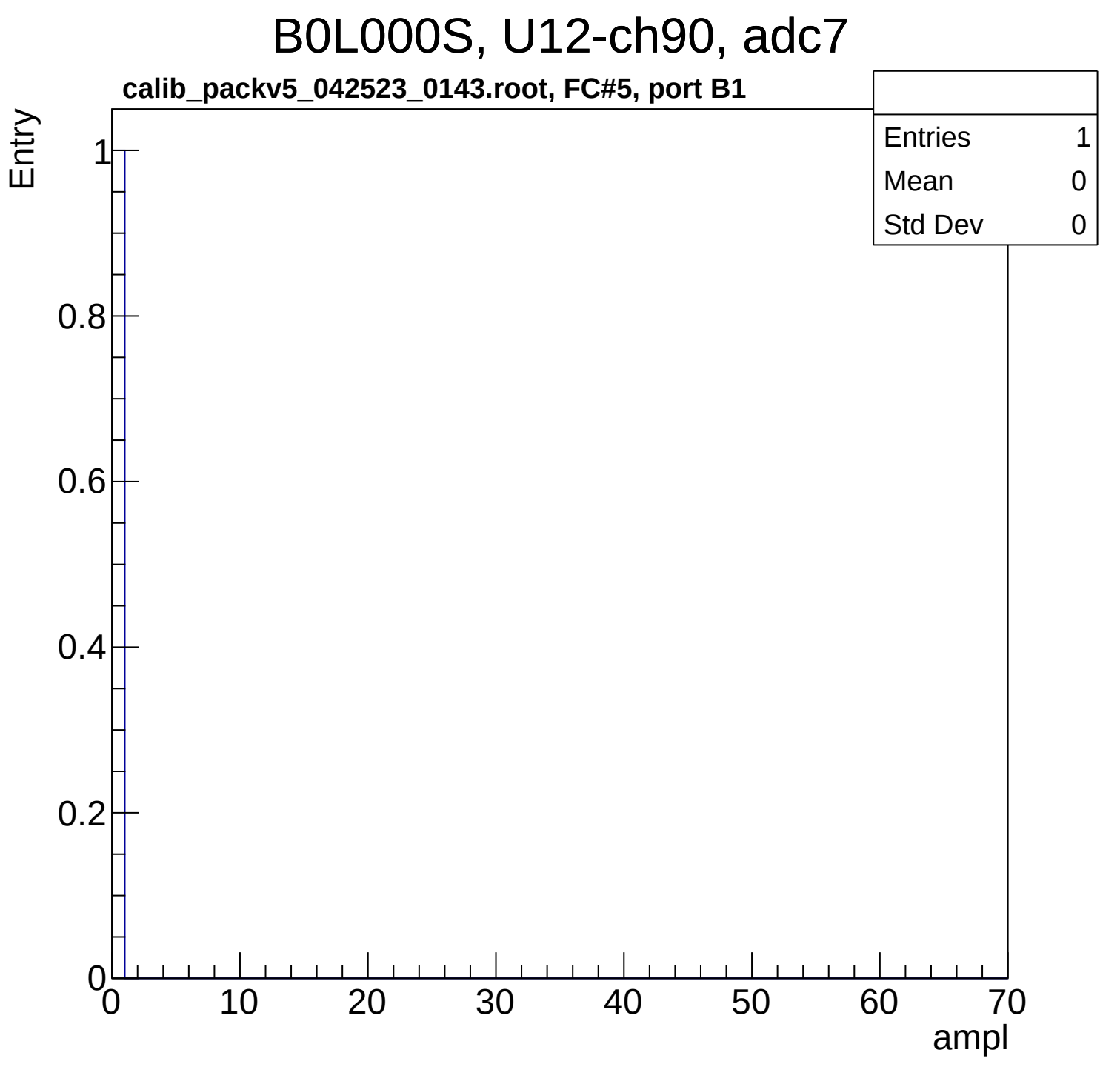
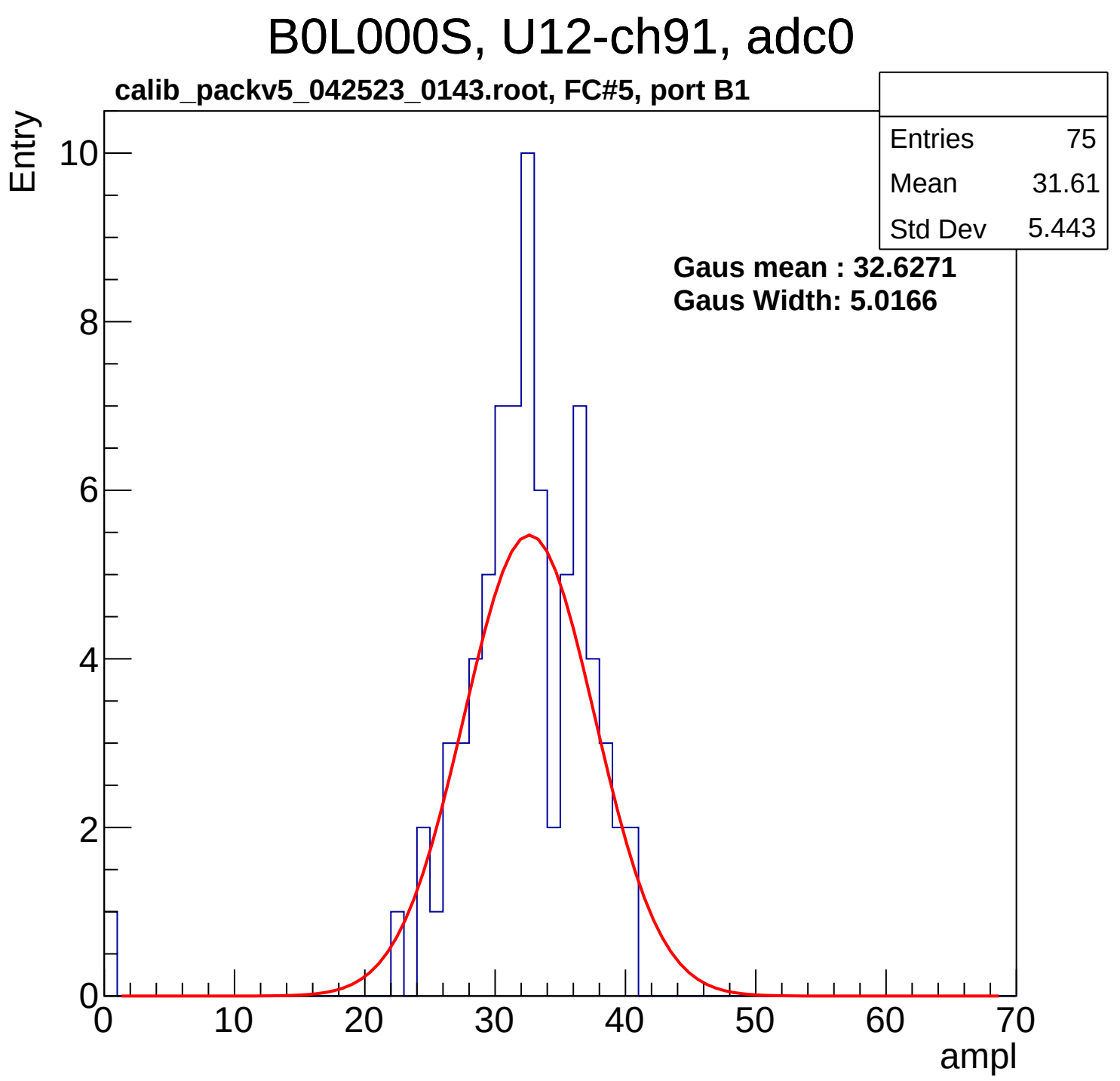
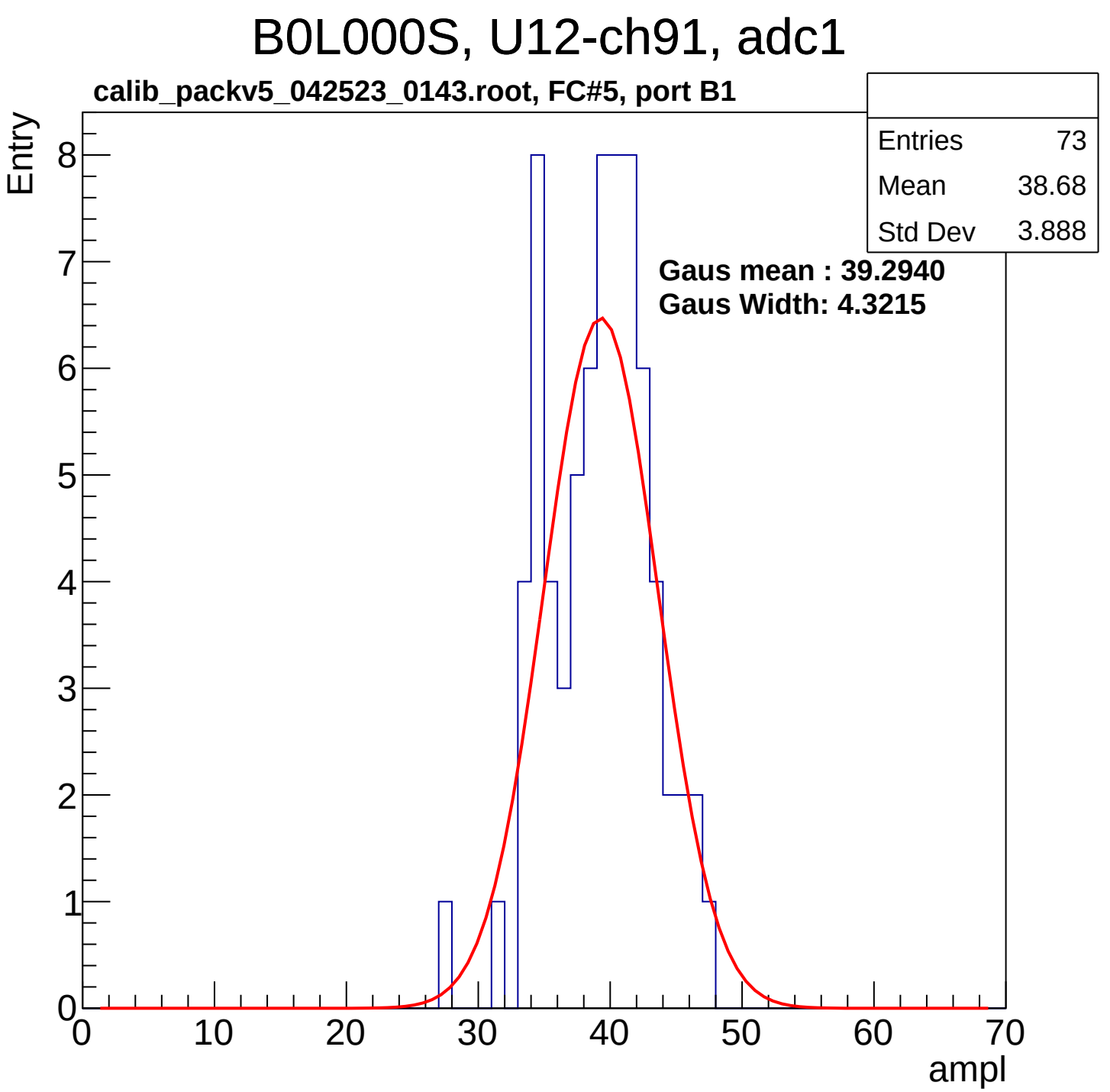
40
50
60
70

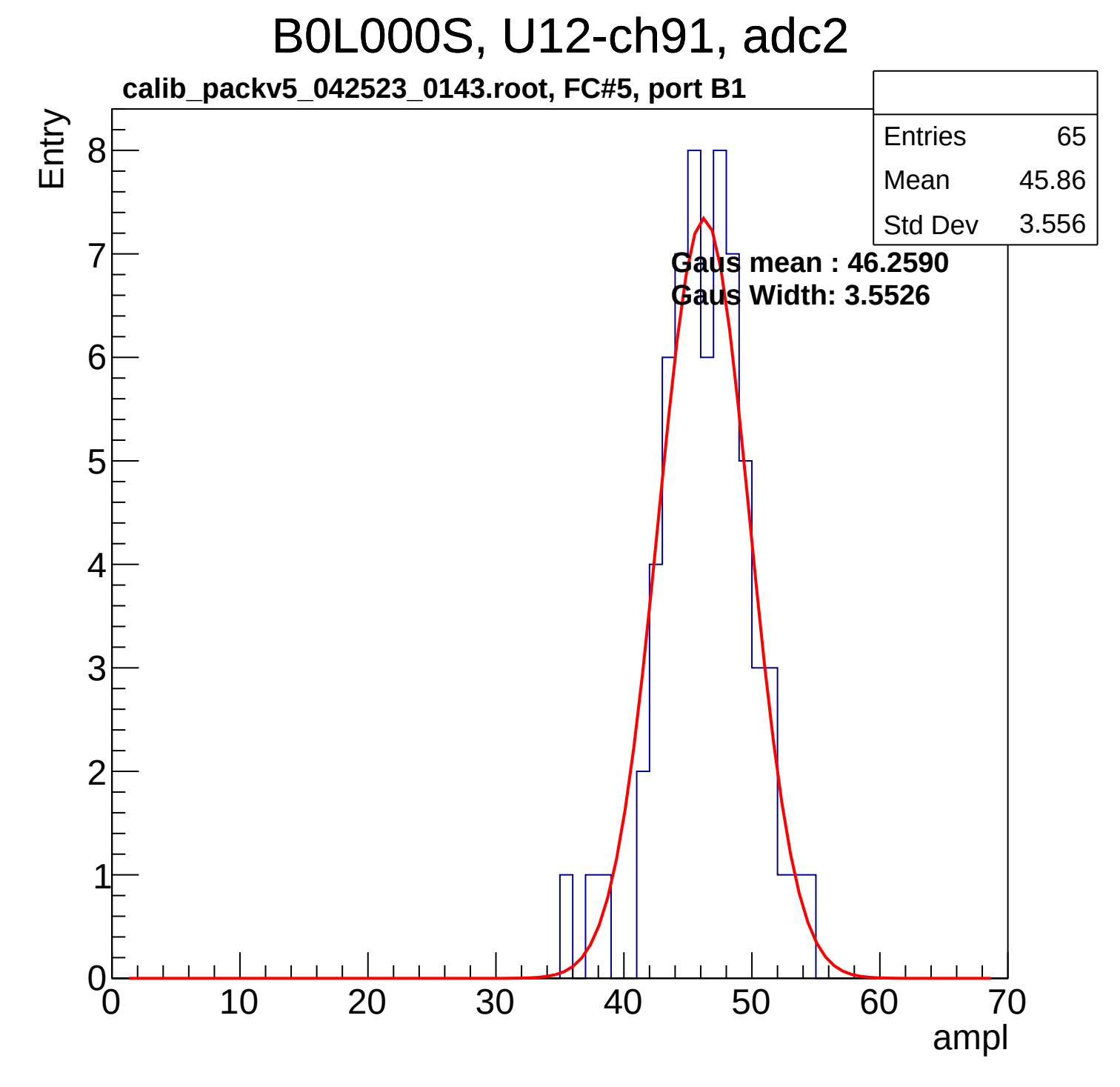

B0L000S, U12-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.22
Std Dev 14.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

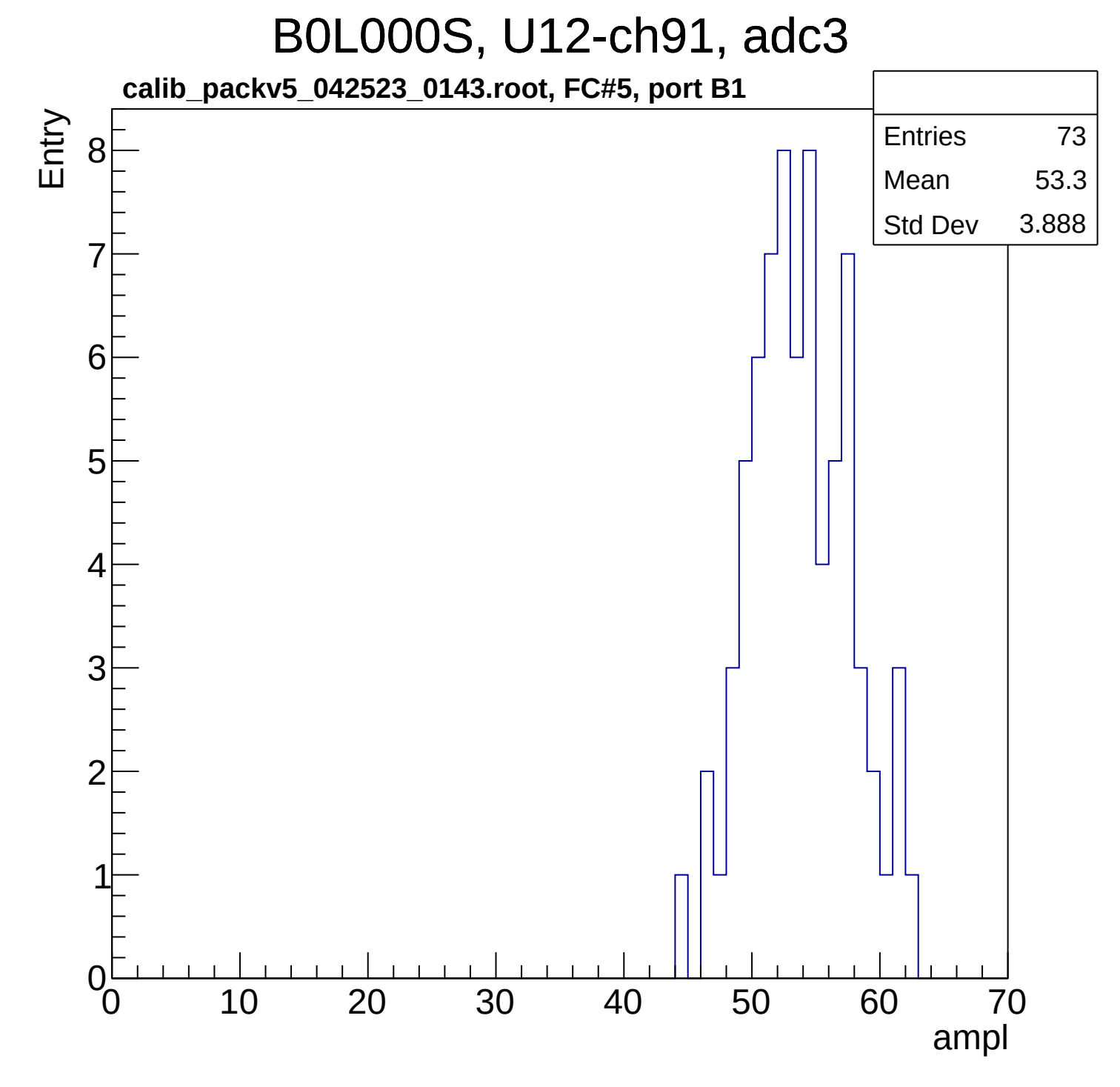

B0L000S, U12-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 31.61
Std Dev 5.443
Gaus mean : 32.6271
Gaus Width: 5.0166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 38.68
Std Dev 3.888
Gaus mean : 39.2940
Gaus Width: 4.3215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 45.86
Std Dev 3.556
Gaus mean : 46.2590
Gaus Width: 3.5526
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 53.3
Std Dev 3.888
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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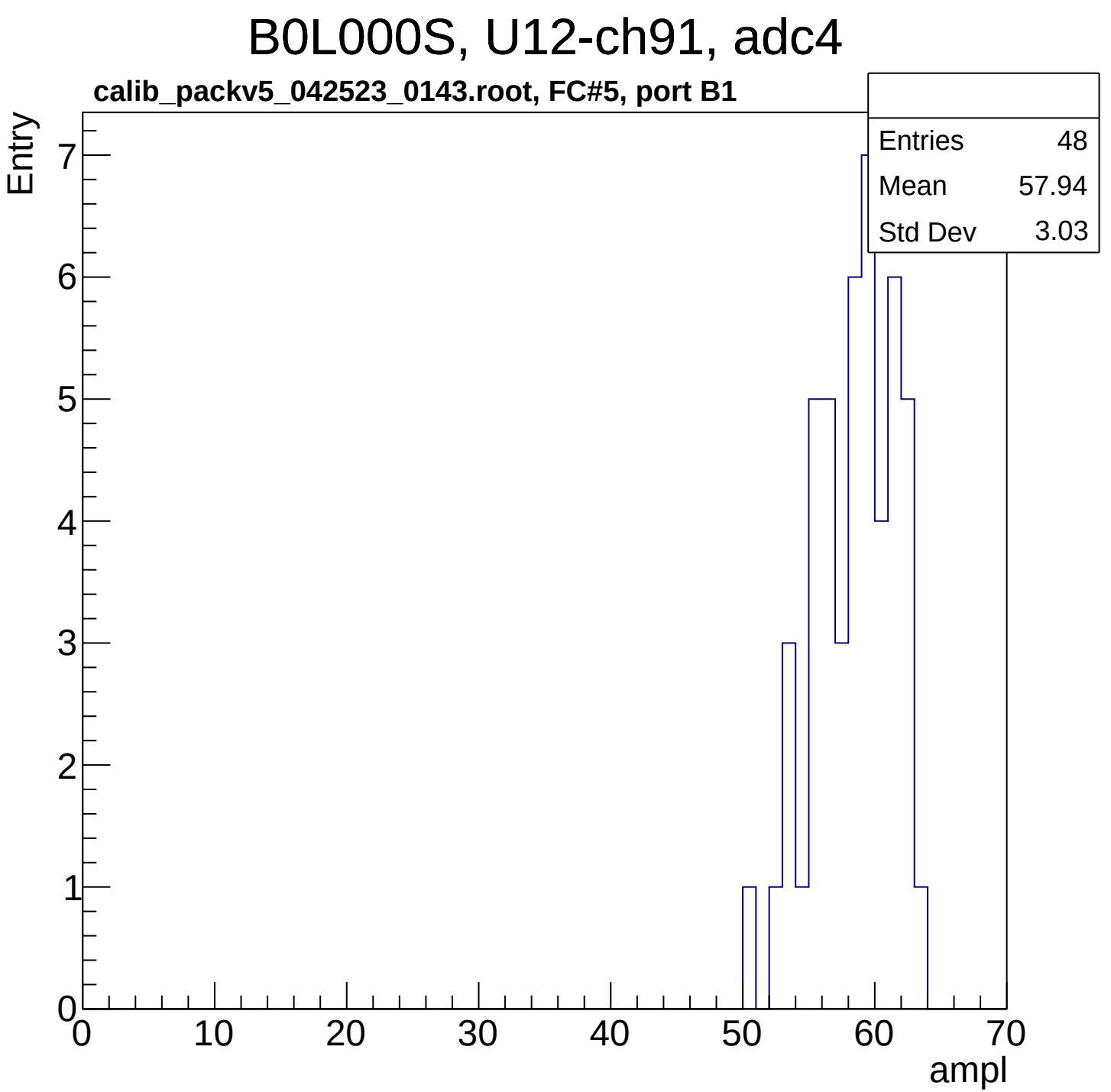
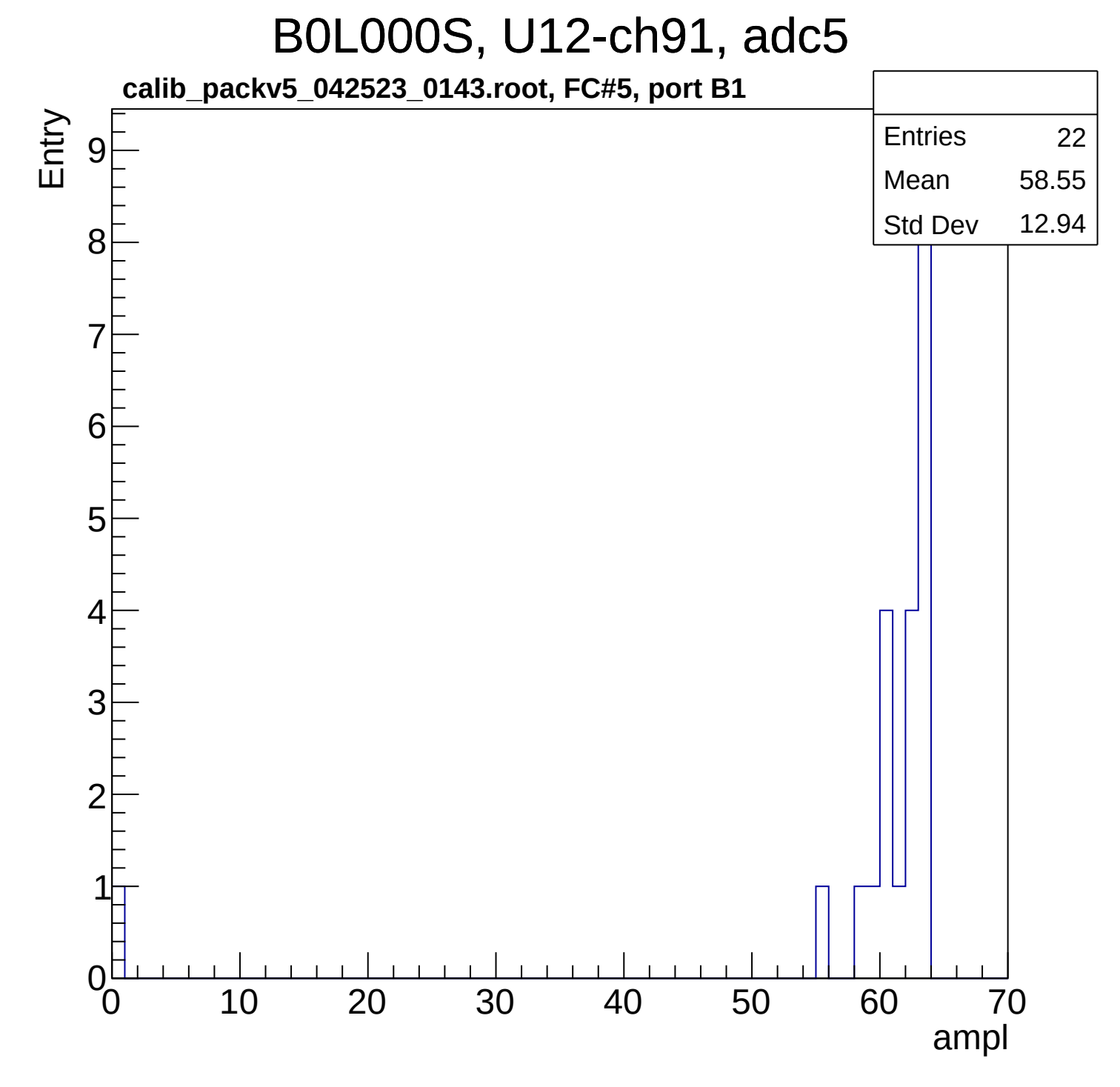

B0L000S, U12-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 57.94
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.55
Std Dev 12.94
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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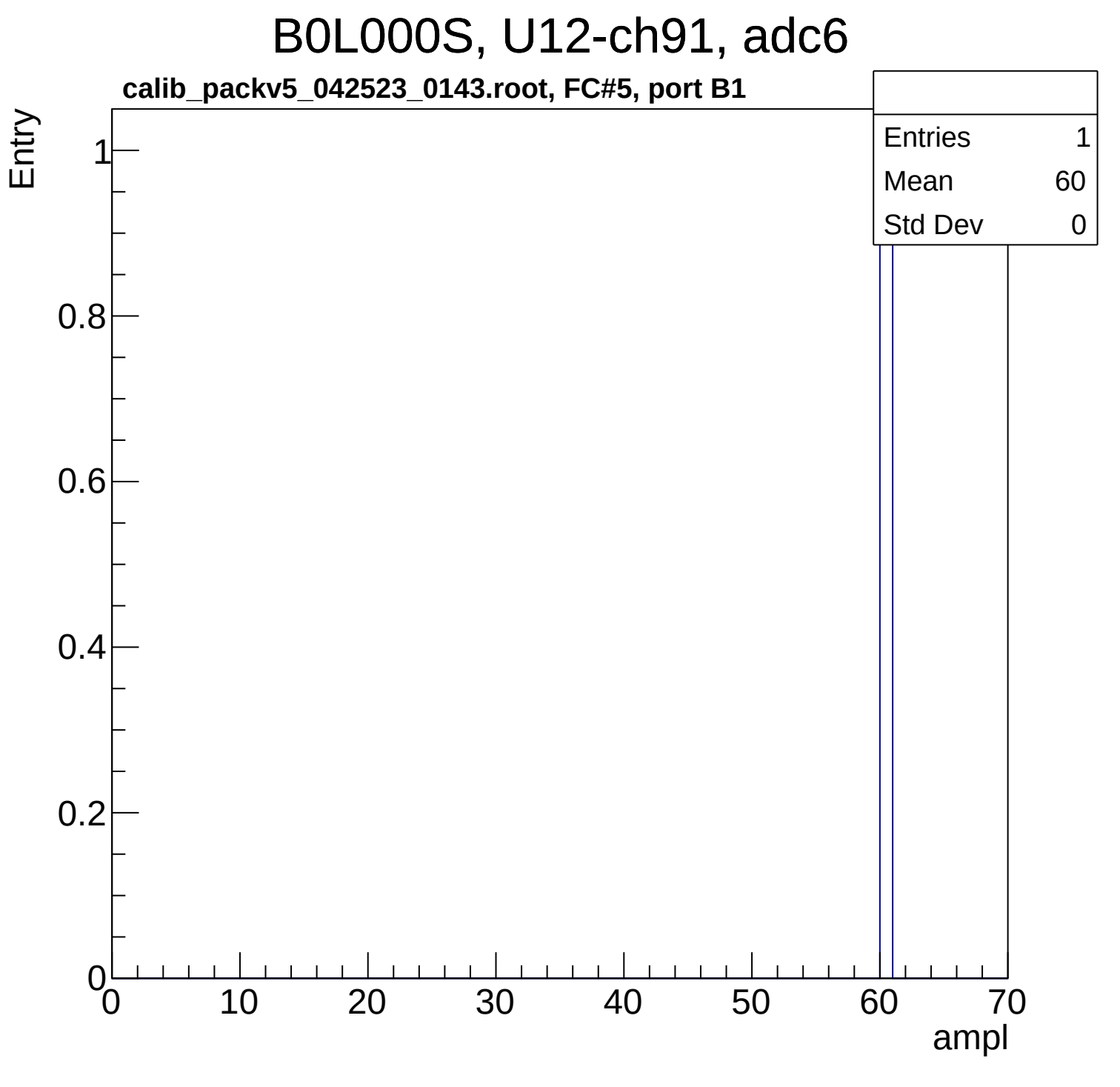
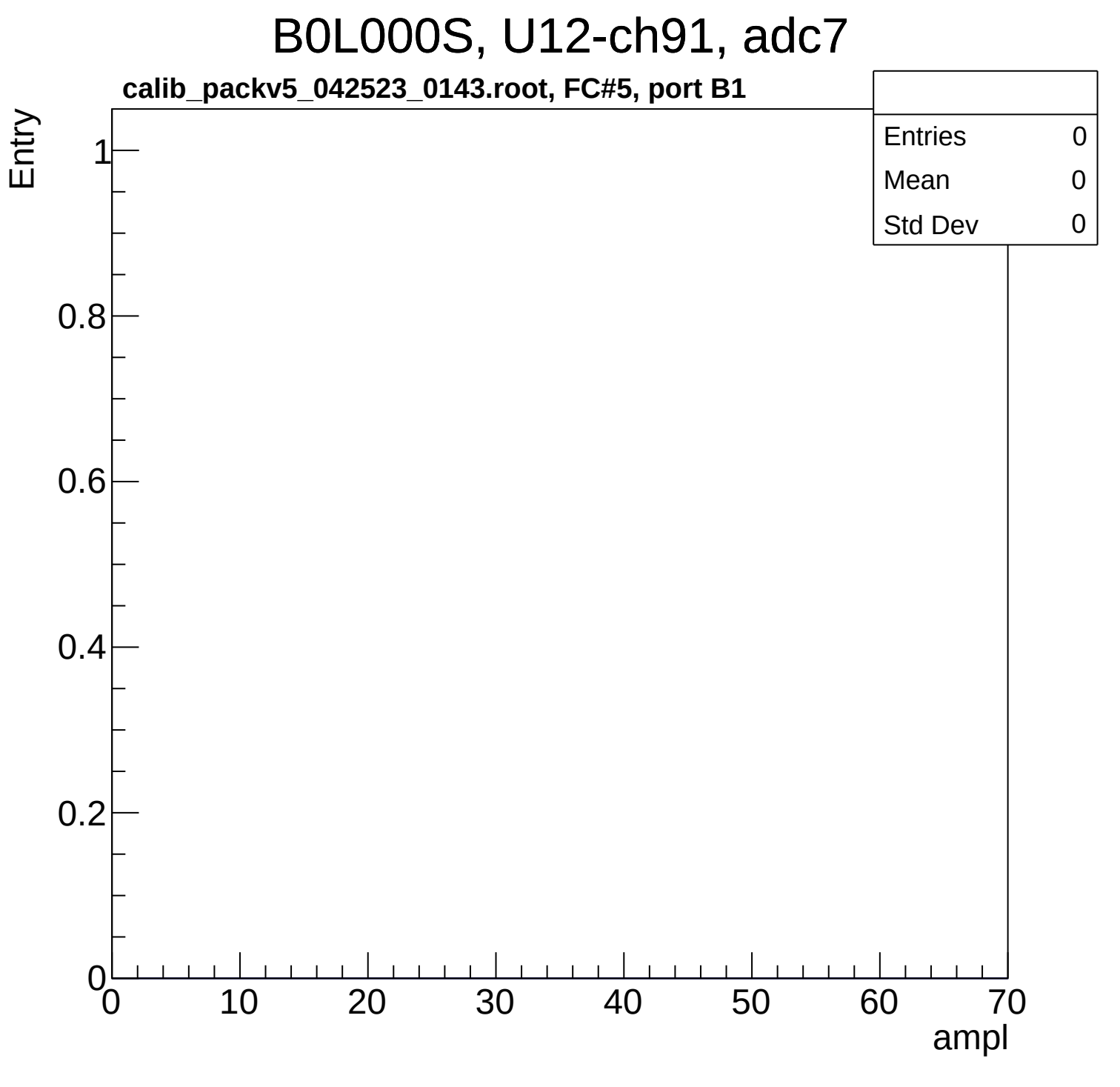

B0L000S, U12-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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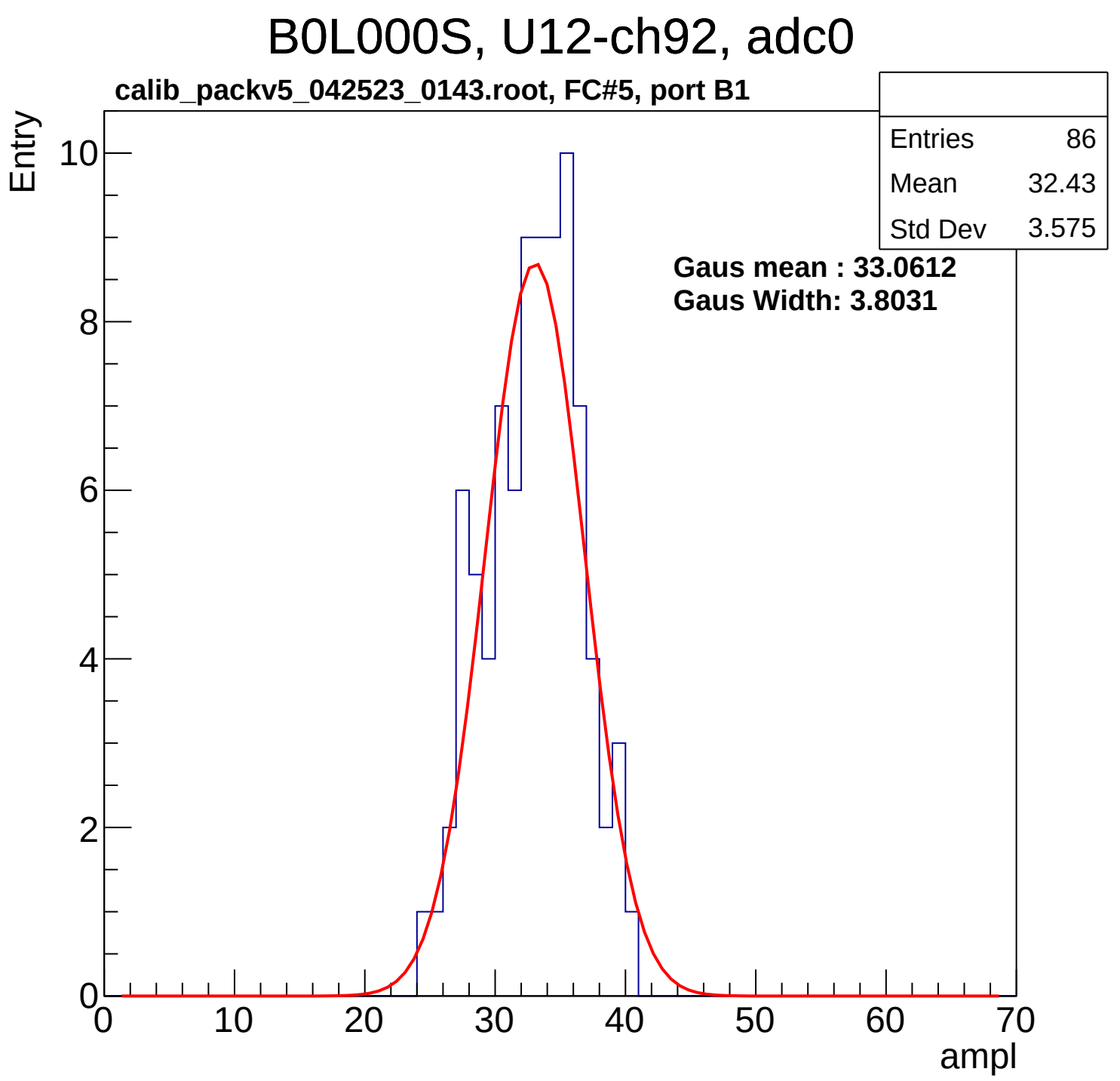
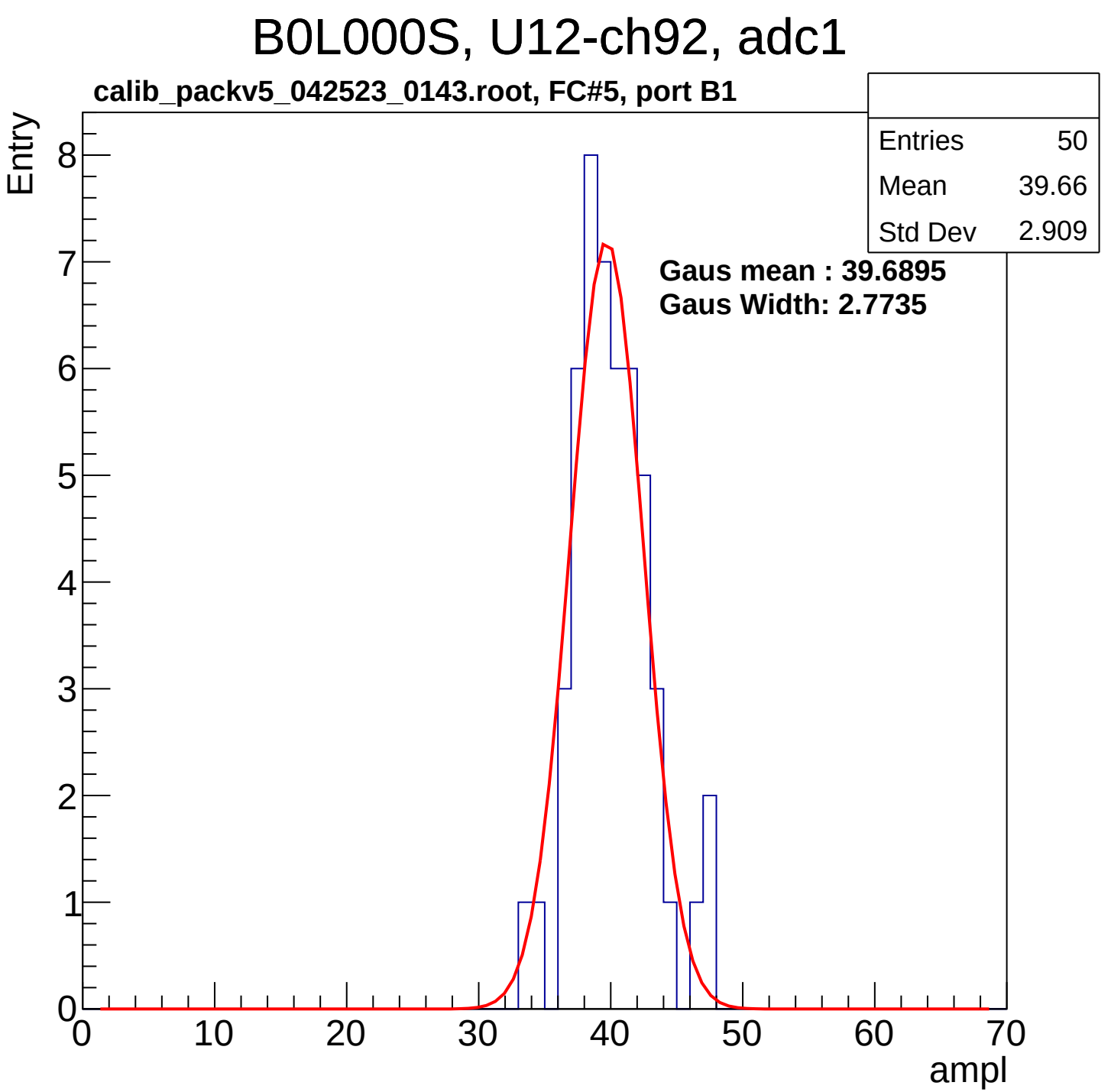
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 32.43
Std Dev 3.575
Gaus mean : 33.0612
Gaus Width: 3.8031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 39.66
Std Dev 2.909
Gaus mean : 39.6895
Gaus Width: 2.7735
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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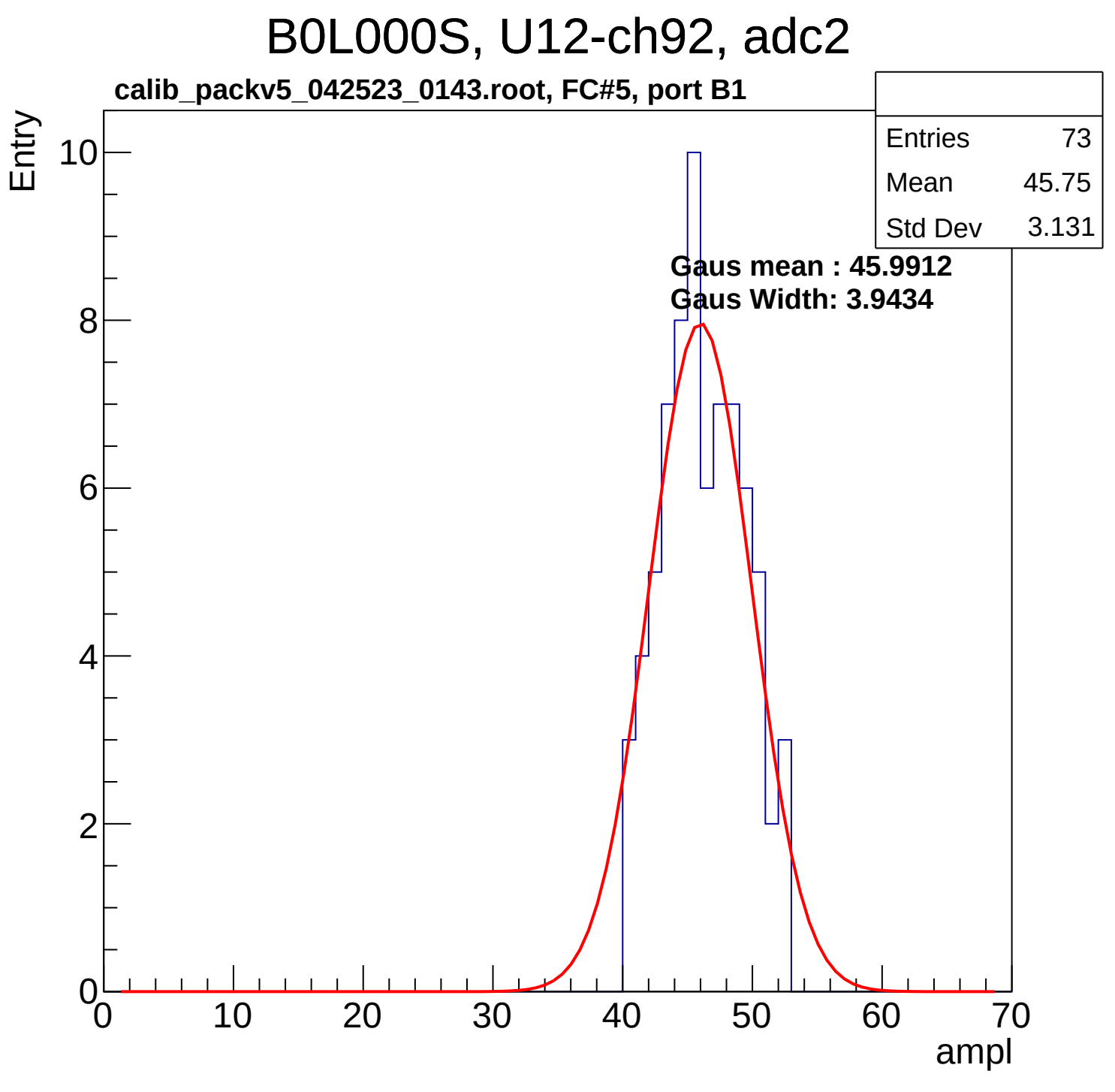
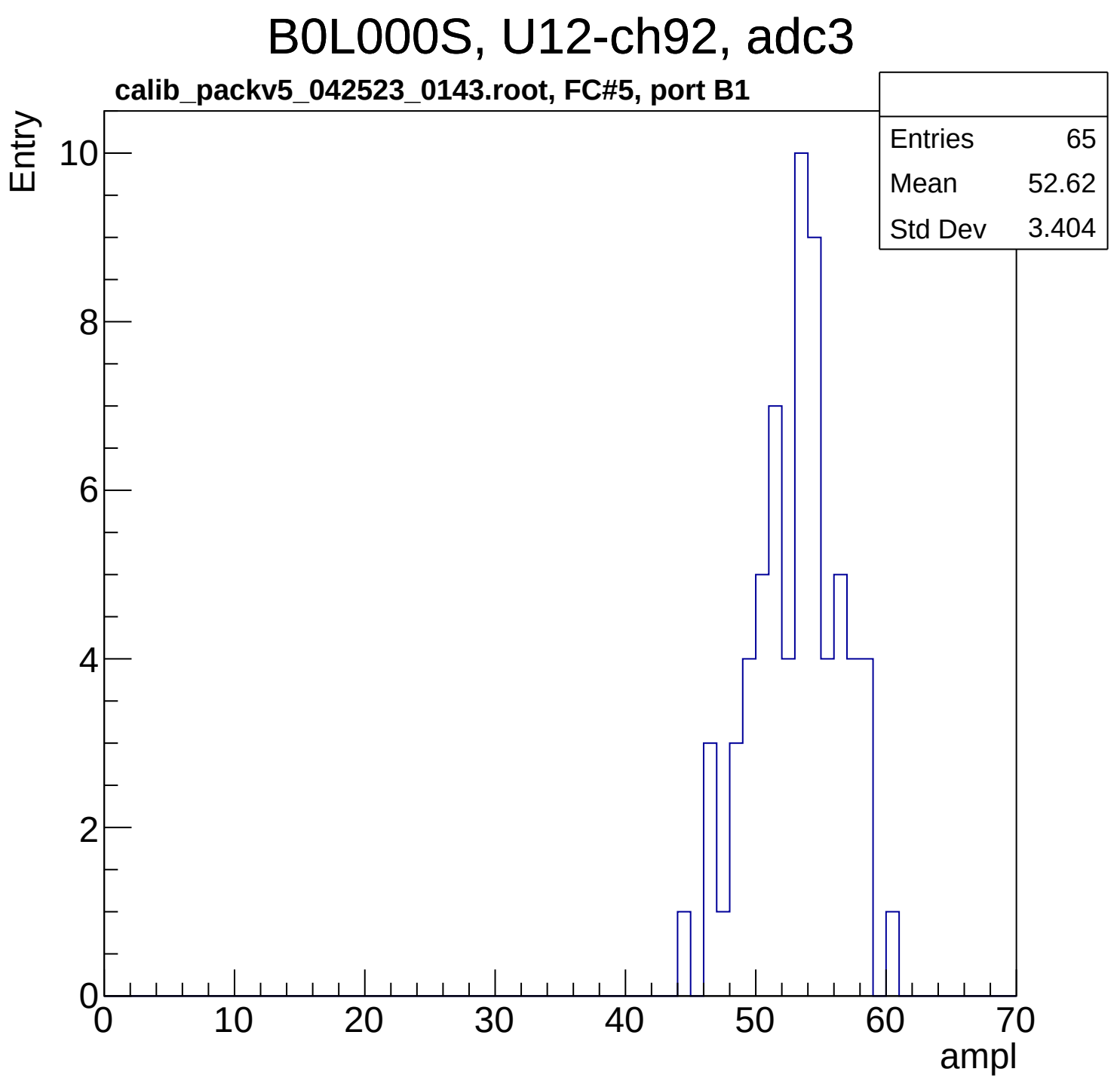

B0L000S, U12-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 45.75
Std Dev 3.131
Gaus mean : 45.9912
Gaus Width: 3.9434
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 52.62
Std Dev 3.404
Entry
ampl
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4
6
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10
0
10
20
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40
50
60
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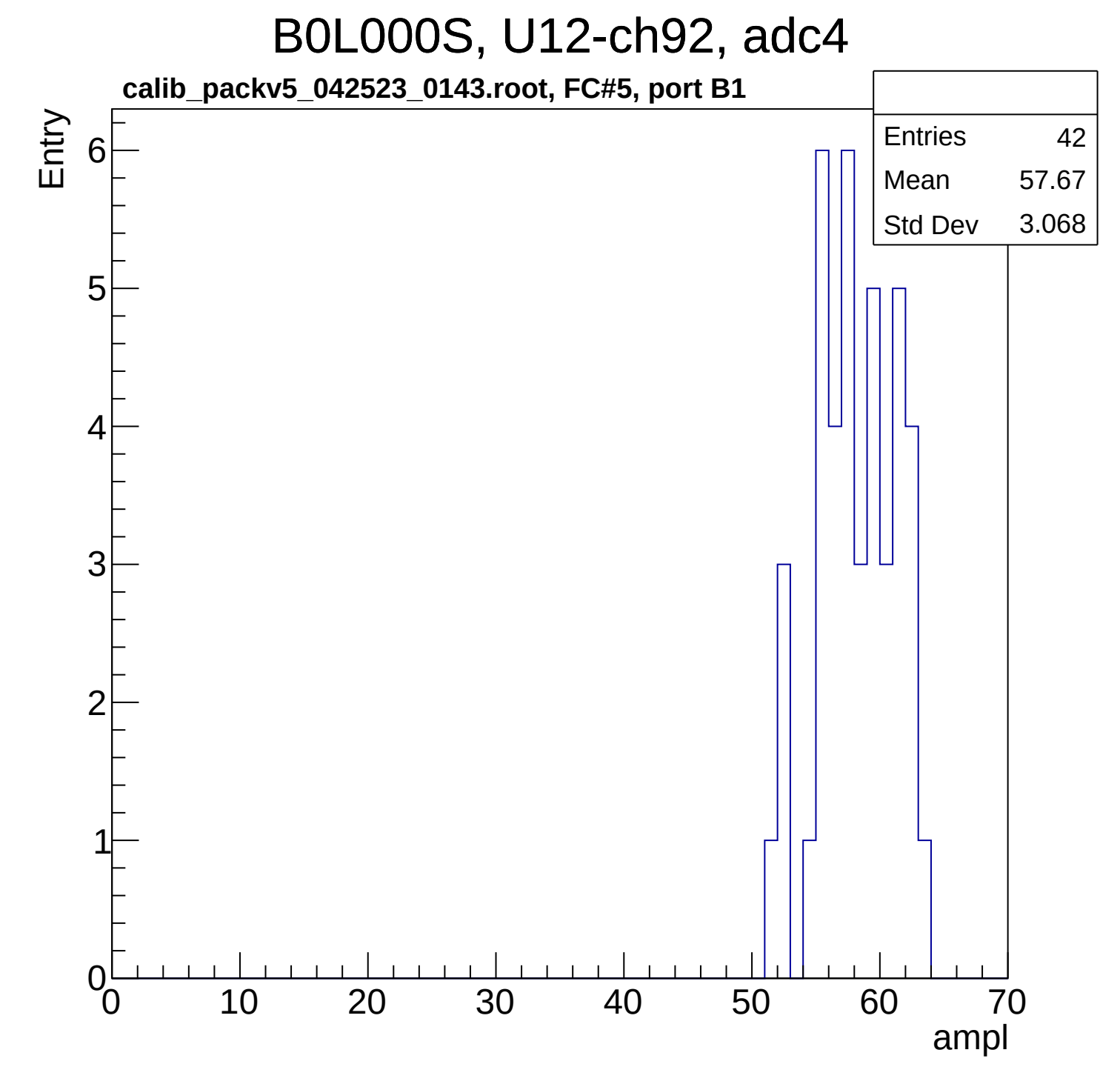
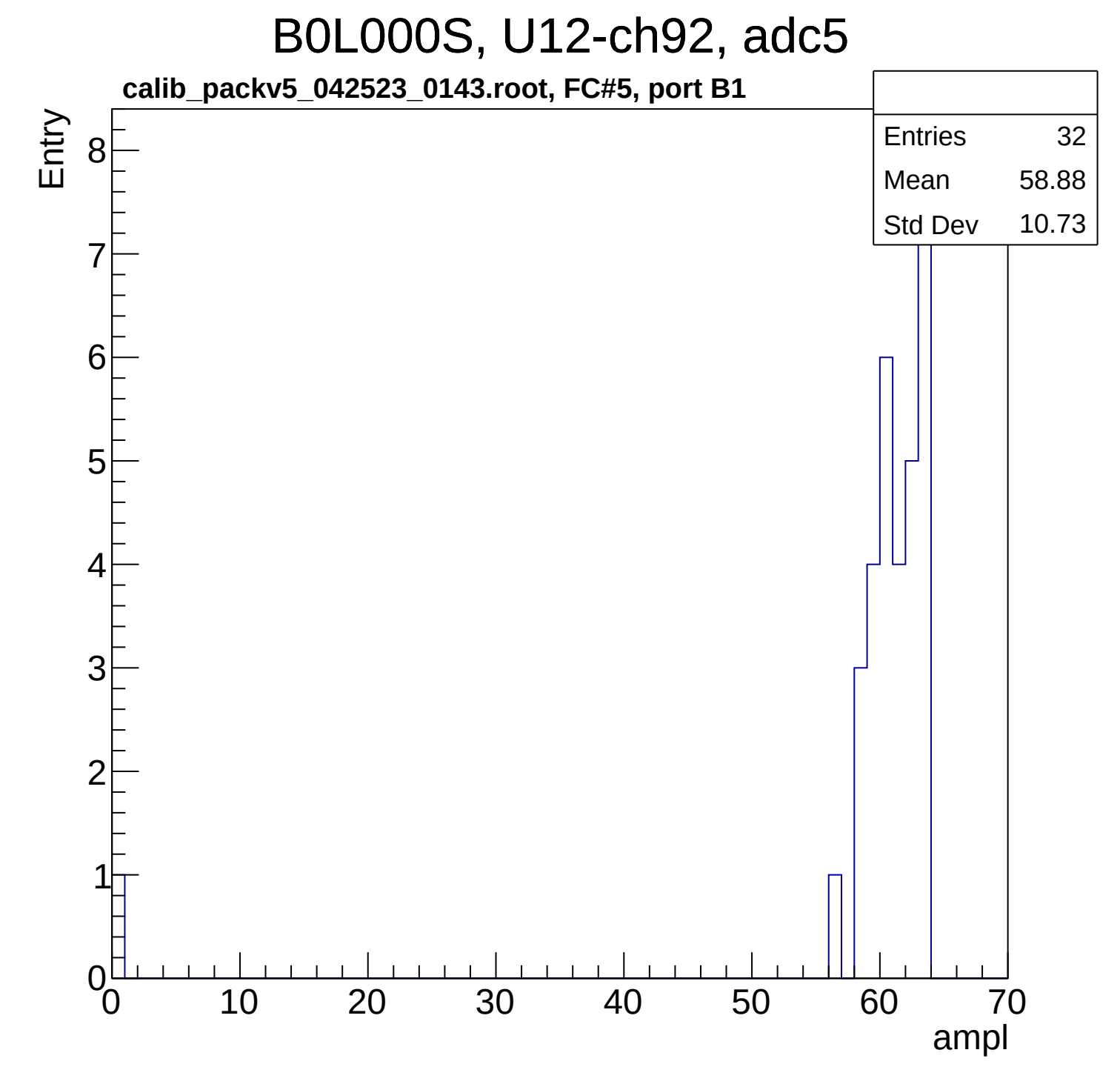

B0L000S, U12-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 57.67
Std Dev 3.068
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.88
Std Dev 10.73
Entry
ampl
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6
7
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60
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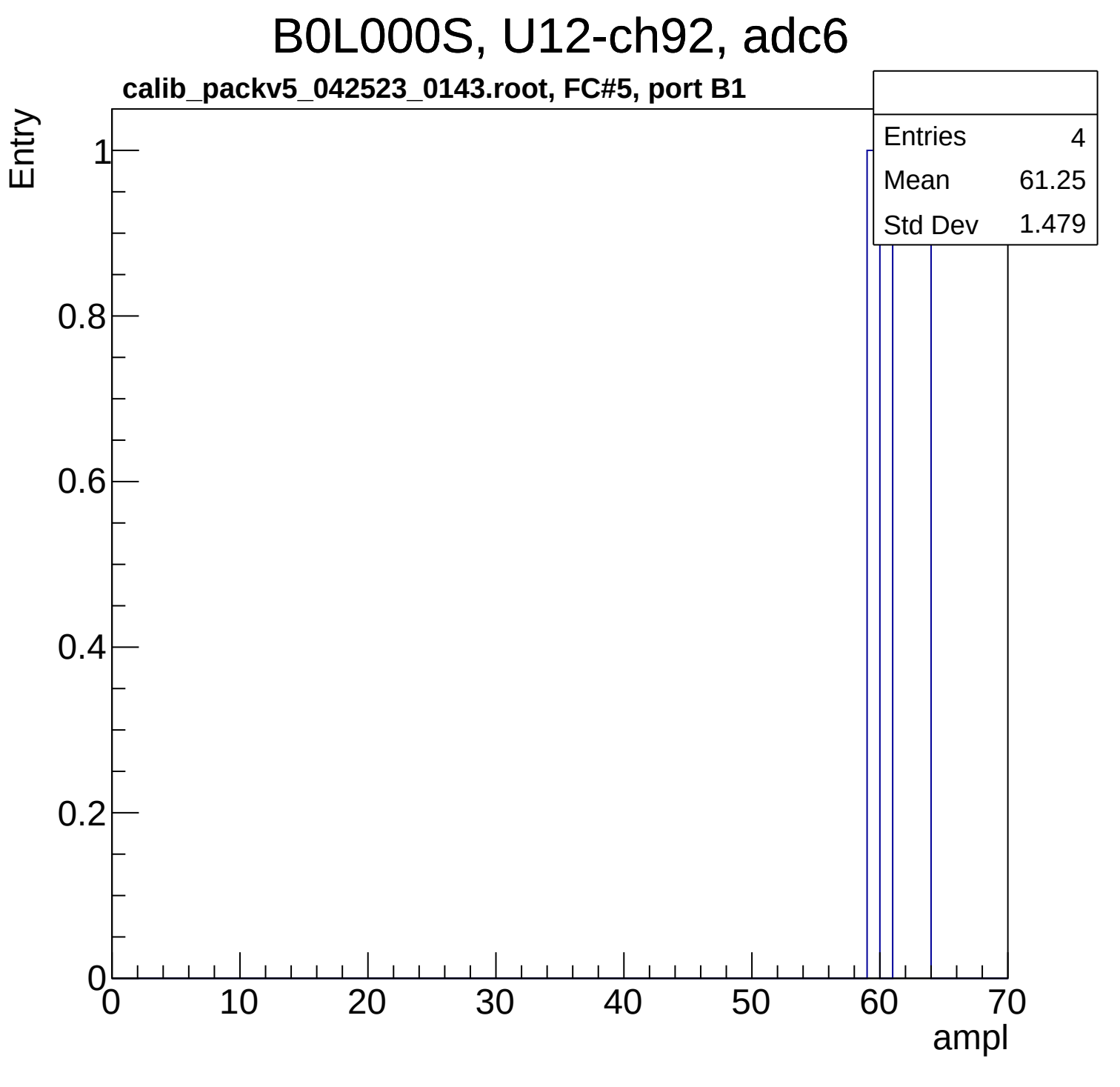
B0L000S, U12-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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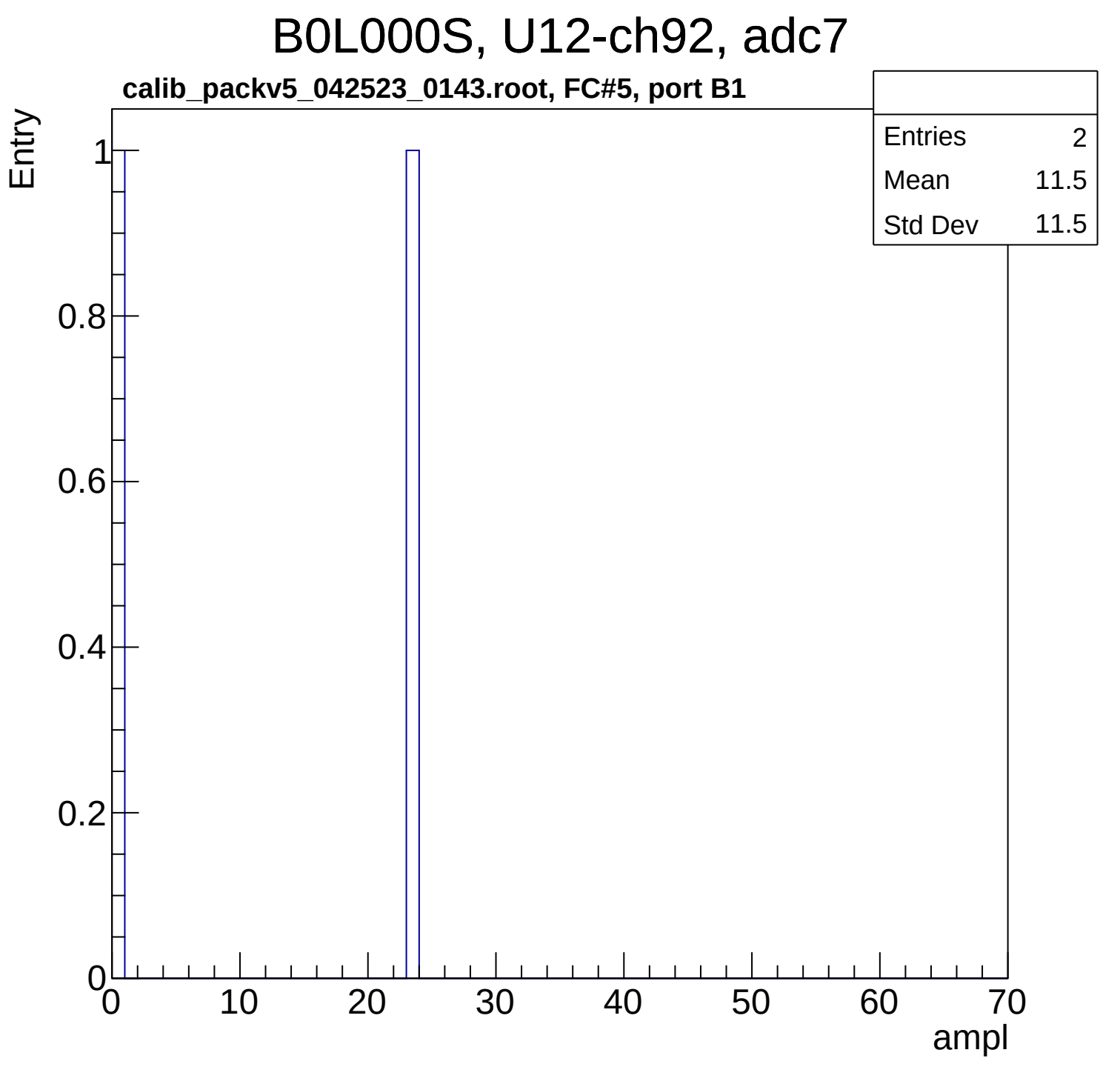
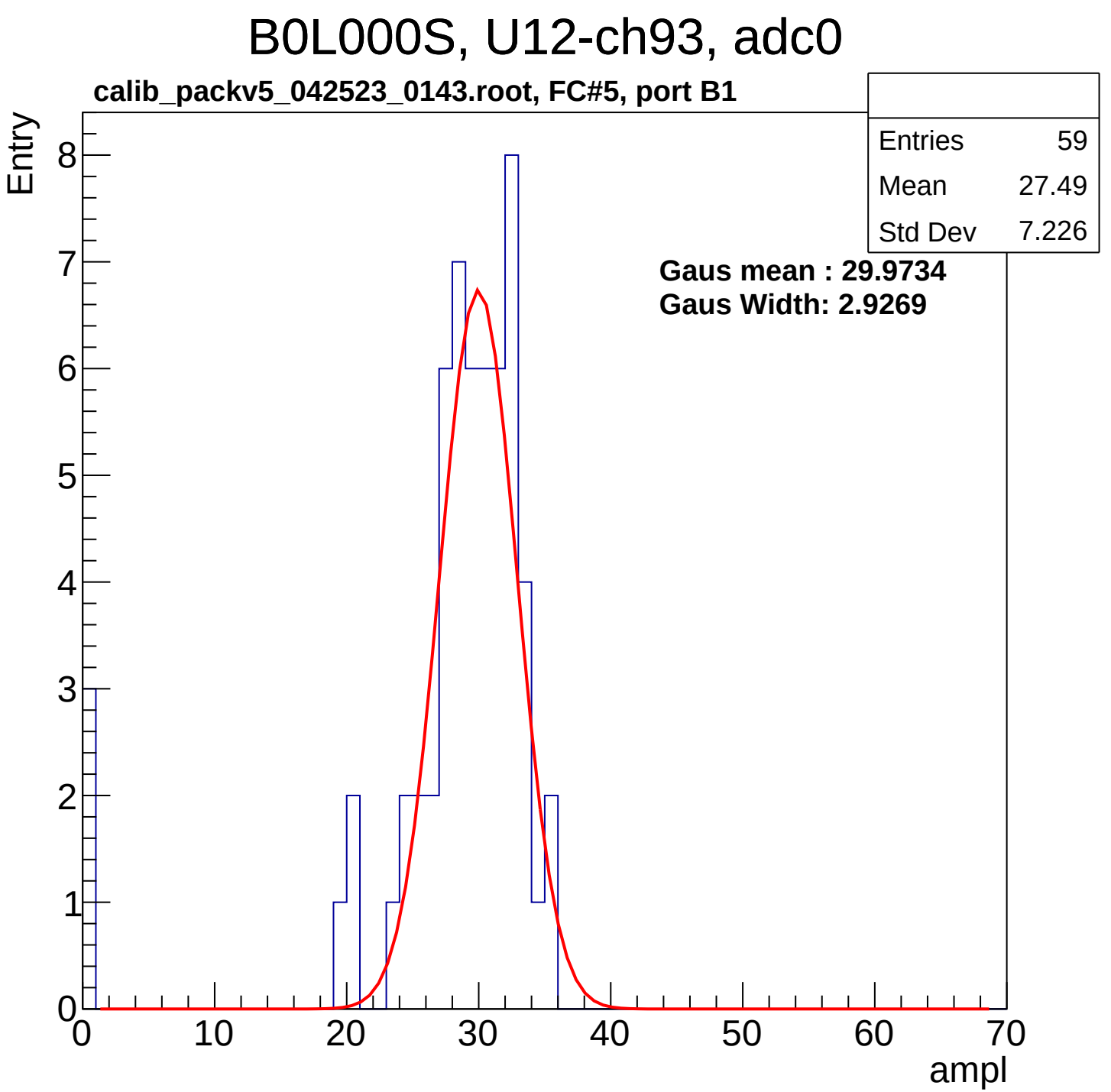

B0L000S, U12-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.49
Std Dev 7.226
Gaus mean : 29.9734
Gaus Width: 2.9269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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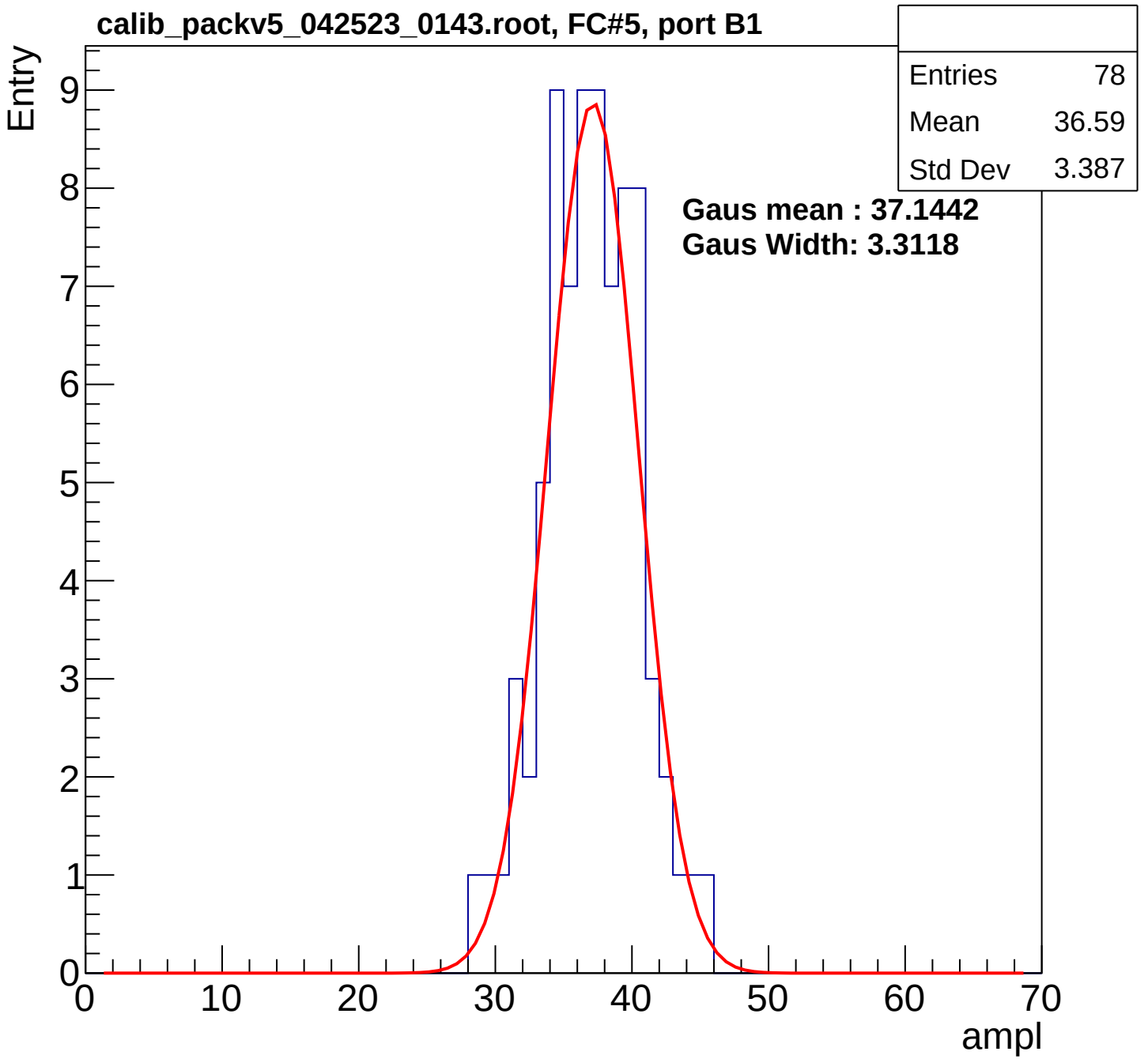
B0L000S, U12-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.59
Std Dev 3.387
Gaus mean : 37.1442
Gaus Width: 3.3118
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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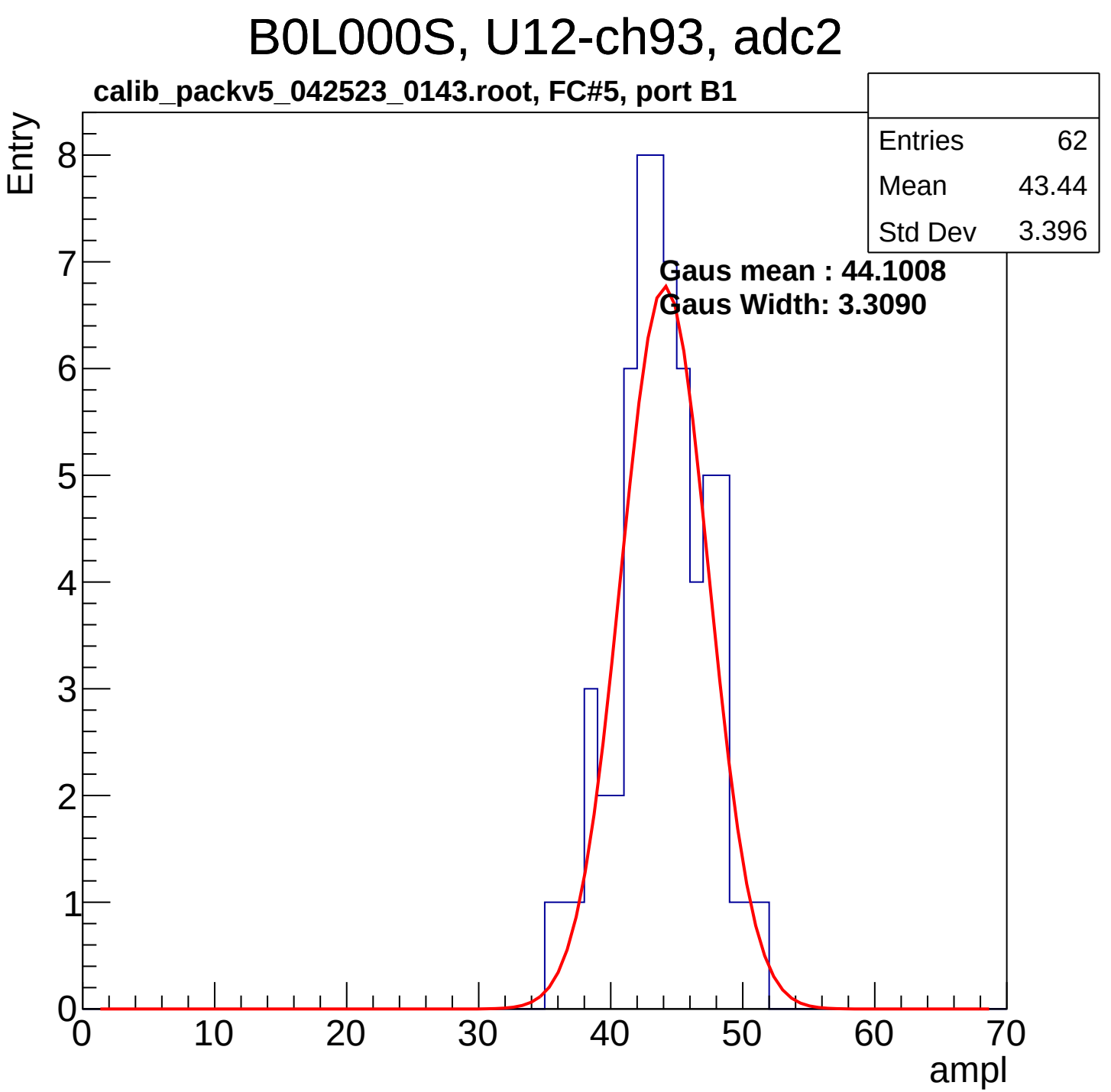
B0L000S, U12-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.44
Std Dev 3.396
Gaus mean : 44.1008
Gaus Width: 3.3090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
50
60
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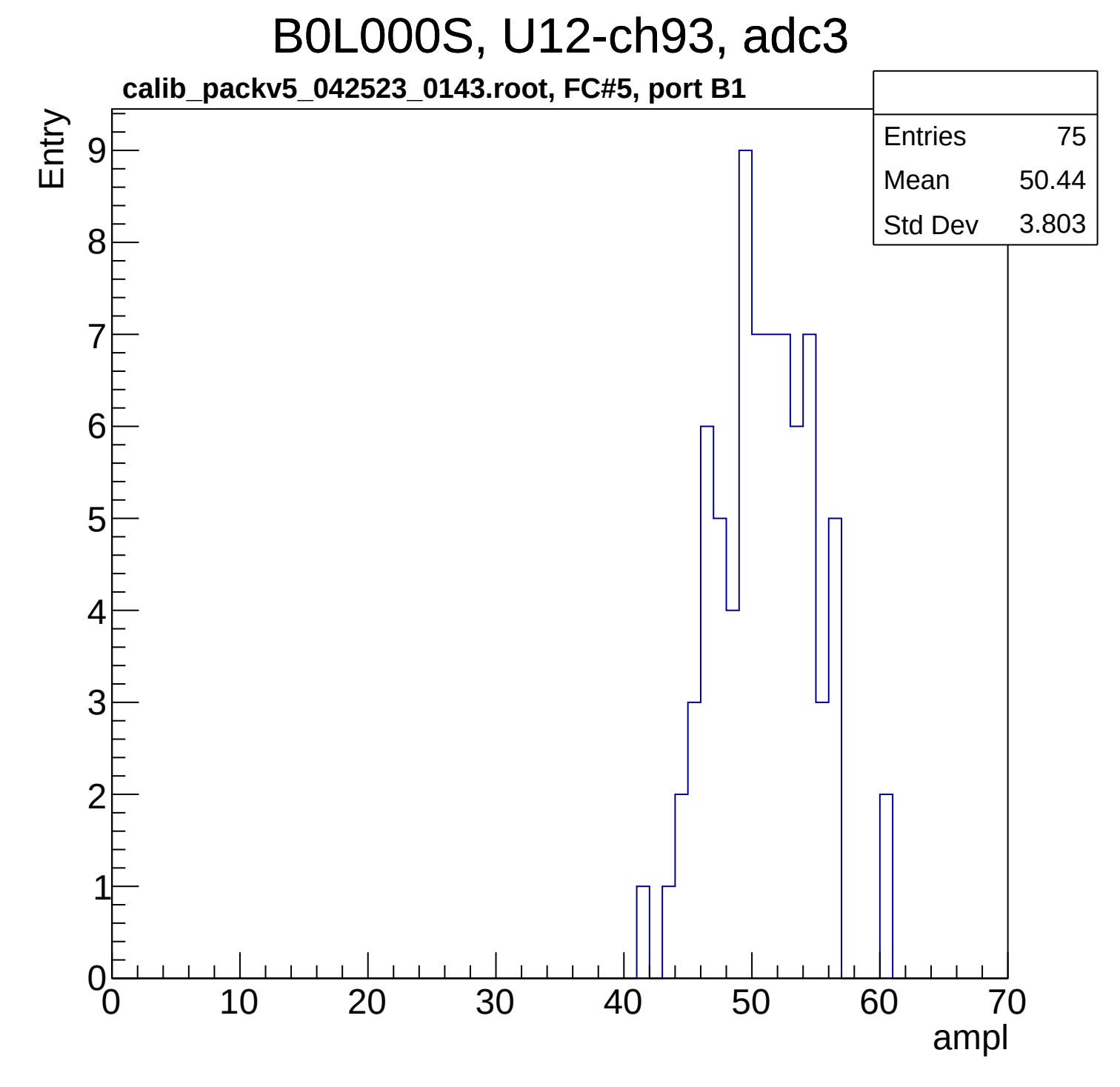
B0L000S, U12-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.44
Std Dev 3.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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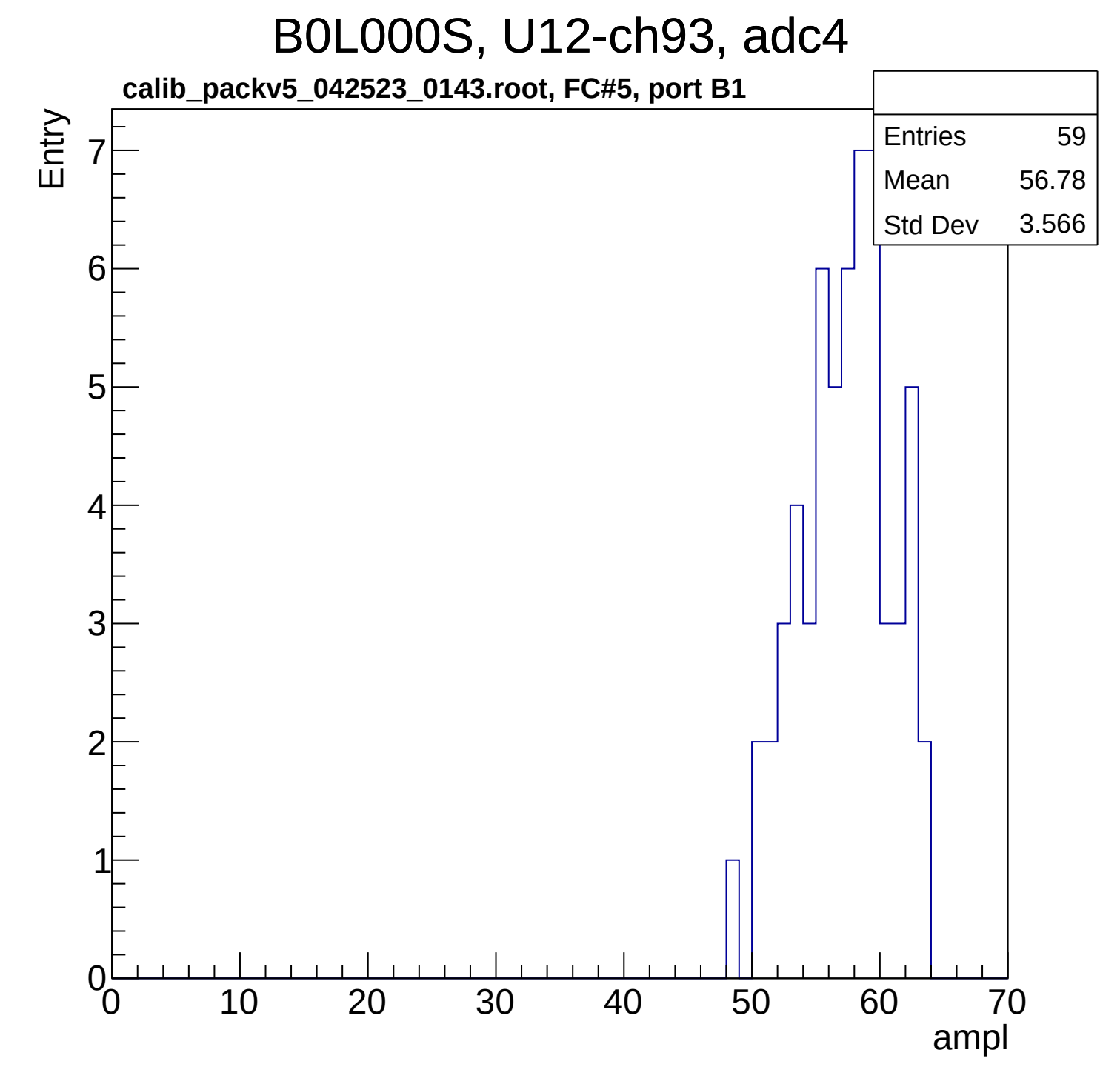
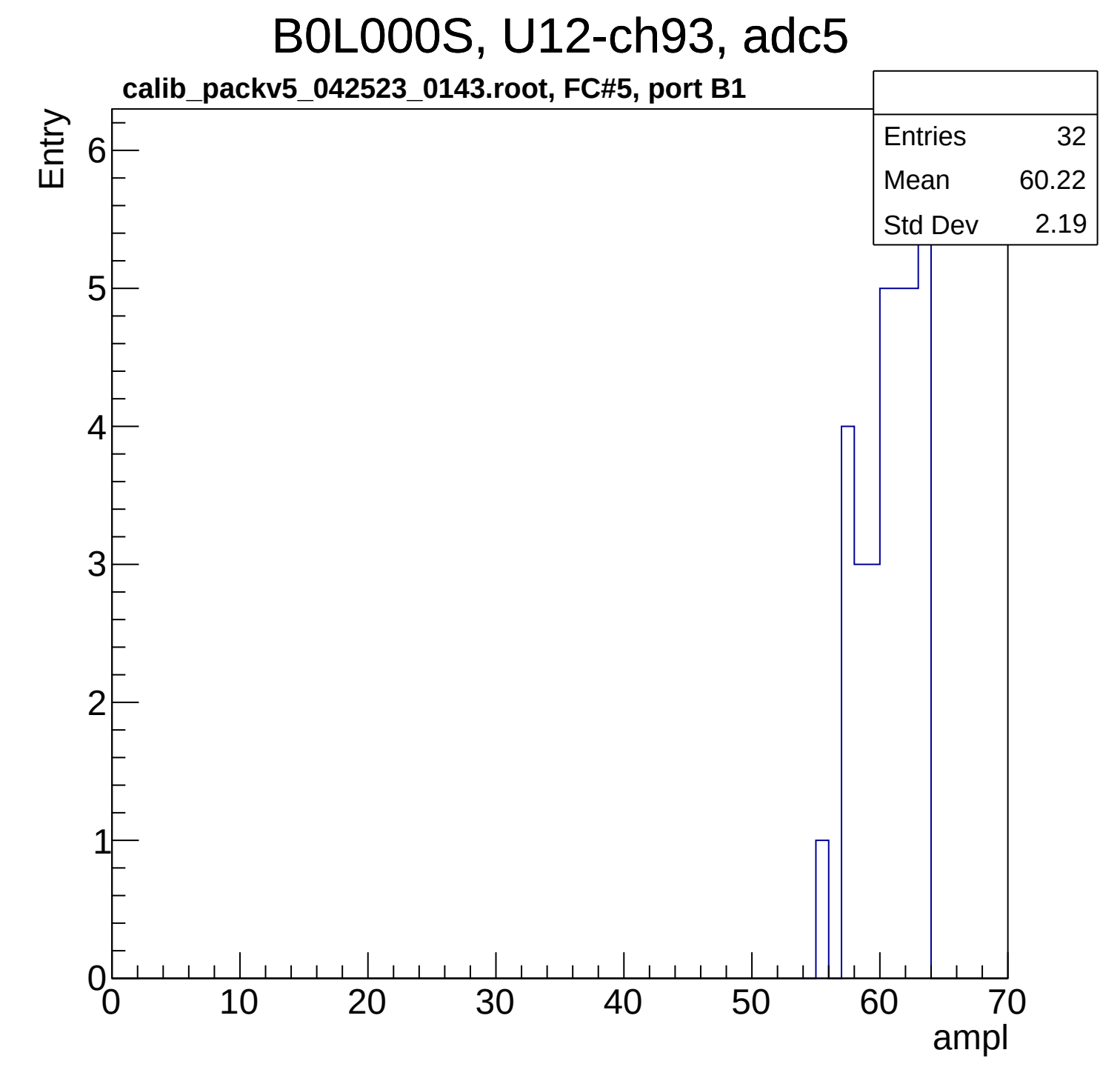
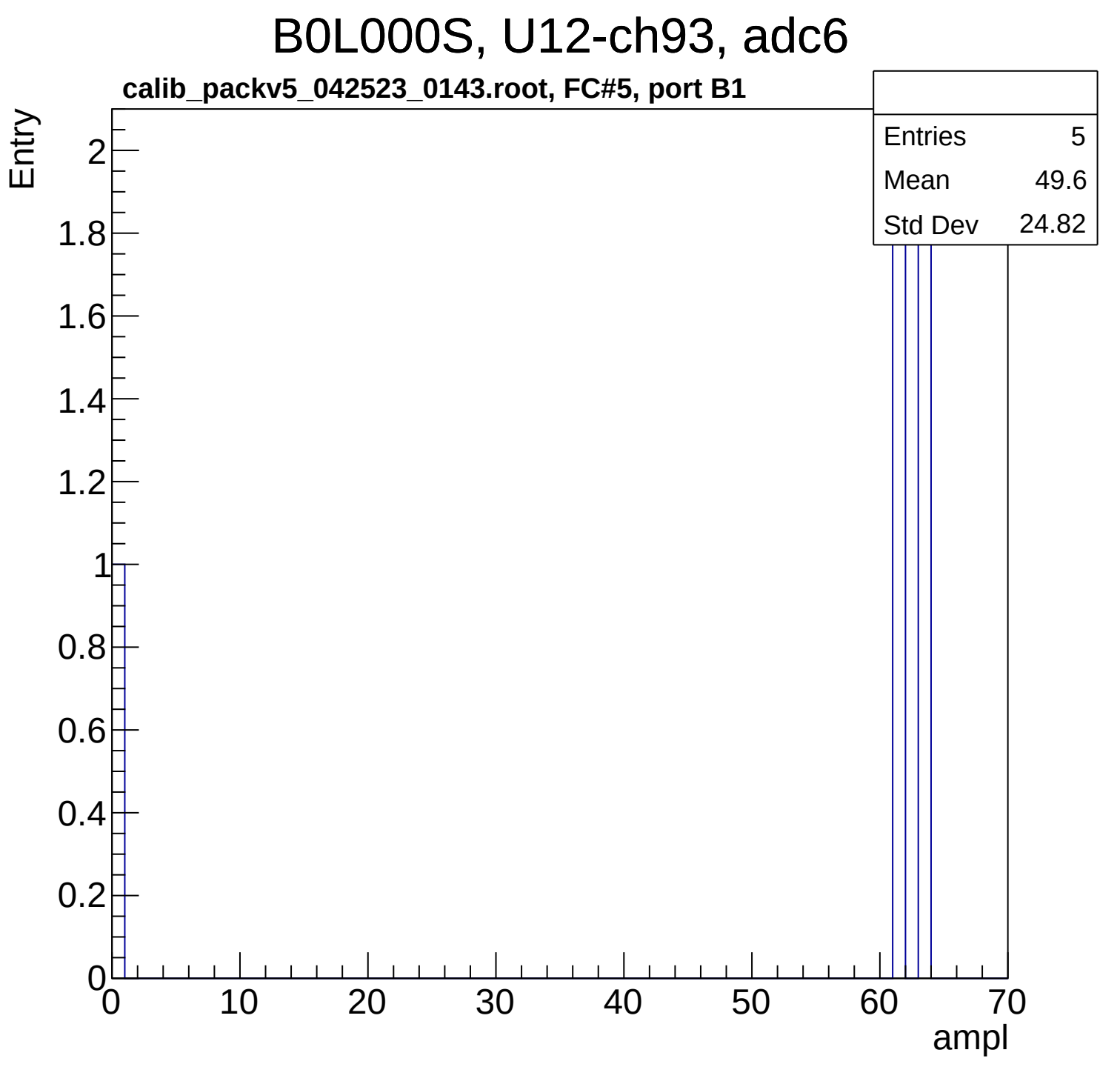
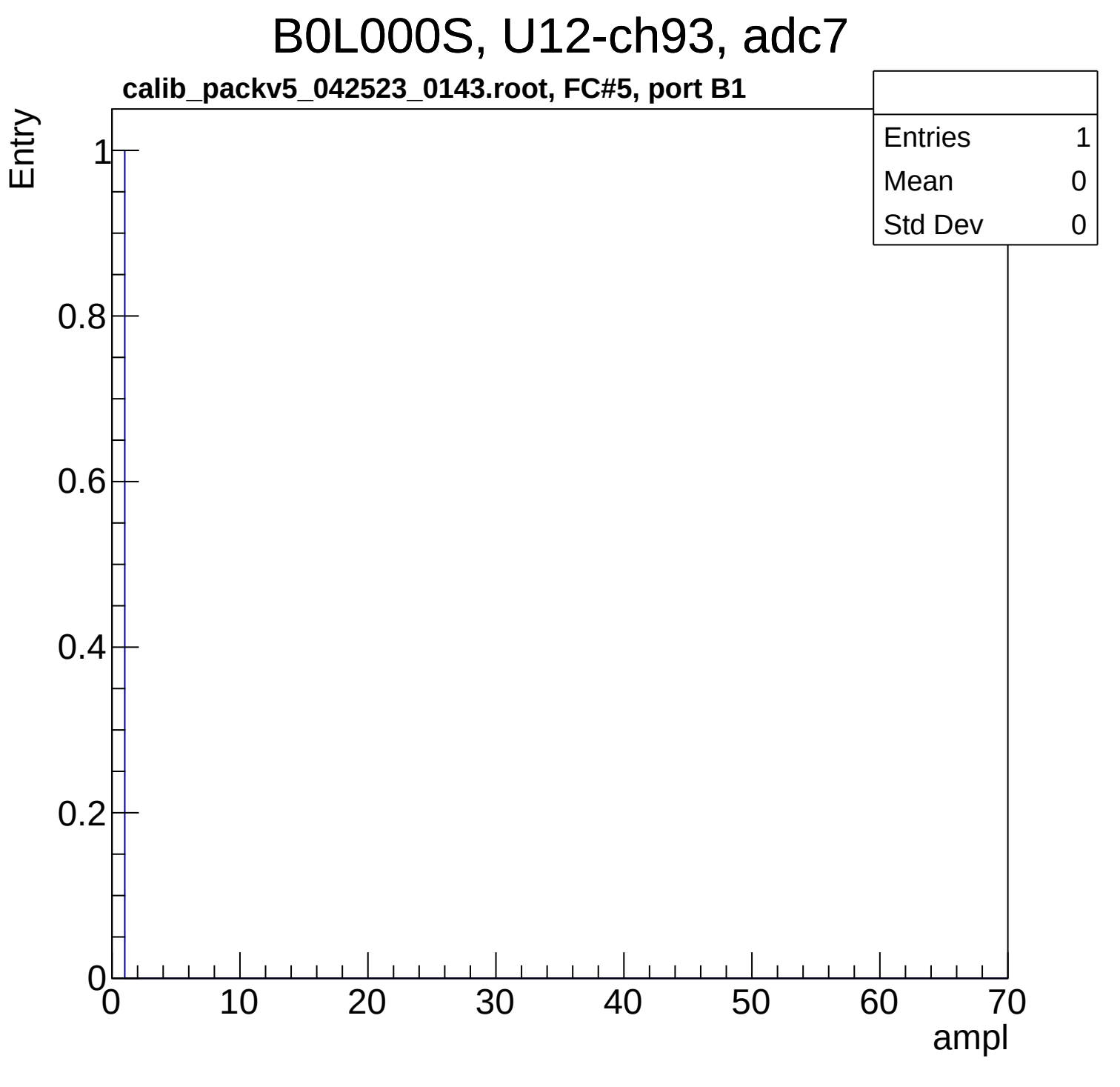
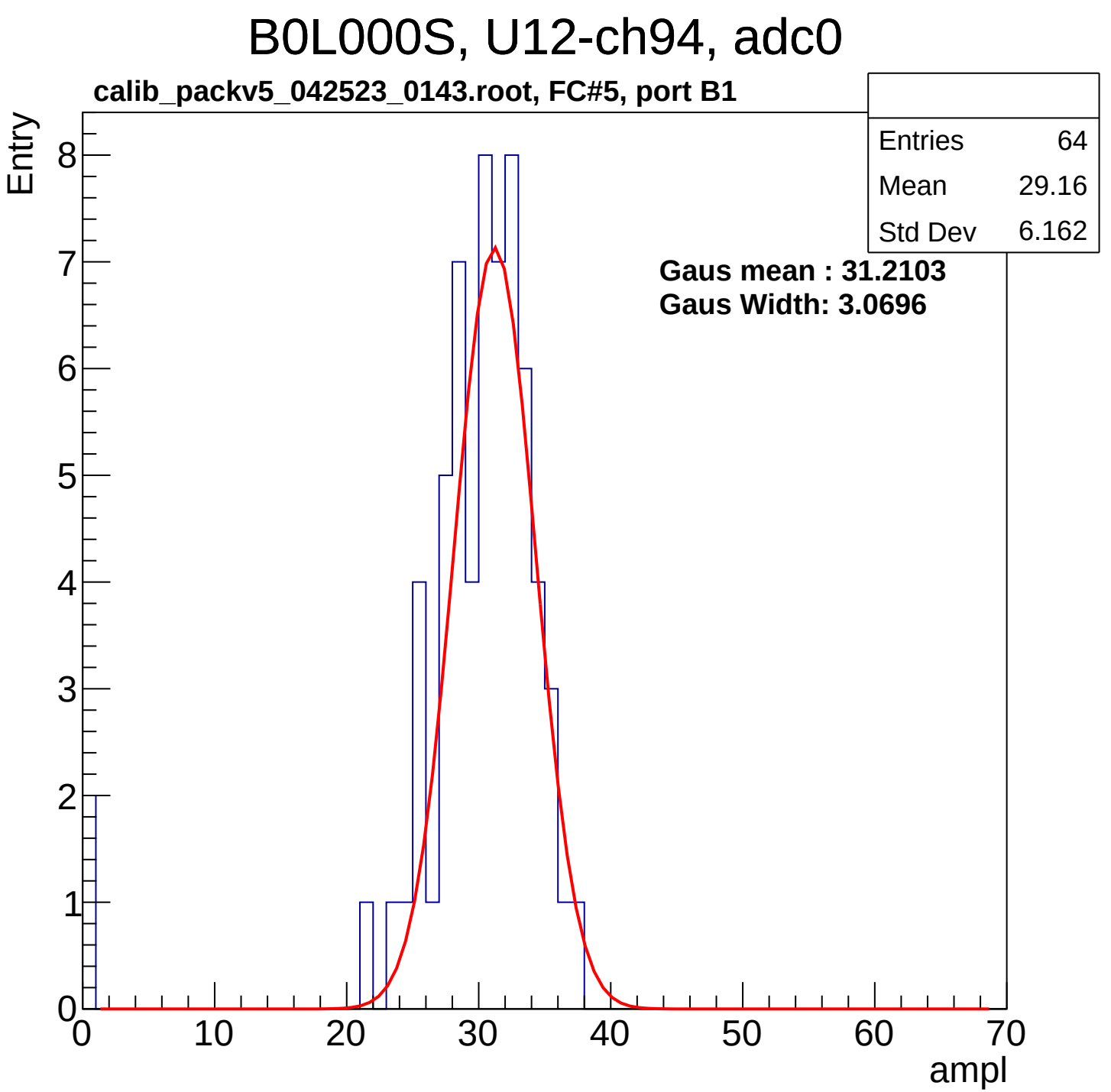
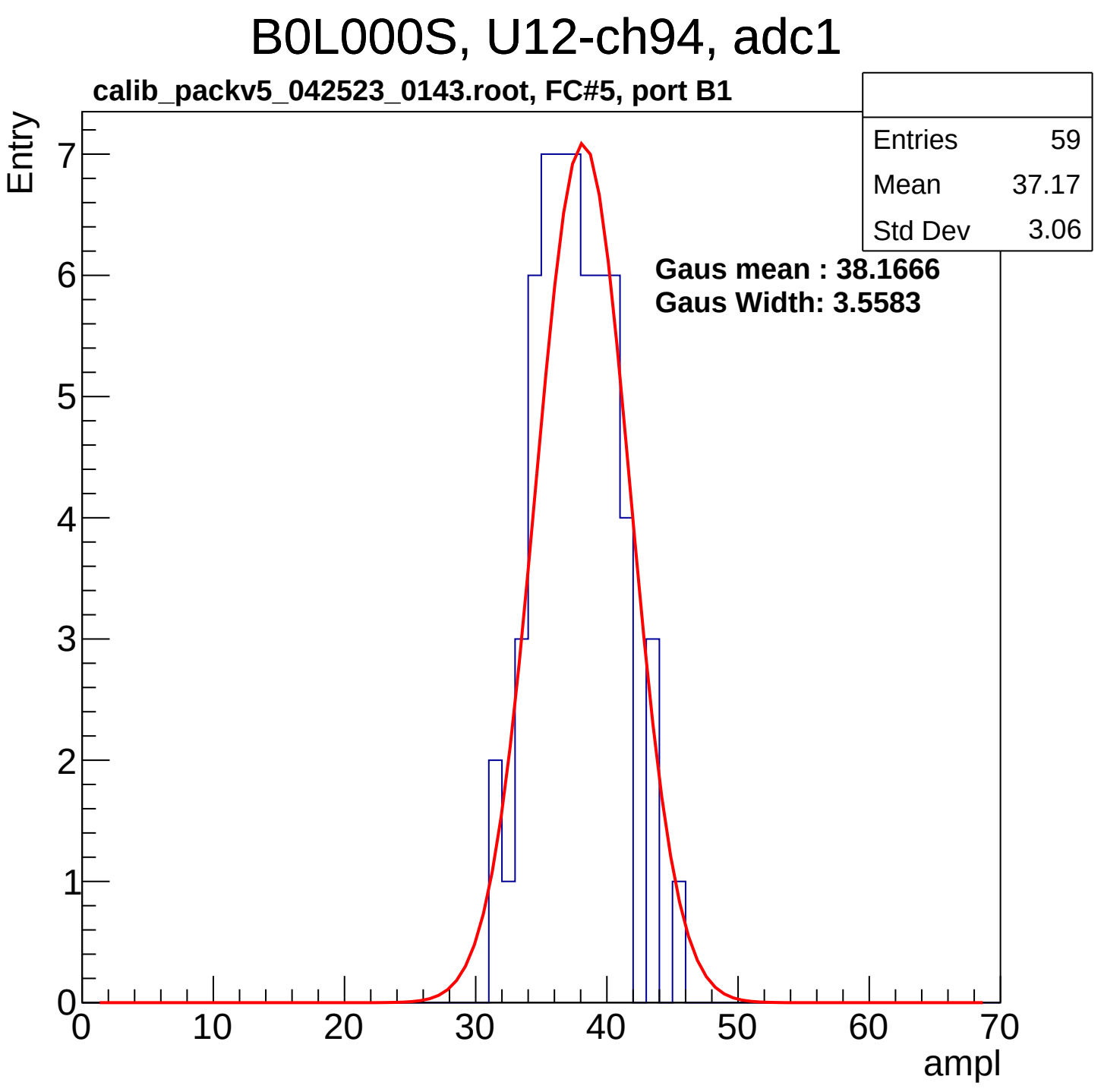

B0L000S, U12-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.78
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

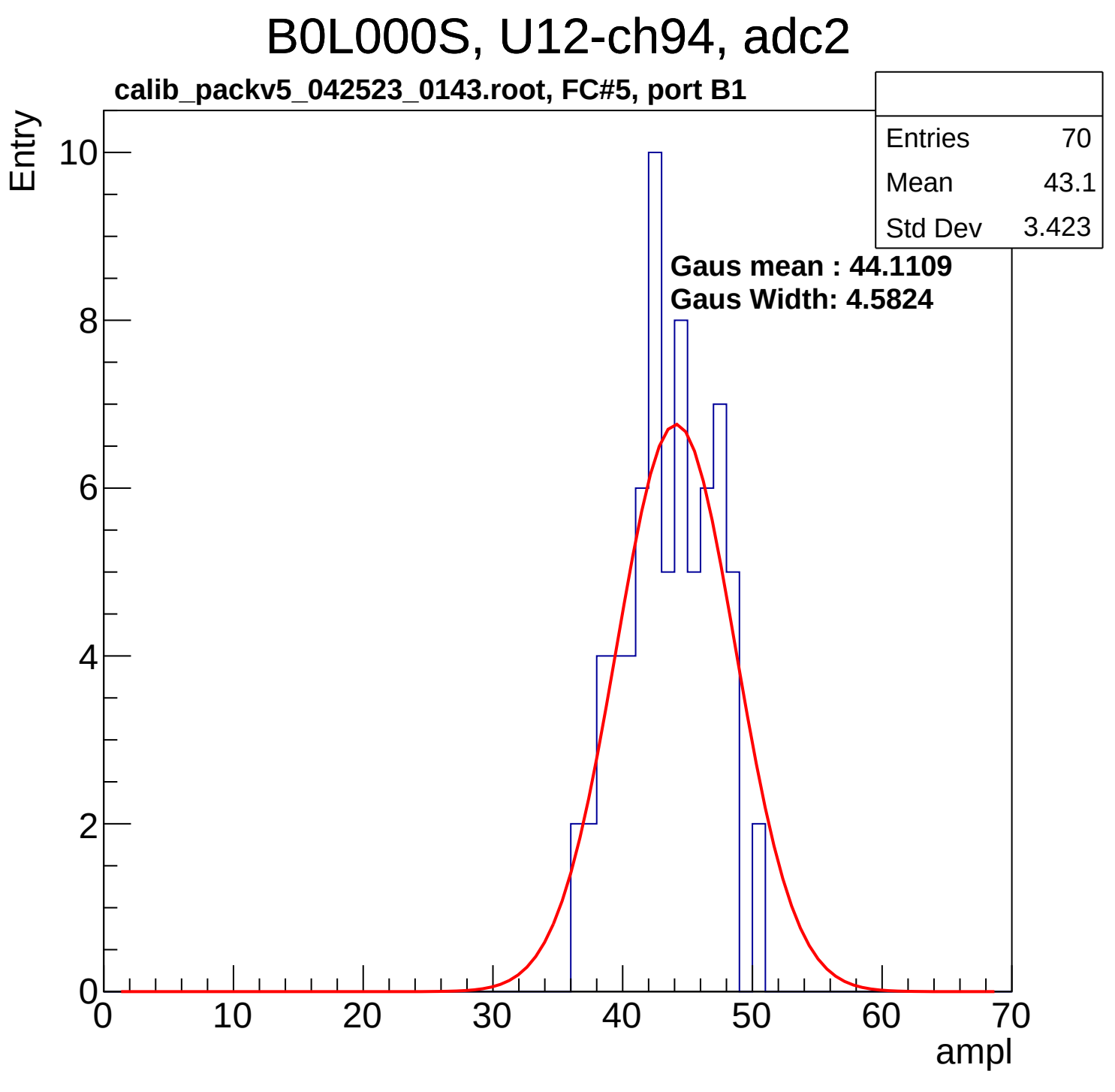

B0L000S, U12-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 60.22
Std Dev 2.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

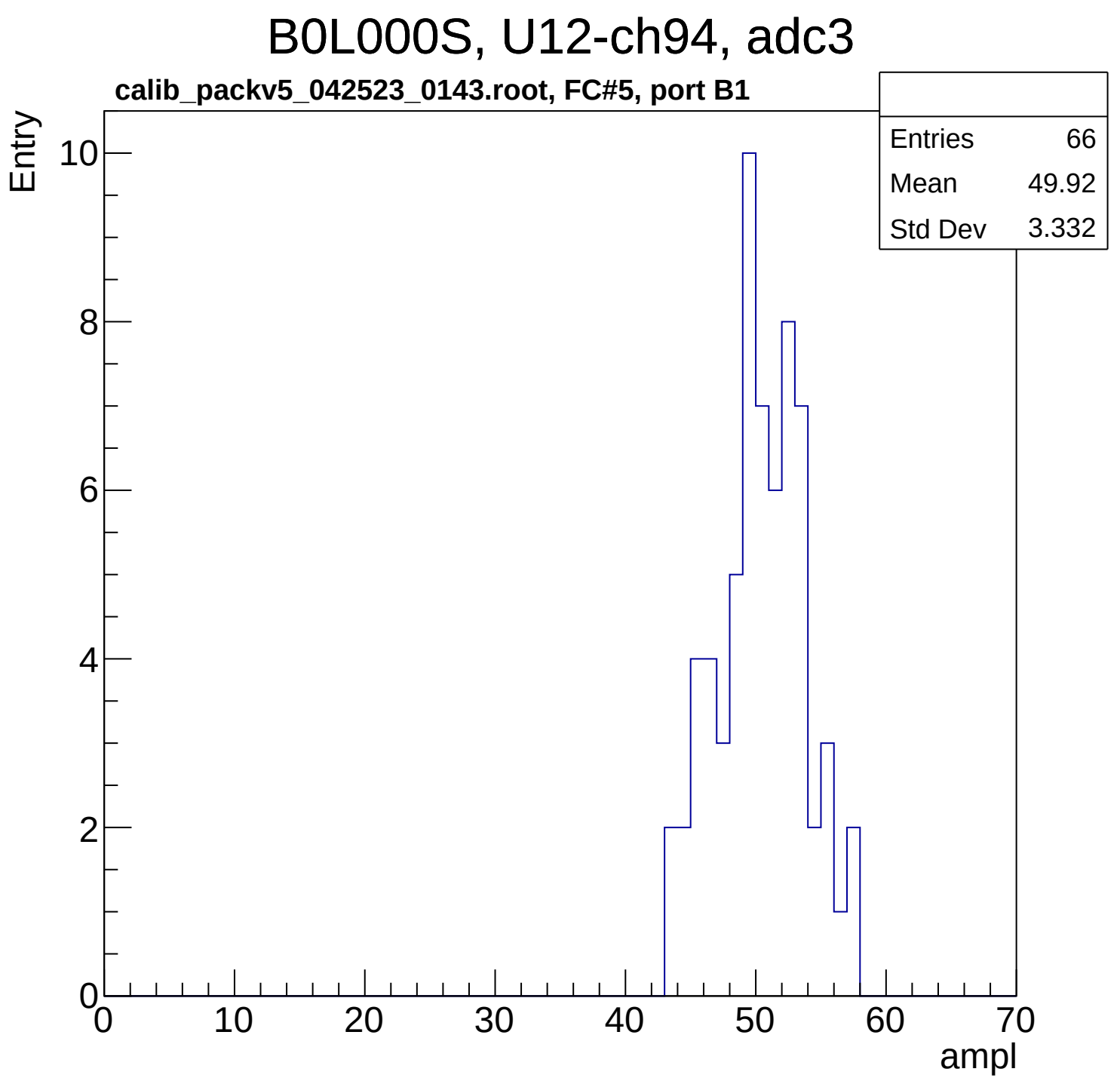

B0L000S, U12-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

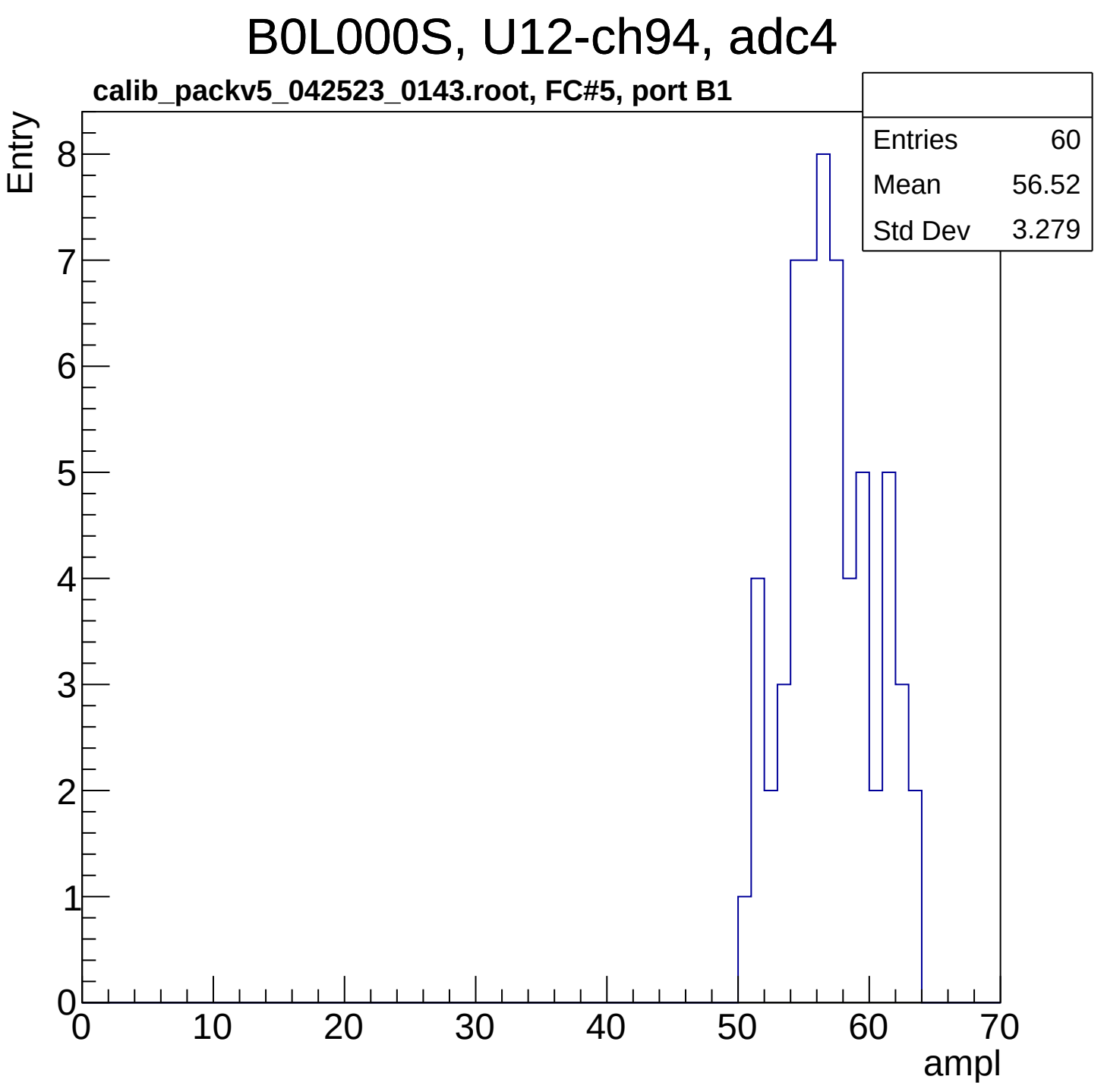

B0L000S, U12-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.16
Std Dev 6.162
Gaus mean : 31.2103
Gaus Width: 3.0696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.17
Std Dev 3.06
Gaus mean : 38.1666
Gaus Width: 3.5583
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 43.1
Std Dev 3.423
Gaus mean : 44.1109
Gaus Width: 4.5824
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.92
Std Dev 3.332
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.52
Std Dev 3.279
Entry
ampl
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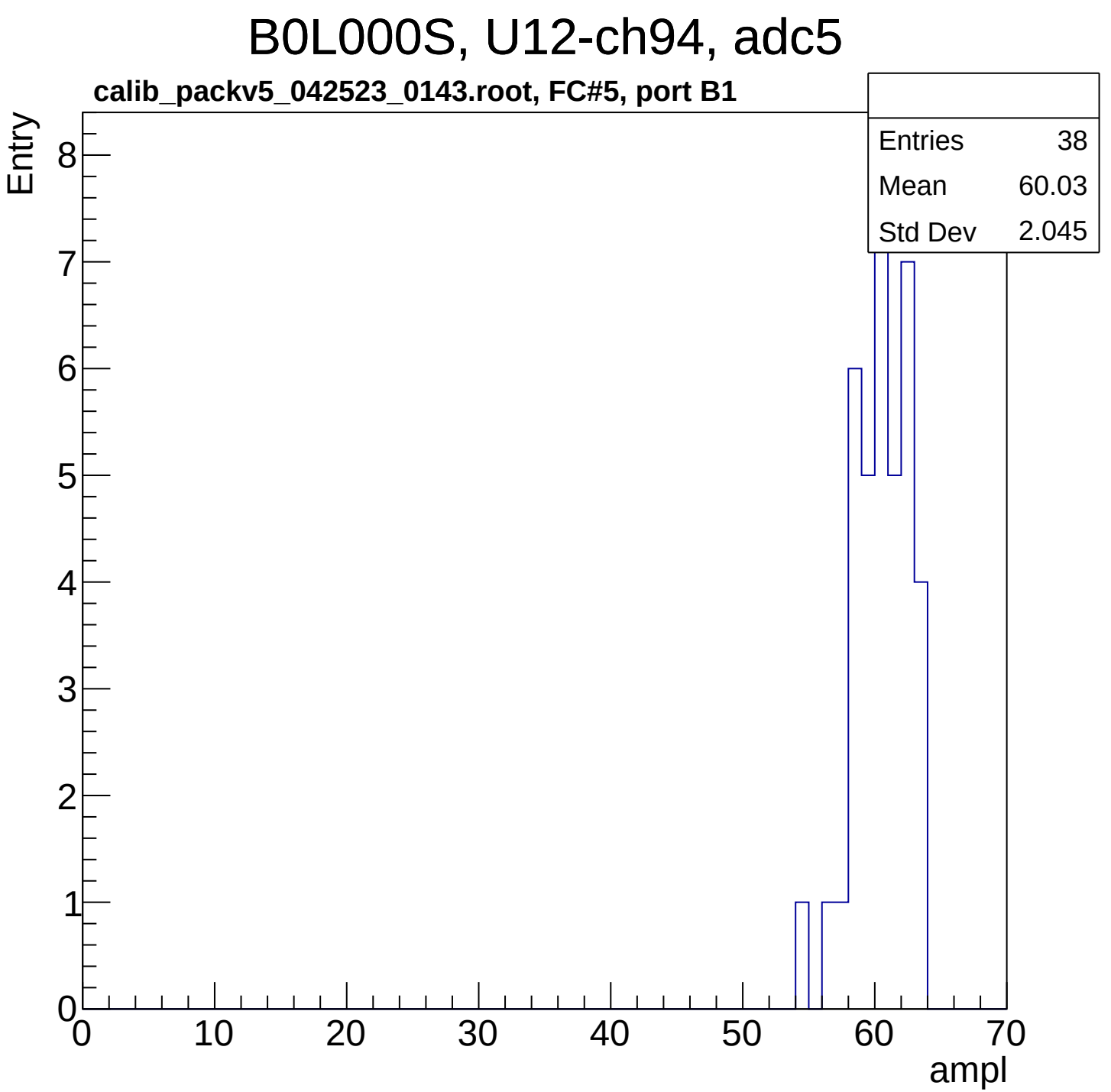
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 60.03
Std Dev 2.045
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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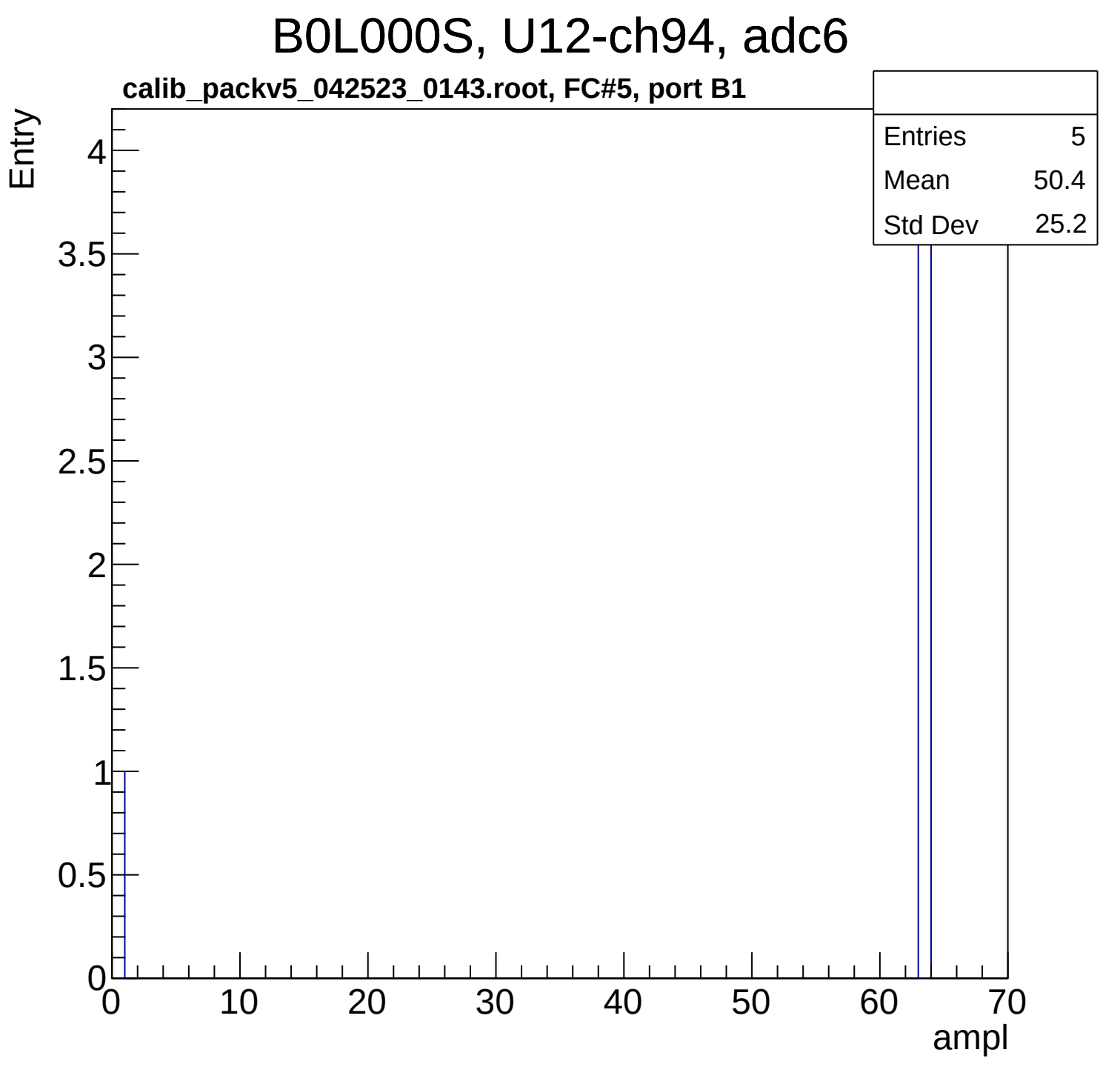
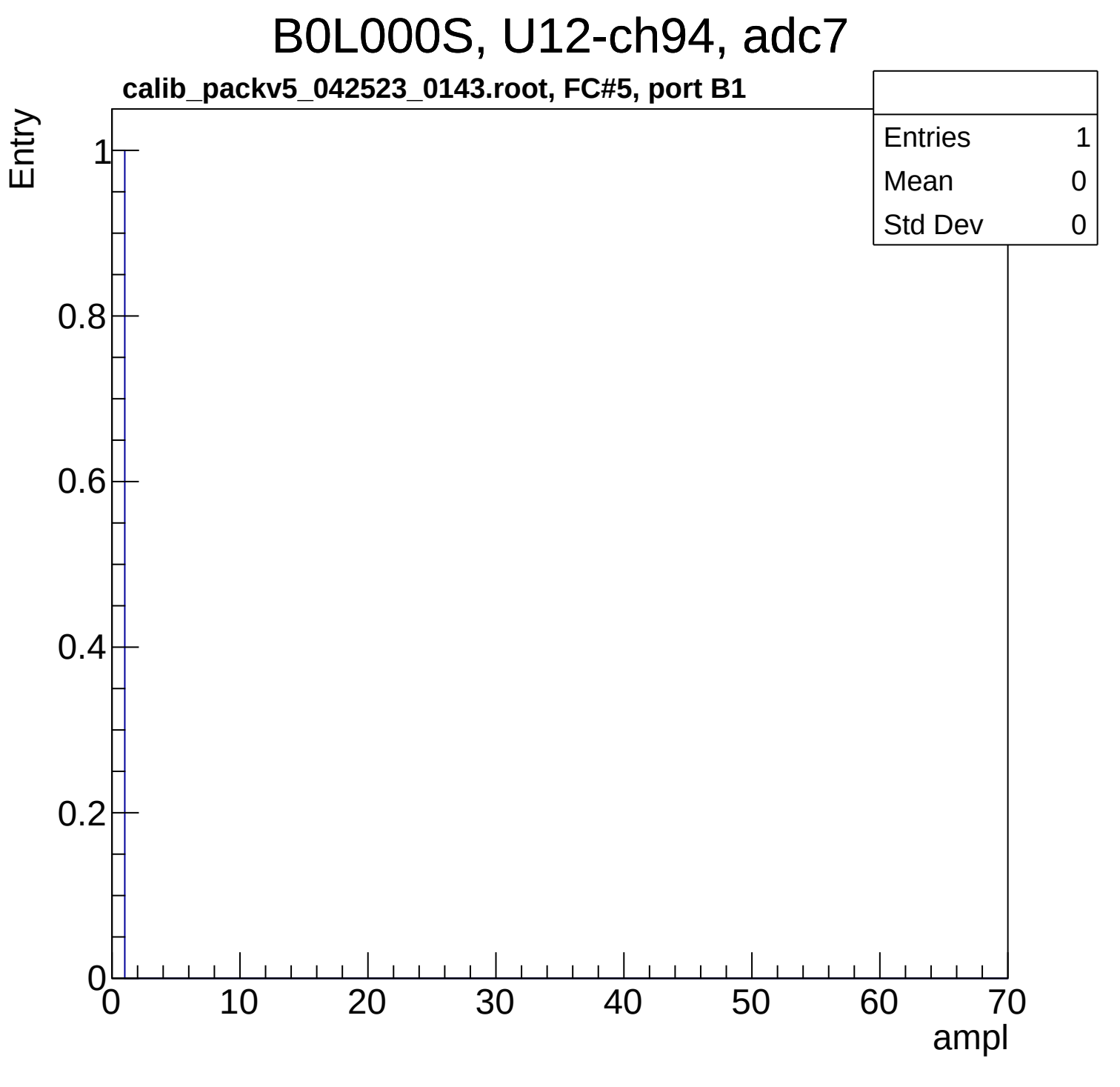

B0L000S, U12-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 50.4
Std Dev 25.2
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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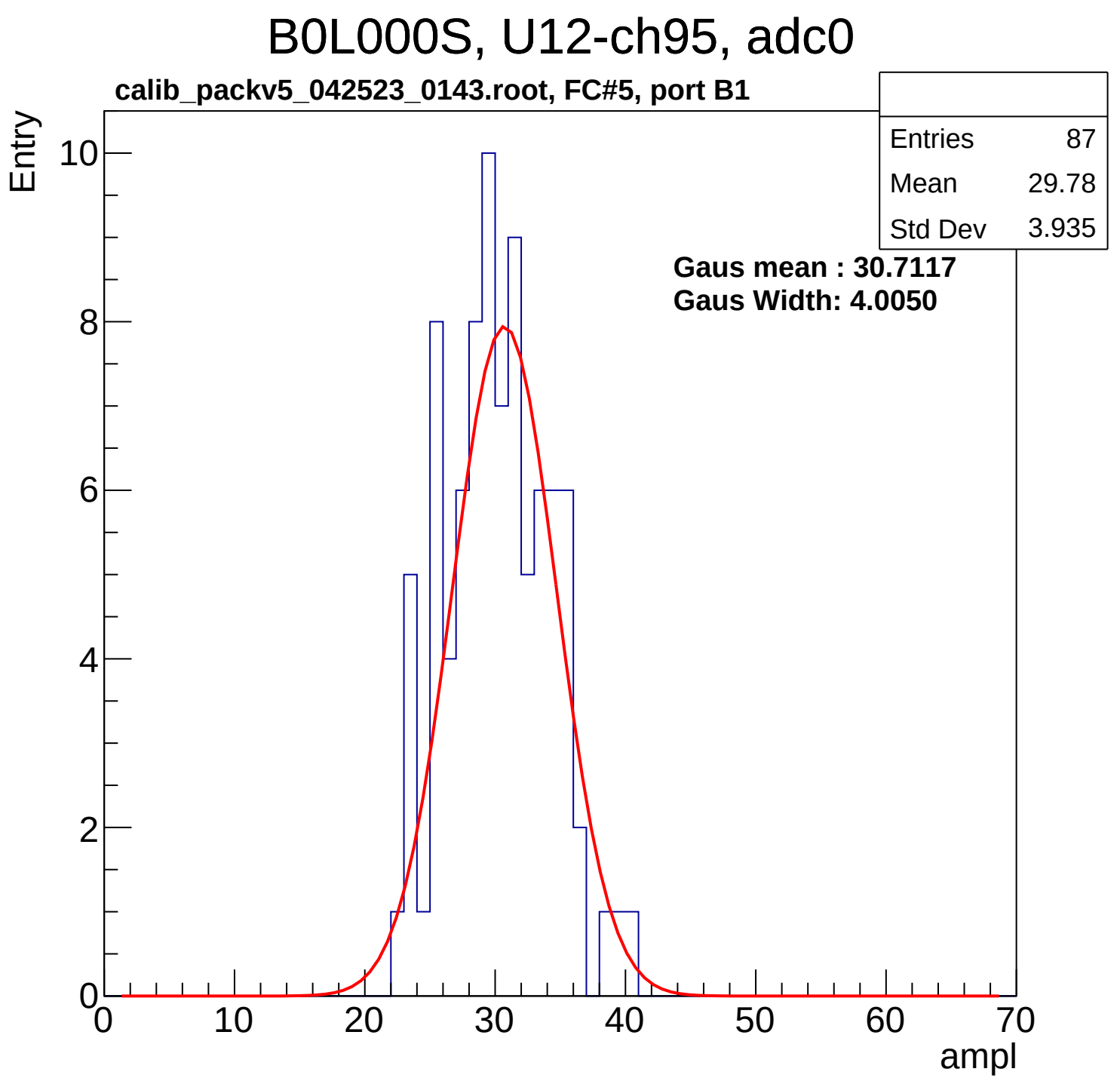
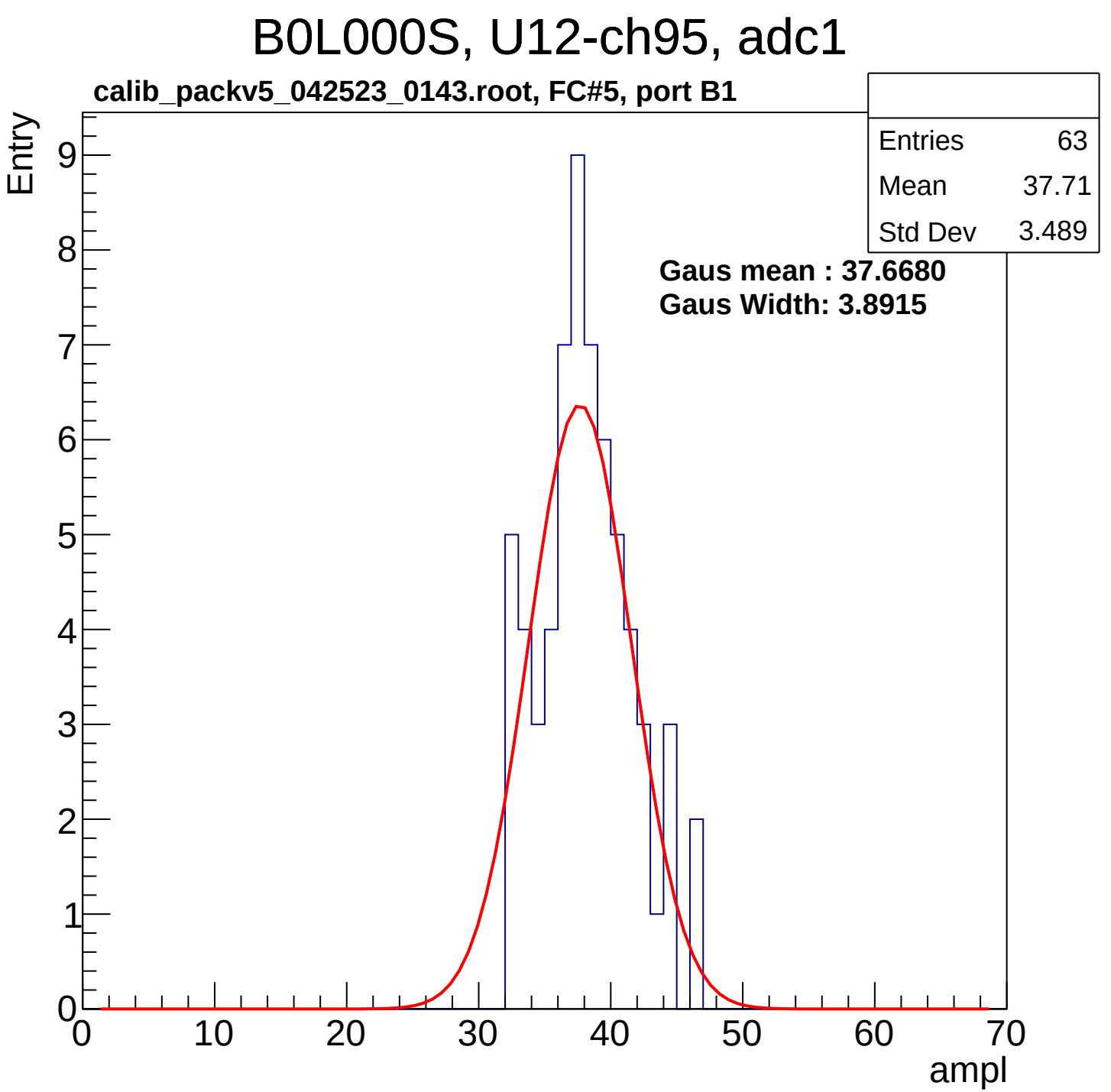
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 29.78
Std Dev 3.935
Gaus mean : 30.7117
Gaus Width: 4.0050
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 37.71
Std Dev 3.489
Gaus mean : 37.6680
Gaus Width: 3.8915
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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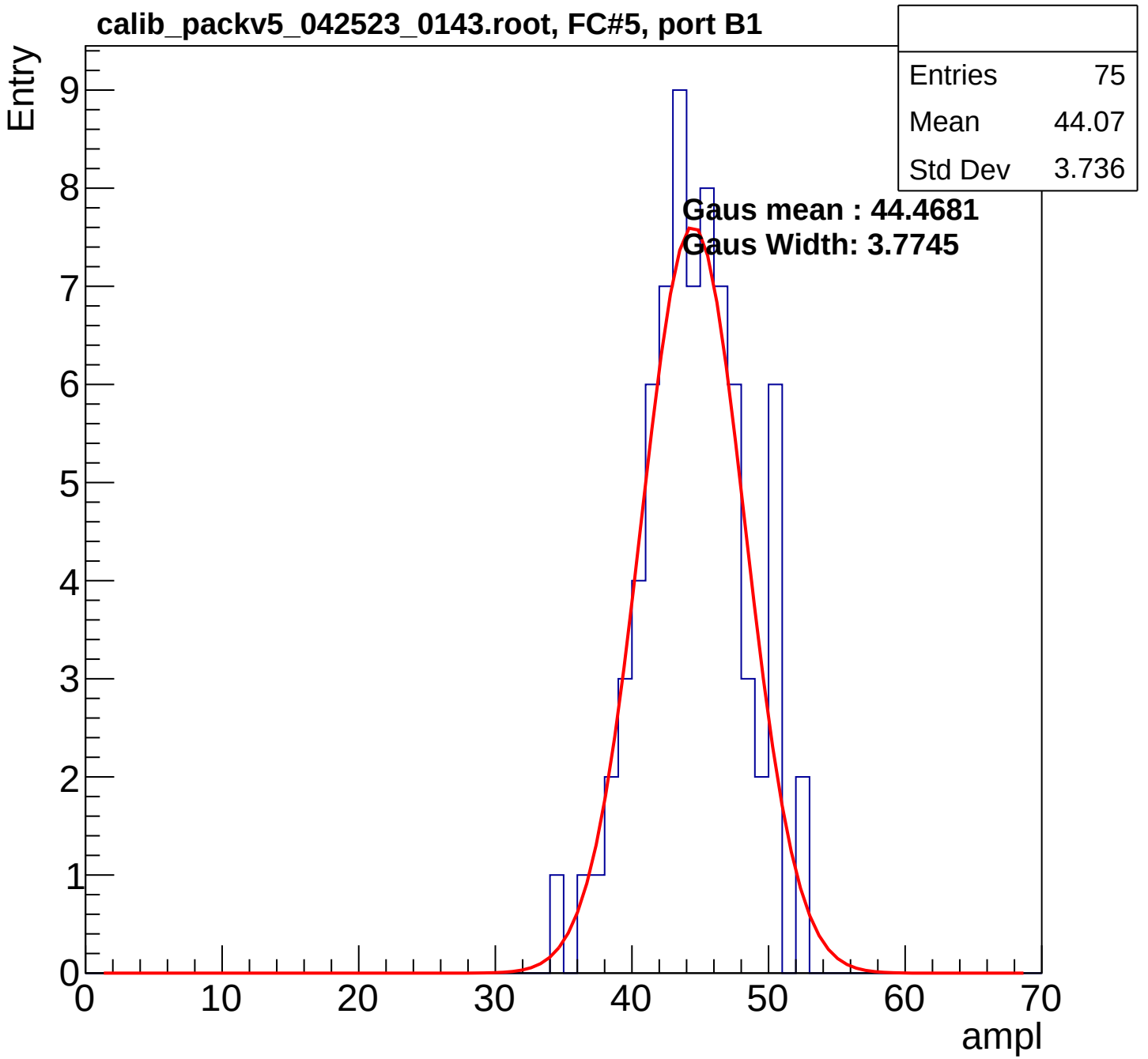
B0L000S, U12-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 44.07
Std Dev 3.736
Gaus mean : 44.4681
Gaus Width: 3.7745
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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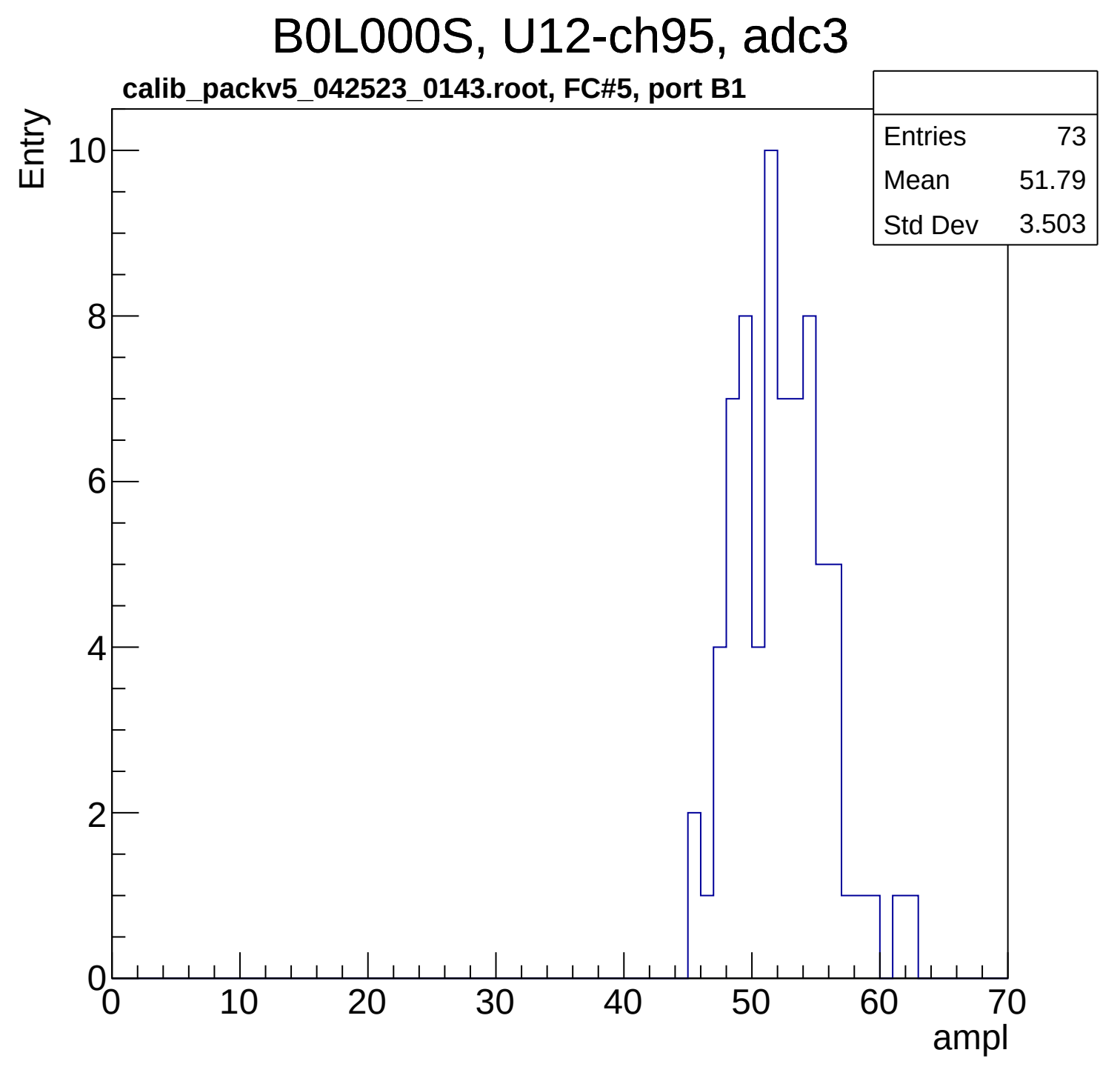
B0L000S, U12-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 51.79
Std Dev 3.503
Entry
ampl
0
2
4
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10
0
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20
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40
50
60
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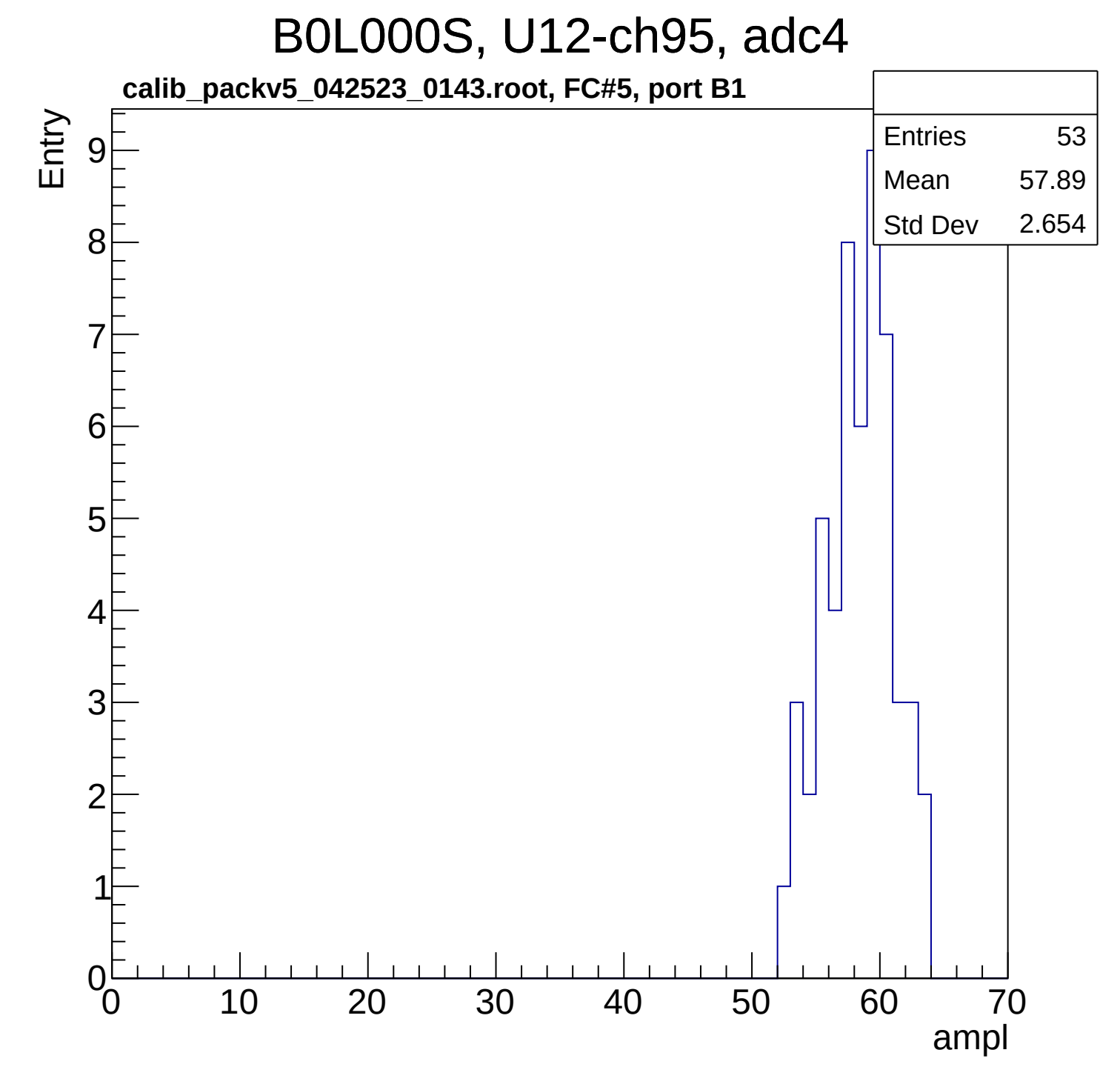
B0L000S, U12-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.89
Std Dev 2.654
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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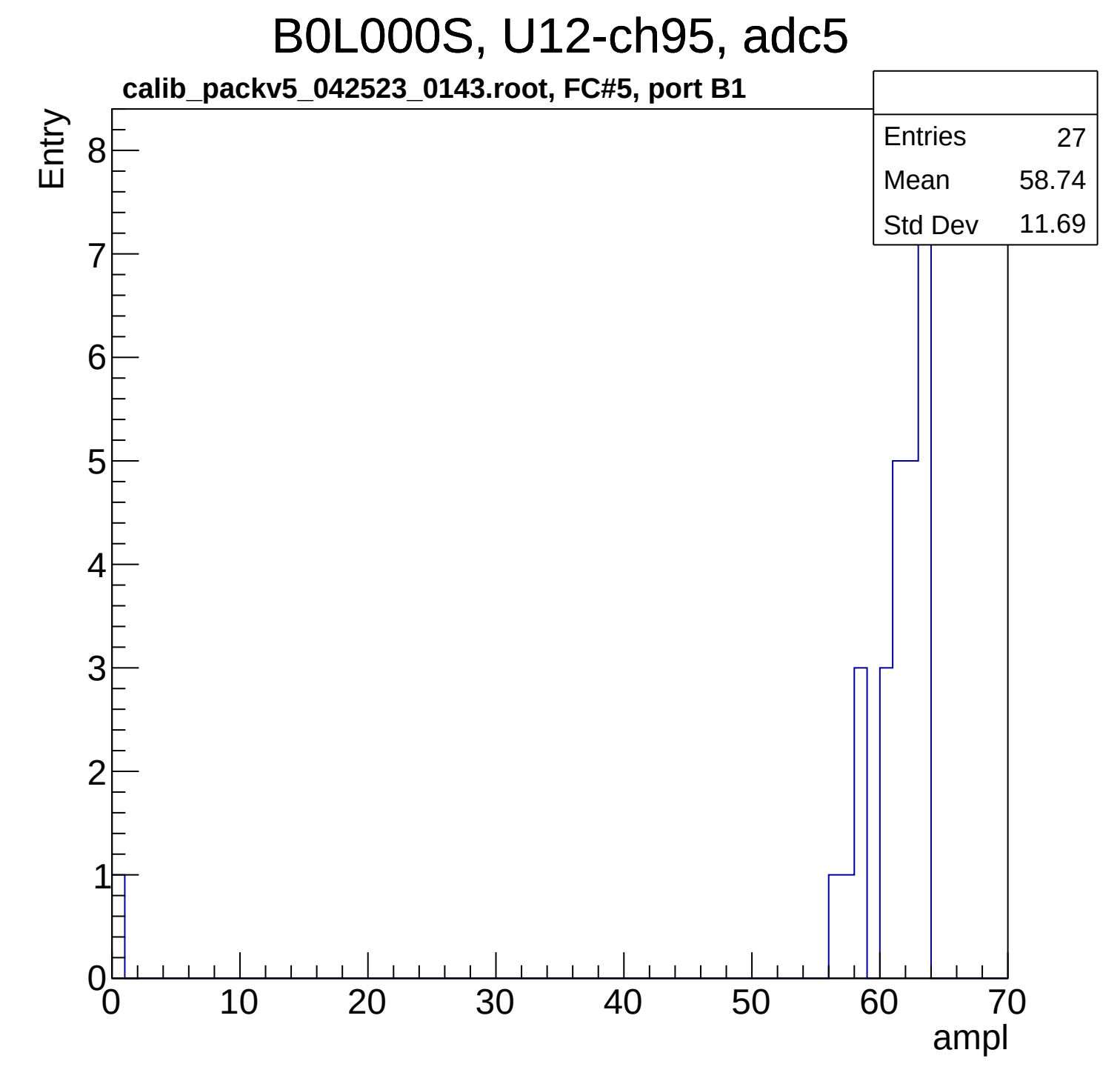
B0L000S, U12-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.74
Std Dev 11.69
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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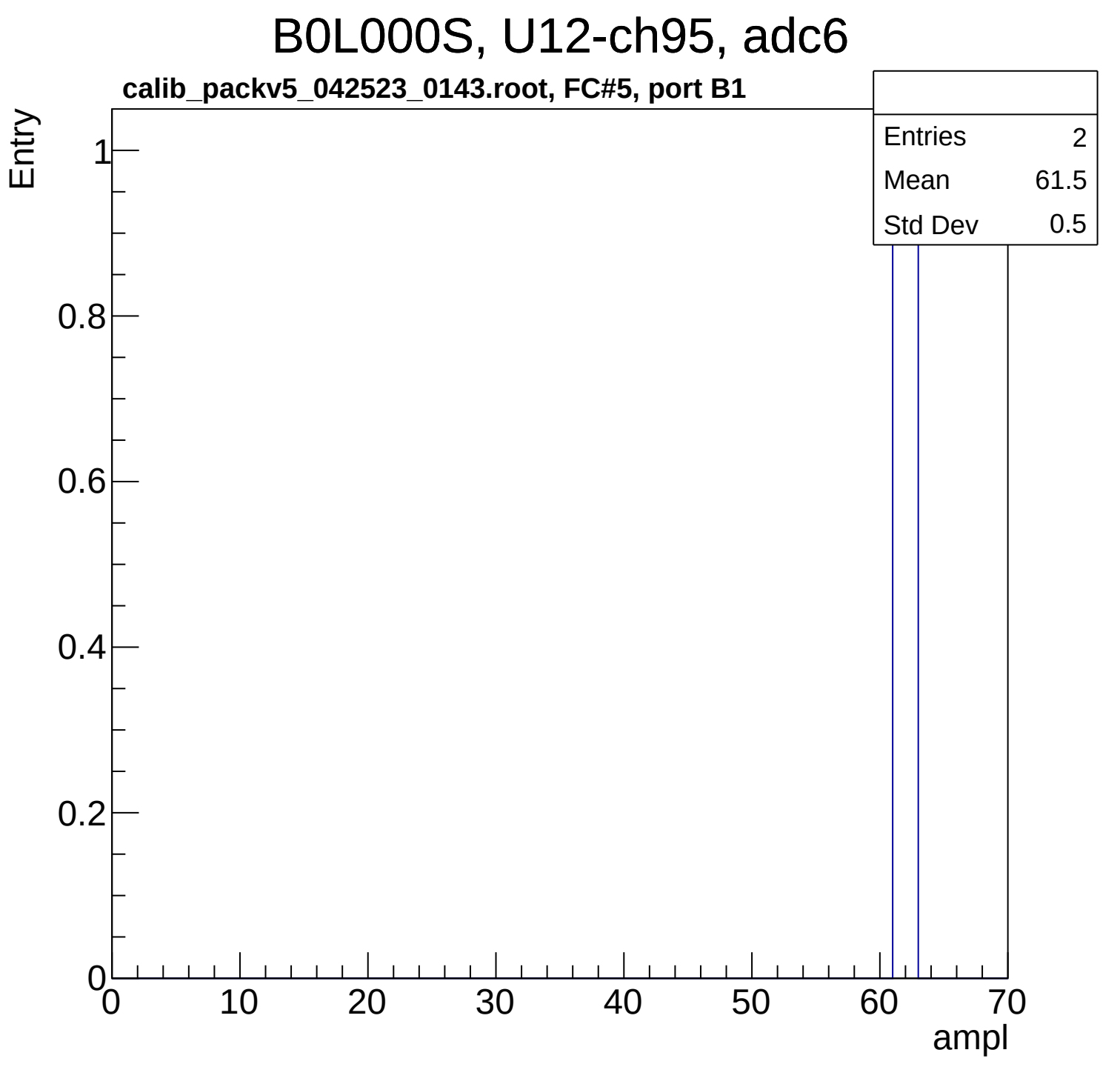
B0L000S, U12-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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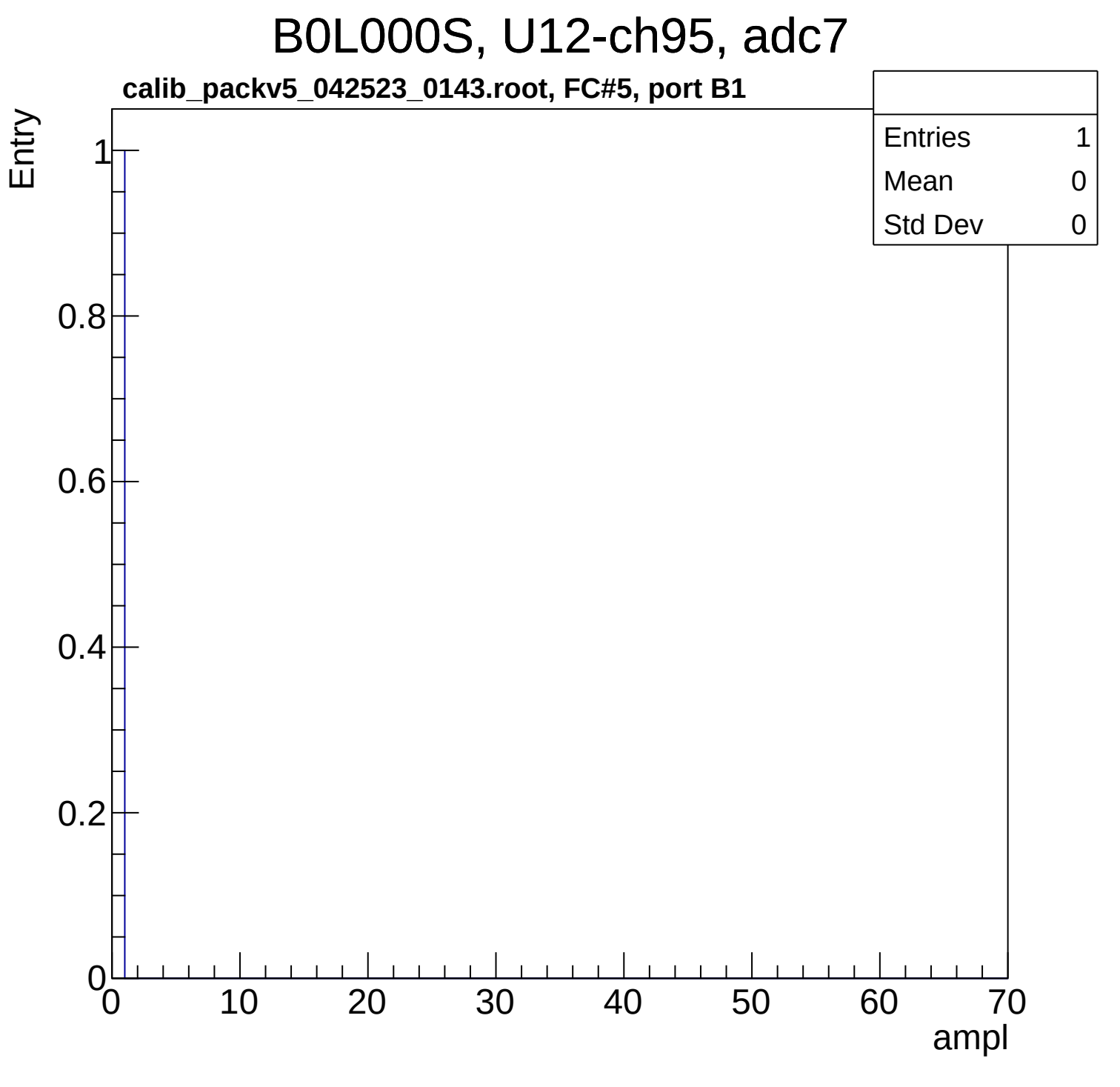
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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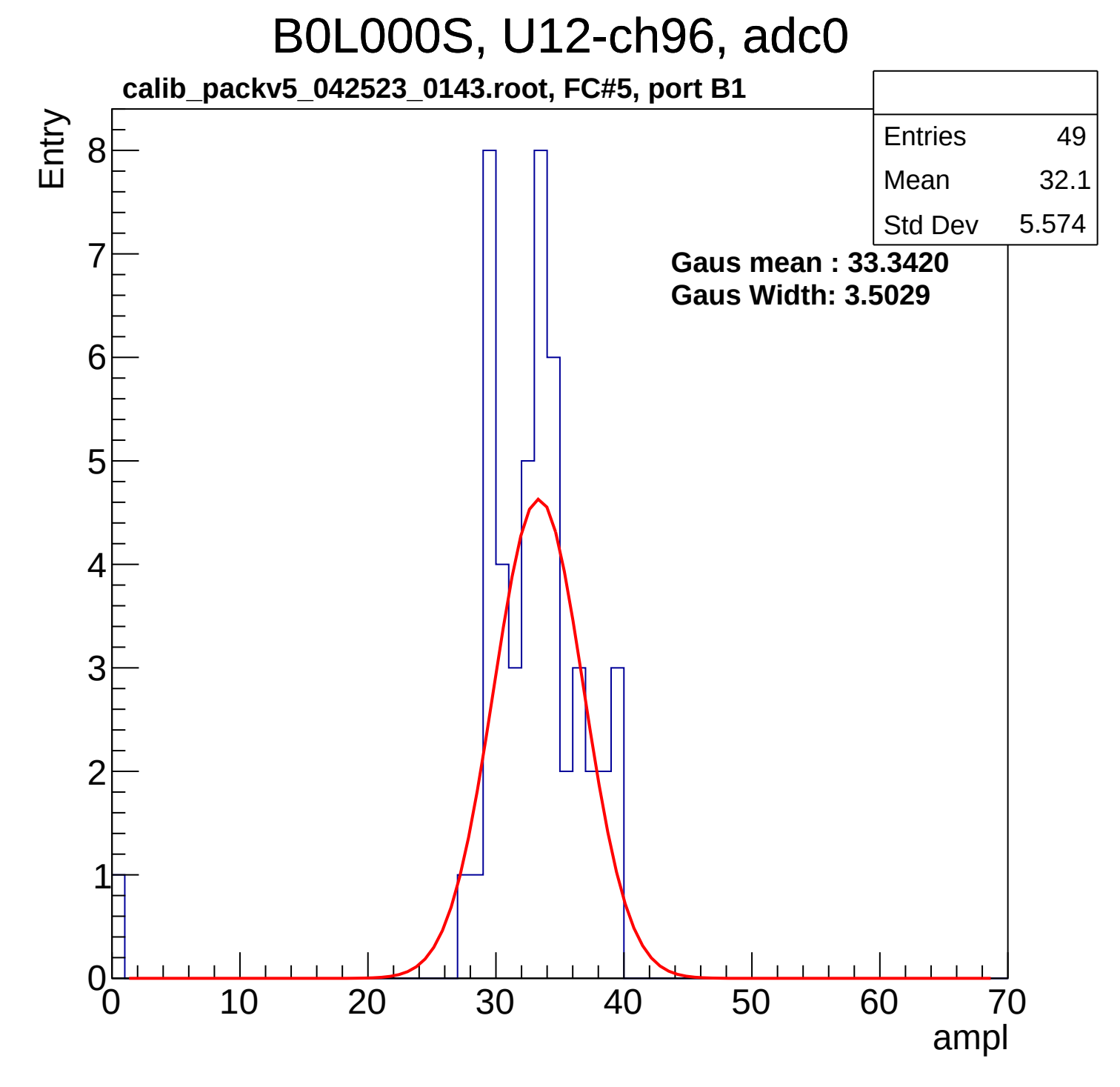
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 32.1
Std Dev 5.574
Gaus mean : 33.3420
Gaus Width: 3.5029
Entry
ampl
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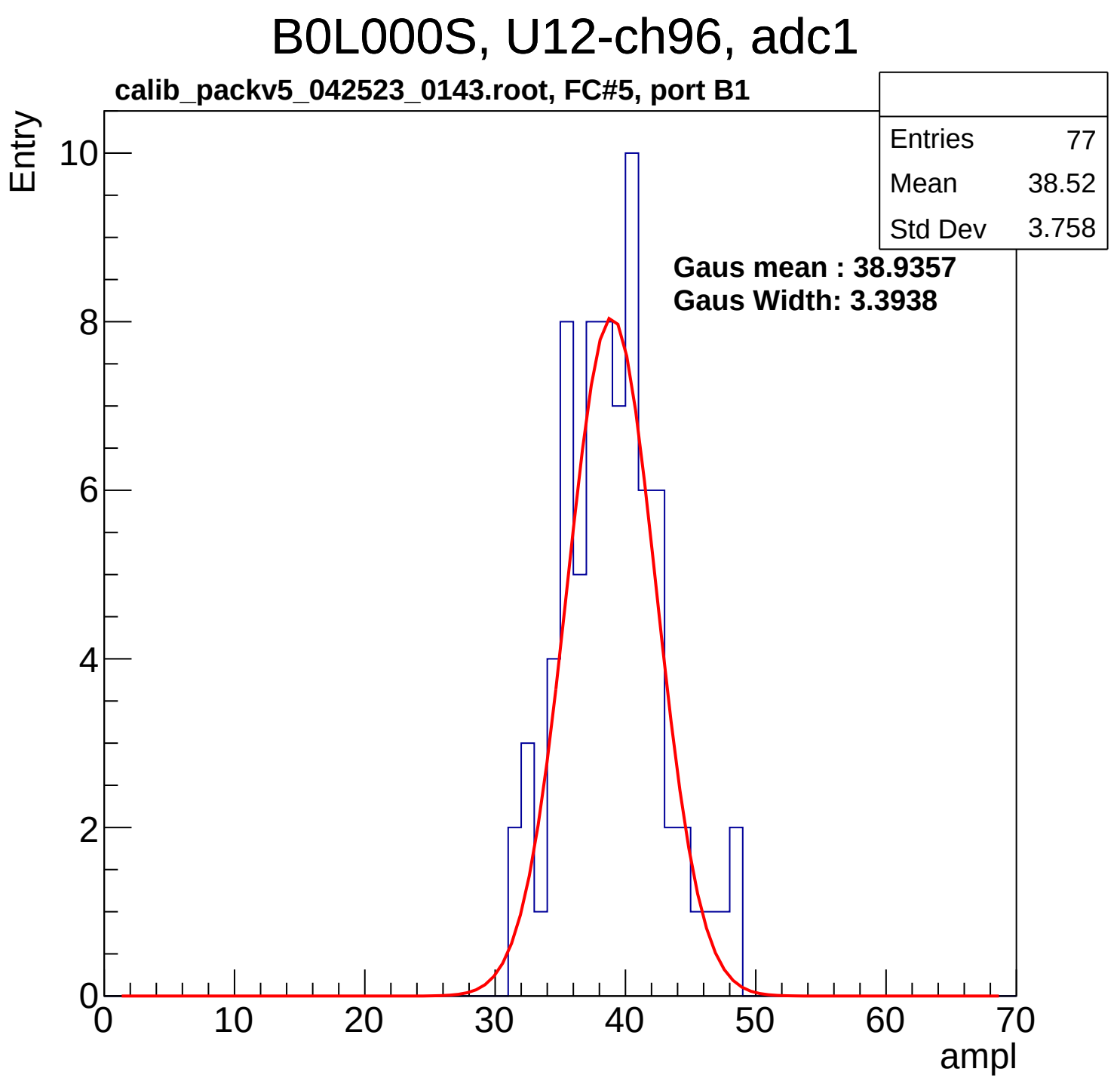
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 38.52
Std Dev 3.758
Gaus mean : 38.9357
Gaus Width: 3.3938
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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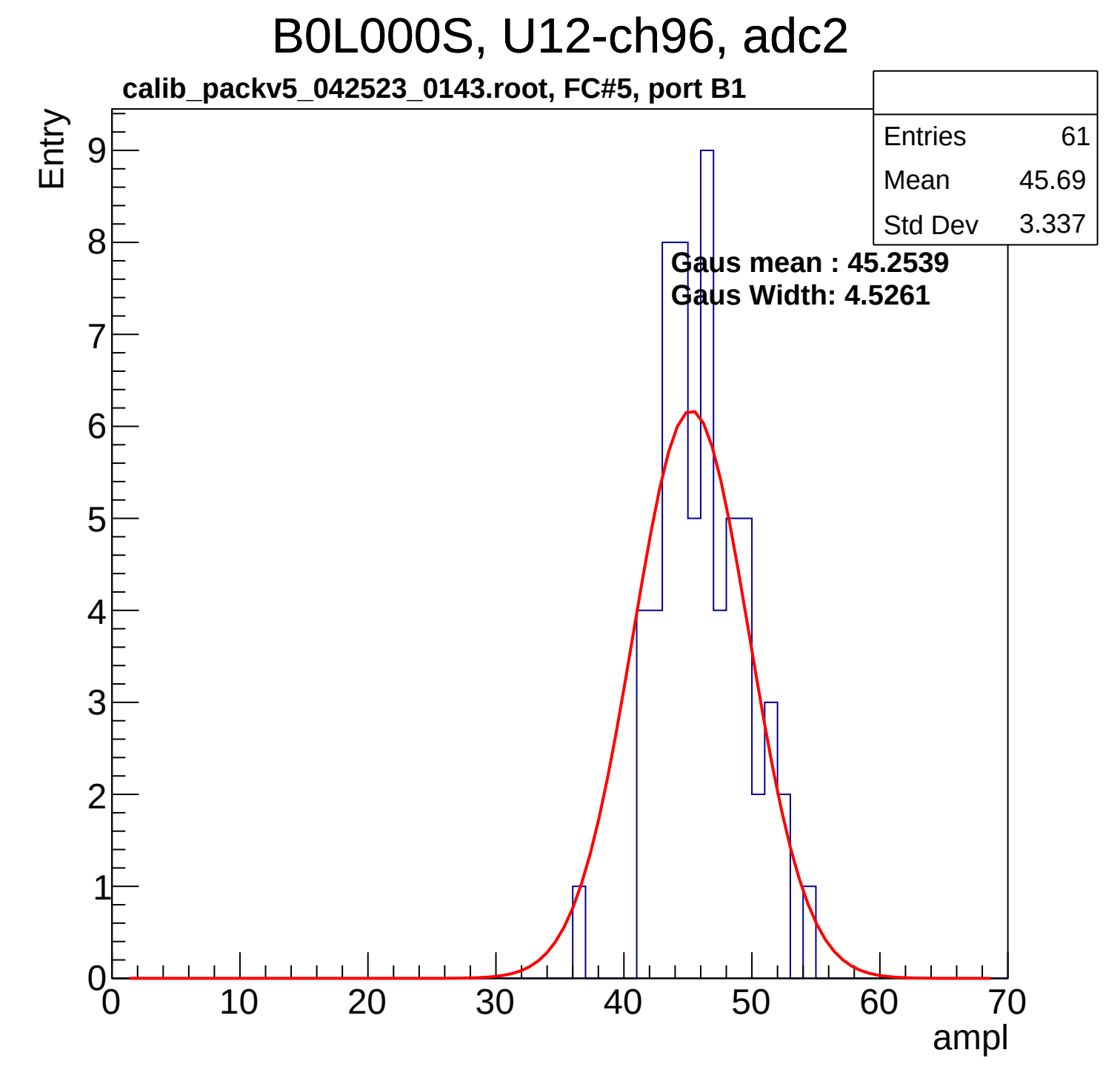
B0L000S, U12-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 45.69
Std Dev 3.337
Gaus mean : 45.2539
Gaus Width: 4.5261
Entry
ampl
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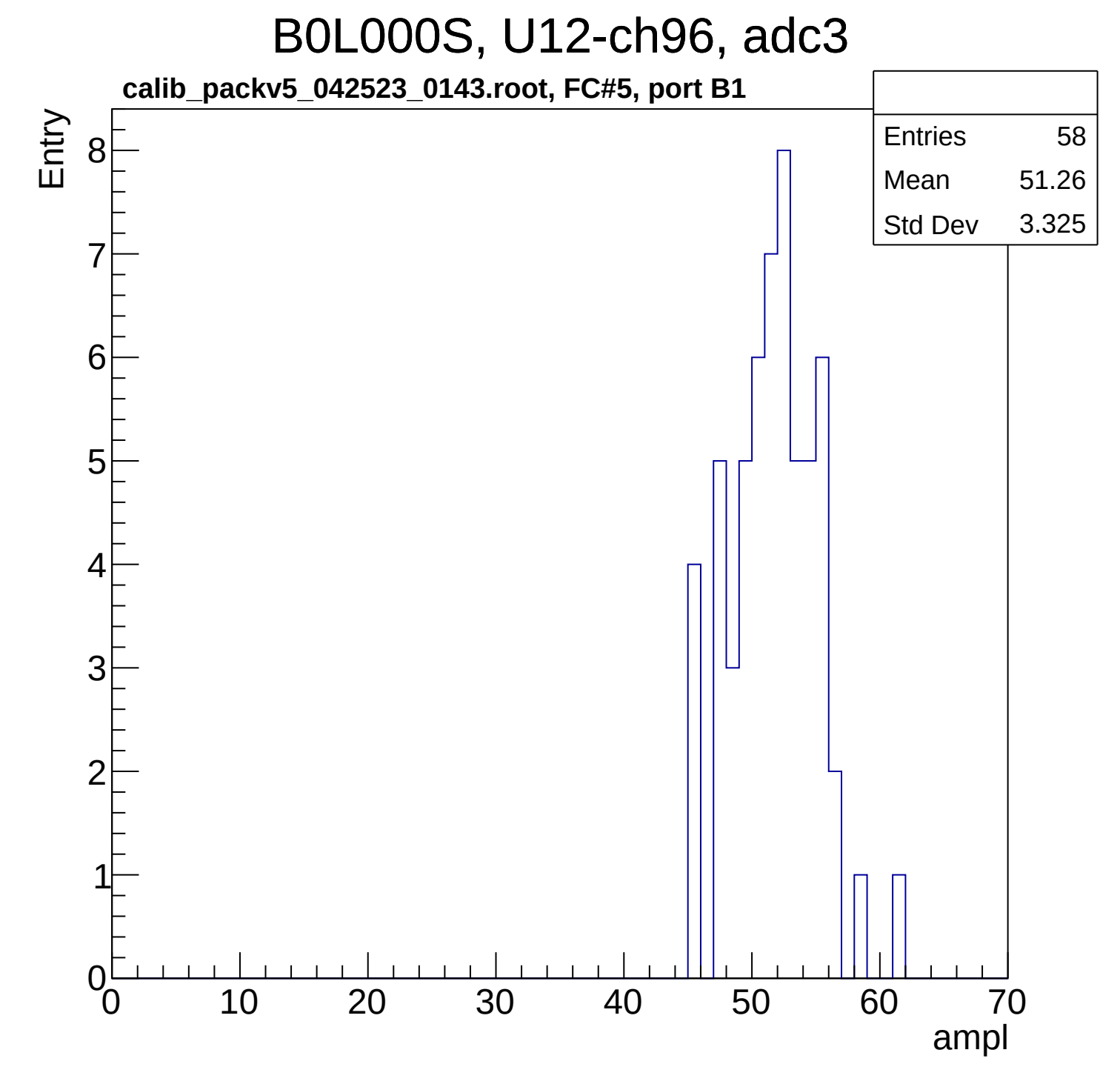
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.26
Std Dev 3.325
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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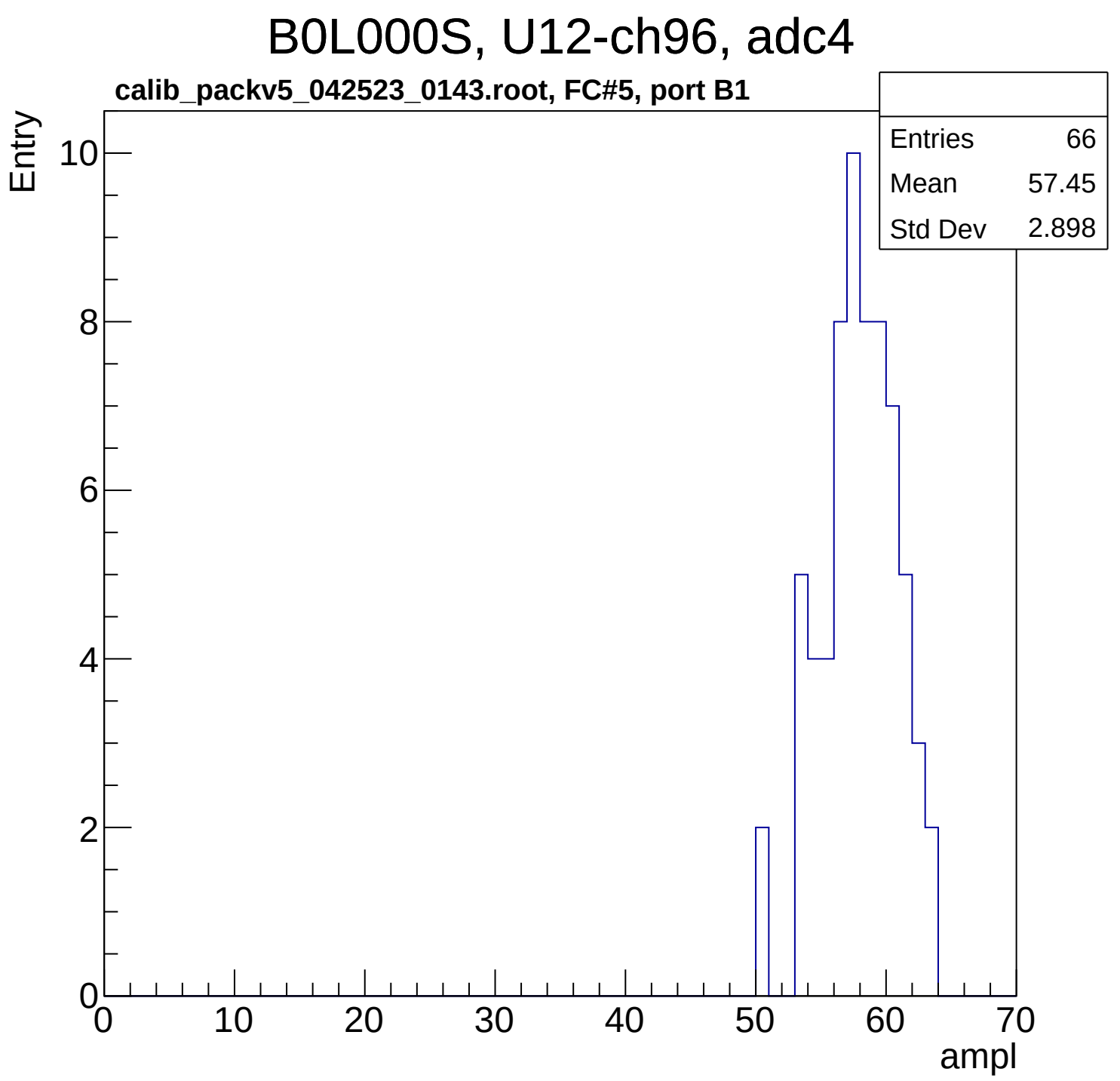
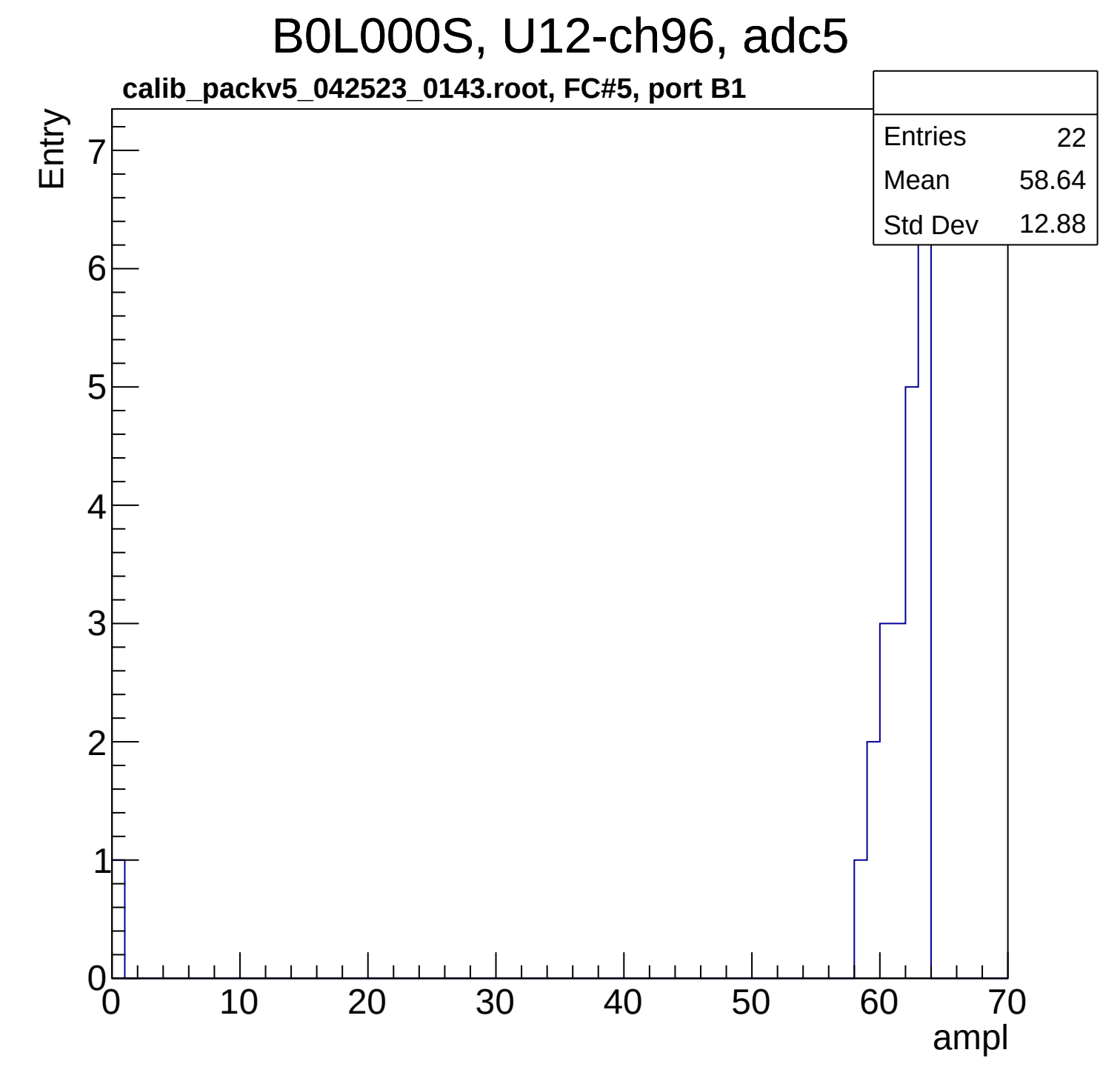

B0L000S, U12-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.45
Std Dev 2.898
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58.64
Std Dev 12.88
Entry
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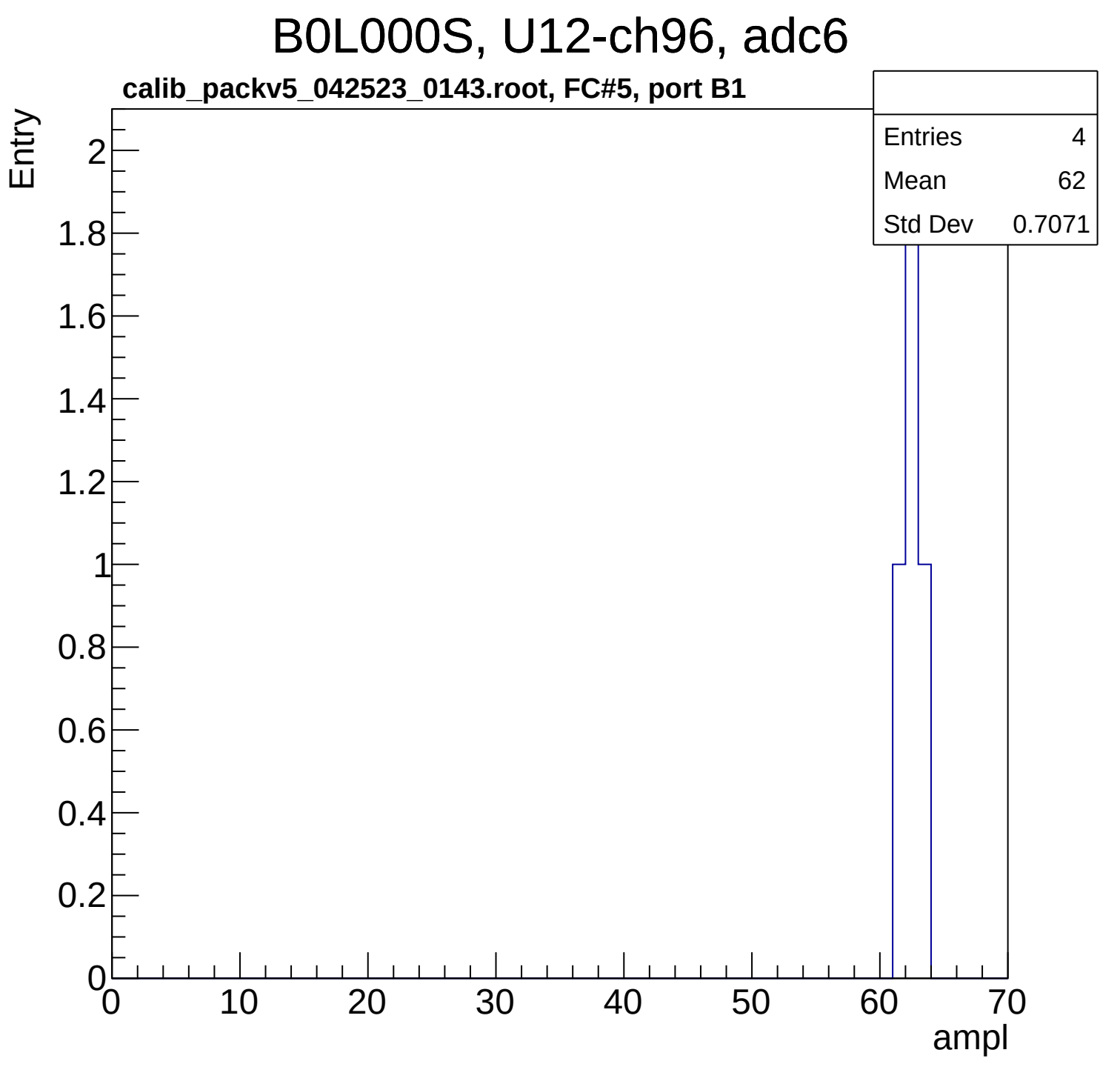
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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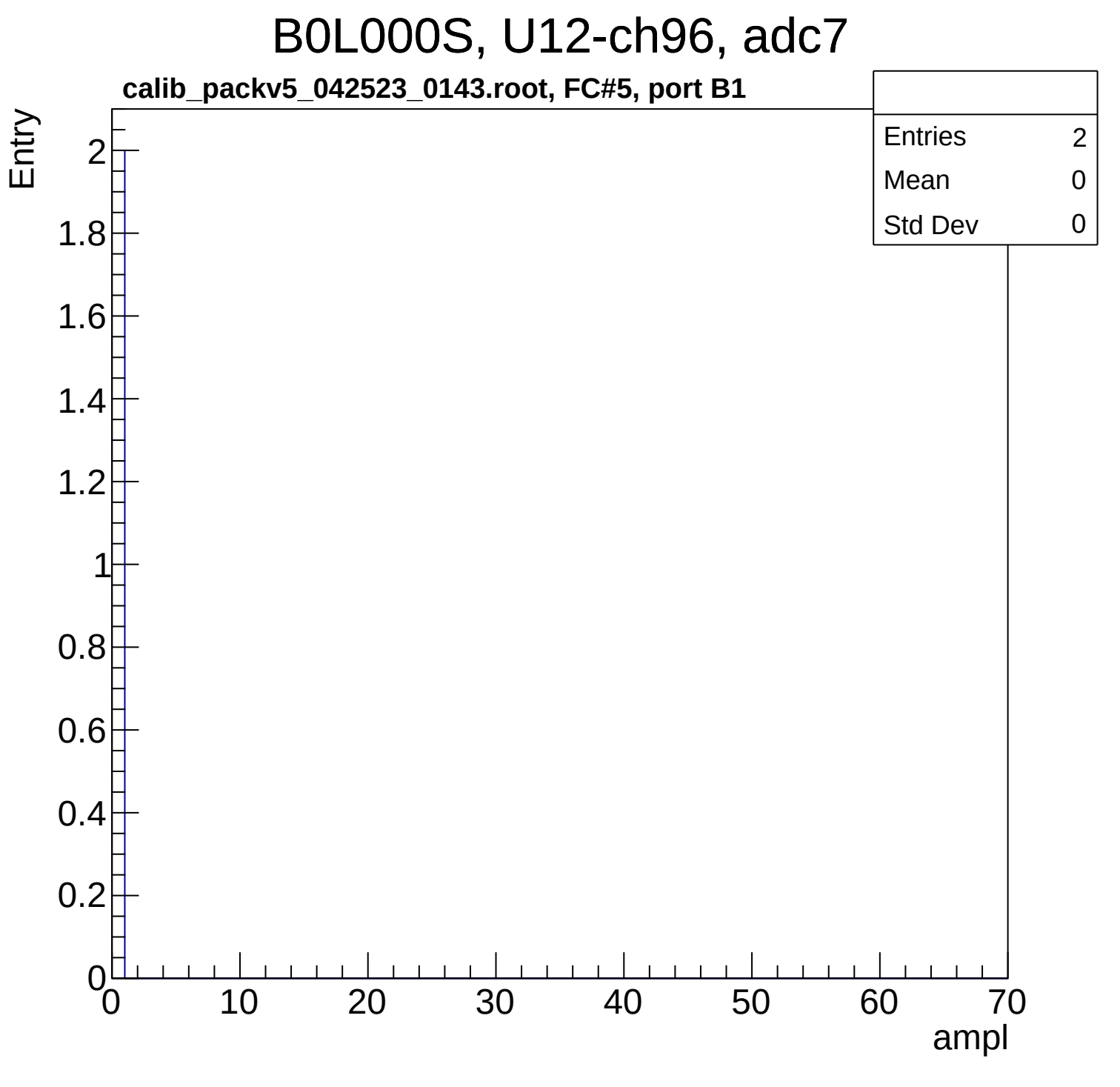
B0L000S, U12-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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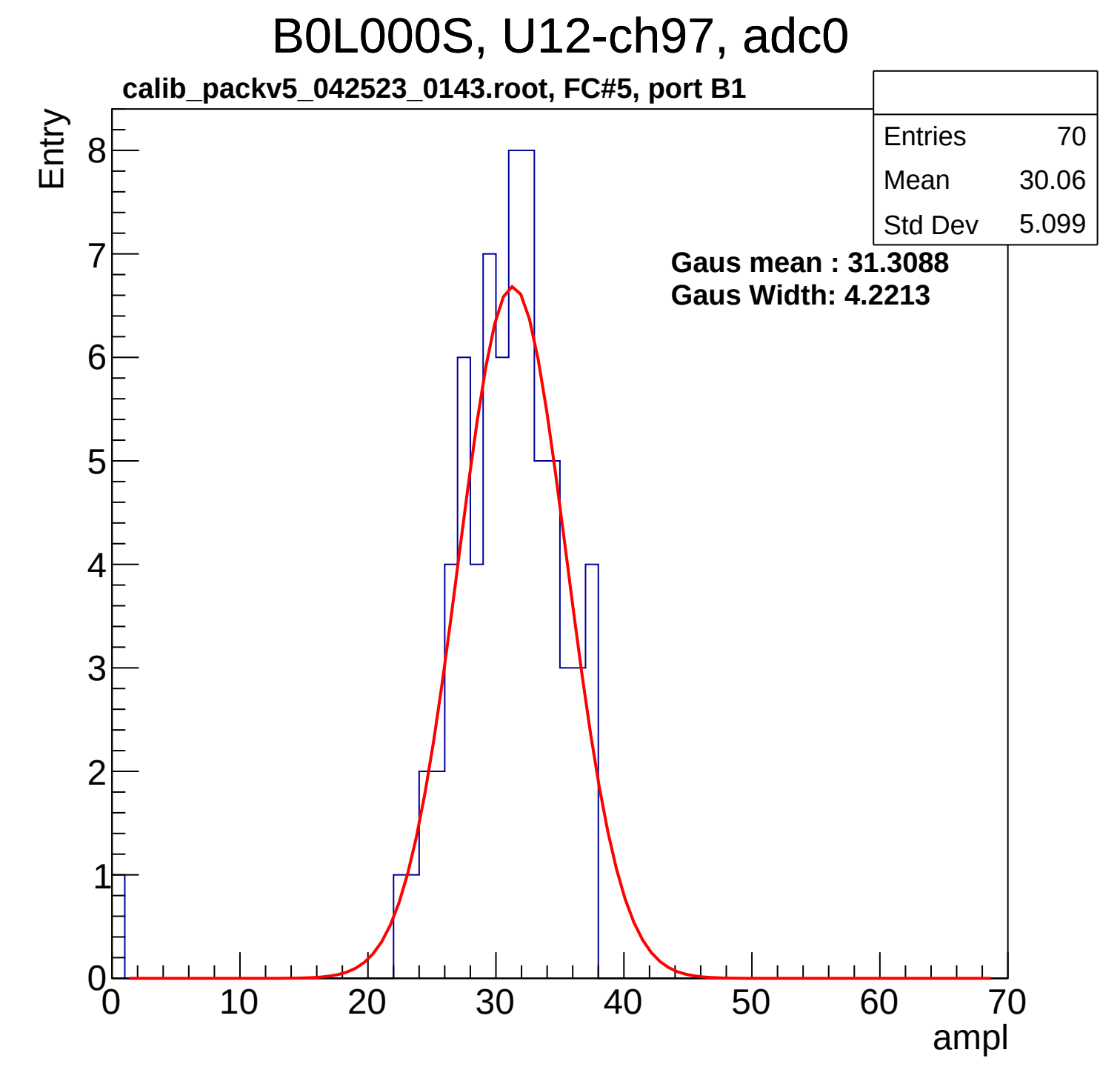
B0L000S, U12-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.06
Std Dev 5.099
Gaus mean : 31.3088
Gaus Width: 4.2213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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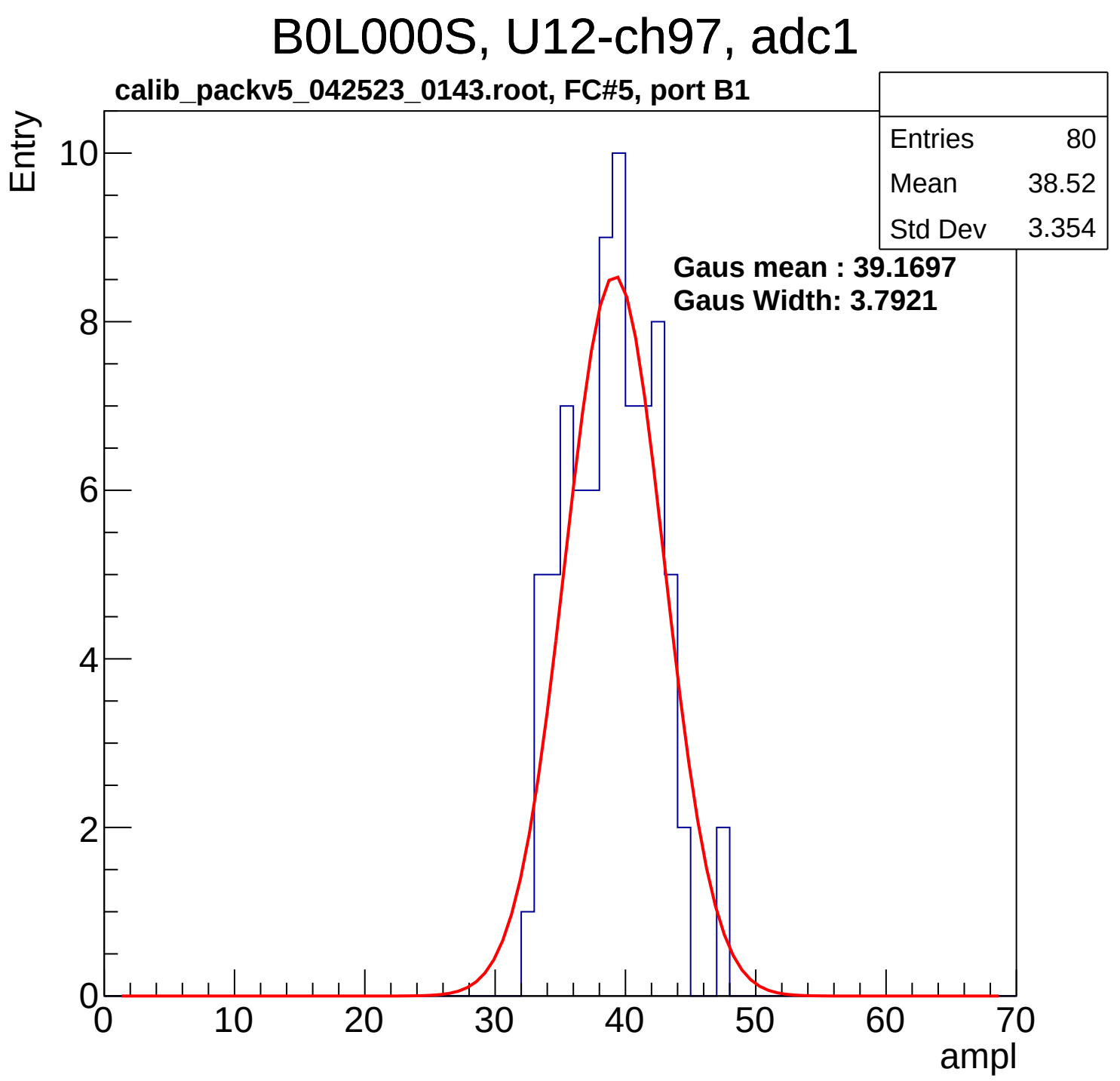
B0L000S, U12-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 38.52
Std Dev 3.354
Gaus mean : 39.1697
Gaus Width: 3.7921
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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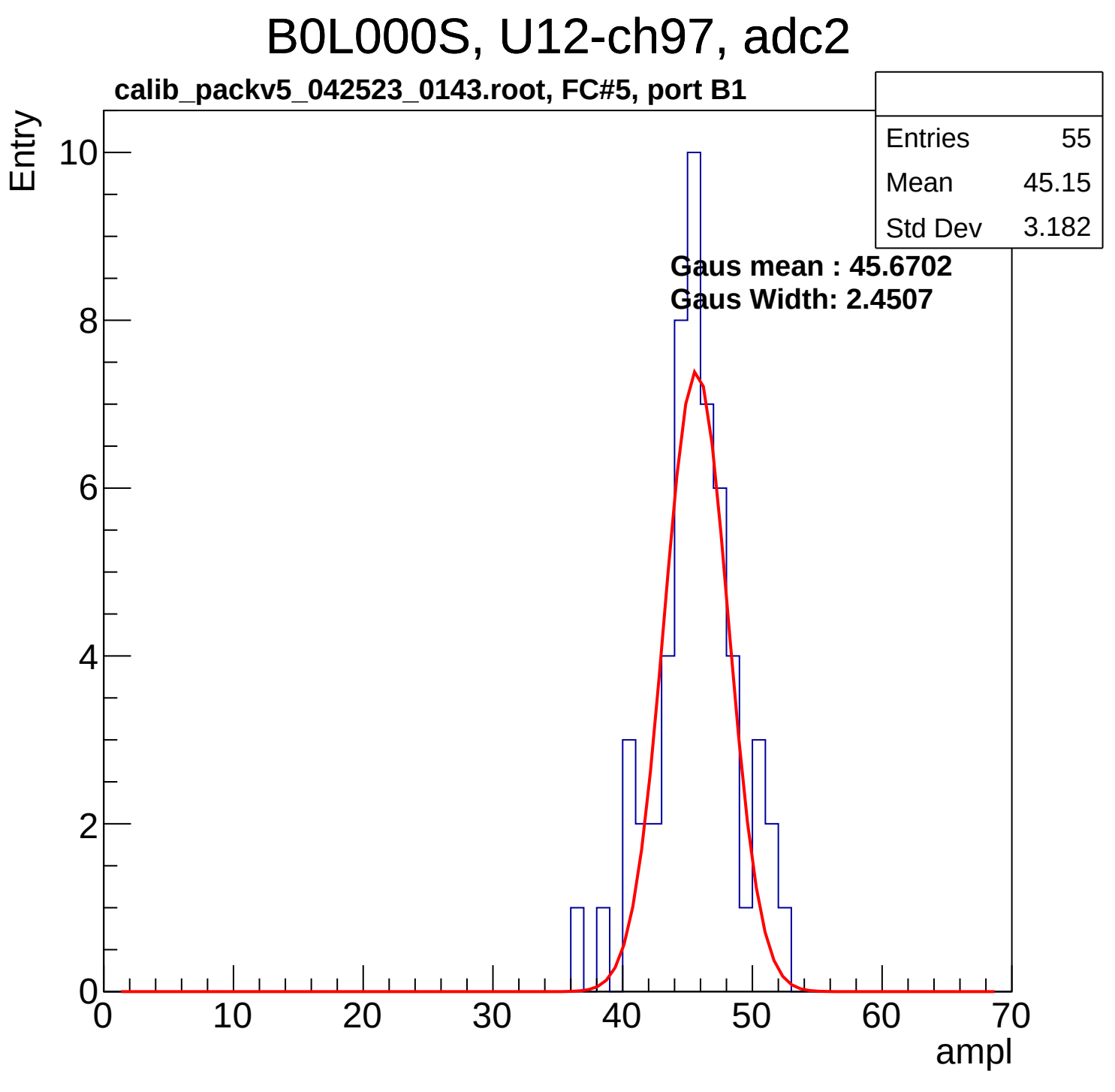
B0L000S, U12-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 45.15
Std Dev 3.182
Gaus mean : 45.6702
Gaus Width: 2.4507
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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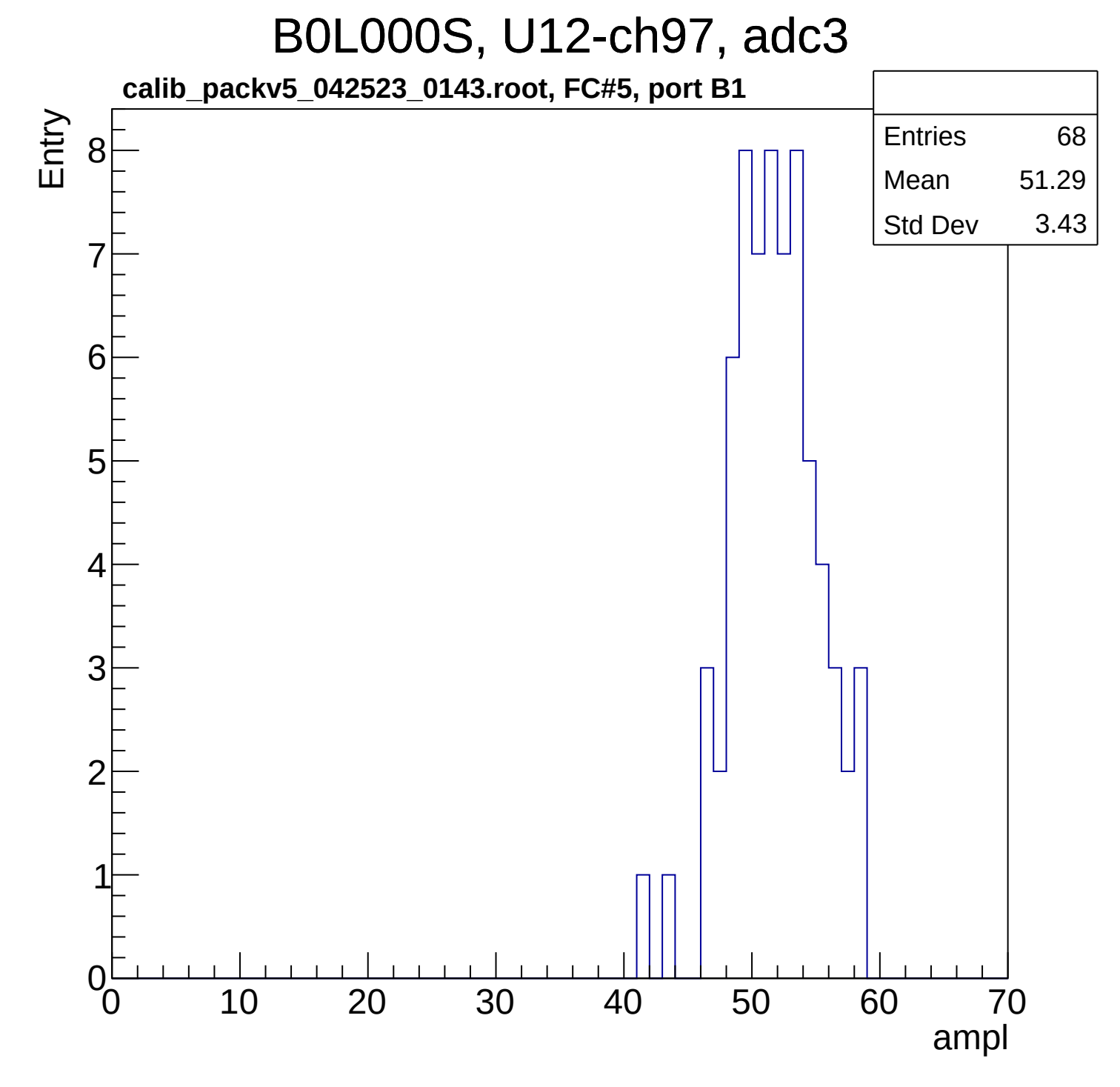
B0L000S, U12-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.29
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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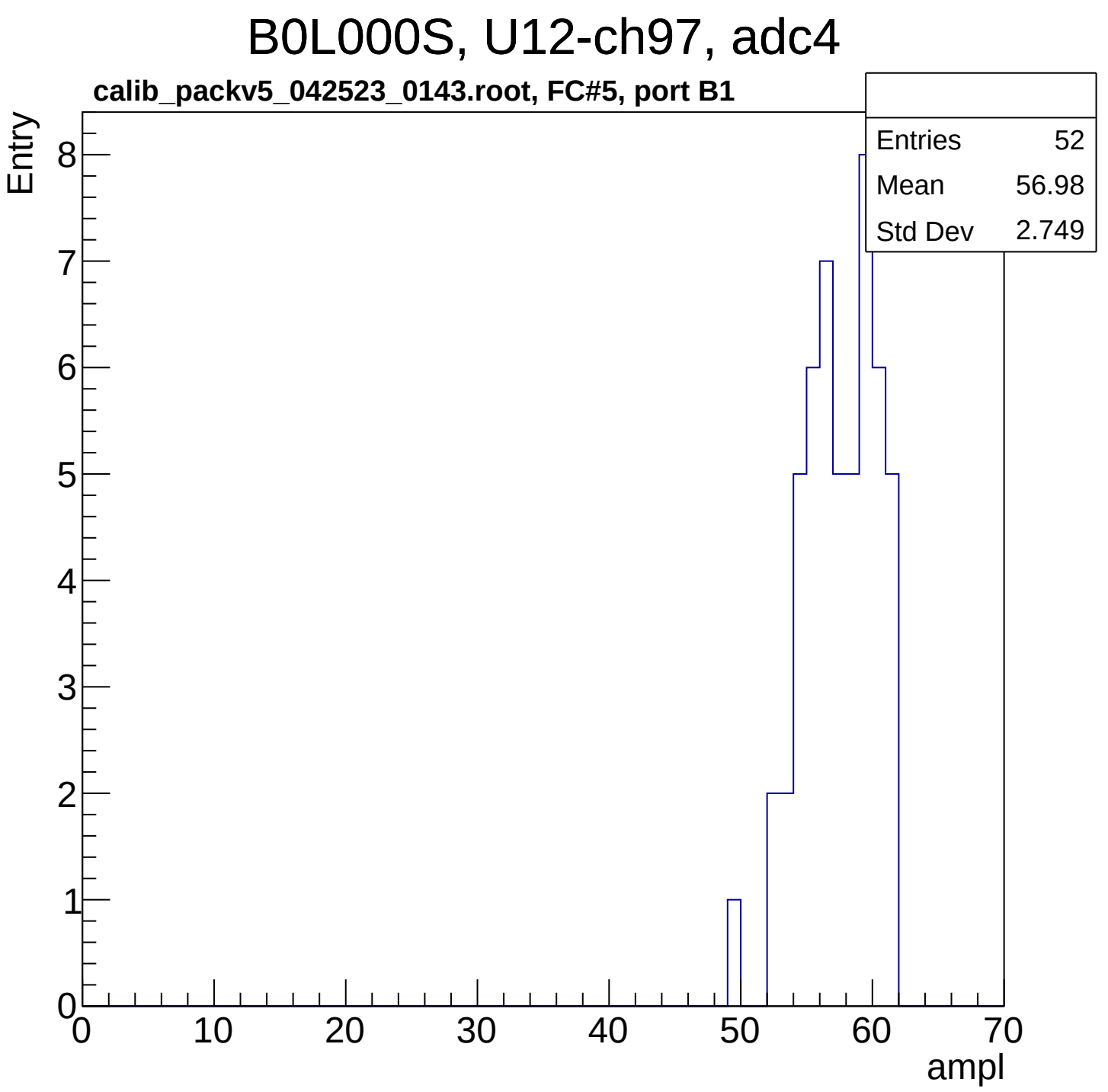
B0L000S, U12-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.98
Std Dev 2.749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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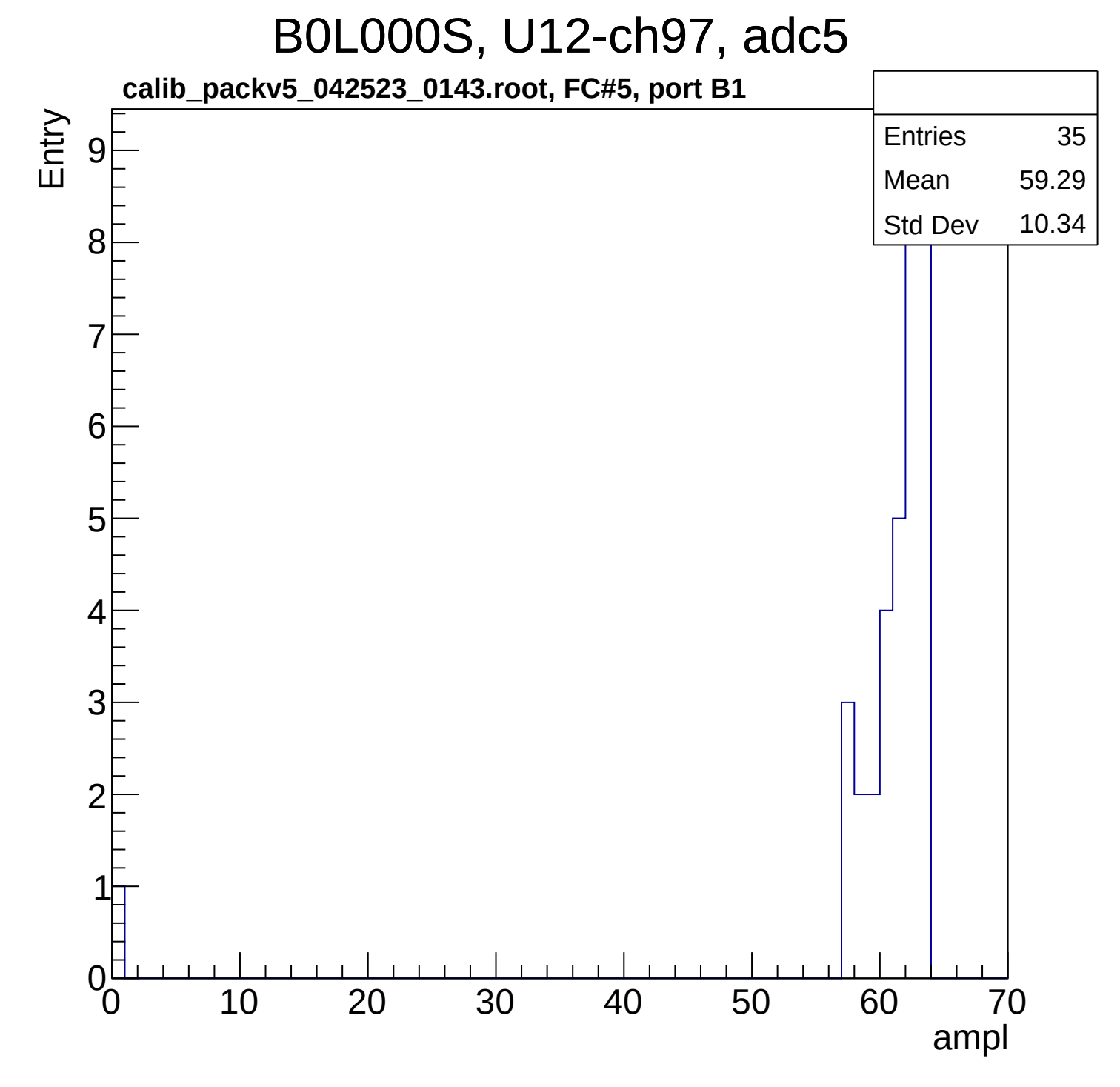
B0L000S, U12-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.29
Std Dev 10.34
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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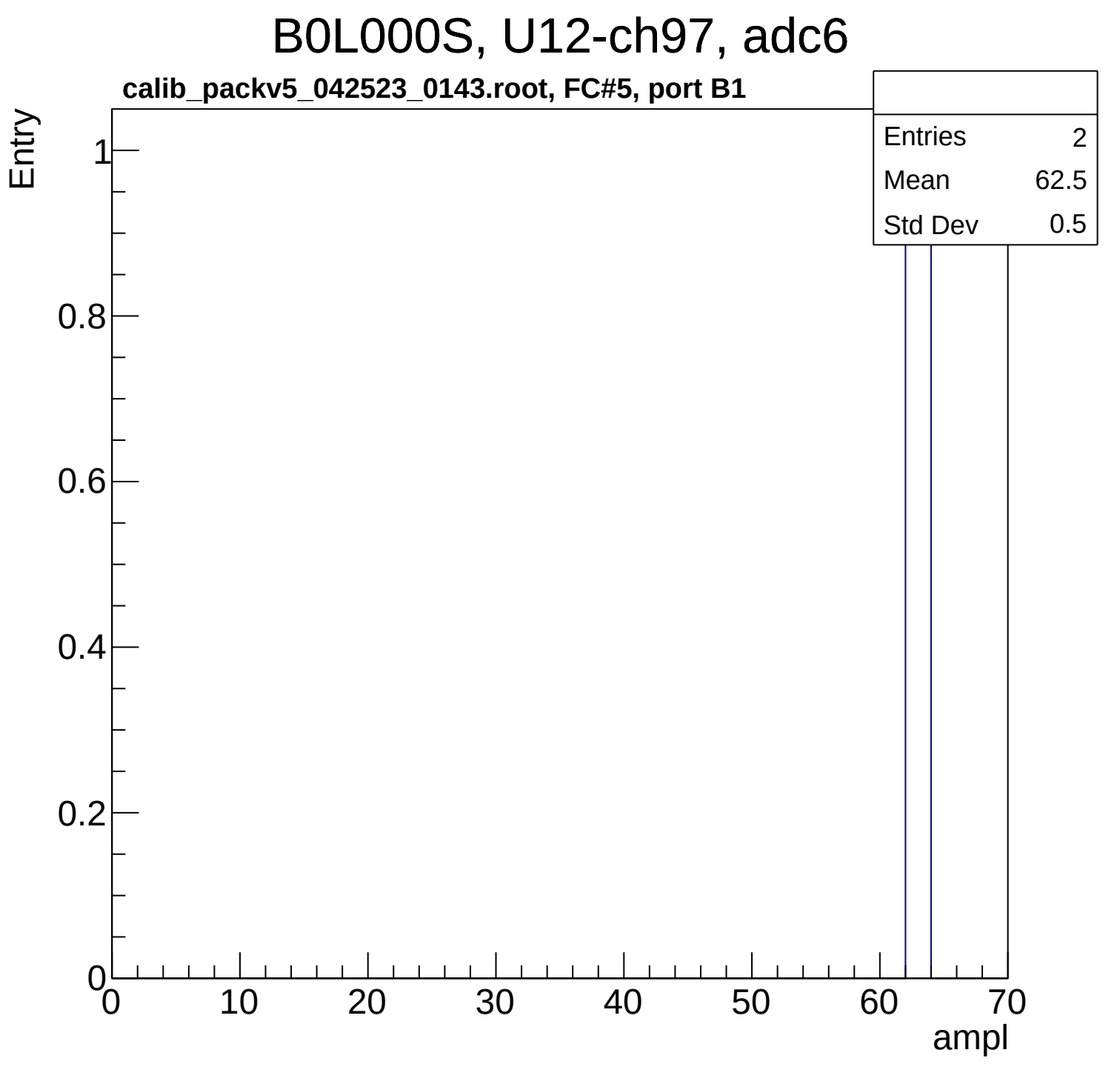
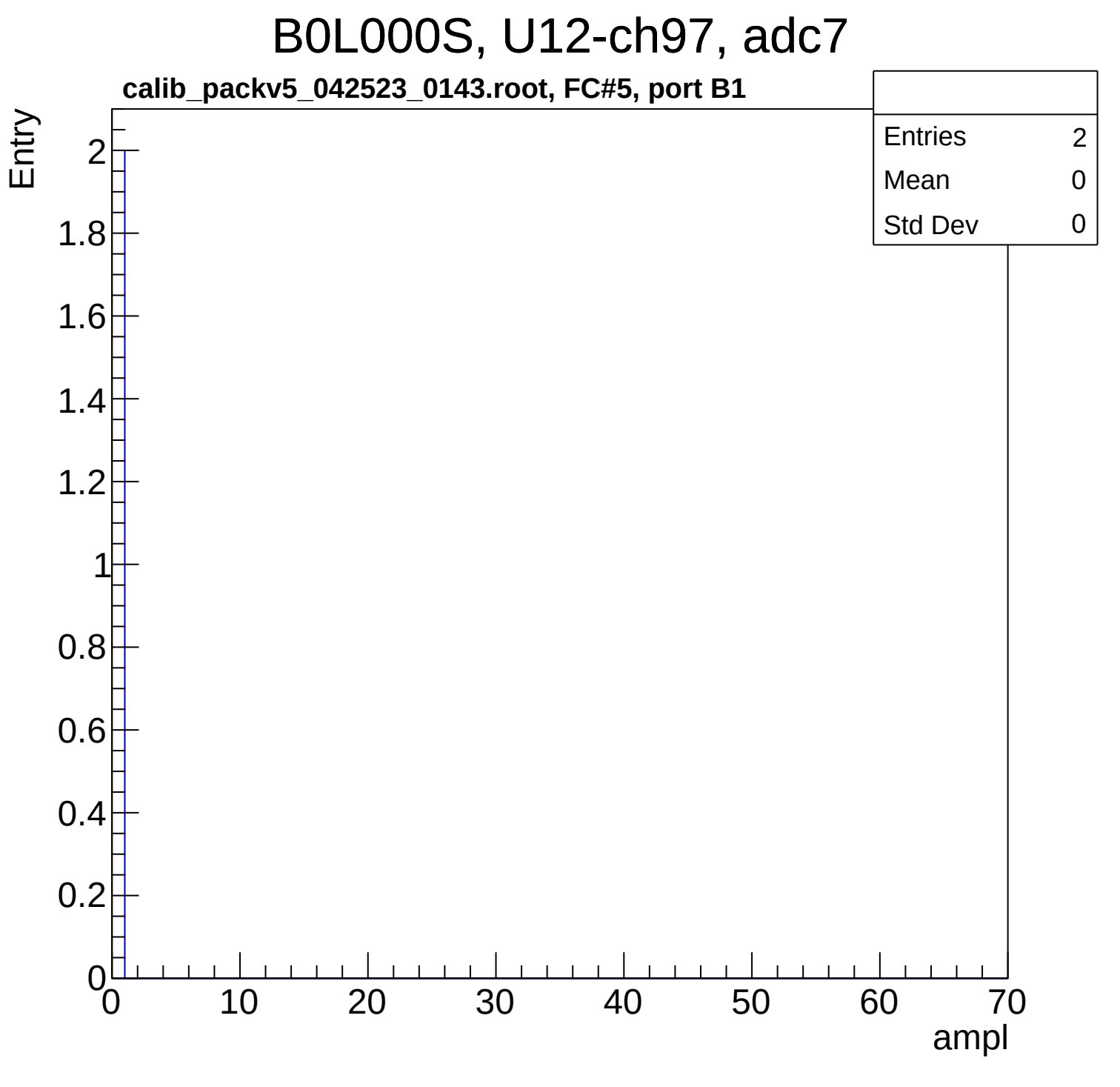
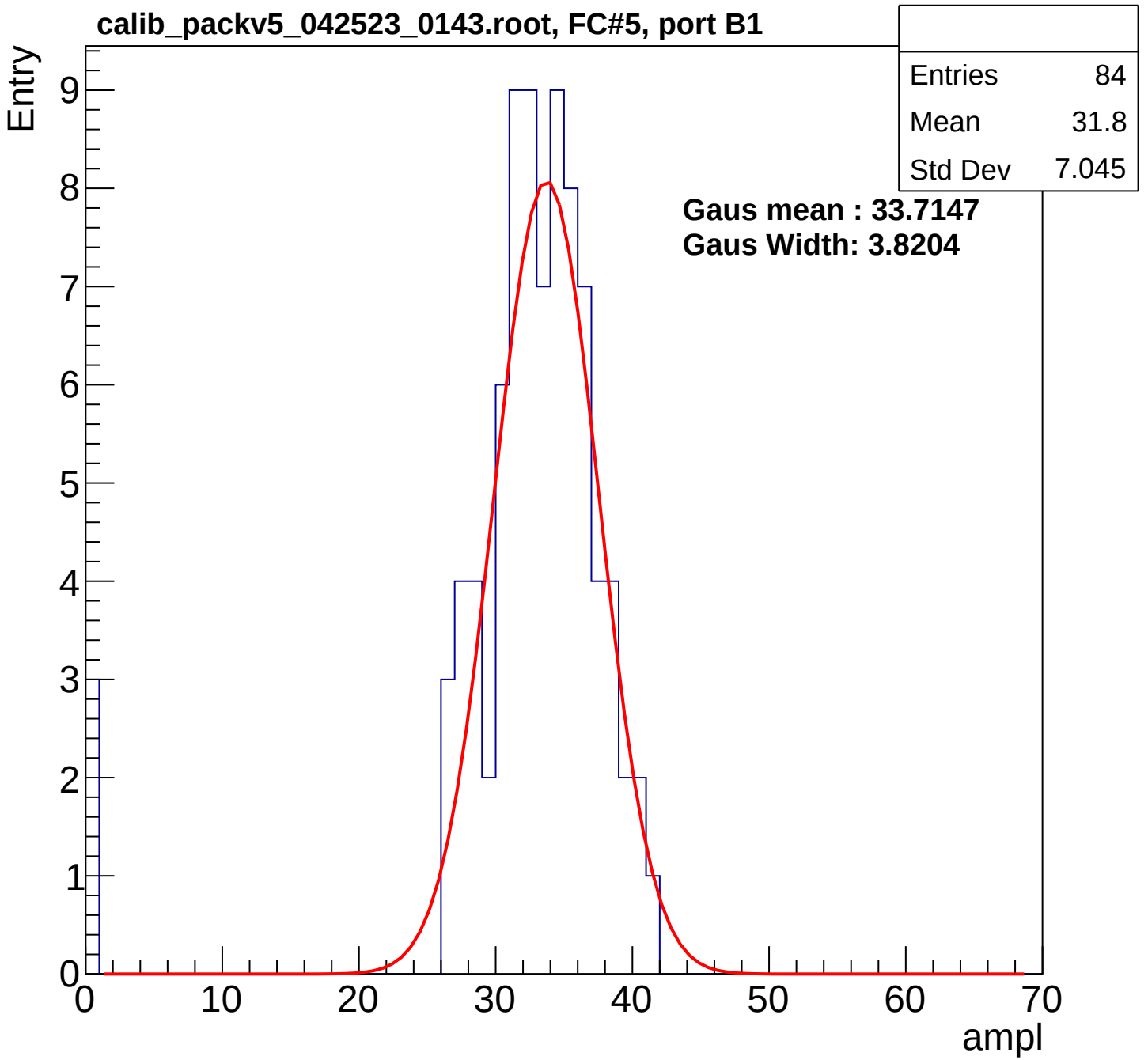
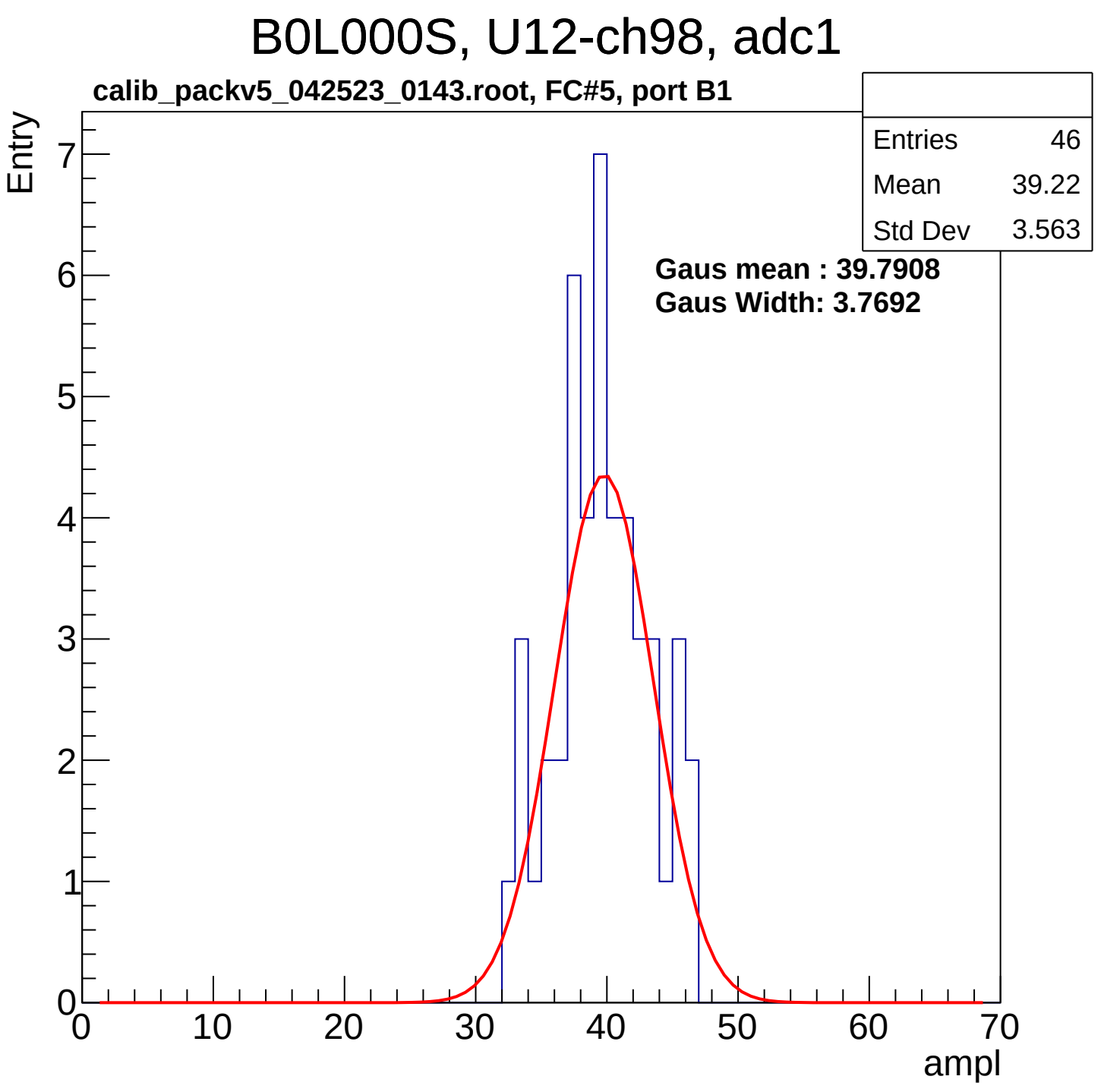
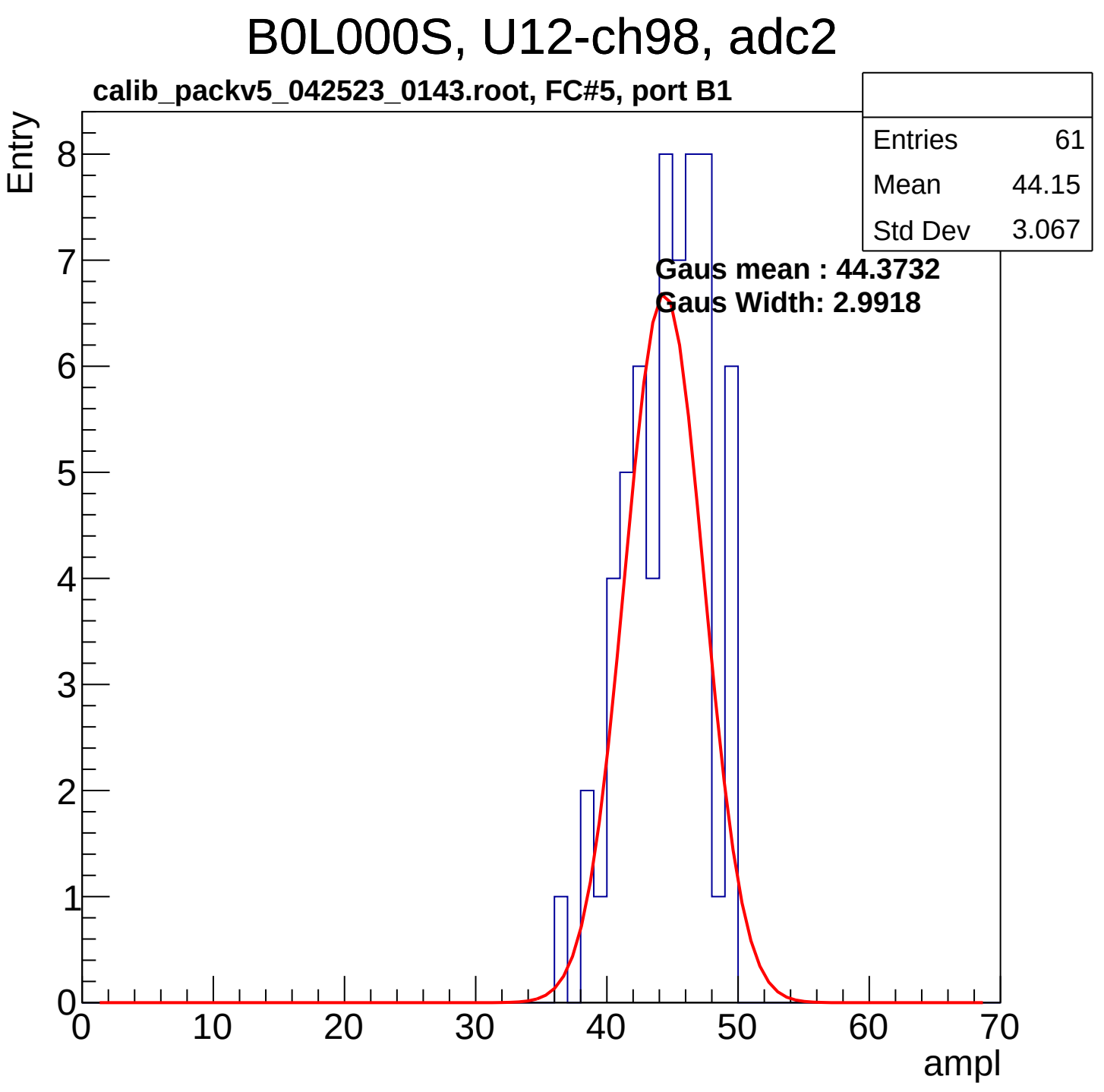
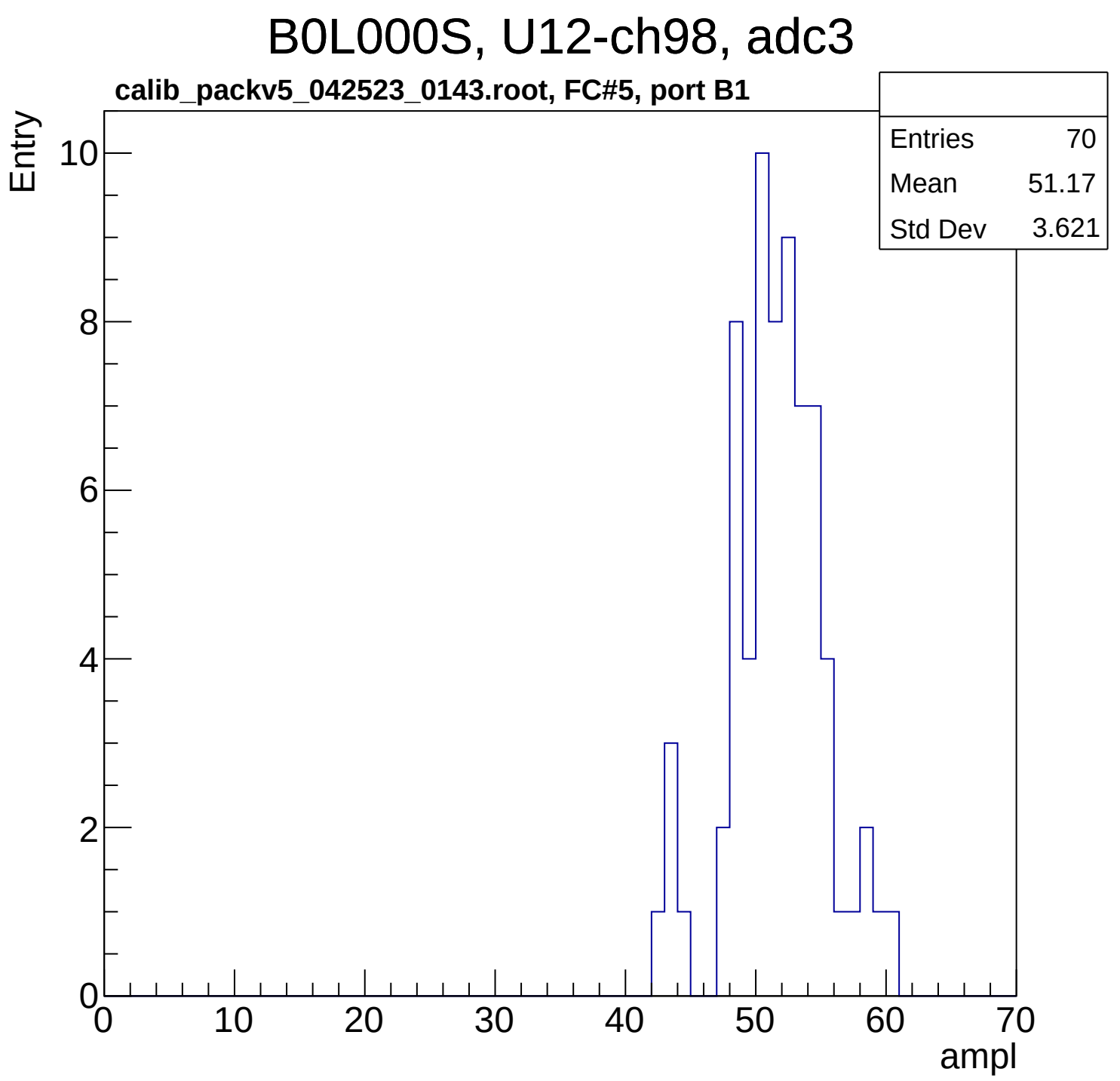

B0L000S, U12-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

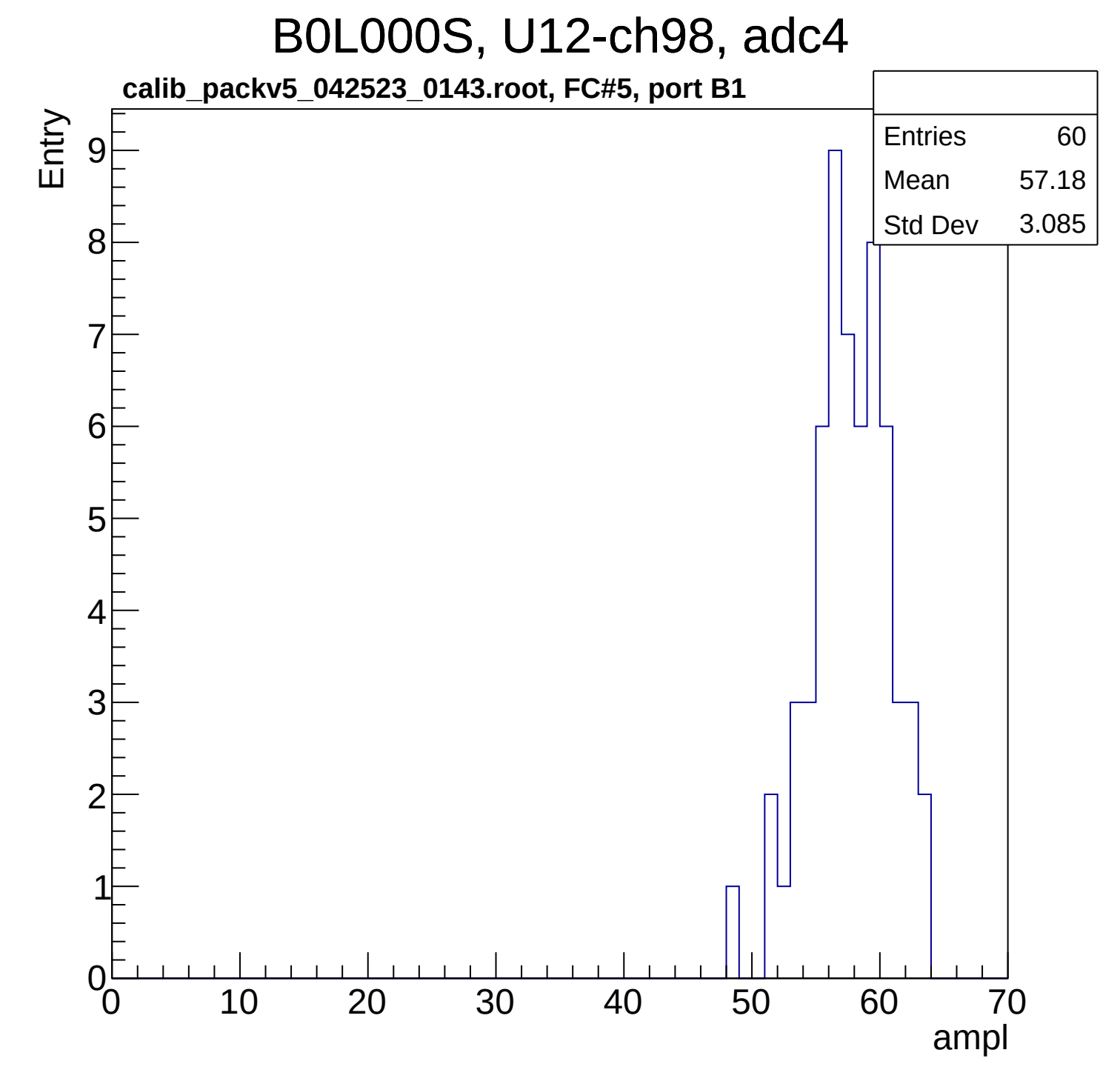

B0L000S, U12-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

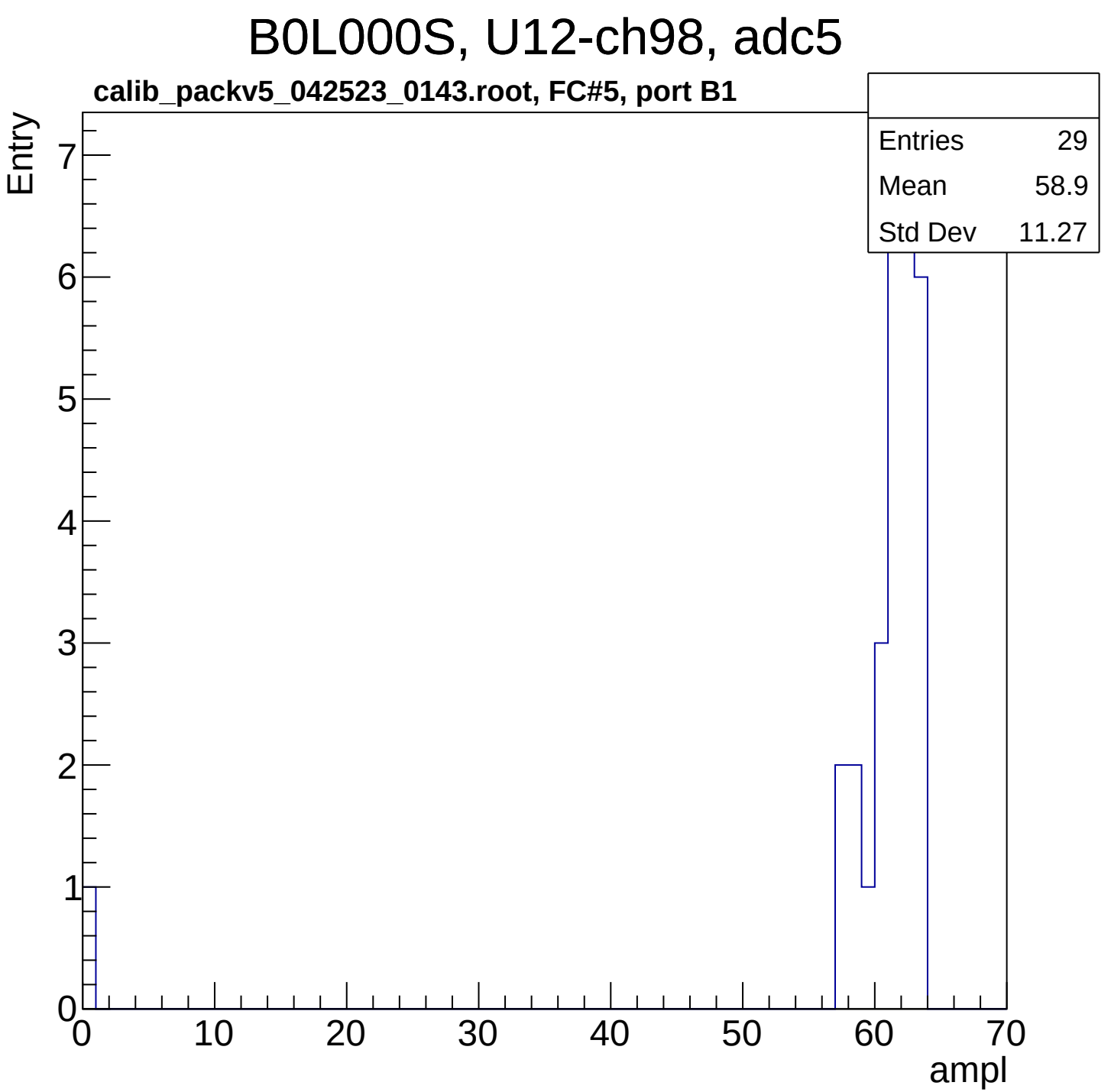

B0L000S, U12-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 31.8
Std Dev 7.045
Gaus mean : 33.7147
Gaus Width: 3.8204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

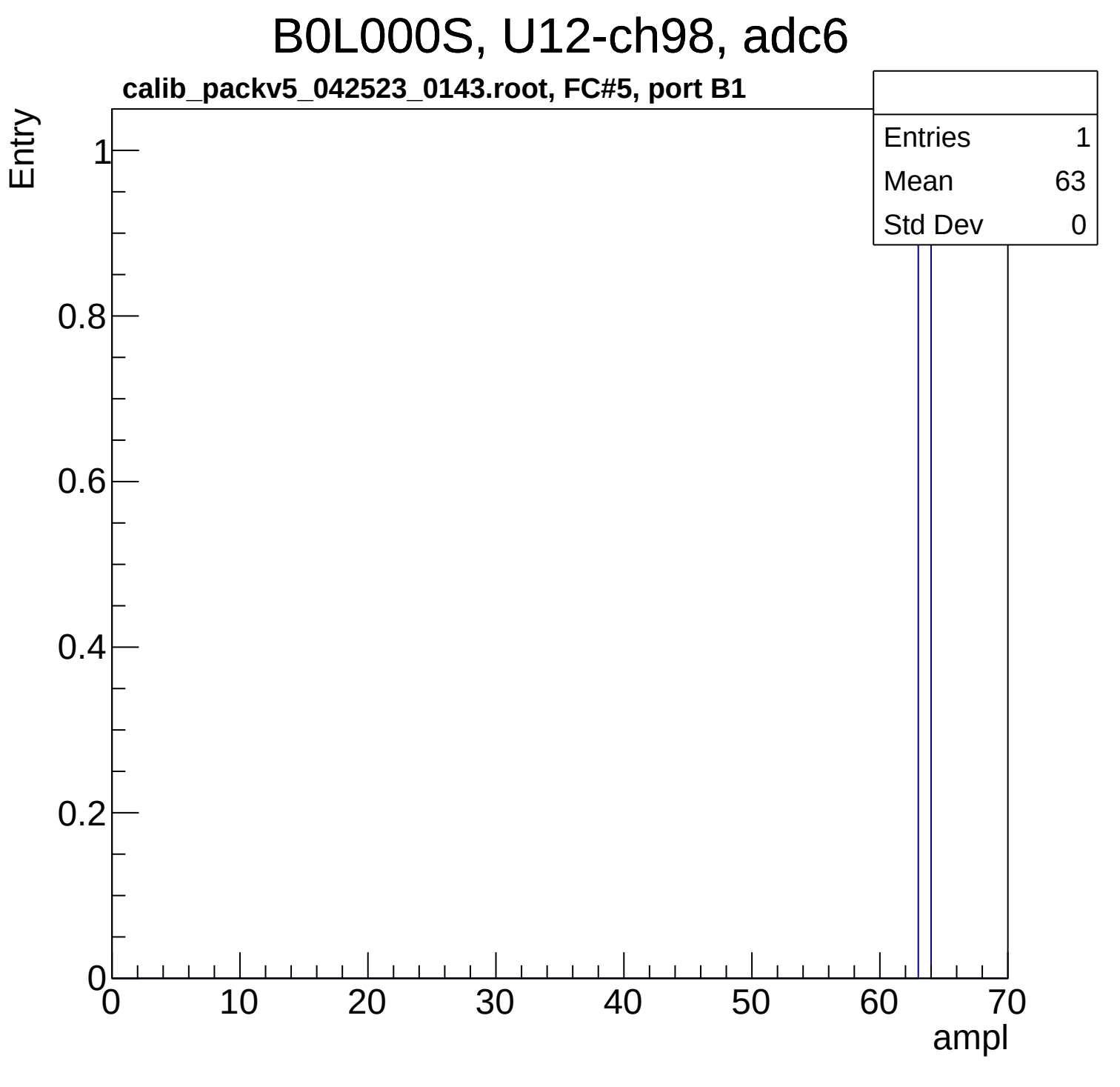

B0L000S, U12-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 39.22
Std Dev 3.563
Gaus mean : 39.7908
Gaus Width: 3.7692
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

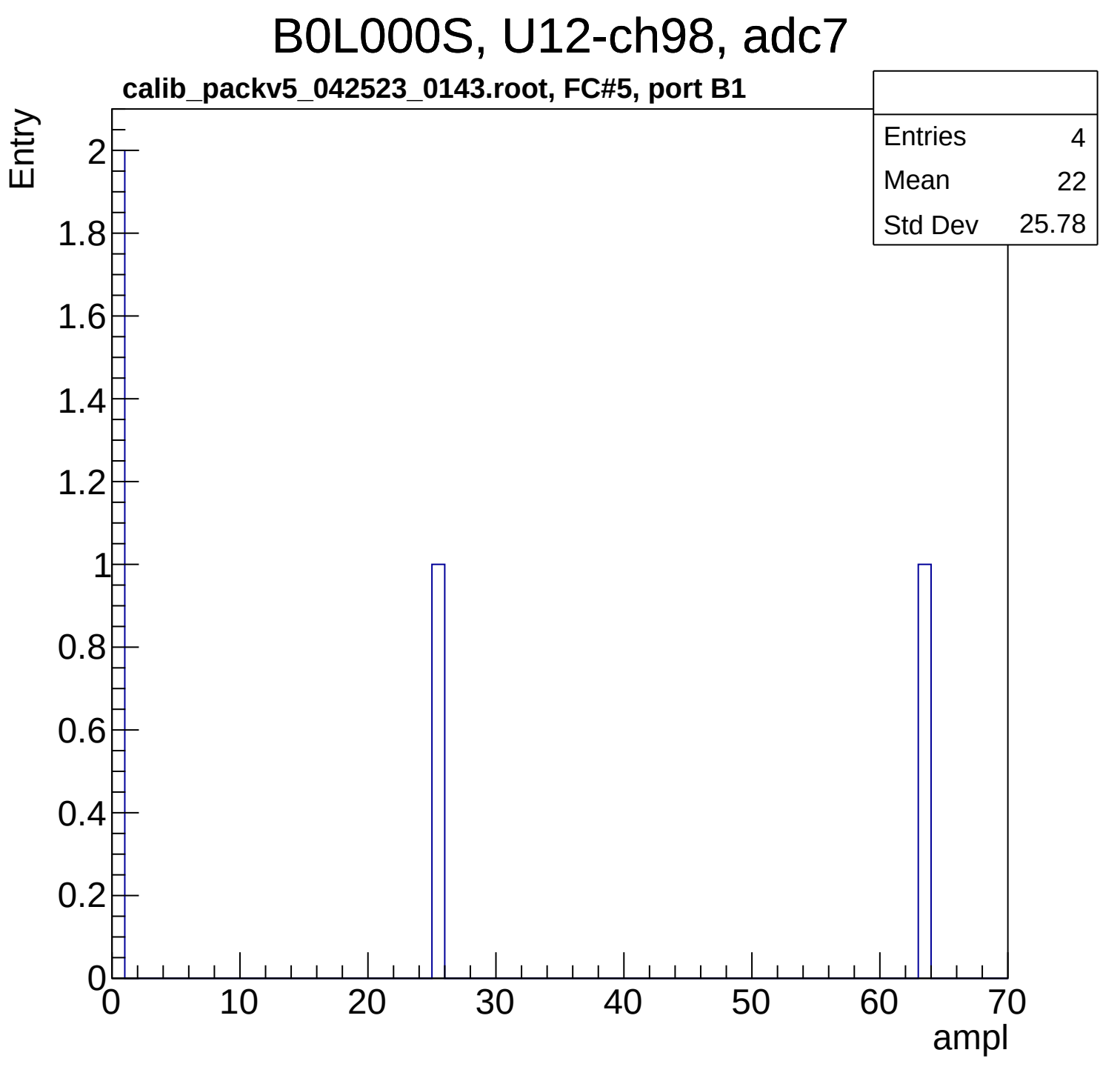

B0L000S, U12-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 44.15
Std Dev 3.067
Gaus mean : 44.3732
Gaus Width: 2.9918
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

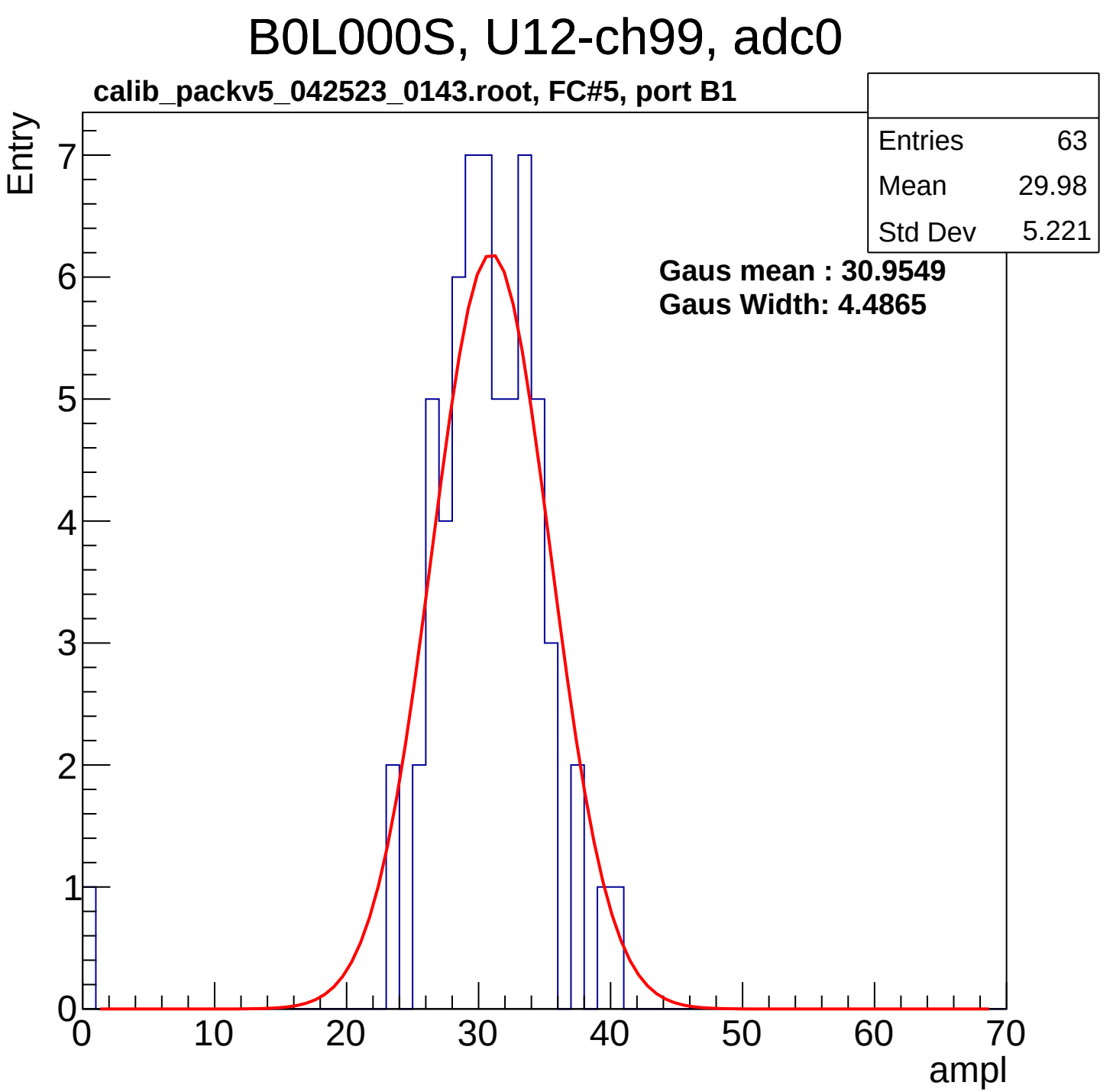

B0L000S, U12-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 51.17
Std Dev 3.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

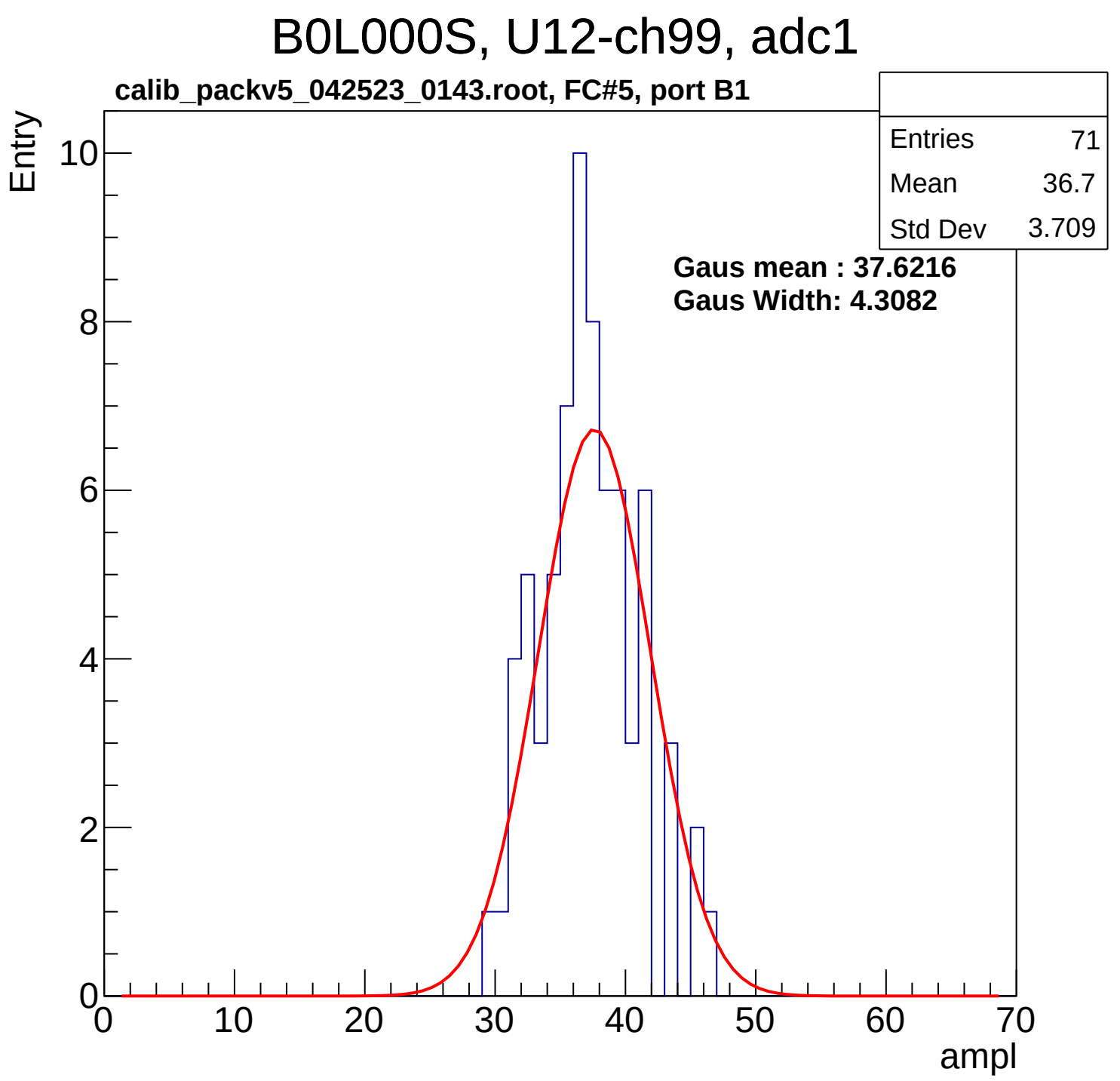

B0L000S, U12-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.18
Std Dev 3.085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

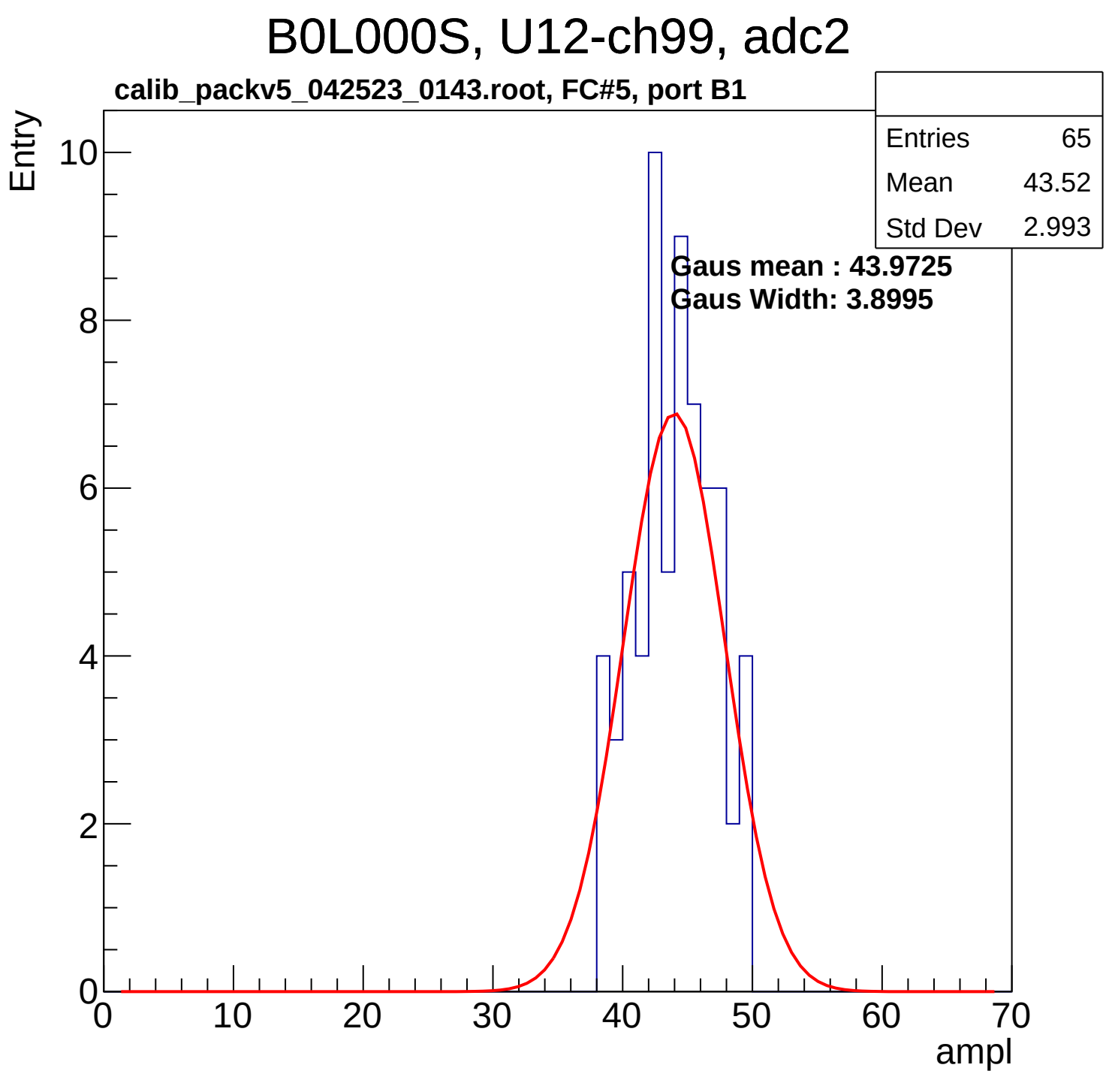

B0L000S, U12-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58.9
Std Dev 11.27
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

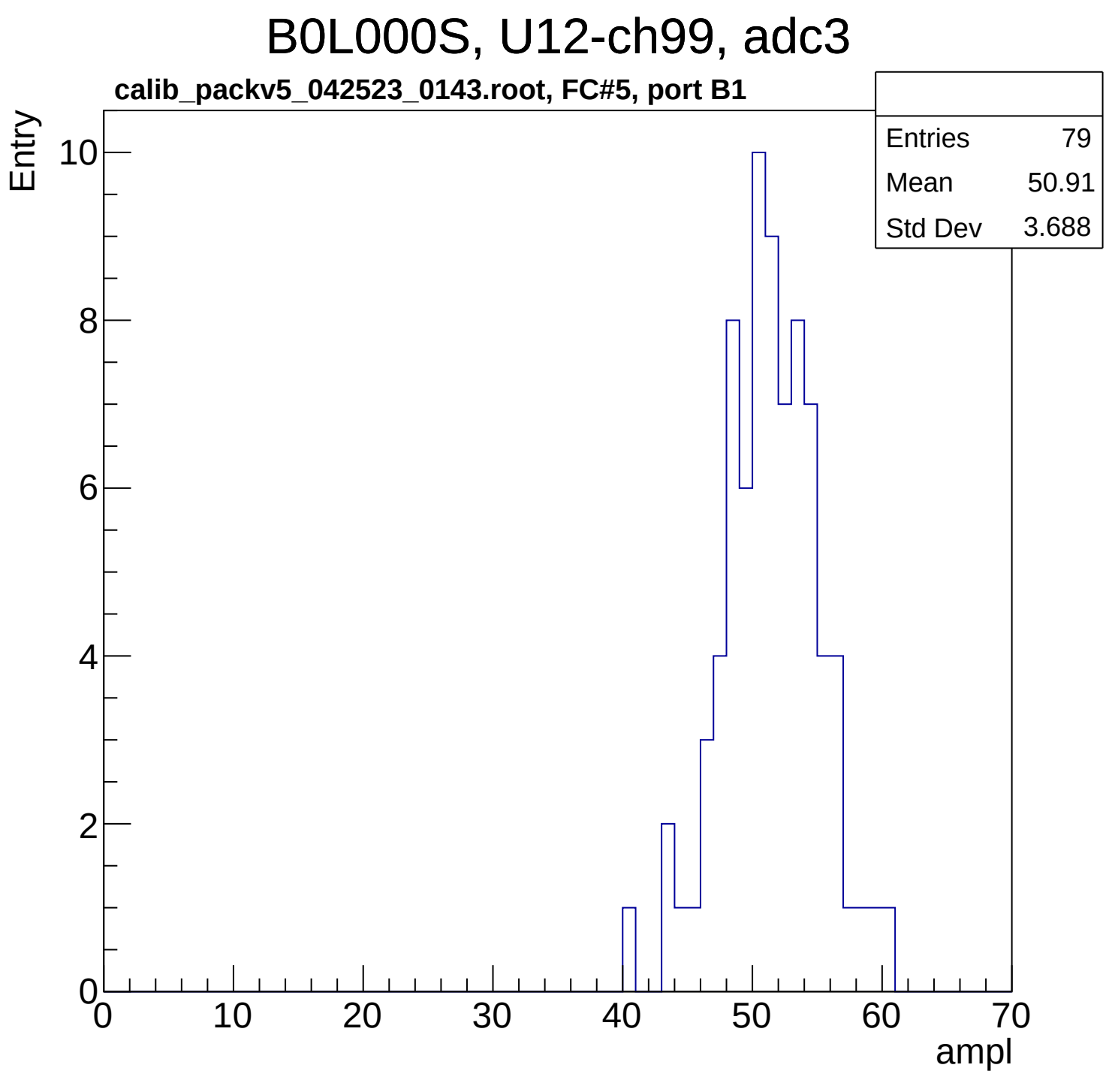

B0L000S, U12-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

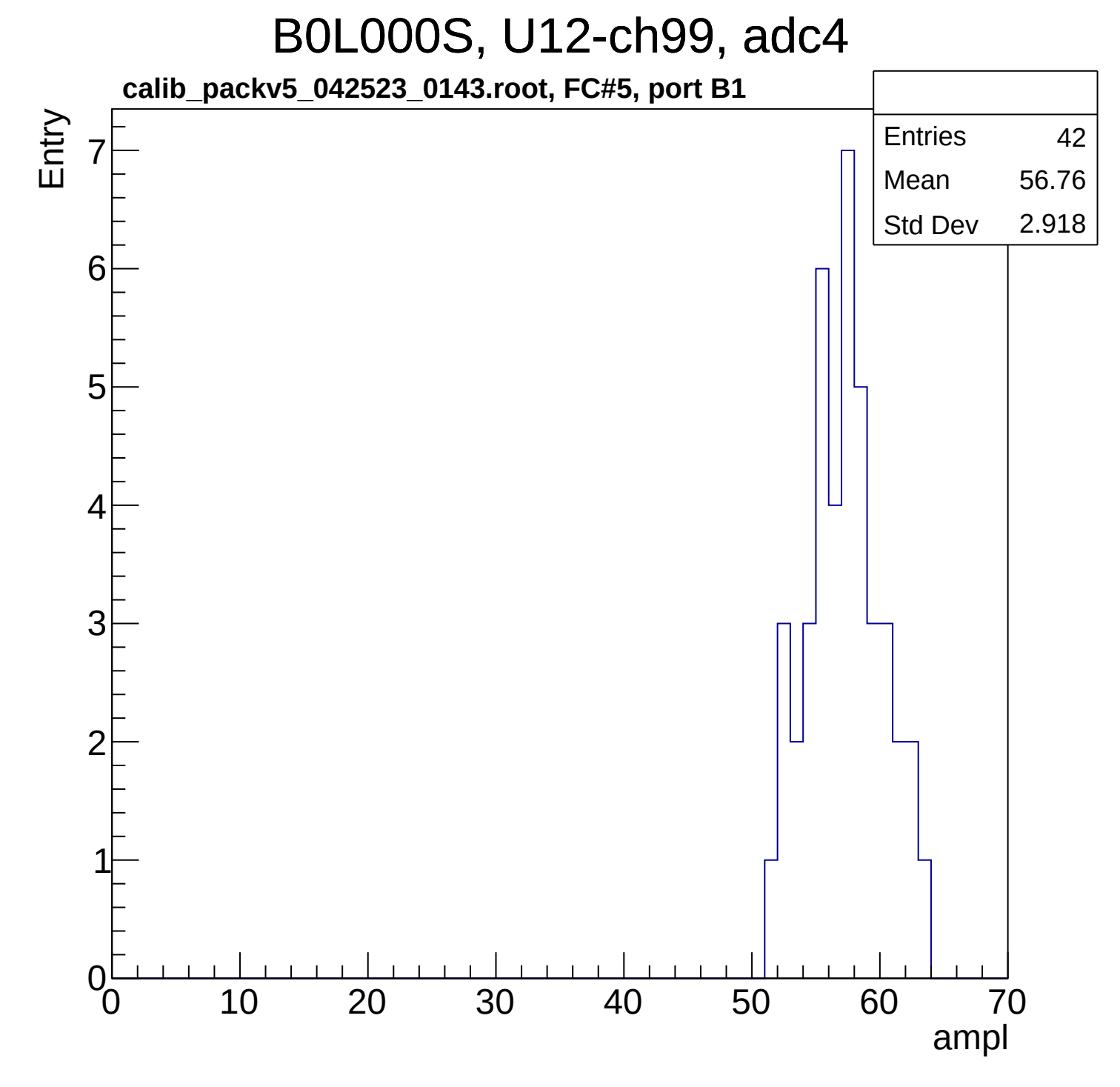

B0L000S, U12-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 22
Std Dev 25.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.98
Std Dev 5.221
Gaus mean : 30.9549
Gaus Width: 4.4865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.7
Std Dev 3.709
Gaus mean : 37.6216
Gaus Width: 4.3082
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.52
Std Dev 2.993
Gaus mean : 43.9725
Gaus Width: 3.8995
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 50.91
Std Dev 3.688
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 56.76
Std Dev 2.918
Entry
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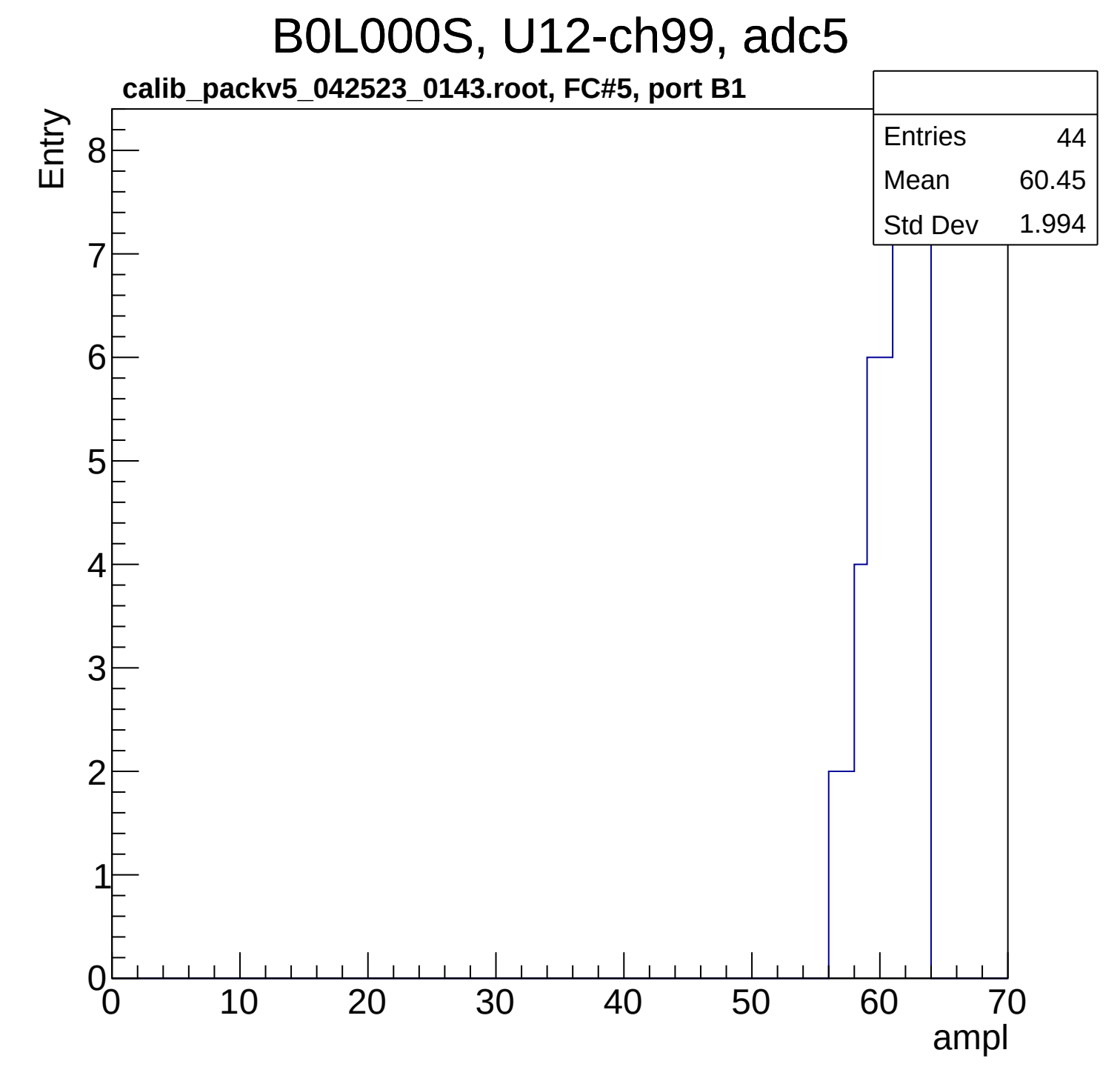
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 60.45
Std Dev 1.994
Entry
ampl
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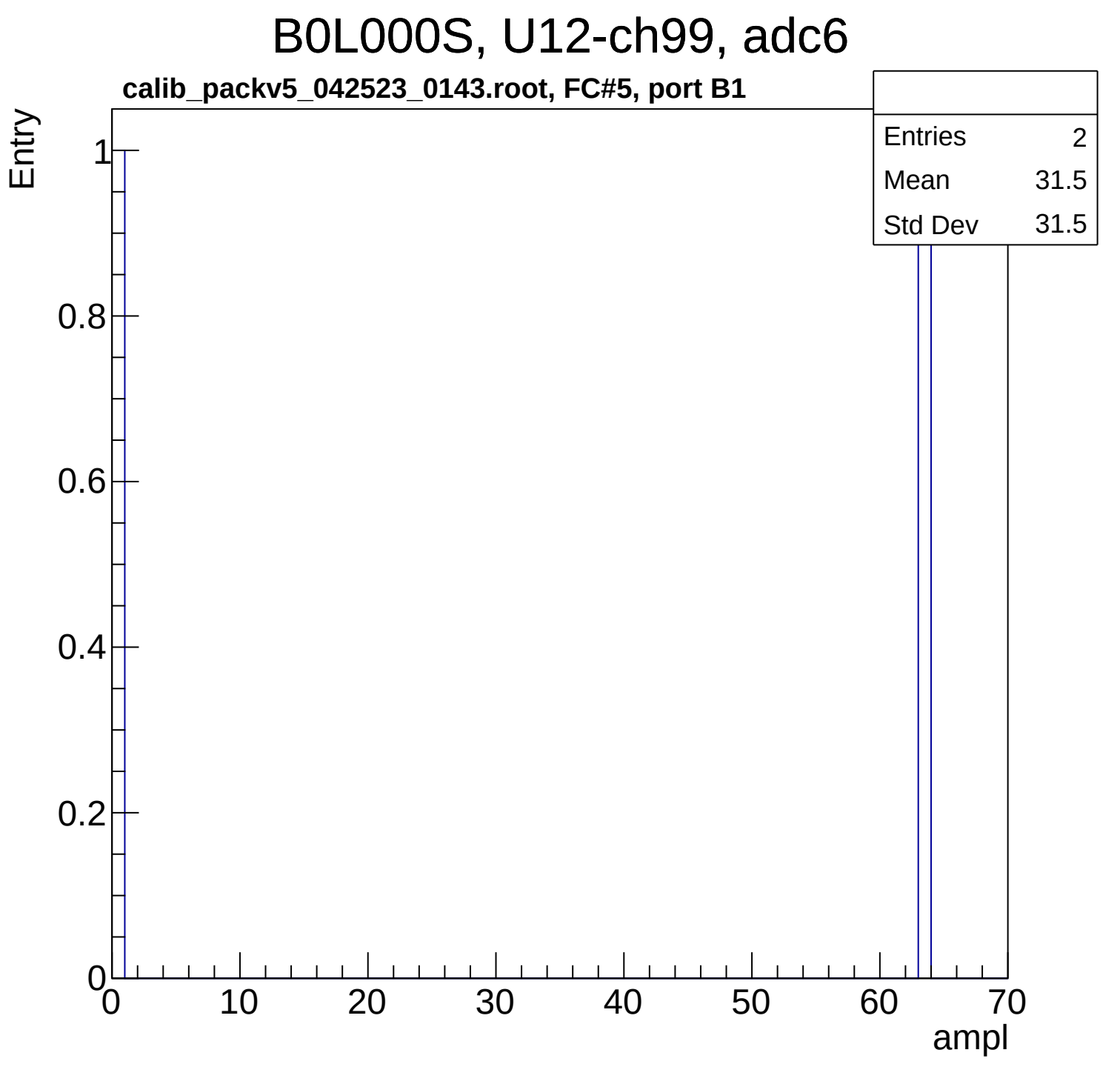
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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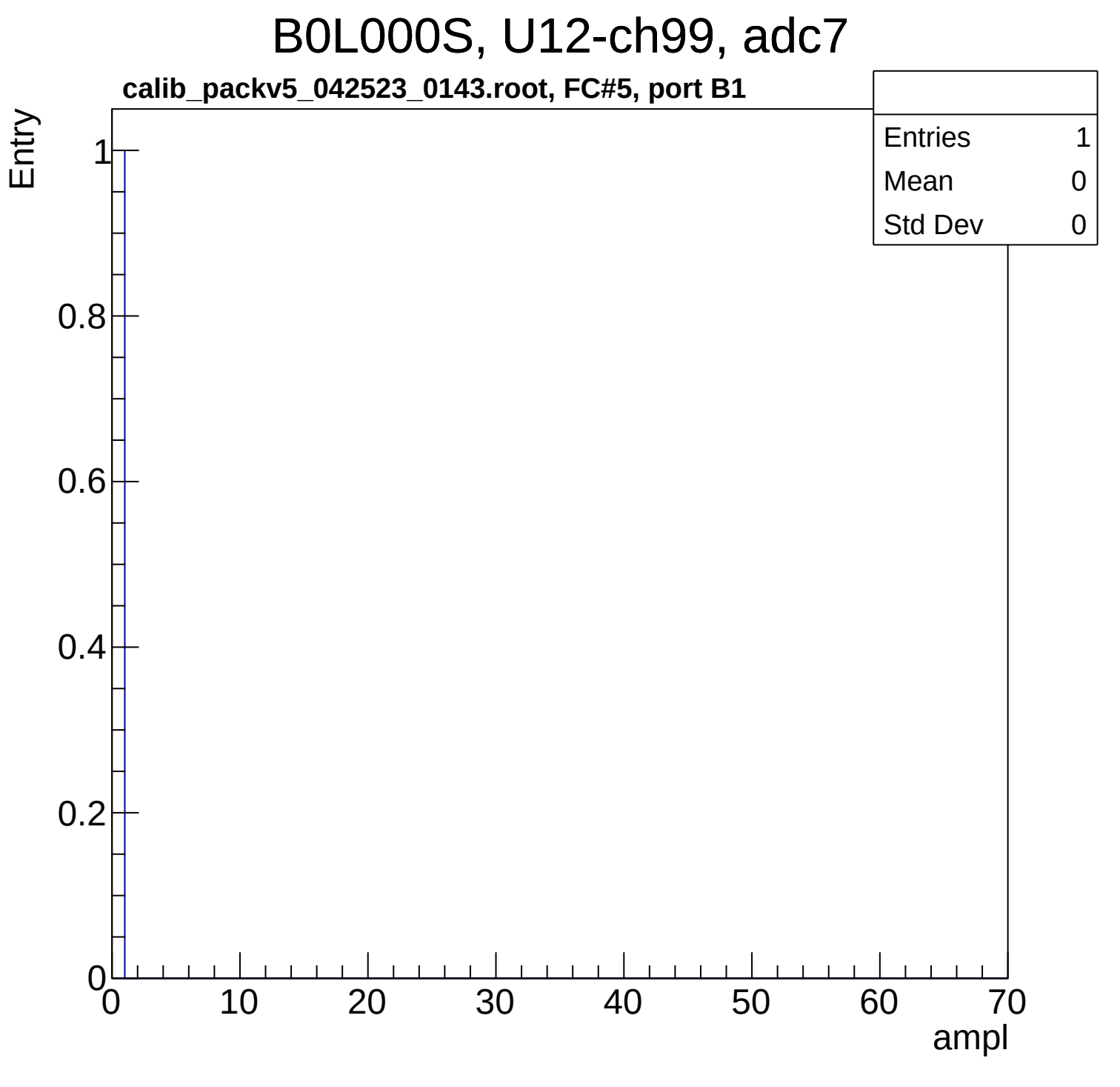
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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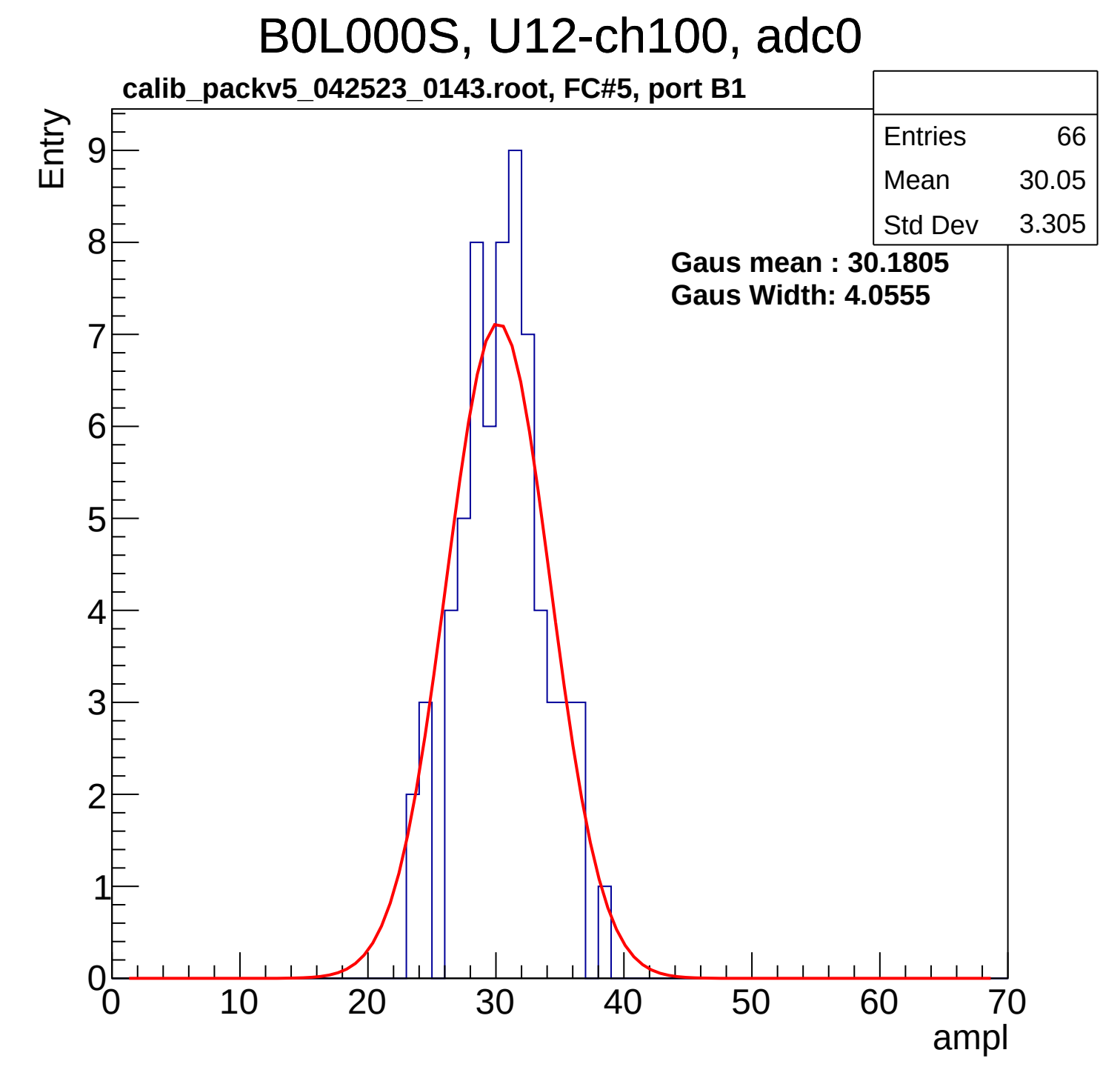
40
50
60
70
ampl


B0L000S, U12-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.05
Std Dev 3.305
Gaus mean : 30.1805
Gaus Width: 4.0555
Entry
ampl
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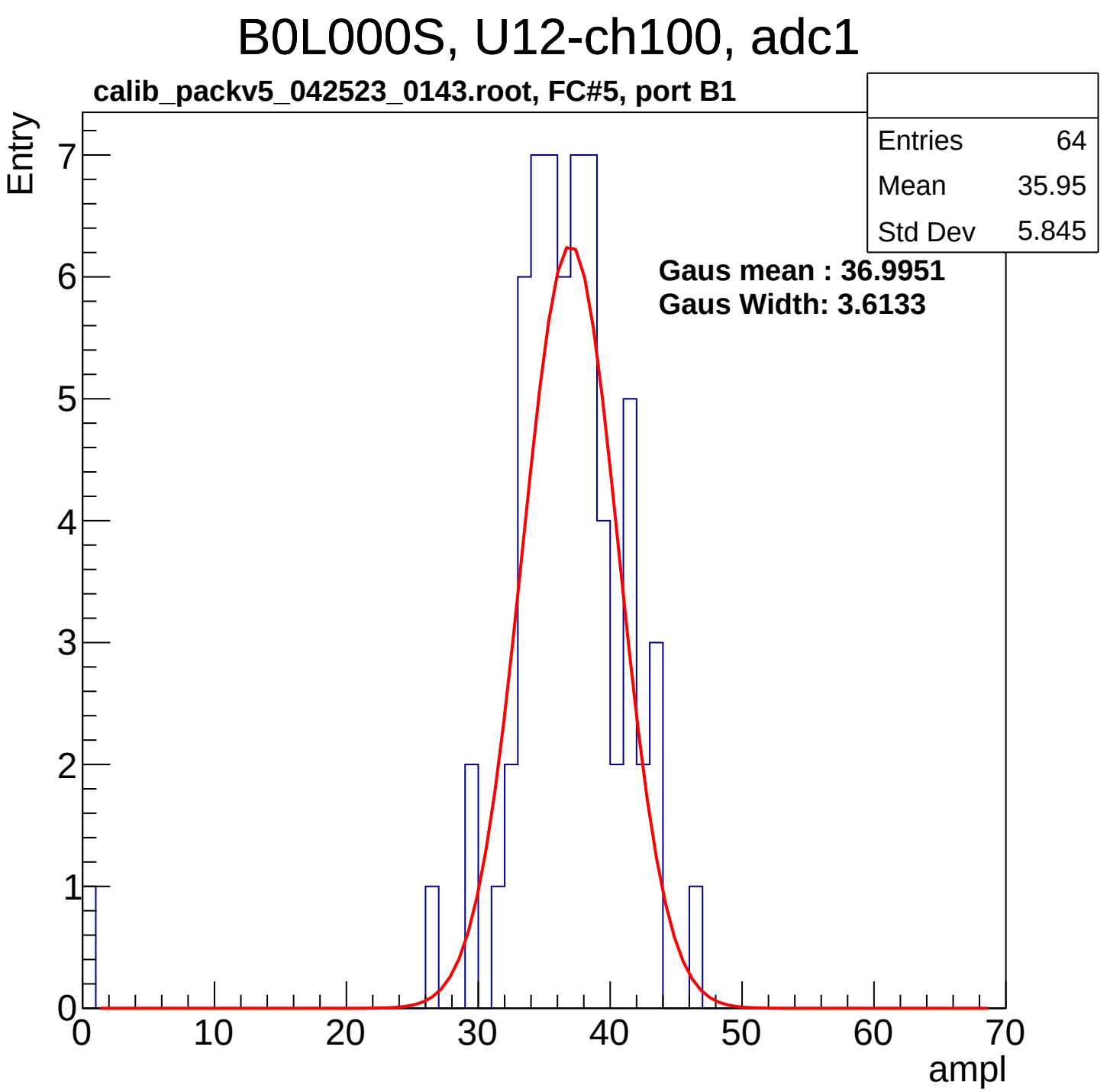
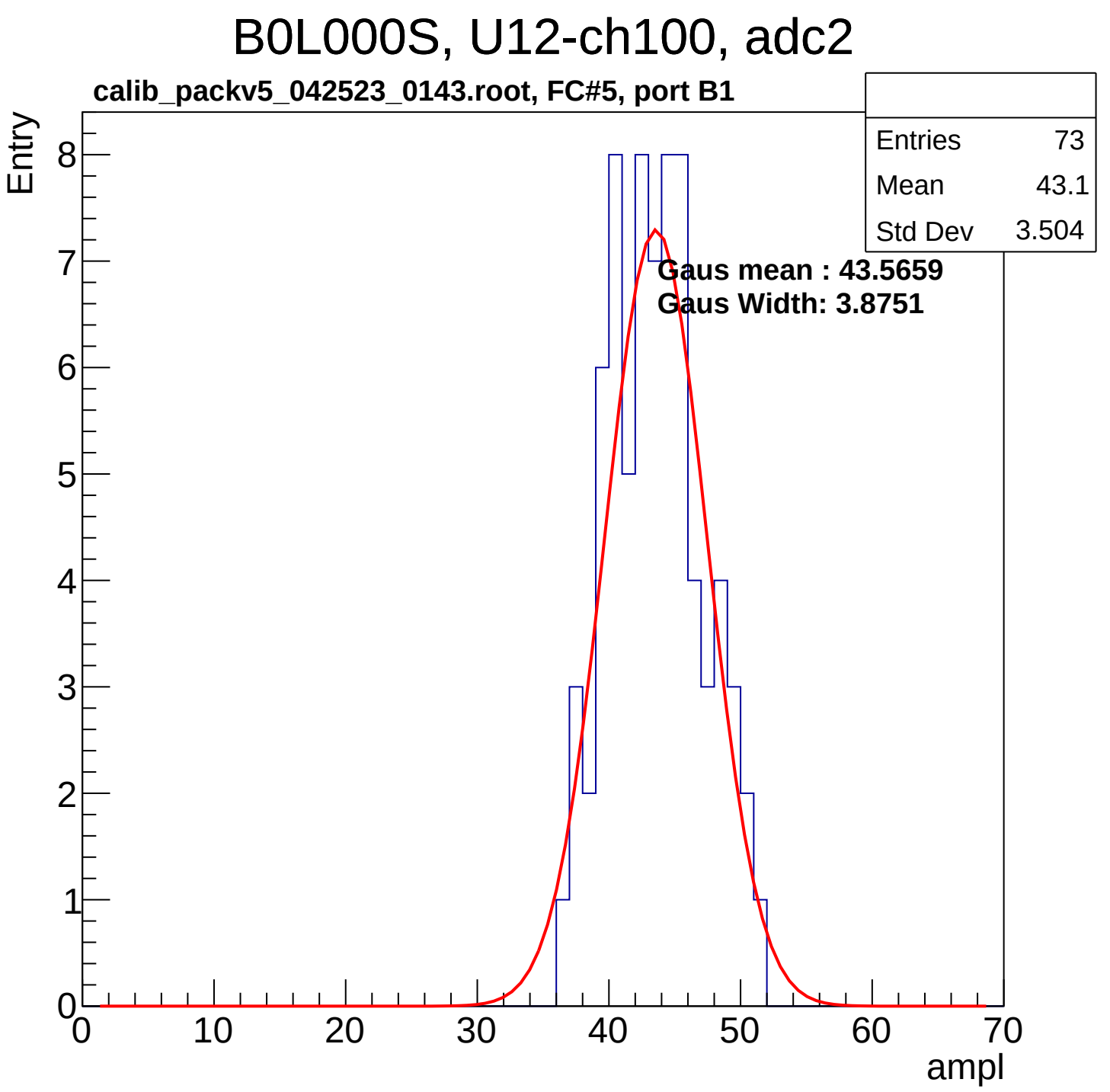
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.95
Std Dev 5.845
Gaus mean : 36.9951
Gaus Width: 3.6133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.1
Std Dev 3.504
Gaus mean : 43.5659
Gaus Width: 3.8751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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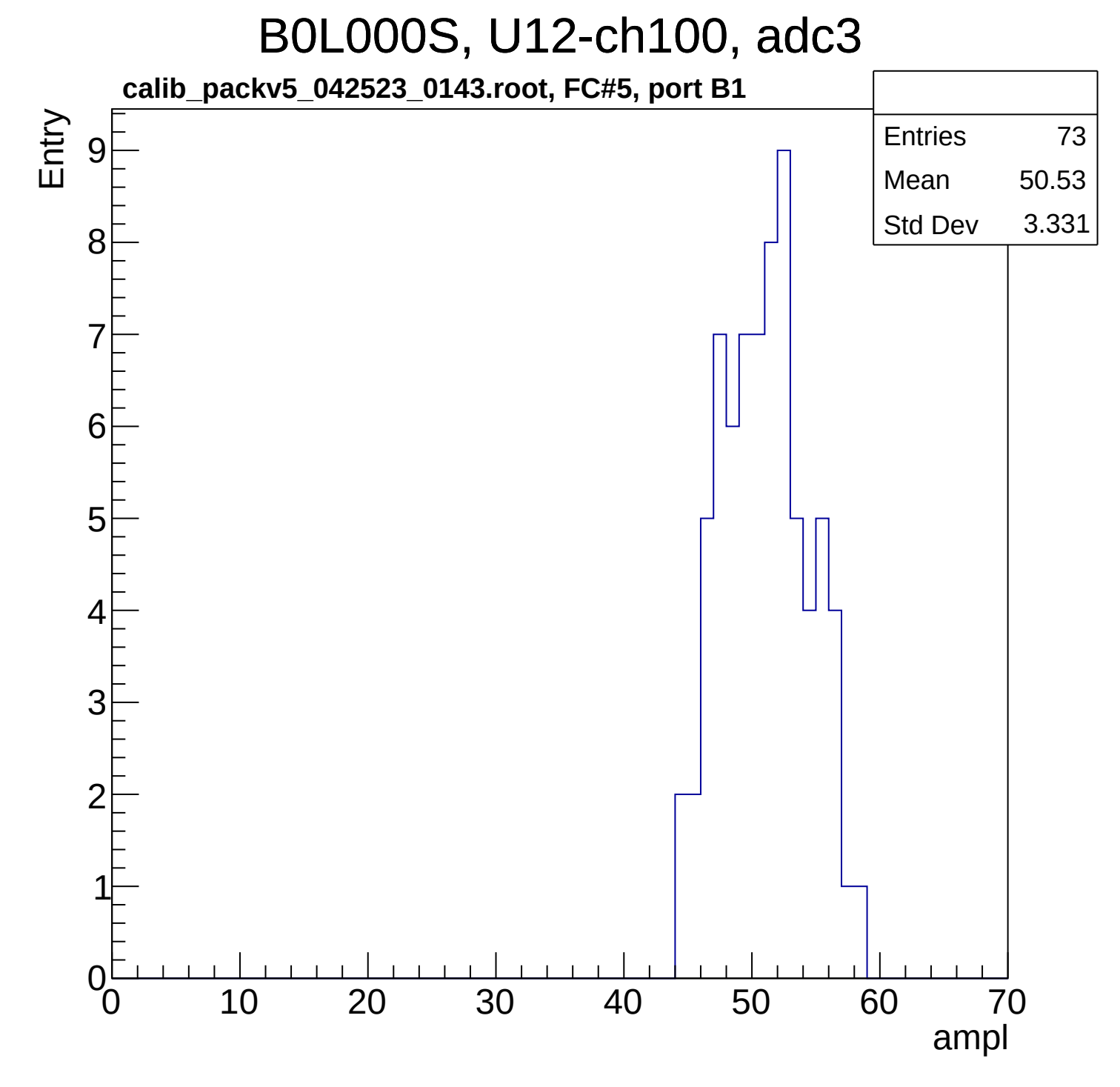
B0L000S, U12-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 50.53
Std Dev 3.331
Entry
ampl
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2
3
4
5
6
7
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9
0
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60
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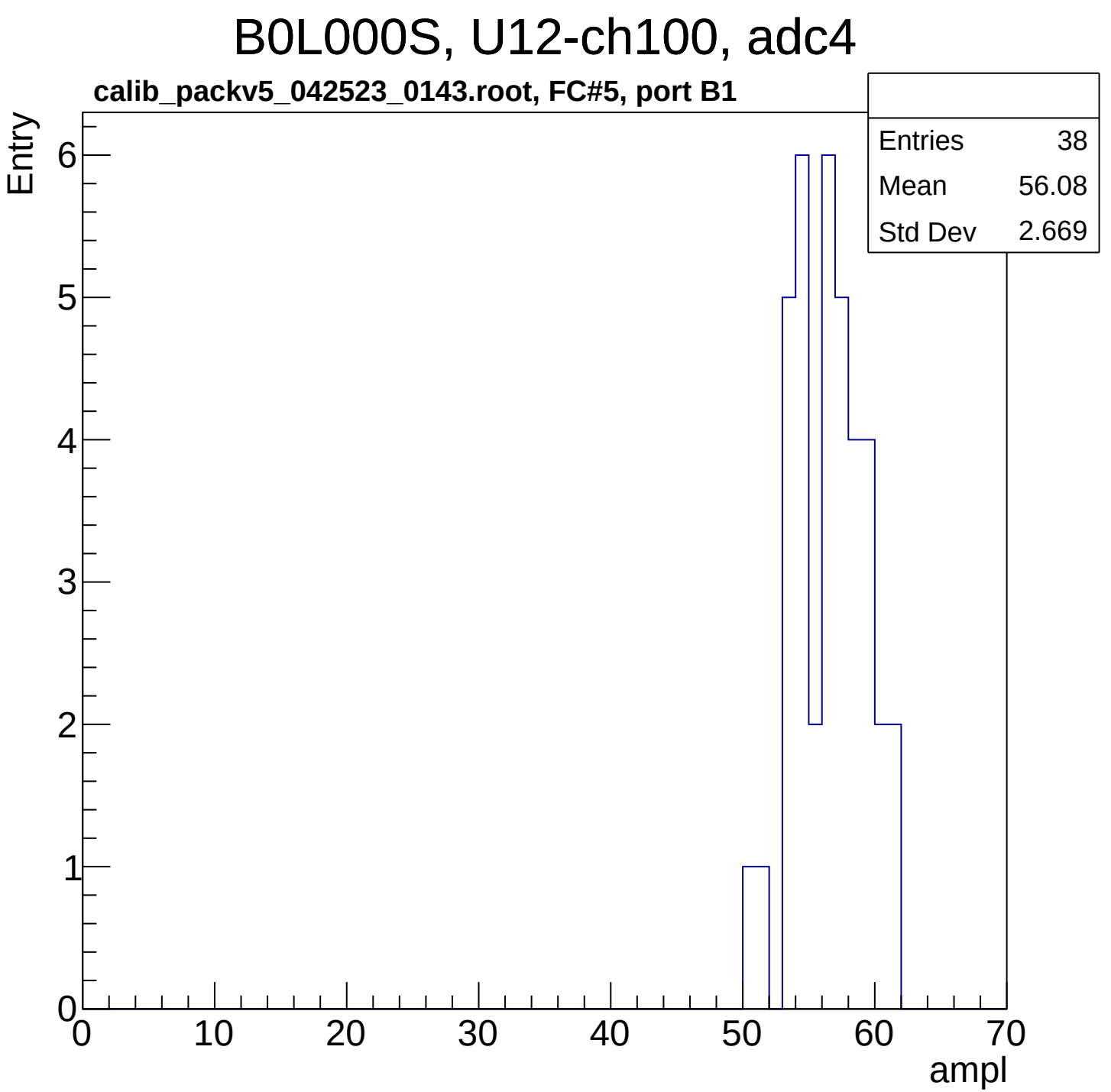
B0L000S, U12-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 56.08
Std Dev 2.669
Entry
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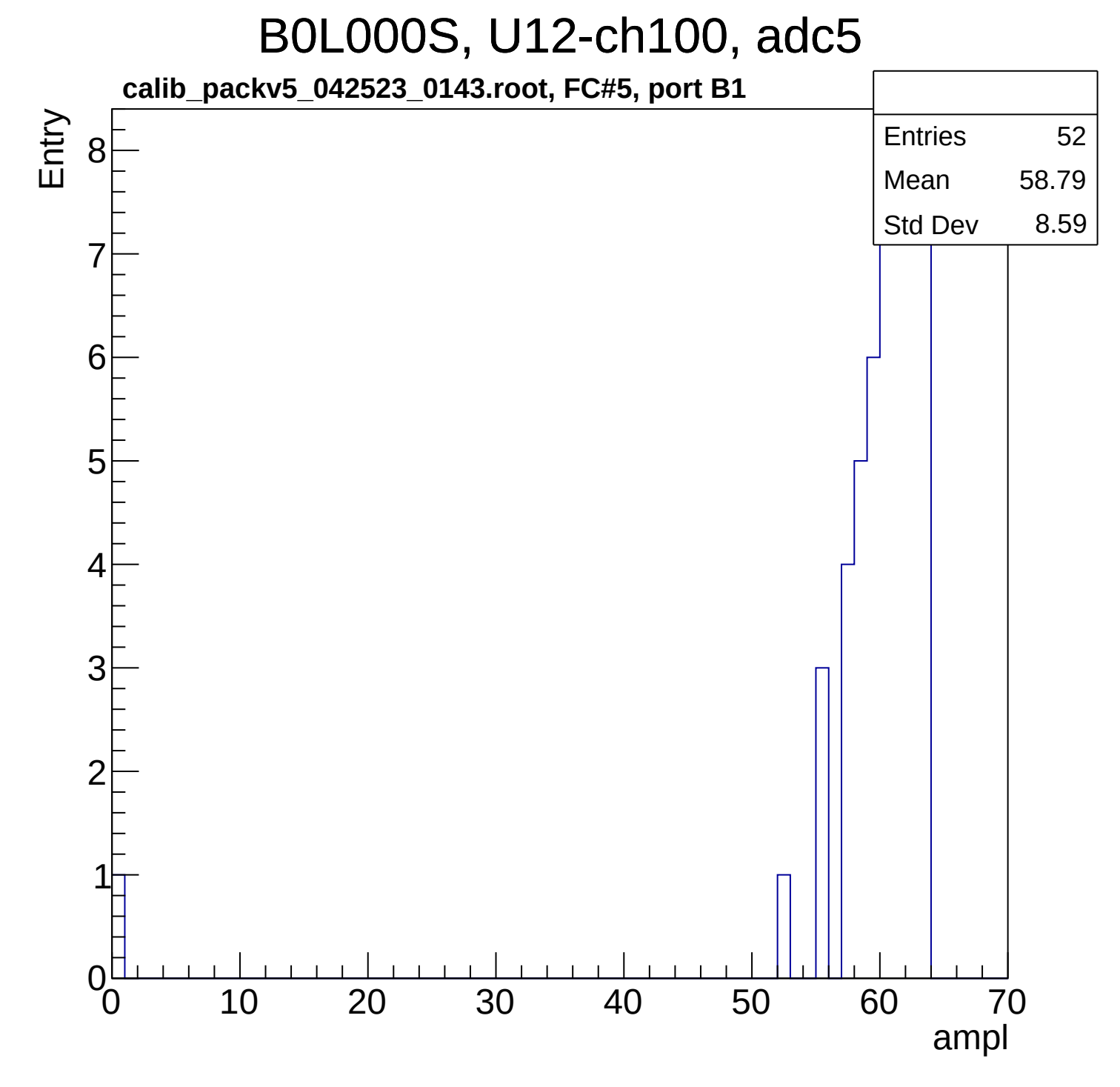
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.79
Std Dev 8.59
Entry
ampl
0
1
2
3
4
5
6
7
8
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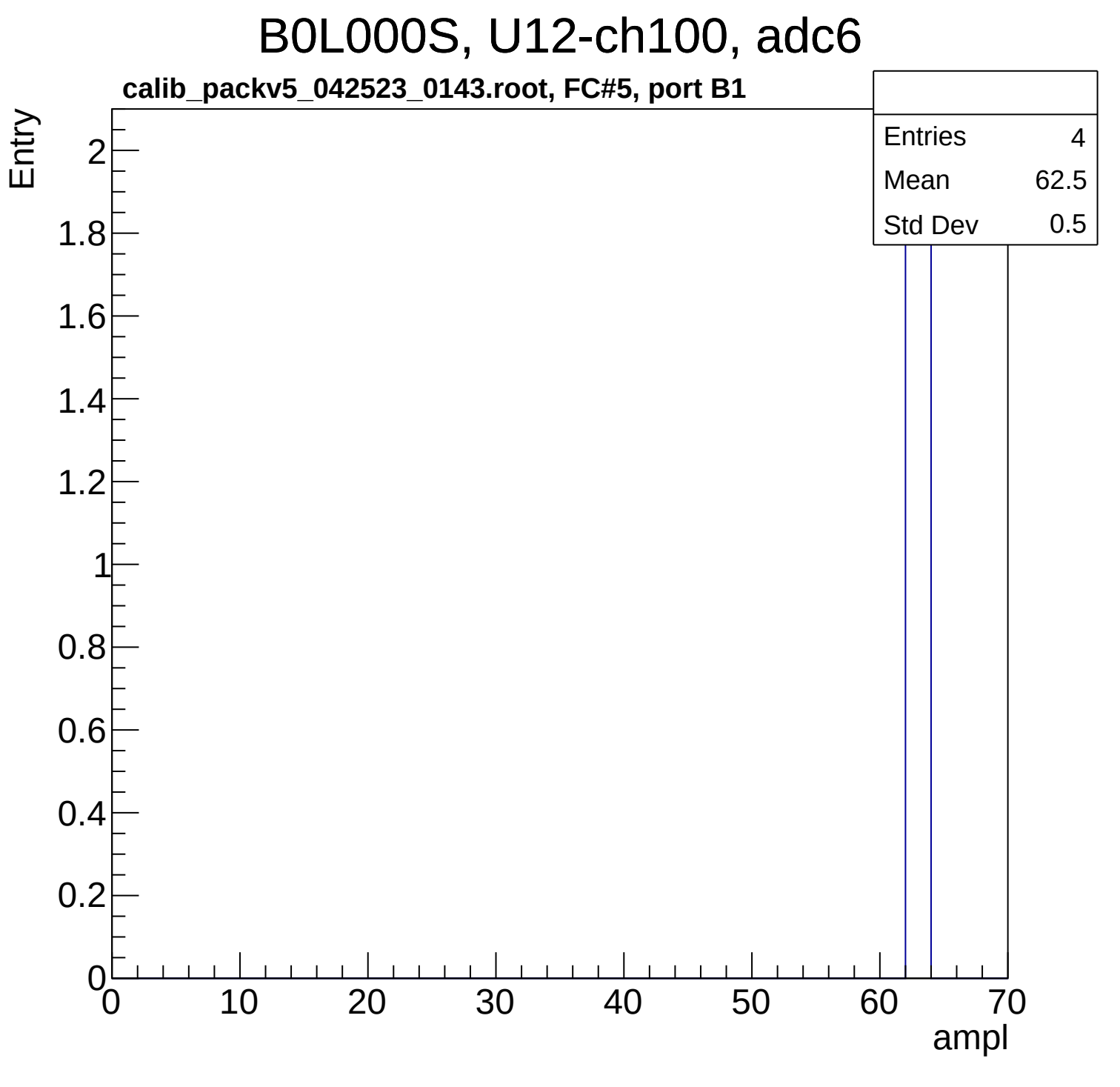
B0L000S, U12-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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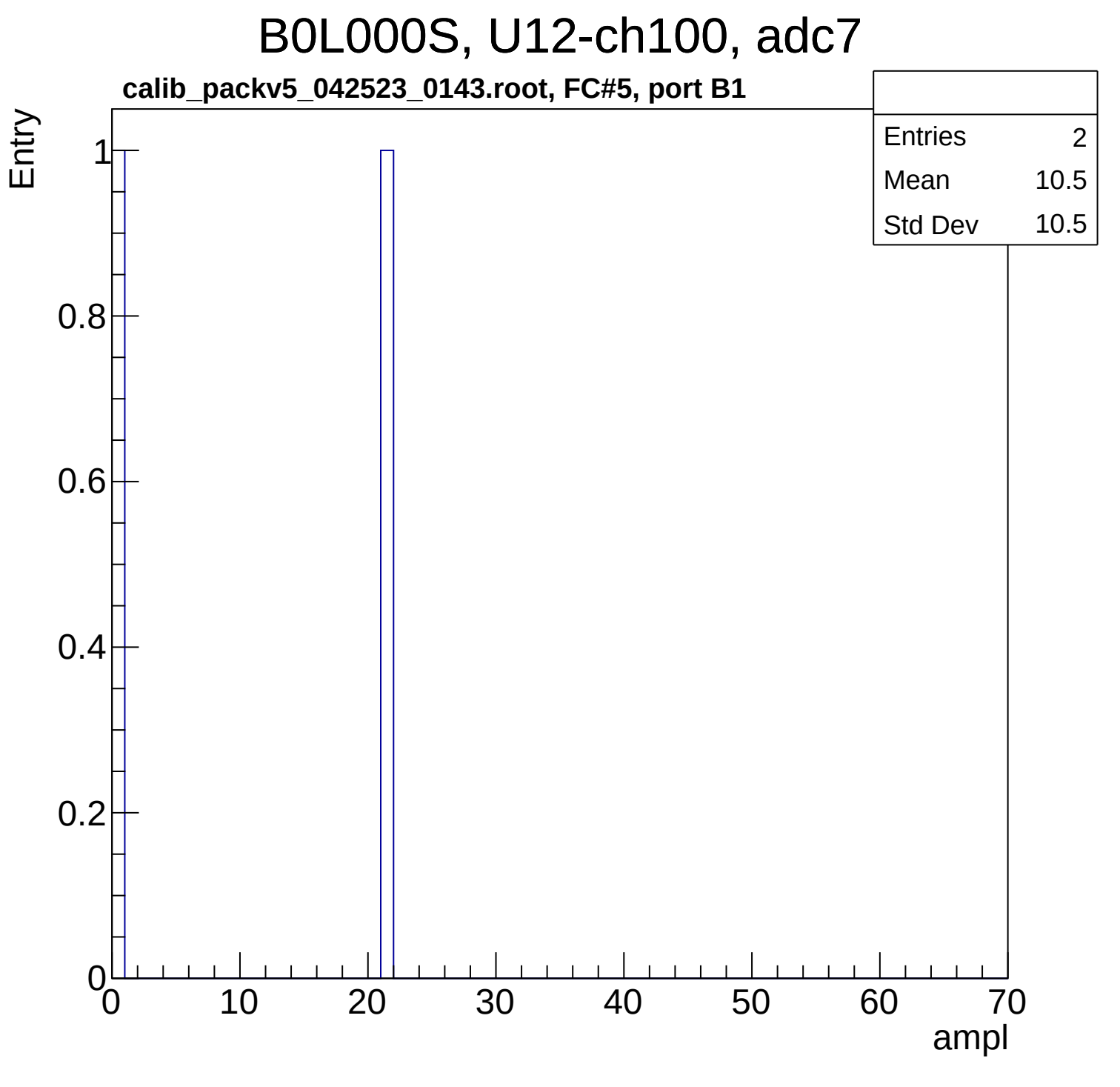
B0L000S, U12-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
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60
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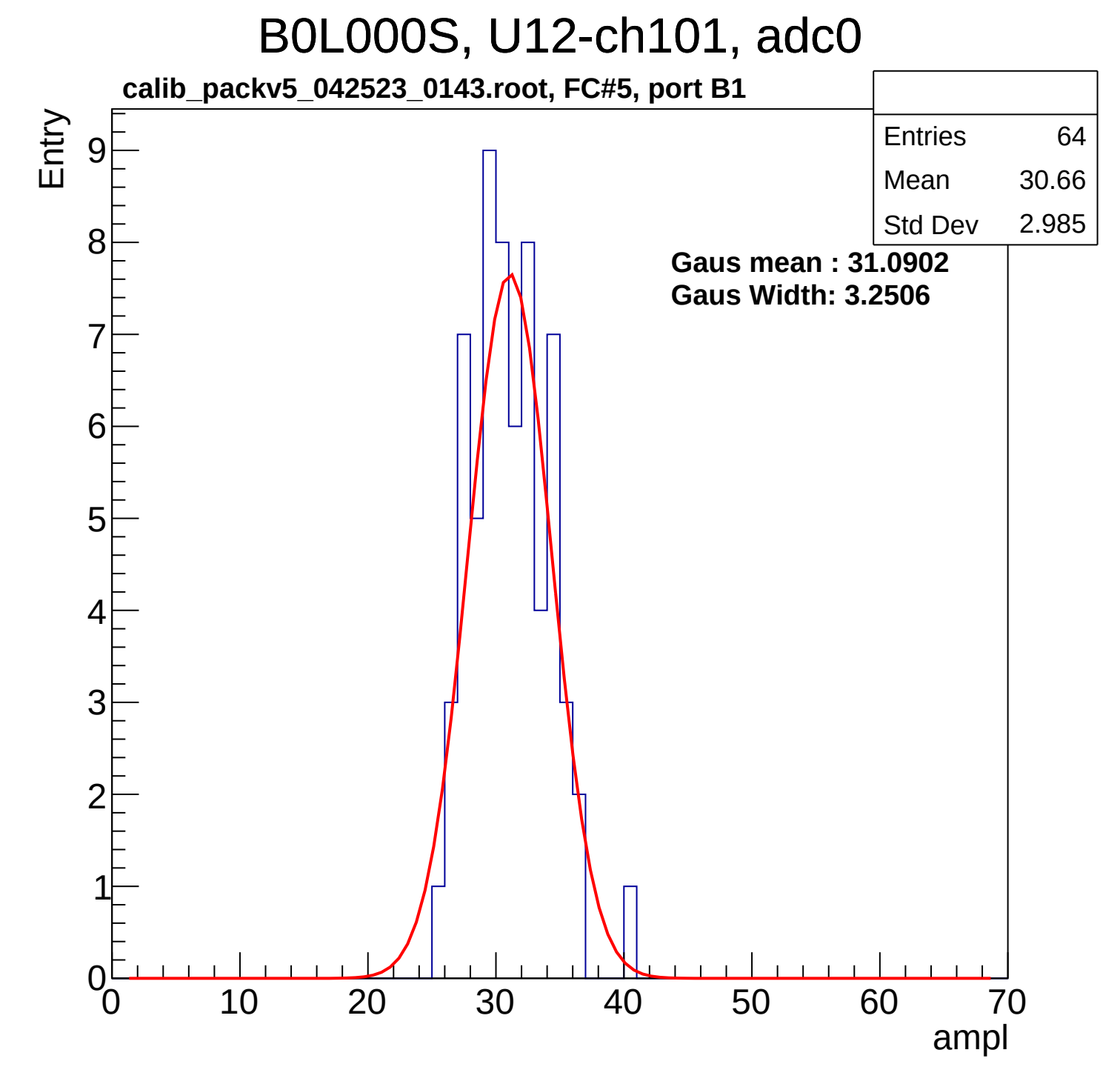
B0L000S, U12-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.66
Std Dev 2.985
Gaus mean : 31.0902
Gaus Width: 3.2506
Entry
ampl
0
1
2
3
4
5
6
7
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9
0
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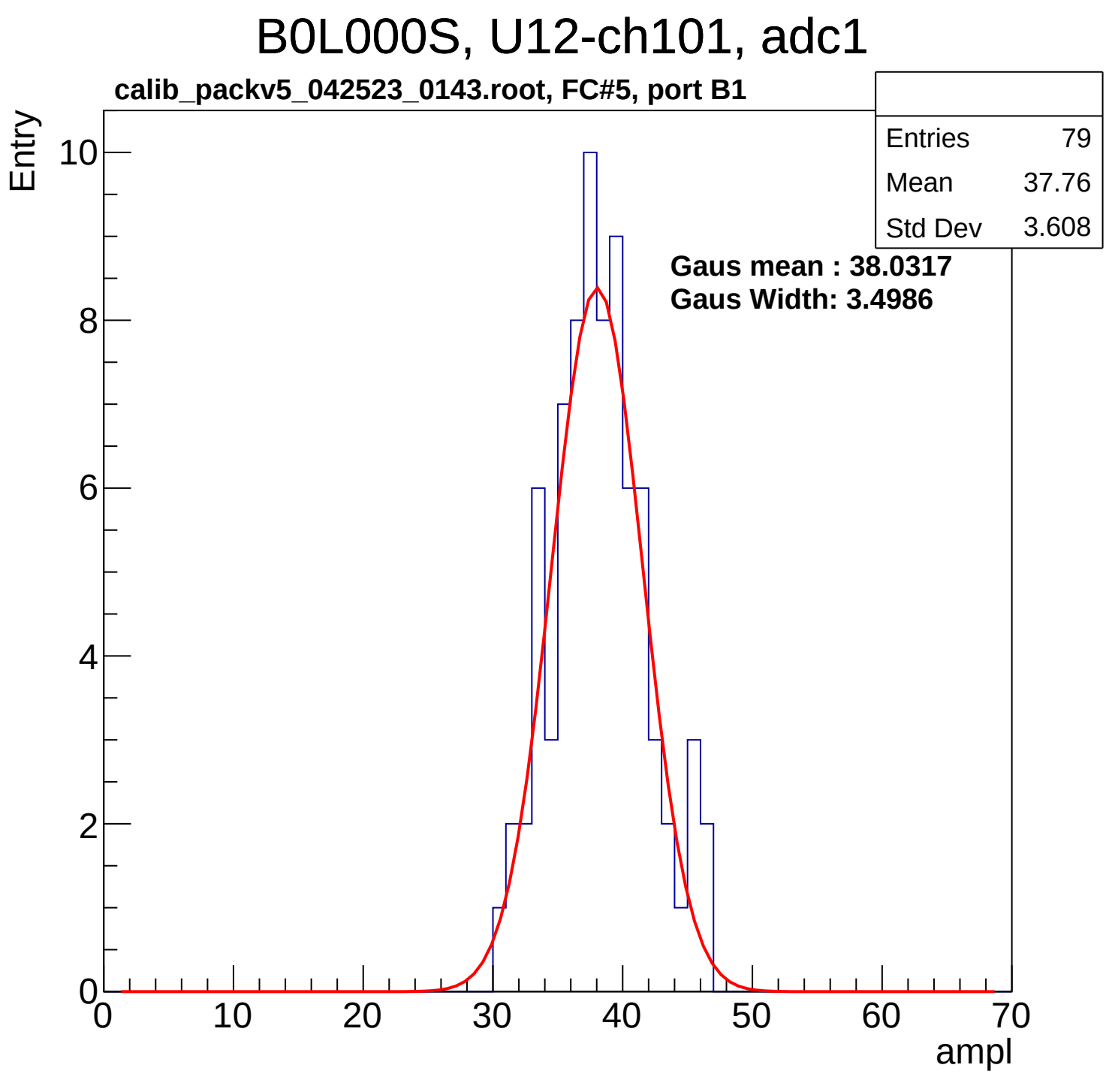
B0L000S, U12-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 37.76
Std Dev 3.608
Gaus mean : 38.0317
Gaus Width: 3.4986
Entry
ampl
0
2
4
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8
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0
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60
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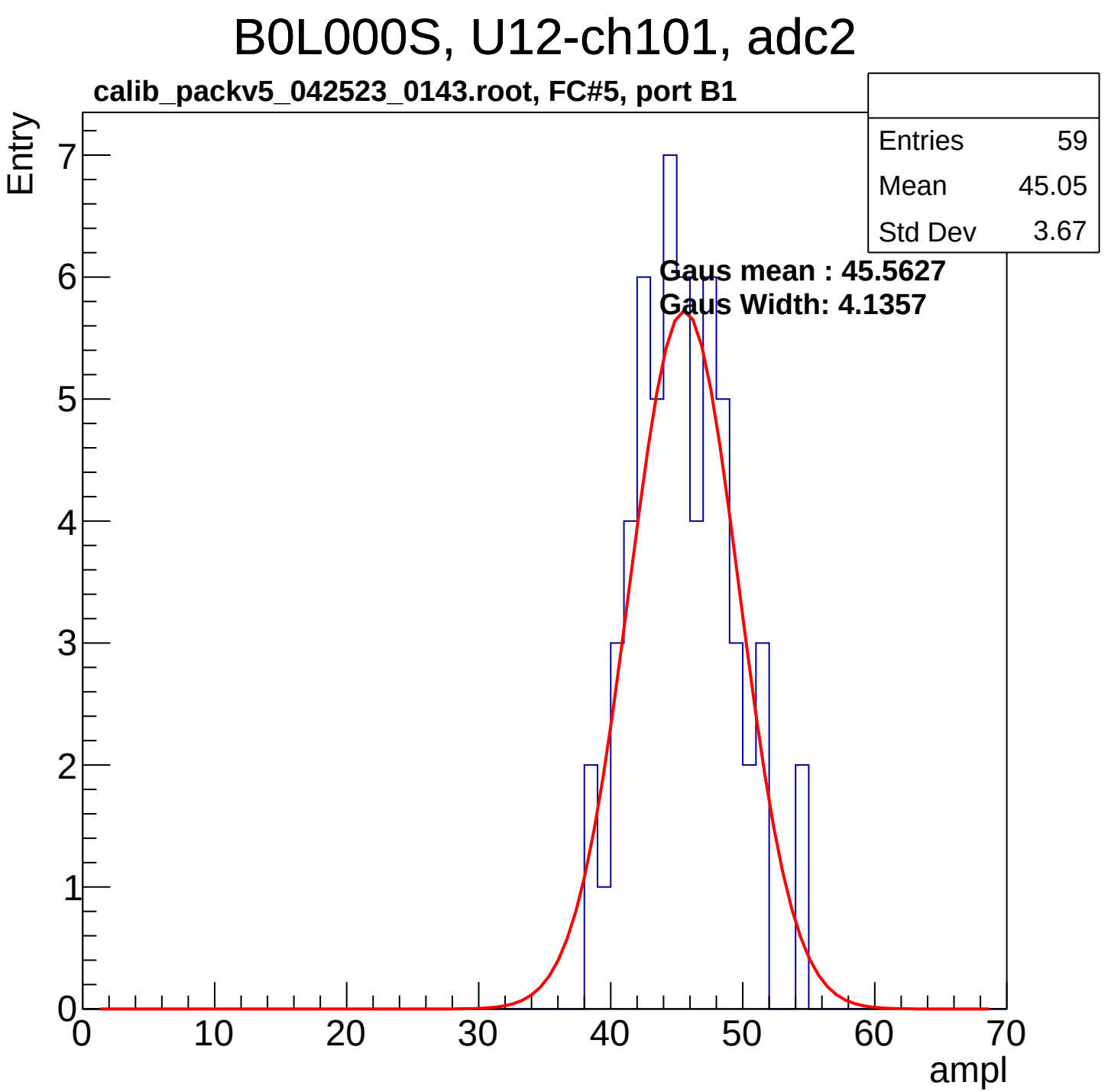
B0L000S, U12-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 45.05
Std Dev 3.67
Gaus mean : 45.5627
Gaus Width: 4.1357
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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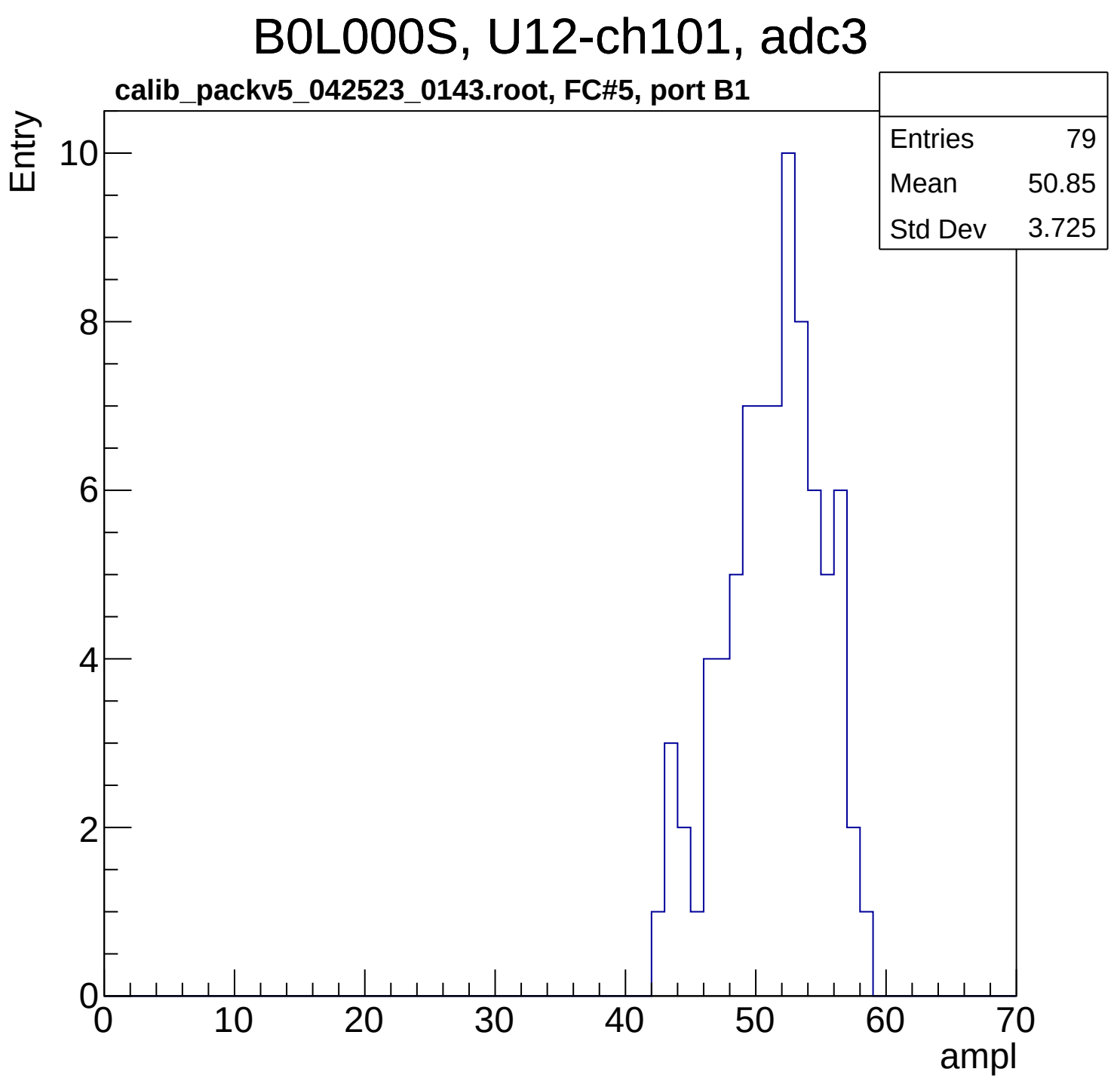
B0L000S, U12-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 50.85
Std Dev 3.725
Entry
ampl
0
2
4
6
8
10
0
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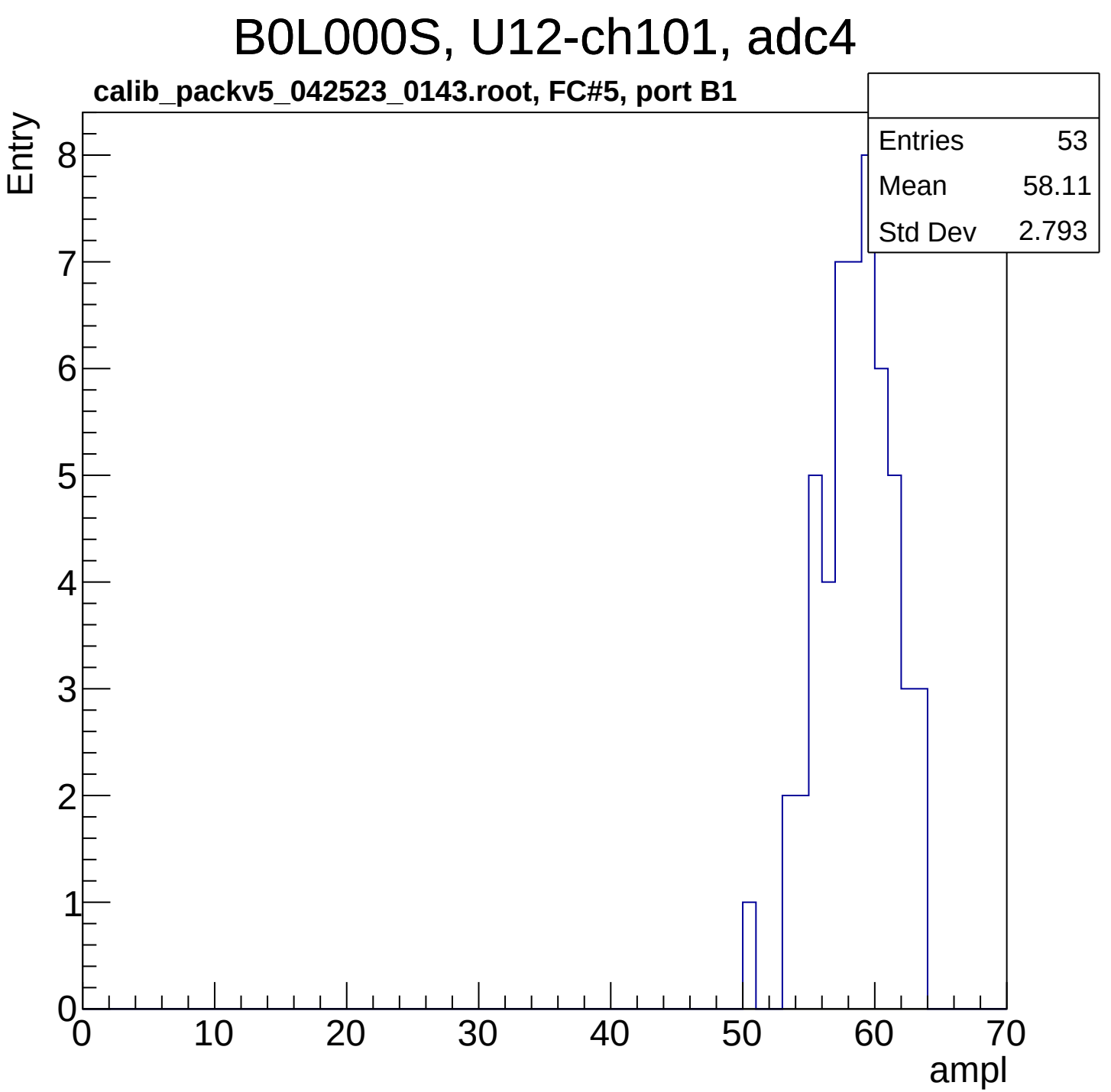
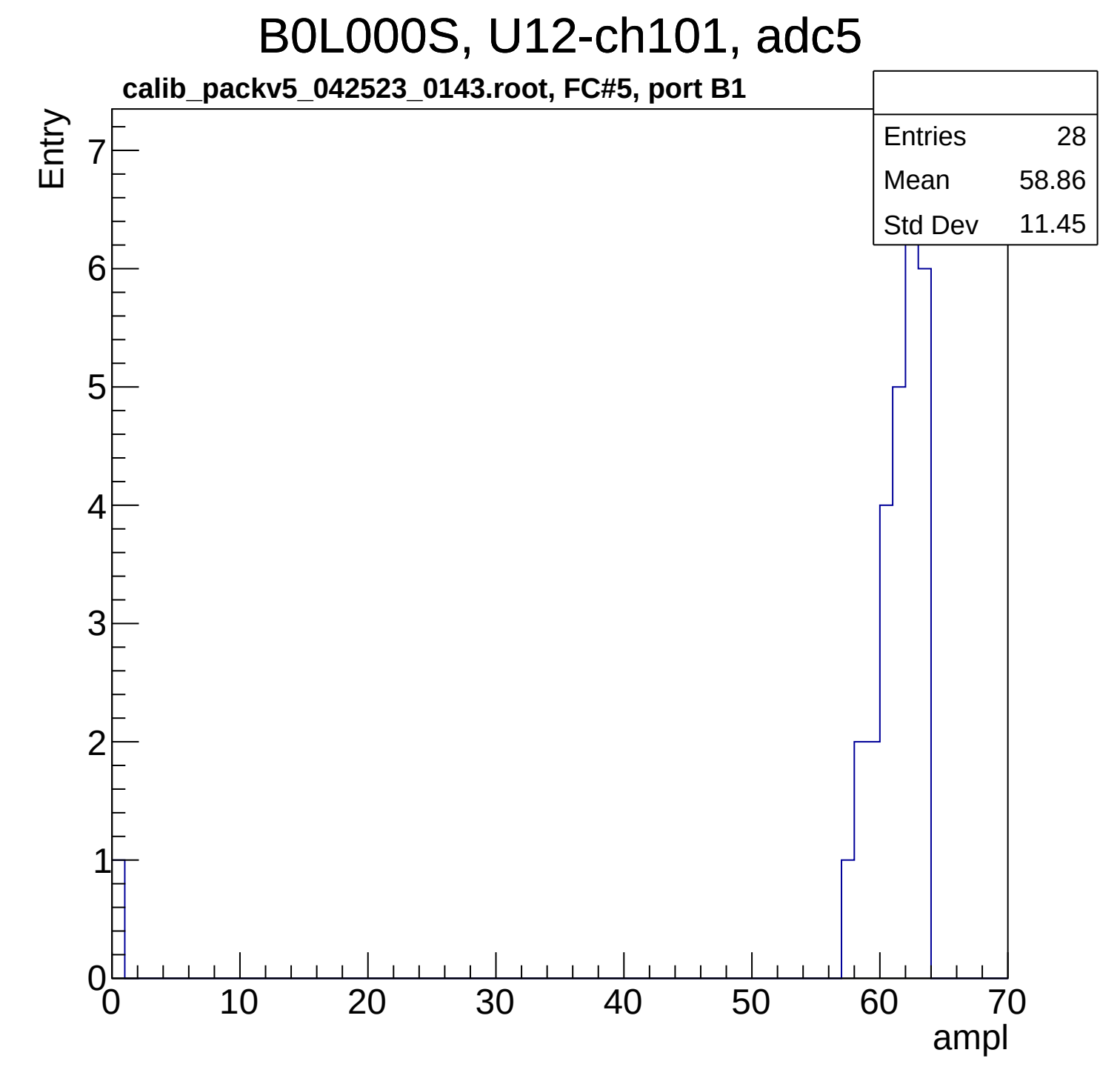

B0L000S, U12-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.11
Std Dev 2.793
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.86
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
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0
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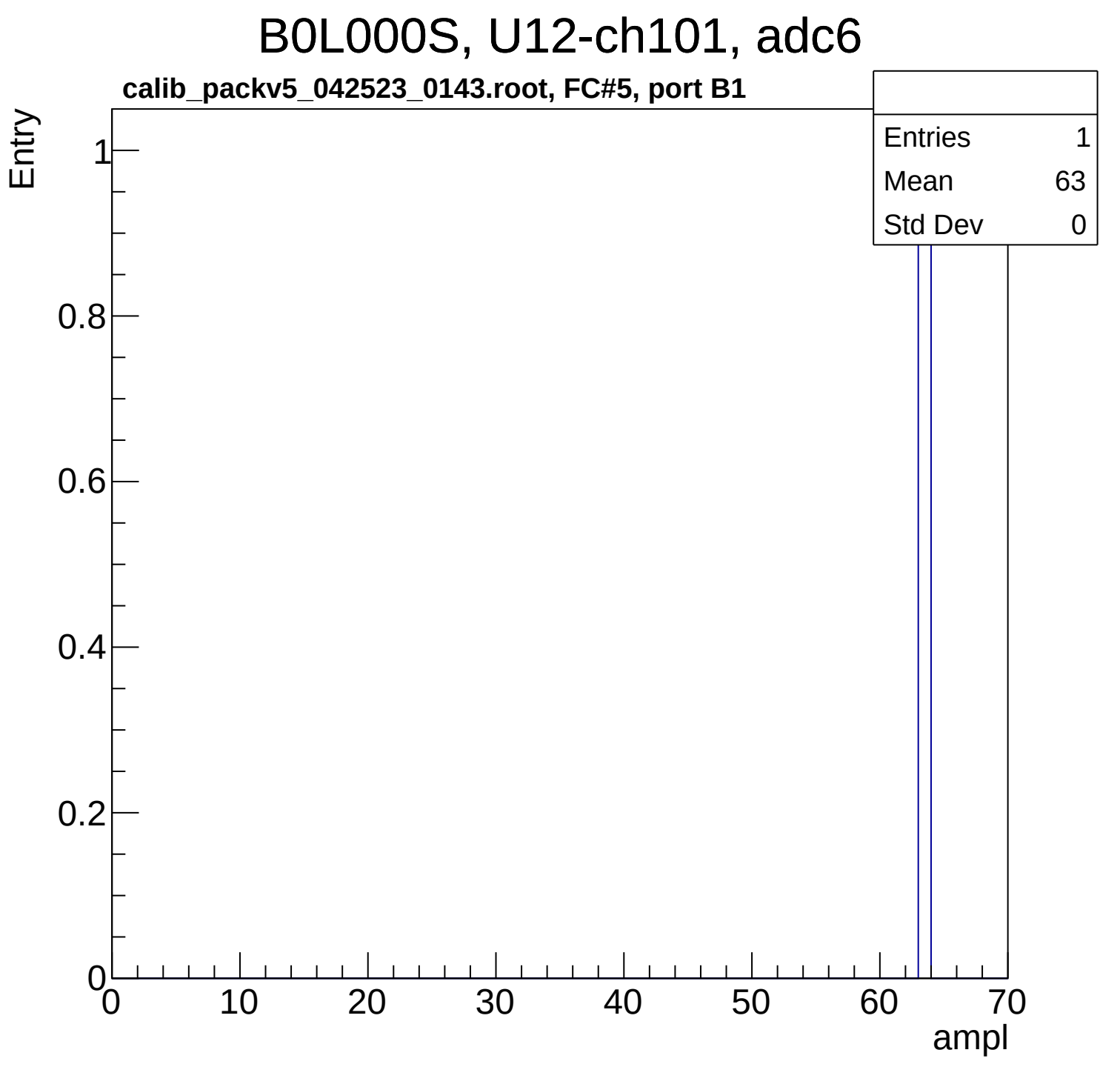
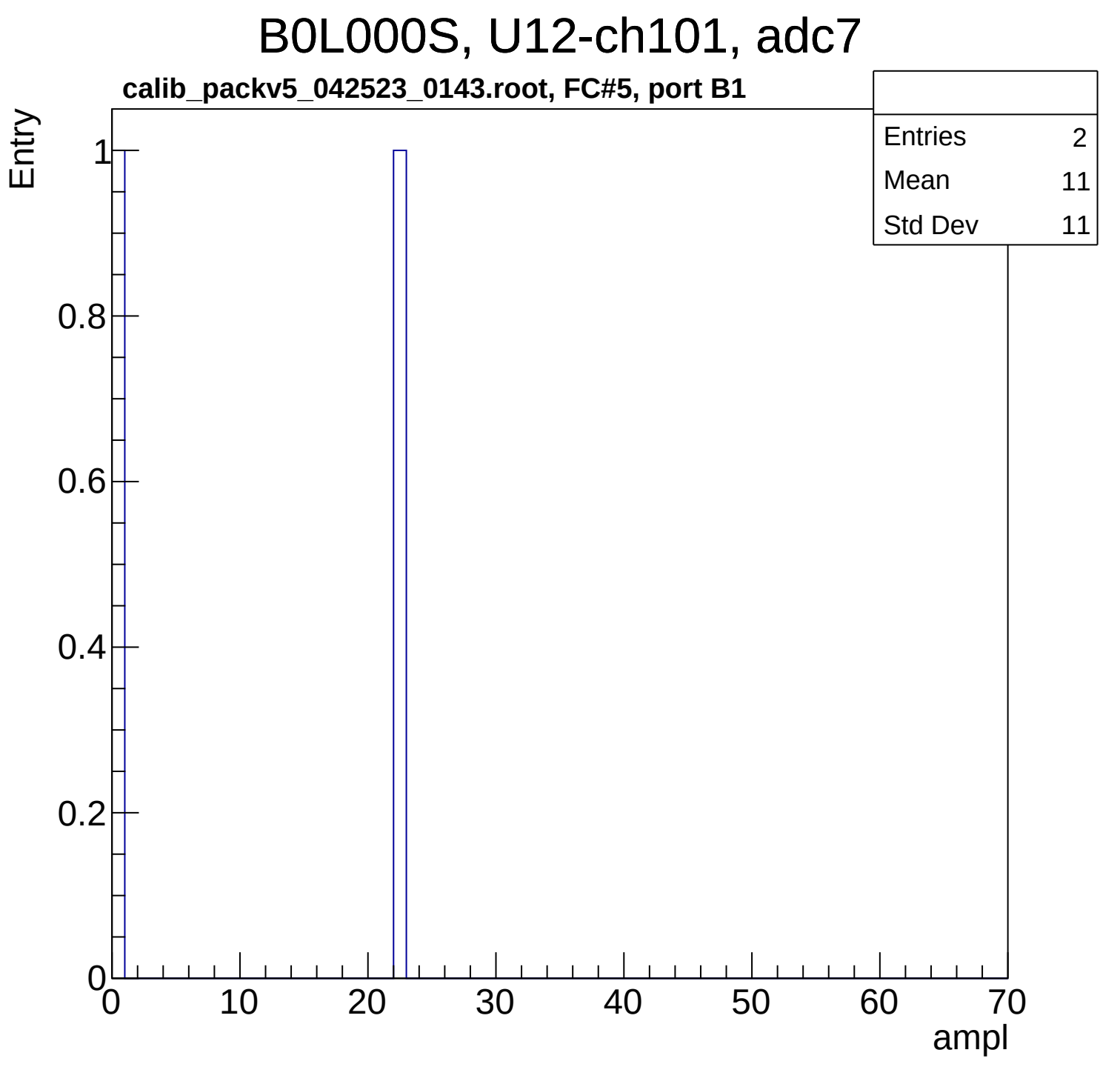
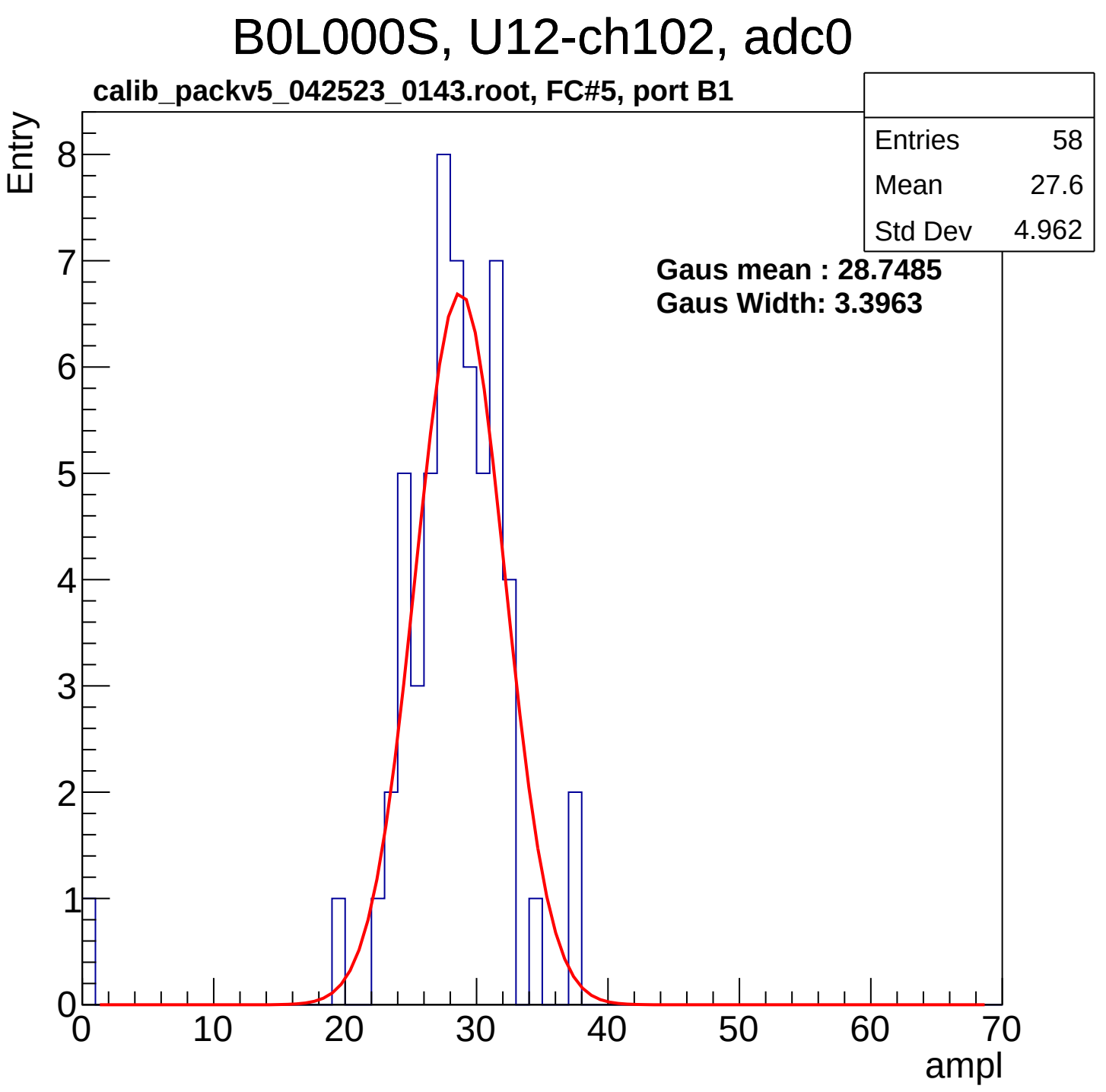
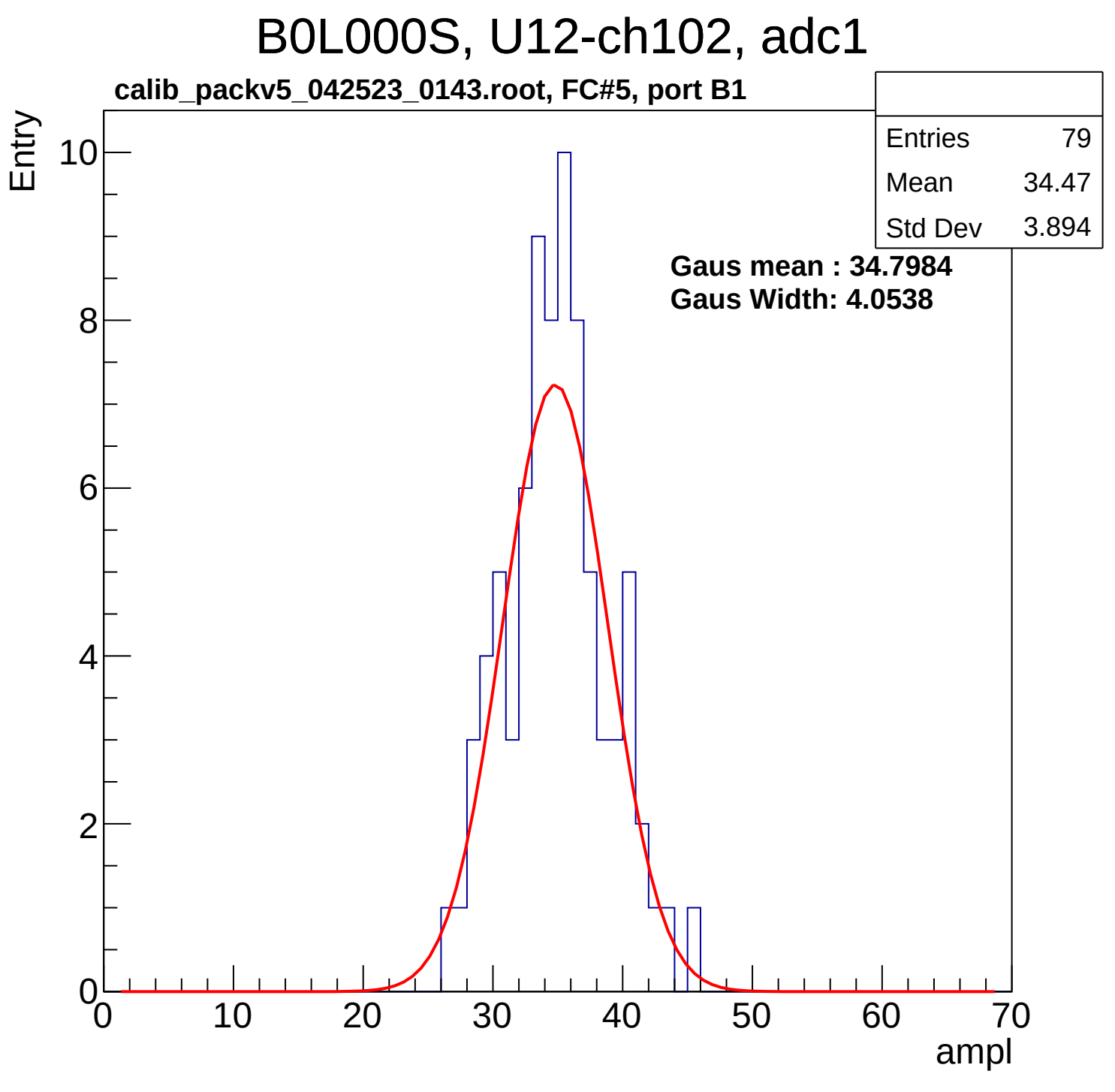

B0L000S, U12-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 27.6
Std Dev 4.962
Gaus mean : 28.7485
Gaus Width: 3.3963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 34.47
Std Dev 3.894
Gaus mean : 34.7984
Gaus Width: 4.0538
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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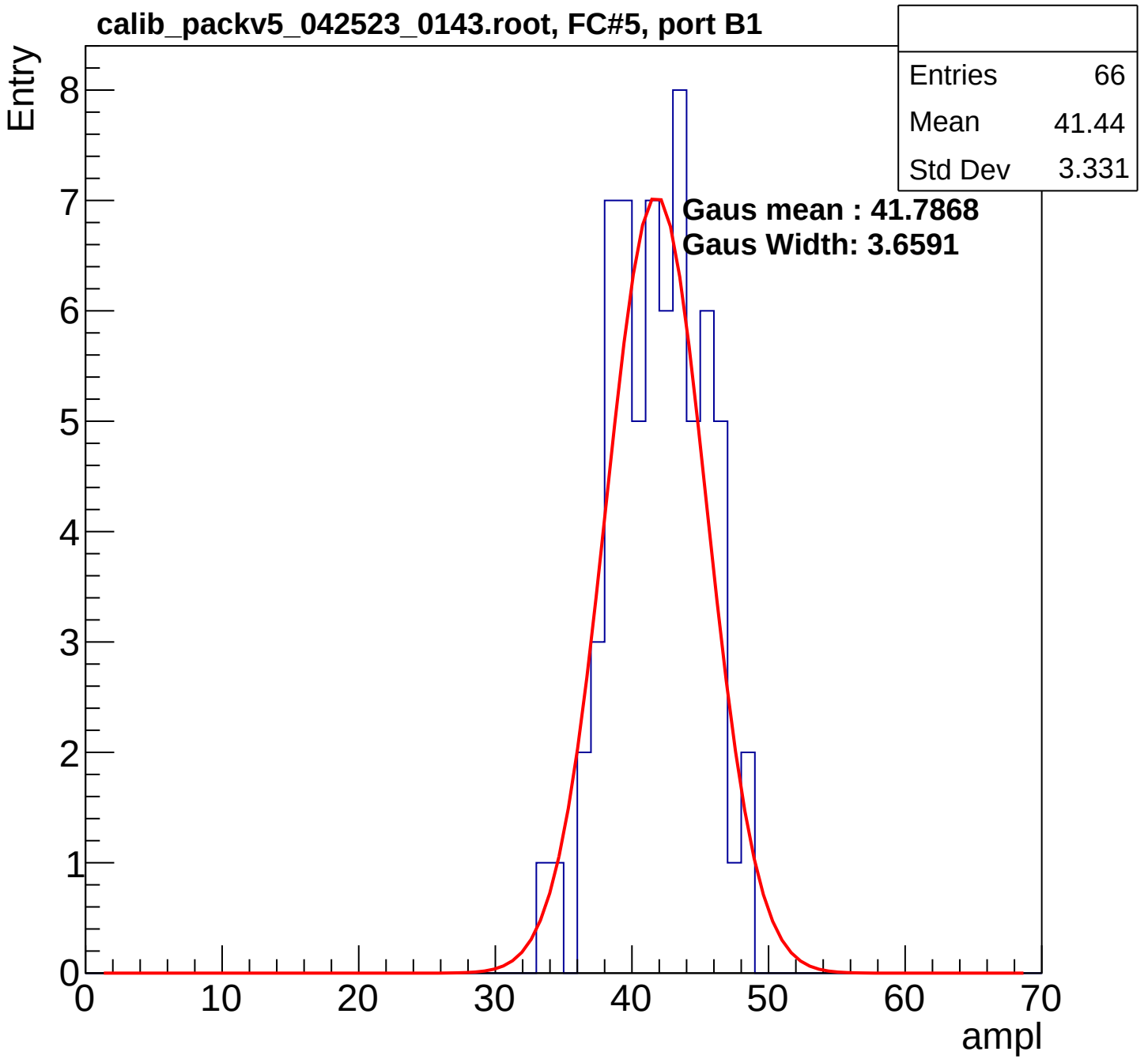
B0L000S, U12-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.44
Std Dev 3.331
Gaus mean : 41.7868
Gaus Width: 3.6591
Entry
ampl
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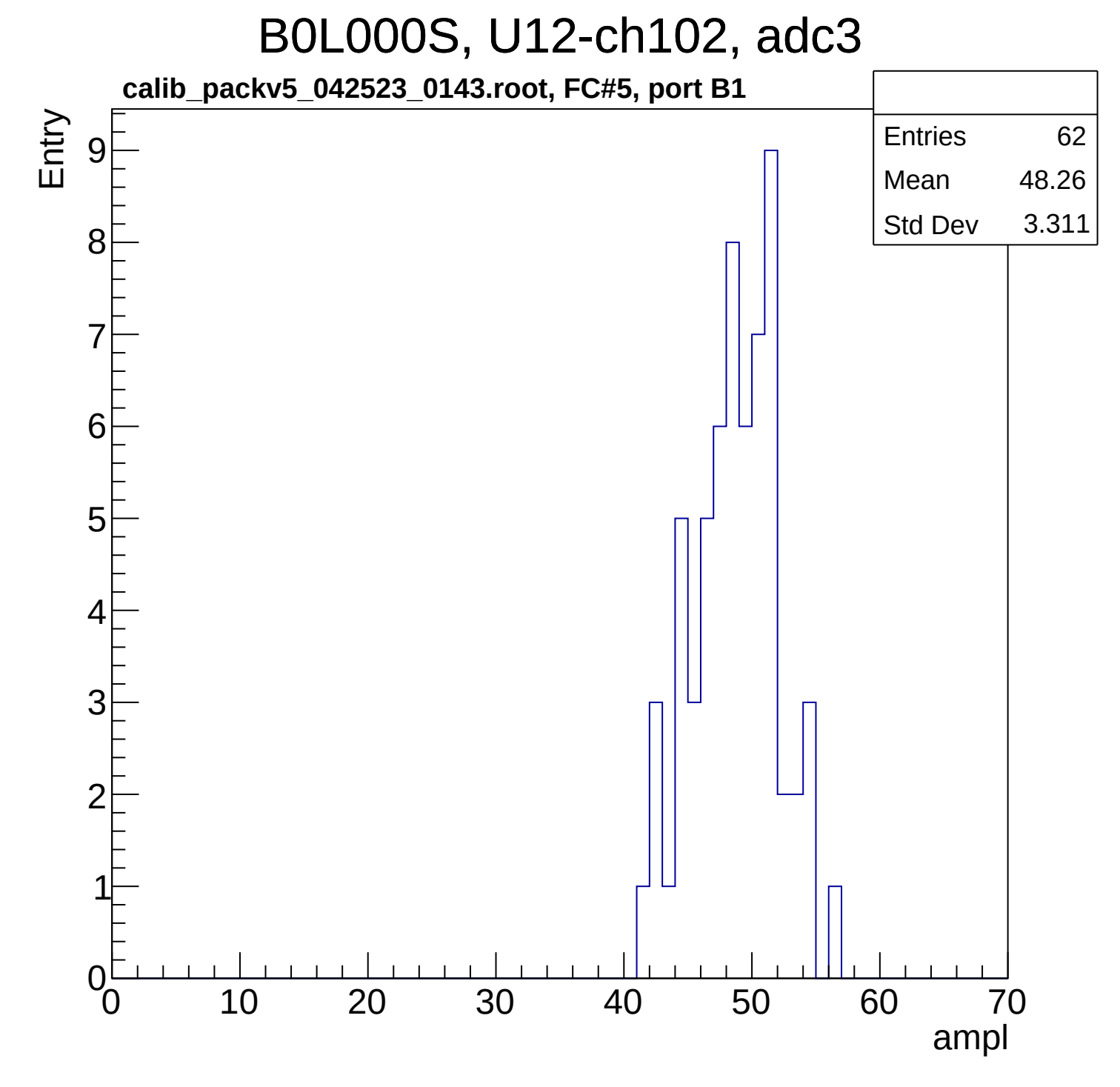
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.26
Std Dev 3.311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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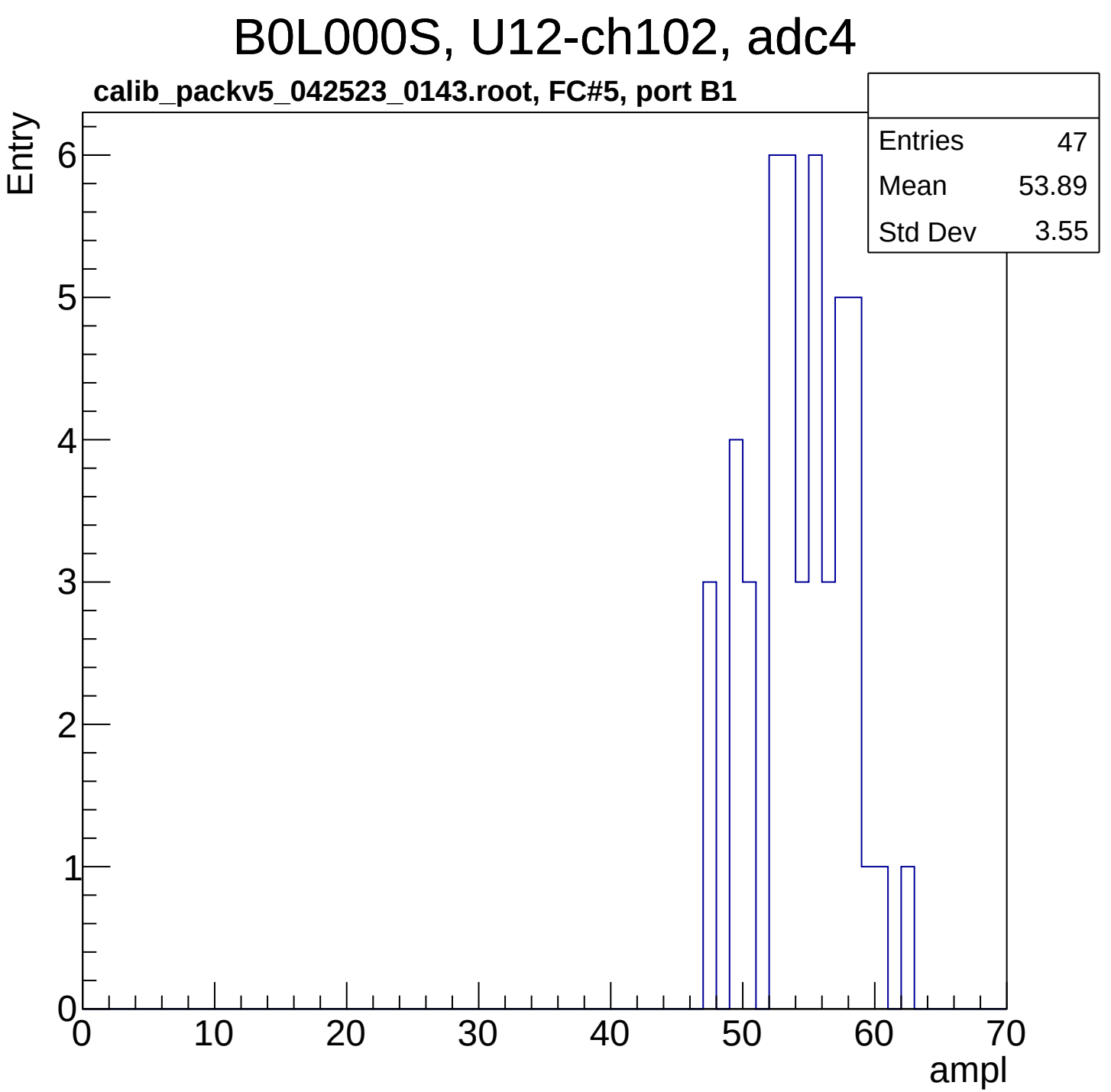
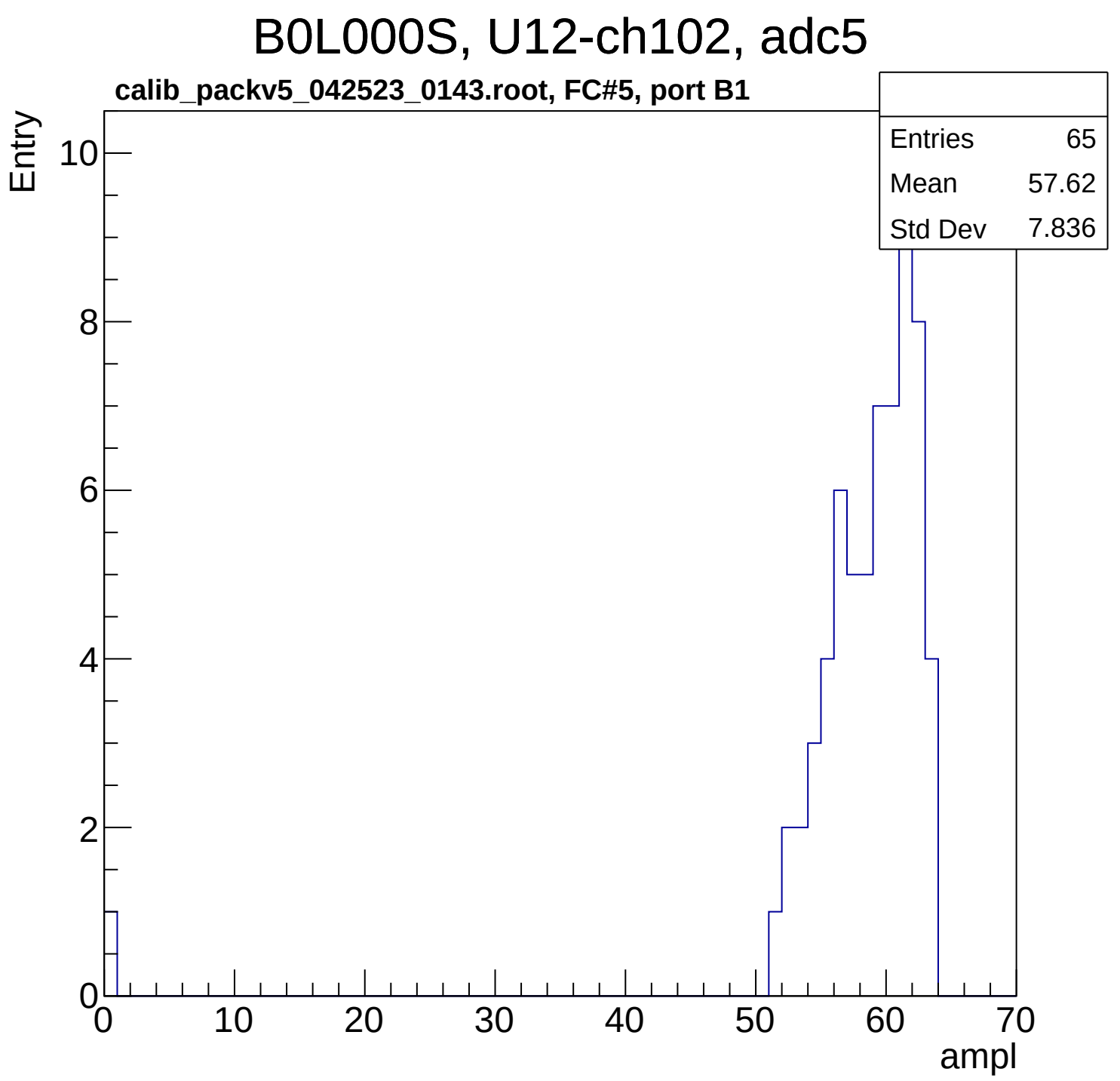

B0L000S, U12-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 53.89
Std Dev 3.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 57.62
Std Dev 7.836
Entry
ampl
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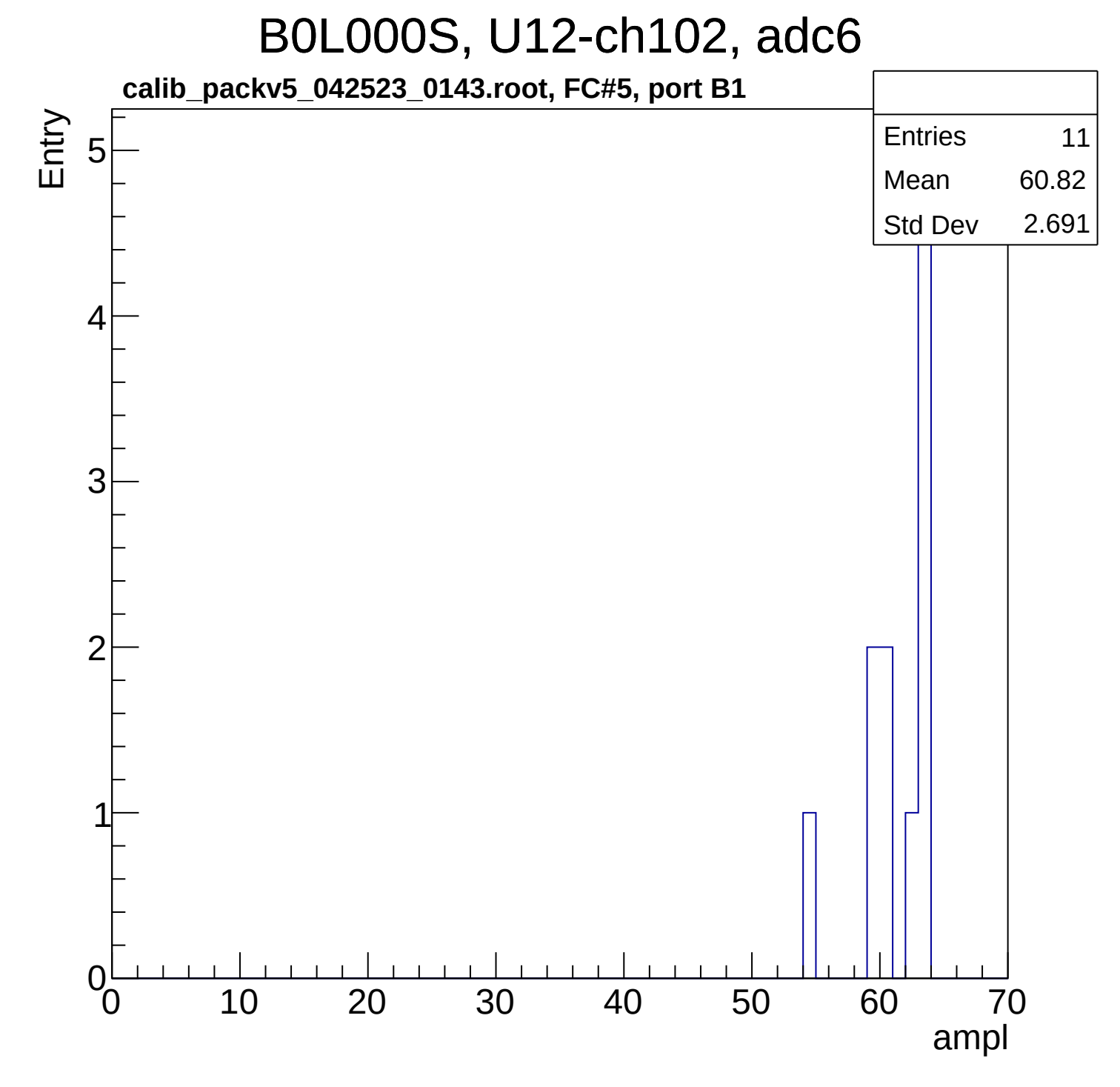
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 60.82
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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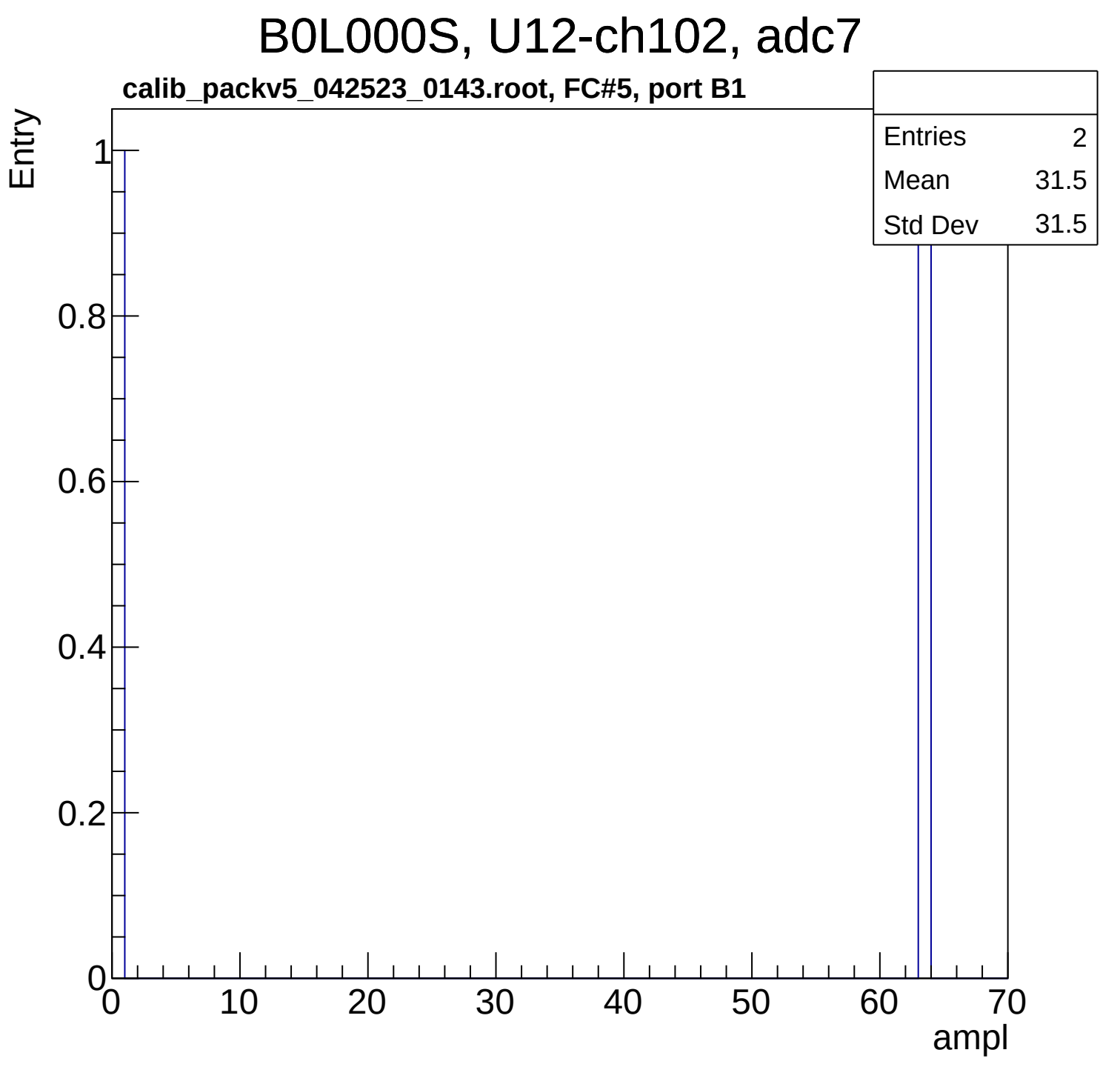
B0L000S, U12-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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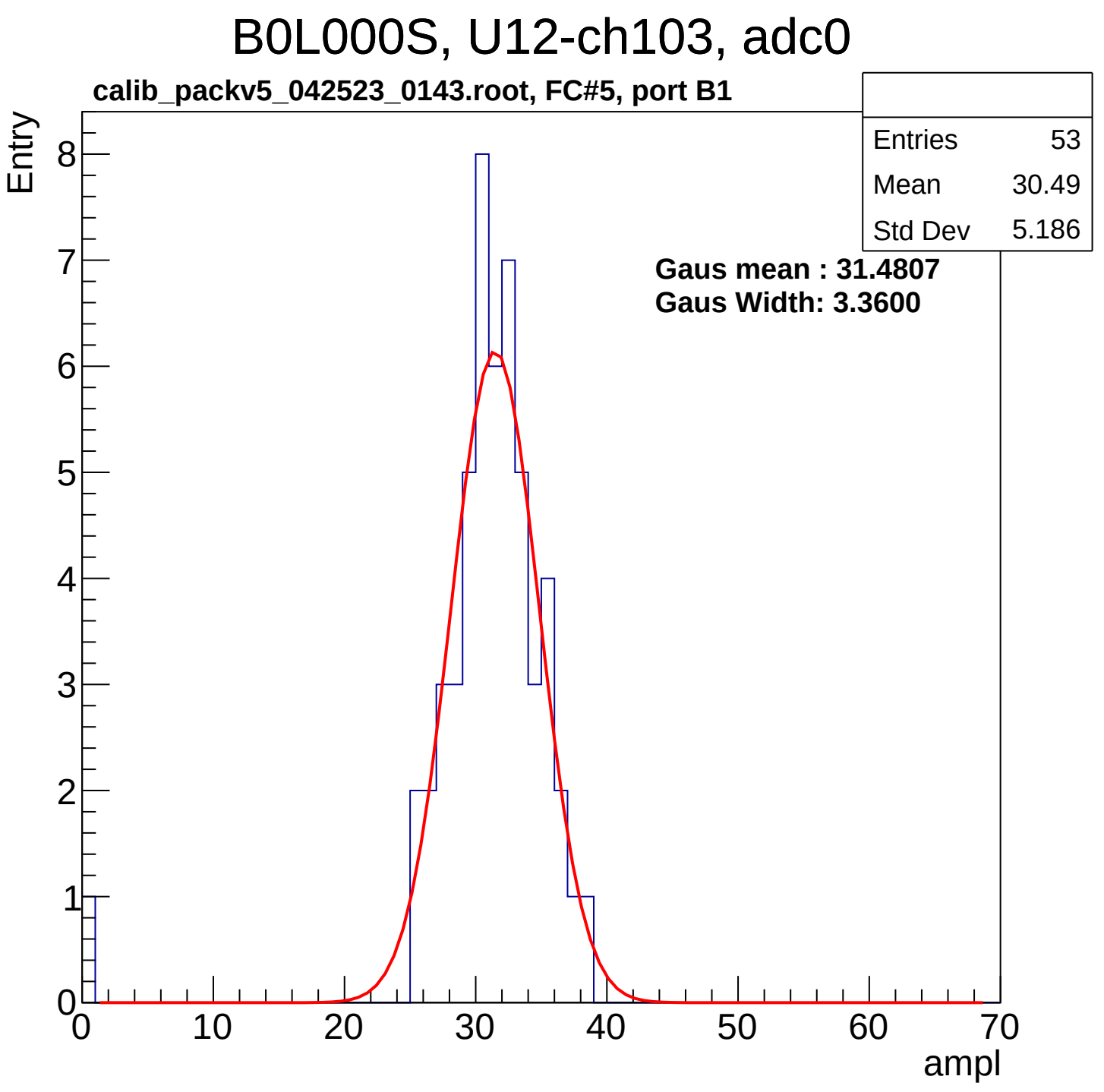
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 30.49
Std Dev 5.186
Gaus mean : 31.4807
Gaus Width: 3.3600
Entry
ampl
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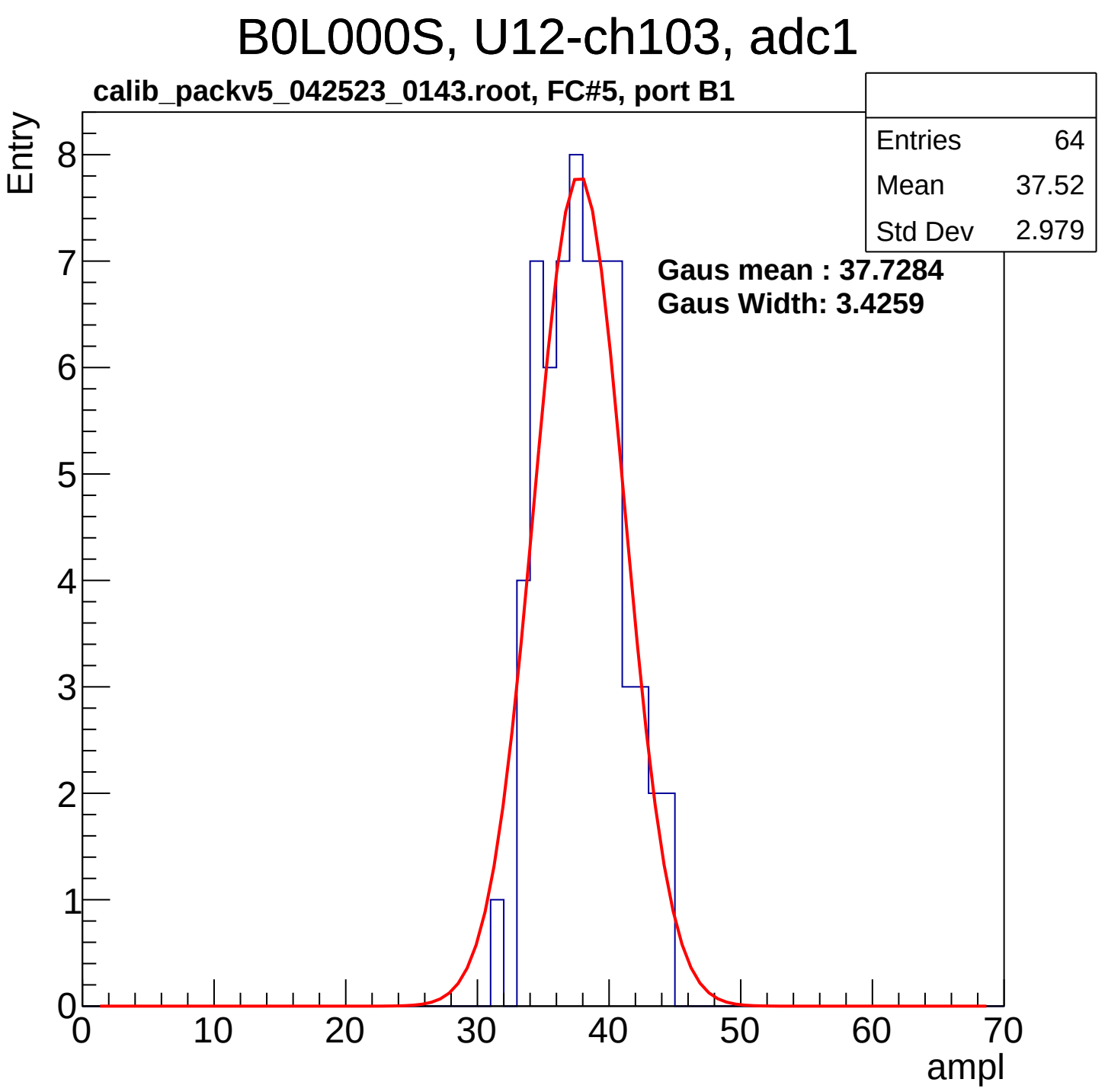
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 37.52
Std Dev 2.979
Gaus mean : 37.7284
Gaus Width: 3.4259
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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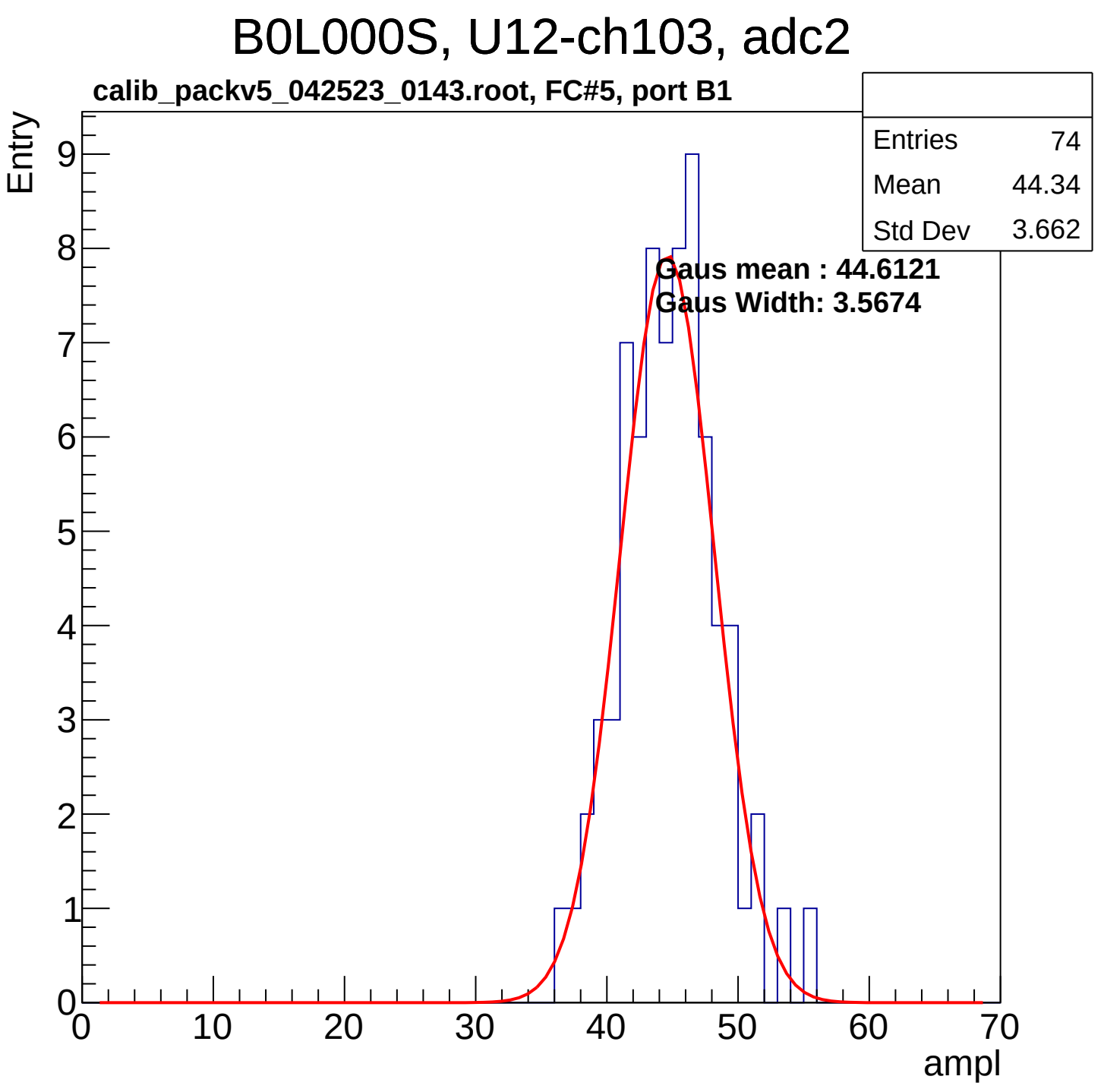
B0L000S, U12-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 44.34
Std Dev 3.662
Gaus mean : 44.6121
Gaus Width: 3.5674
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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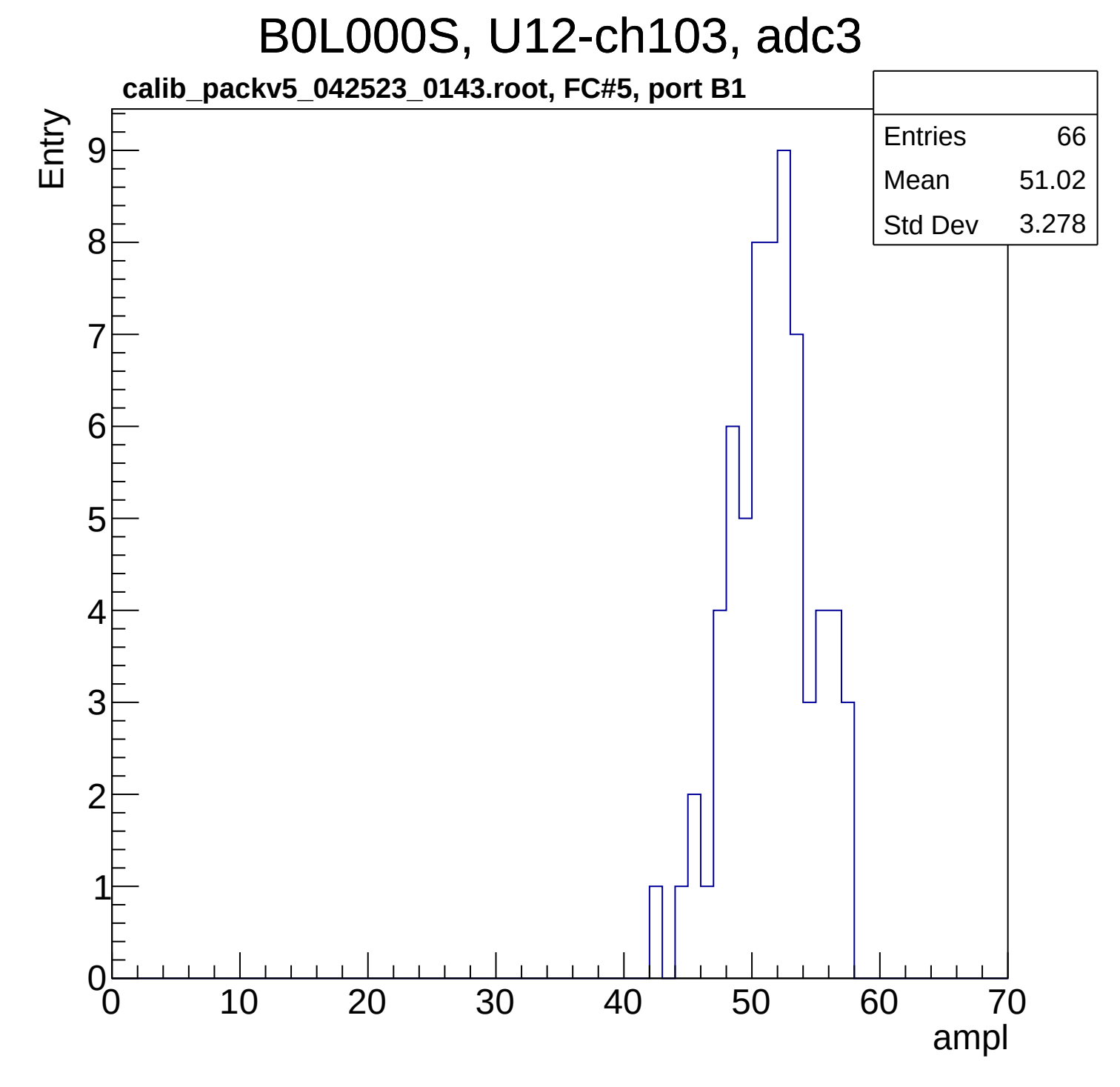
B0L000S, U12-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.02
Std Dev 3.278
Entry
ampl
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4
5
6
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0
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60
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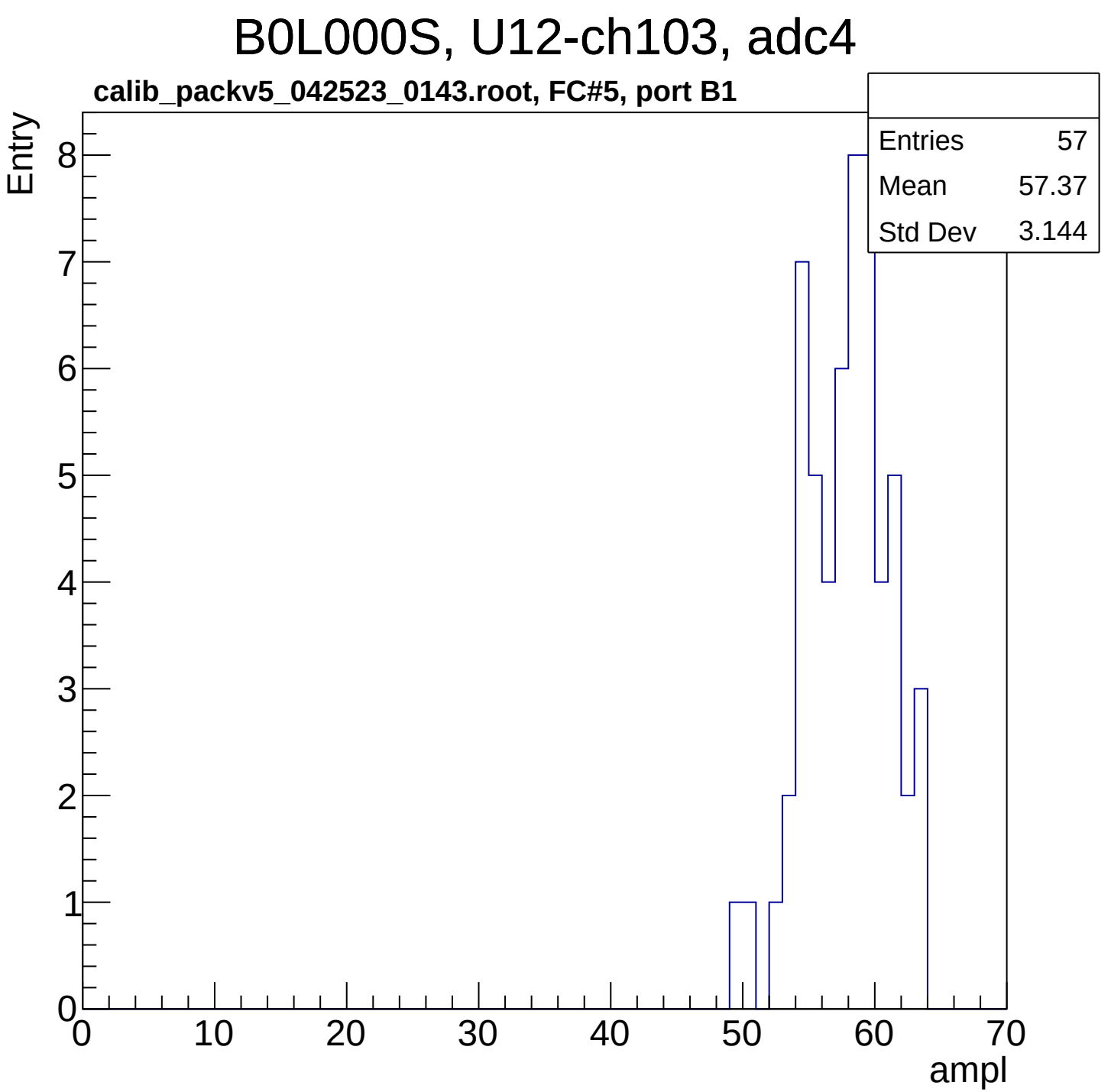
B0L000S, U12-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.37
Std Dev 3.144
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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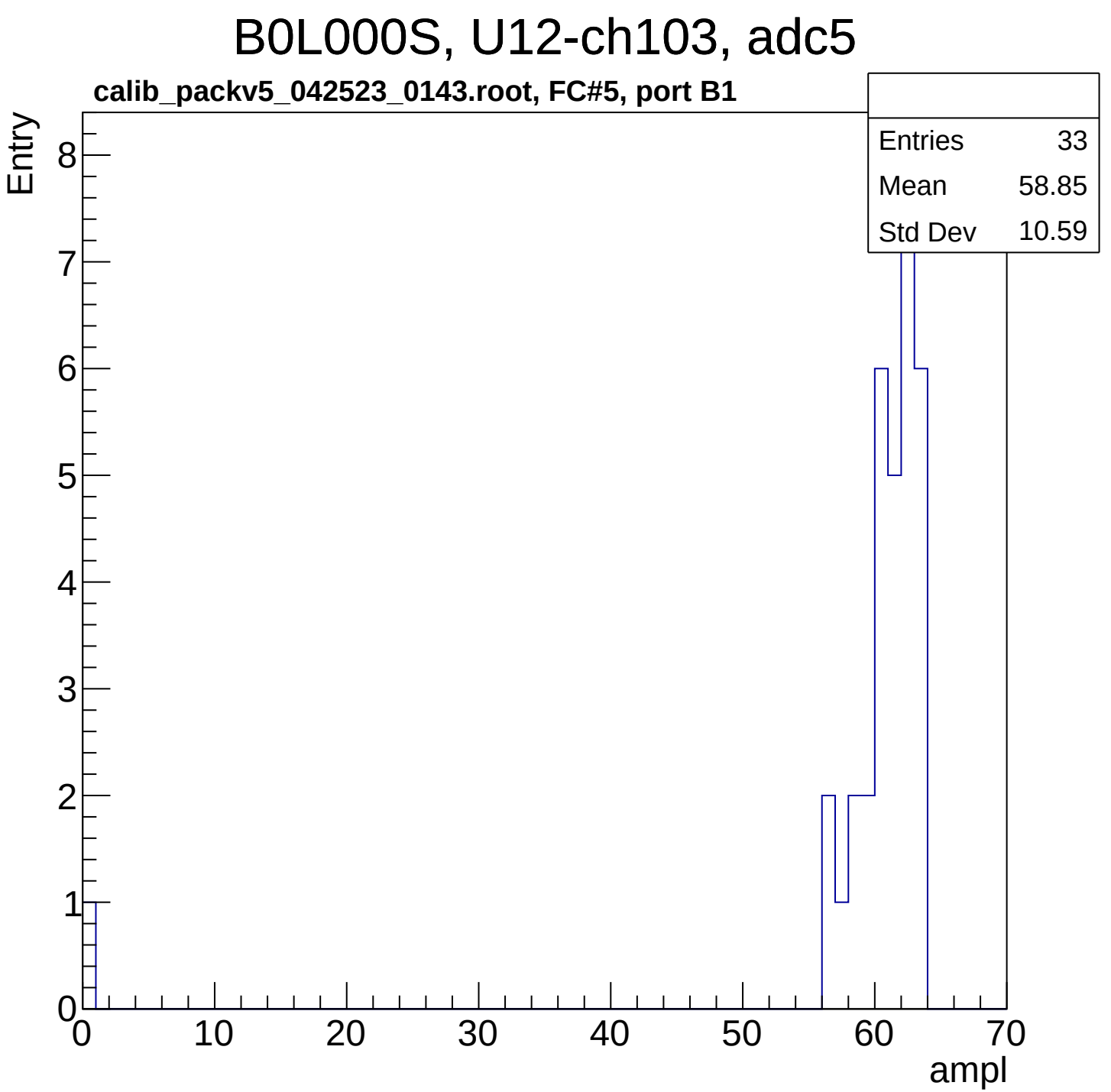
B0L000S, U12-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.85
Std Dev 10.59
Entry
ampl
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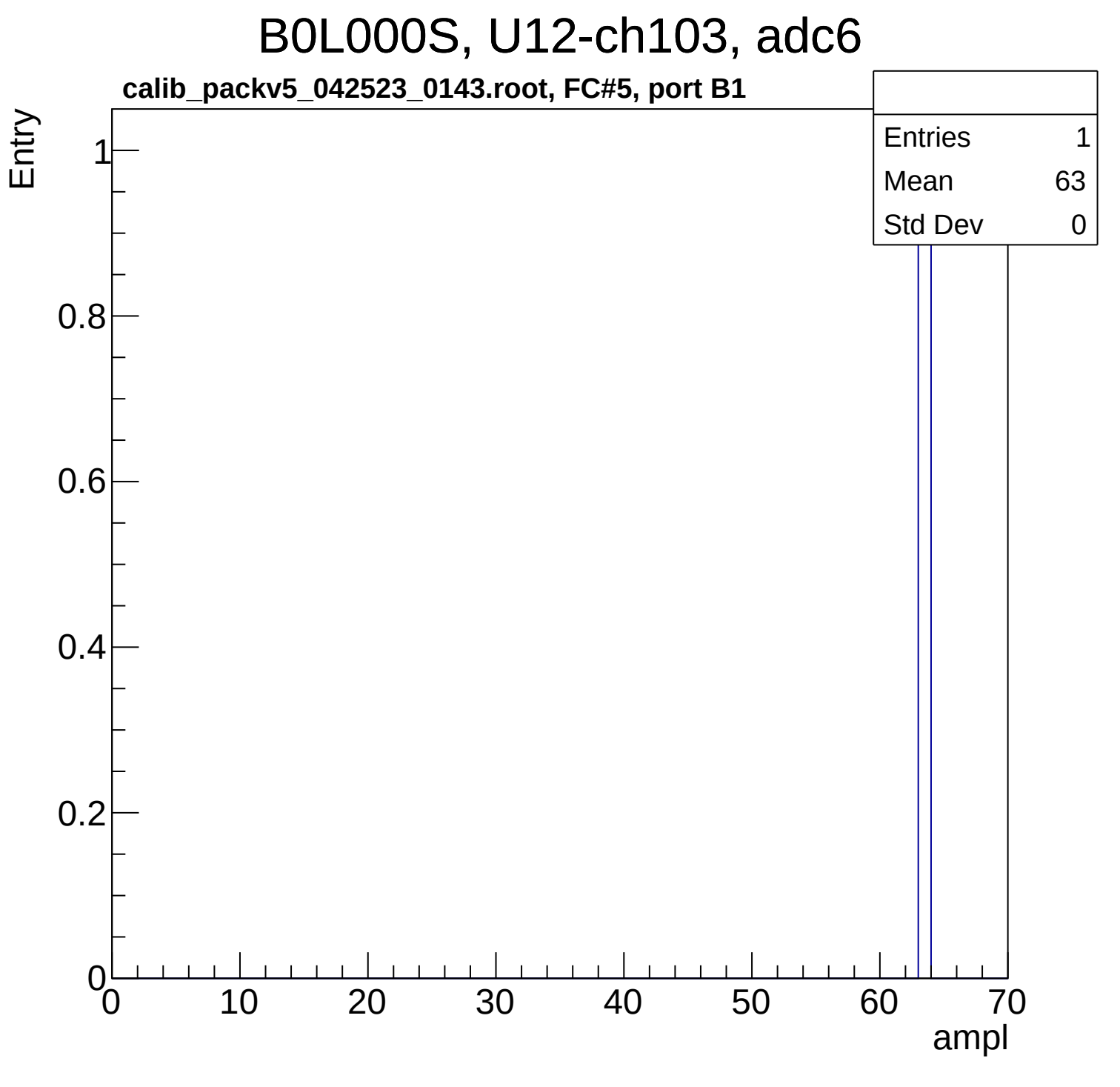
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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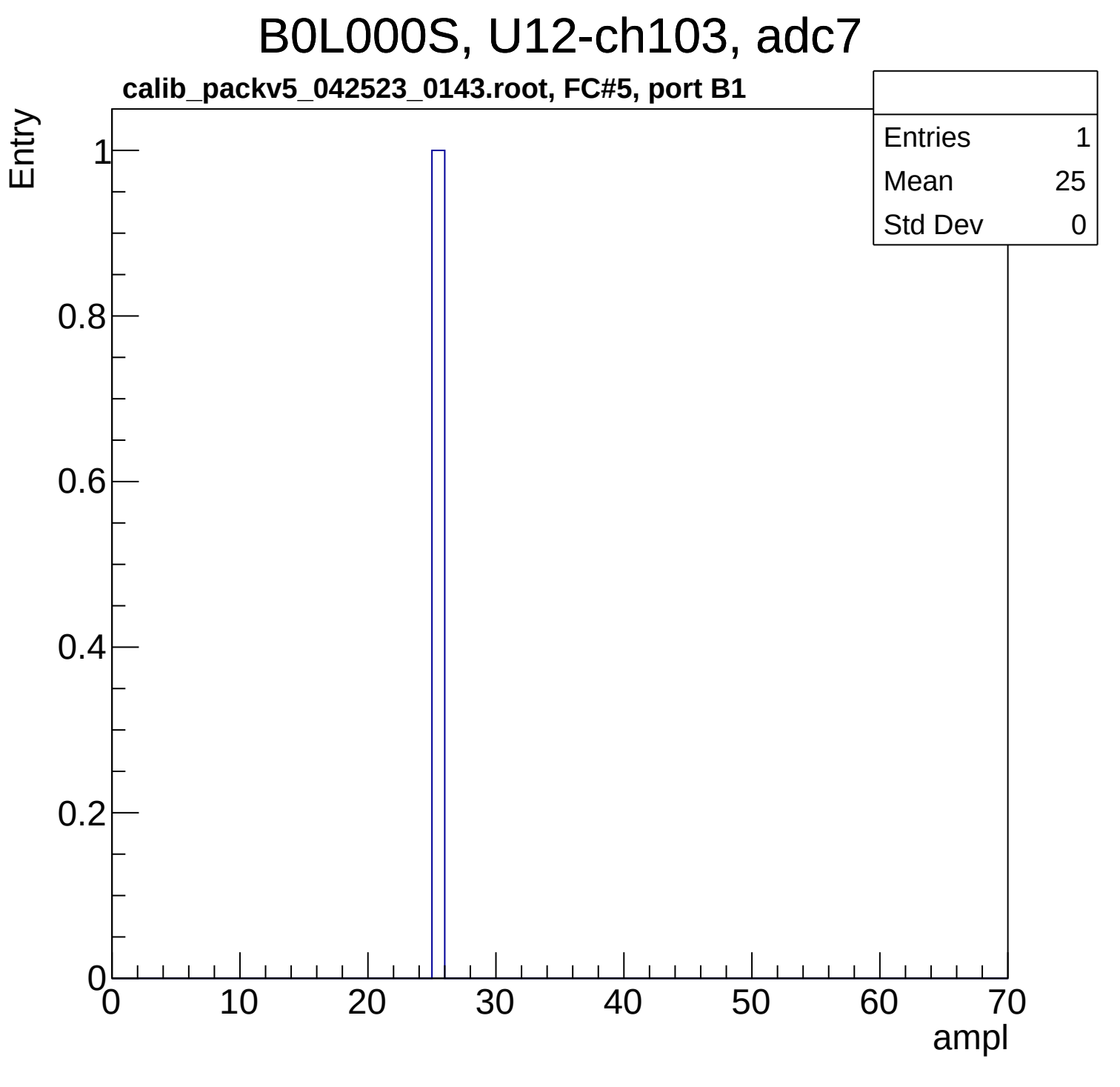
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 25
Std Dev 0
Entry
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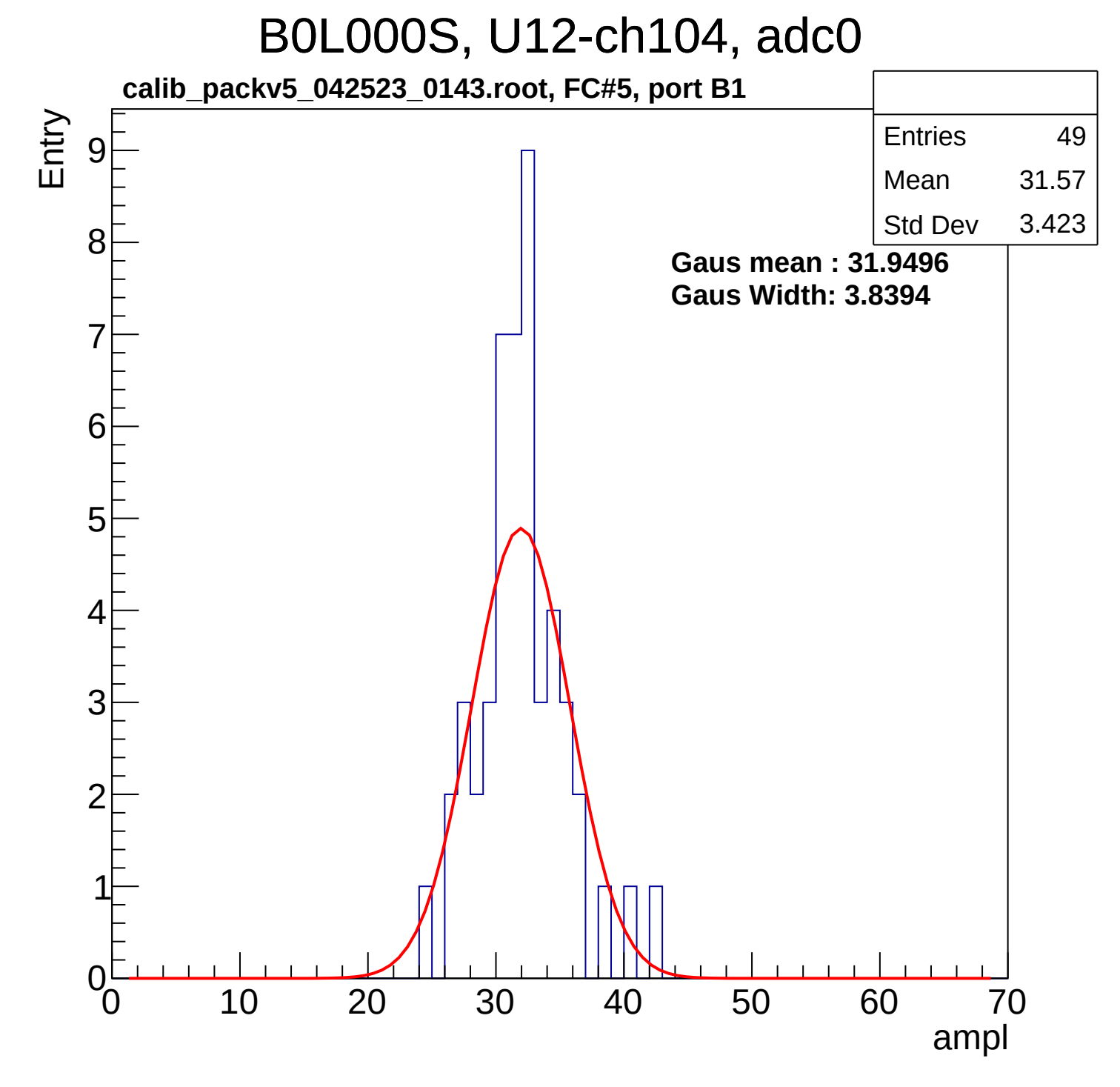
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 31.57
Std Dev 3.423
Gaus mean : 31.9496
Gaus Width: 3.8394
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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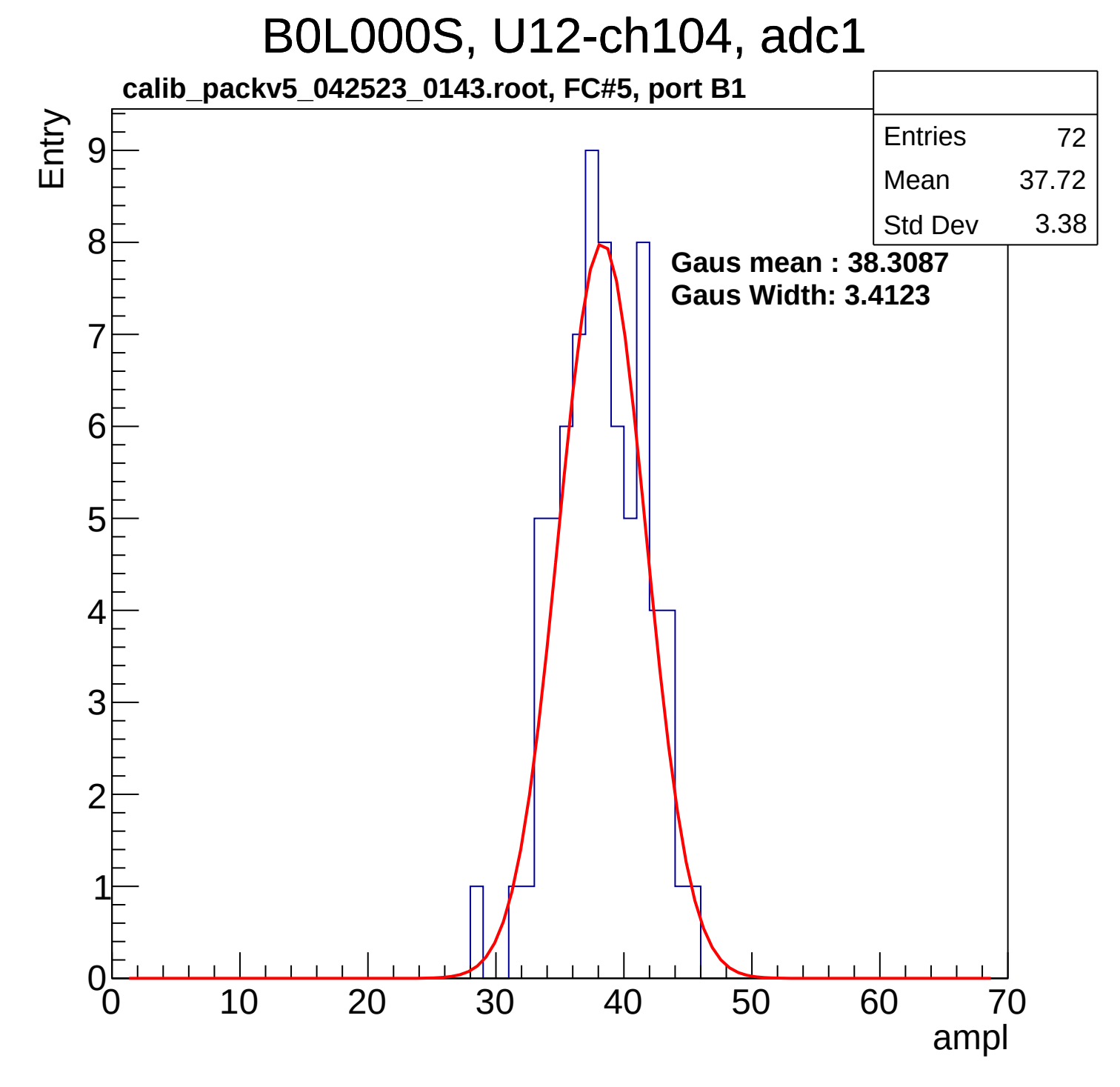
B0L000S, U12-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.72
Std Dev 3.38
Gaus mean : 38.3087
Gaus Width: 3.4123
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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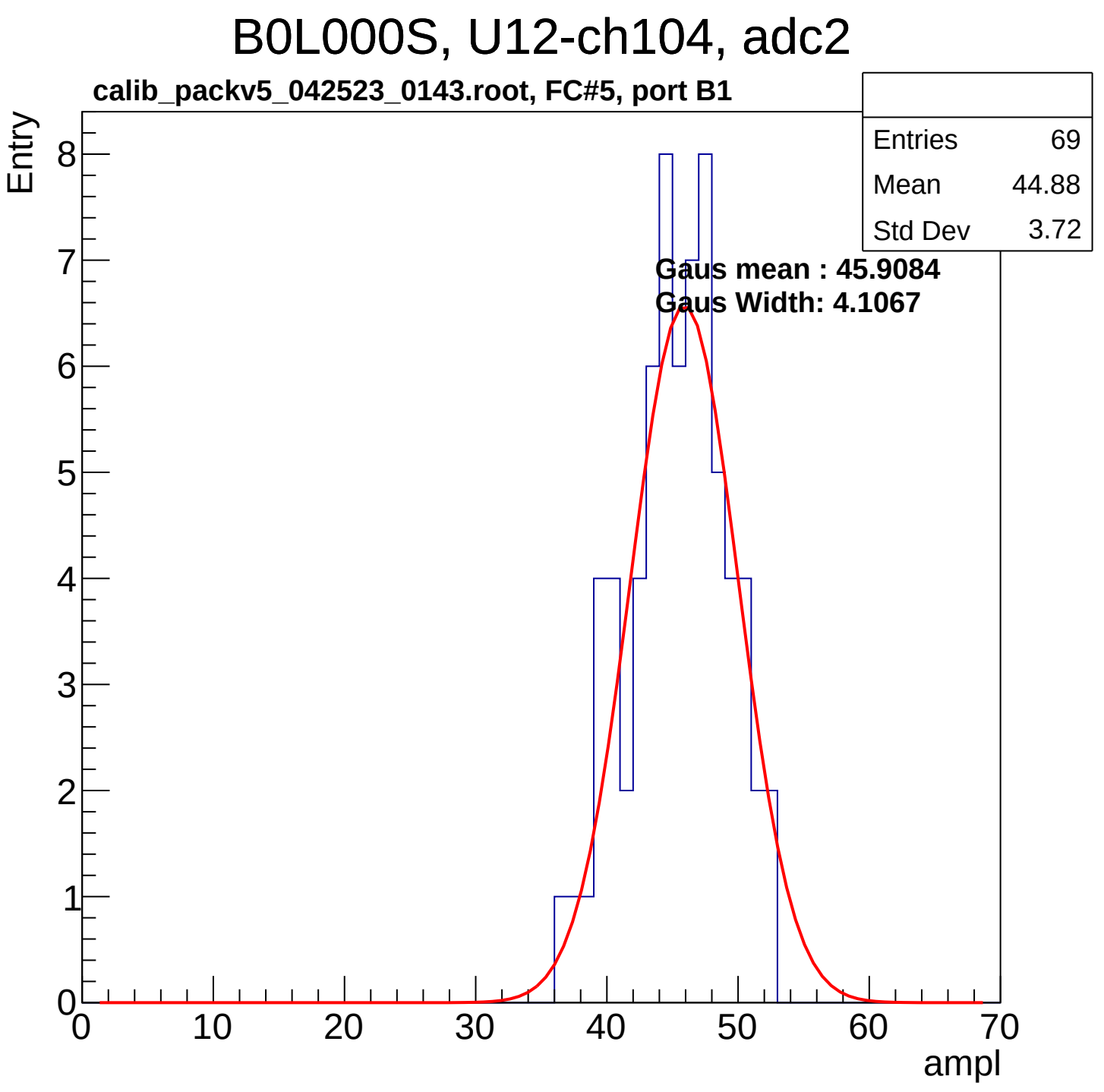
B0L000S, U12-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.88
Std Dev 3.72
Gaus mean : 45.9084
Gaus Width: 4.1067
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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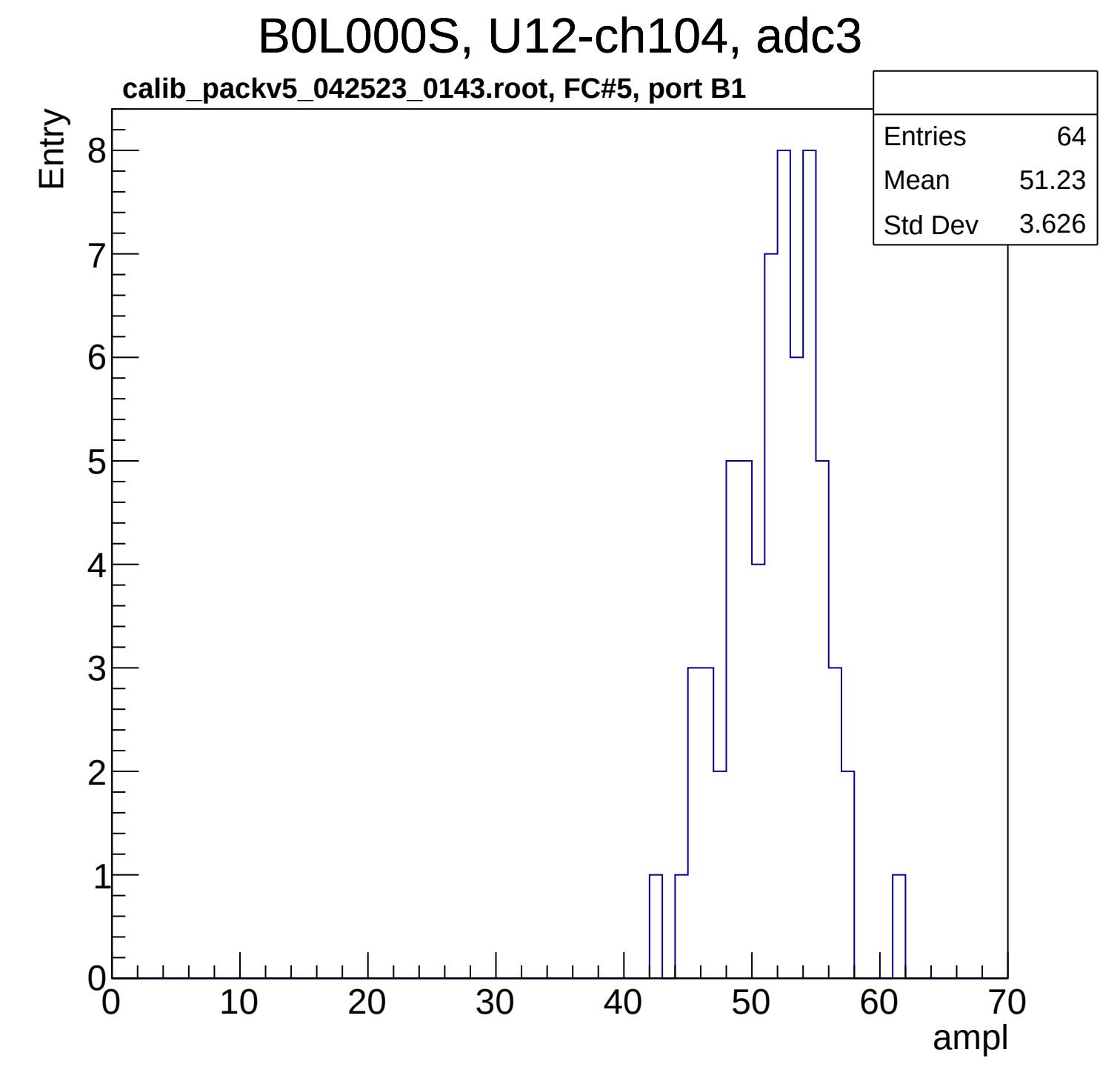
B0L000S, U12-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 51.23
Std Dev 3.626
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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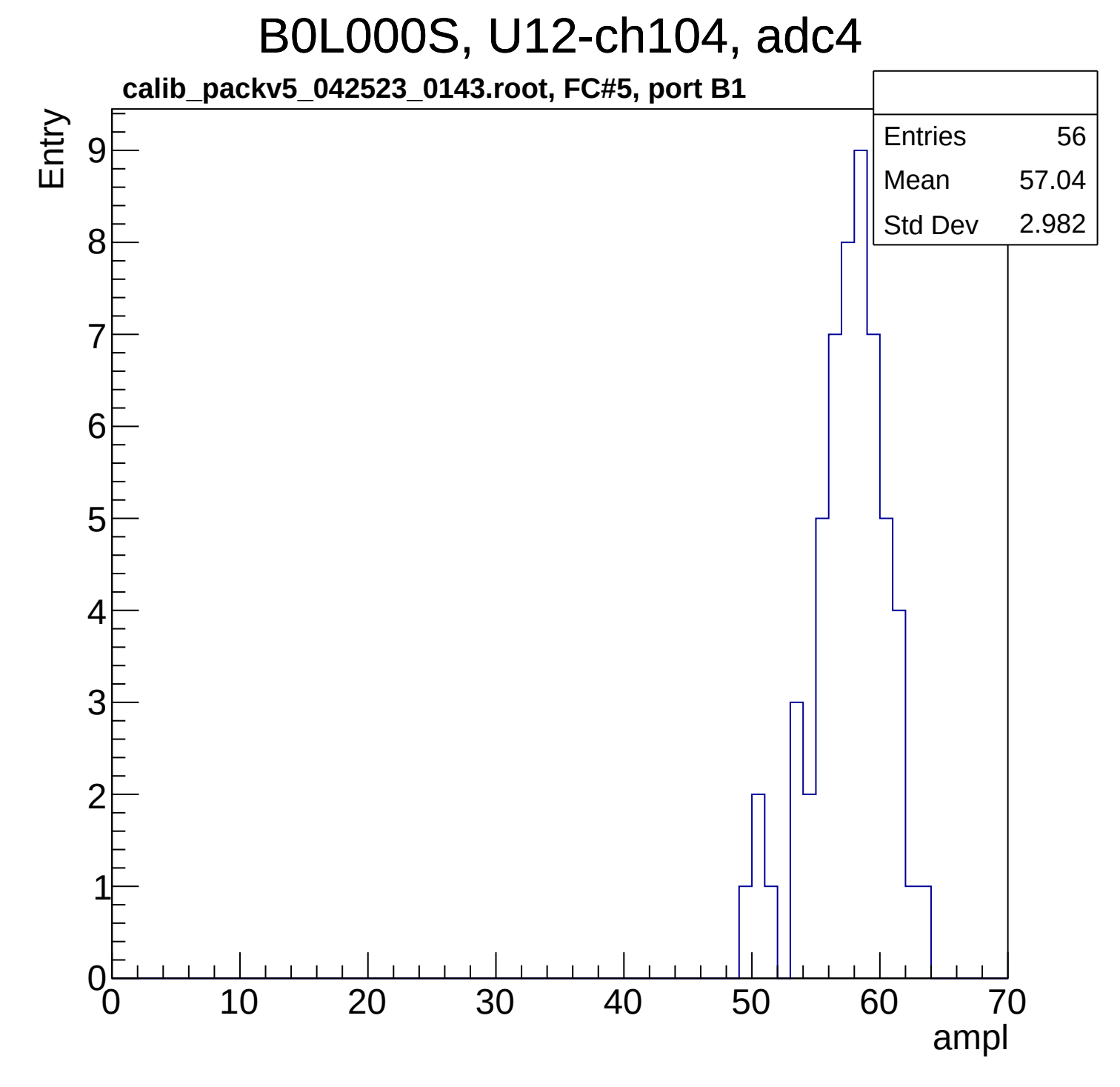
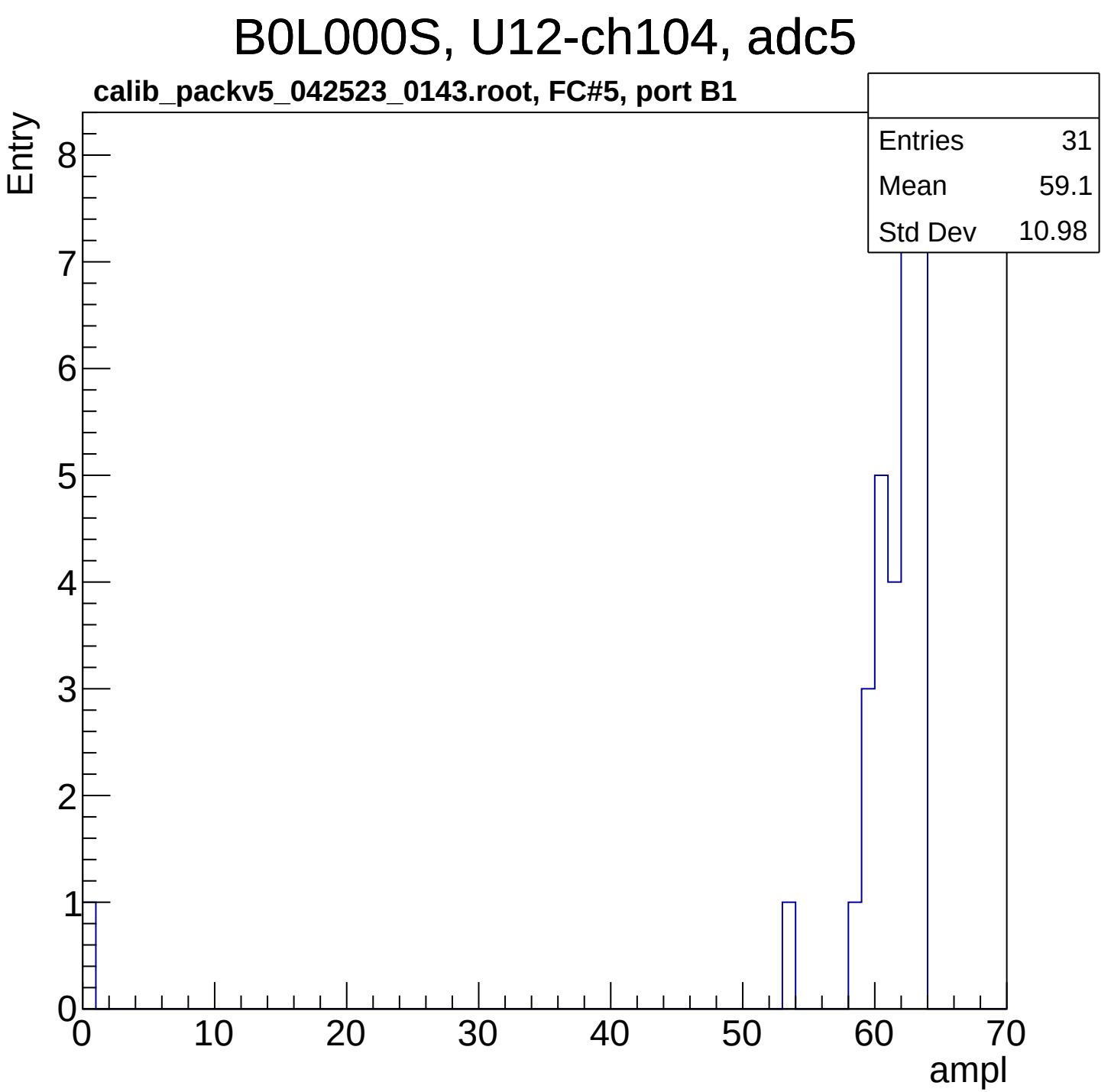
60
70


B0L000S, U12-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.04
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.1
Std Dev 10.98
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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40
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60
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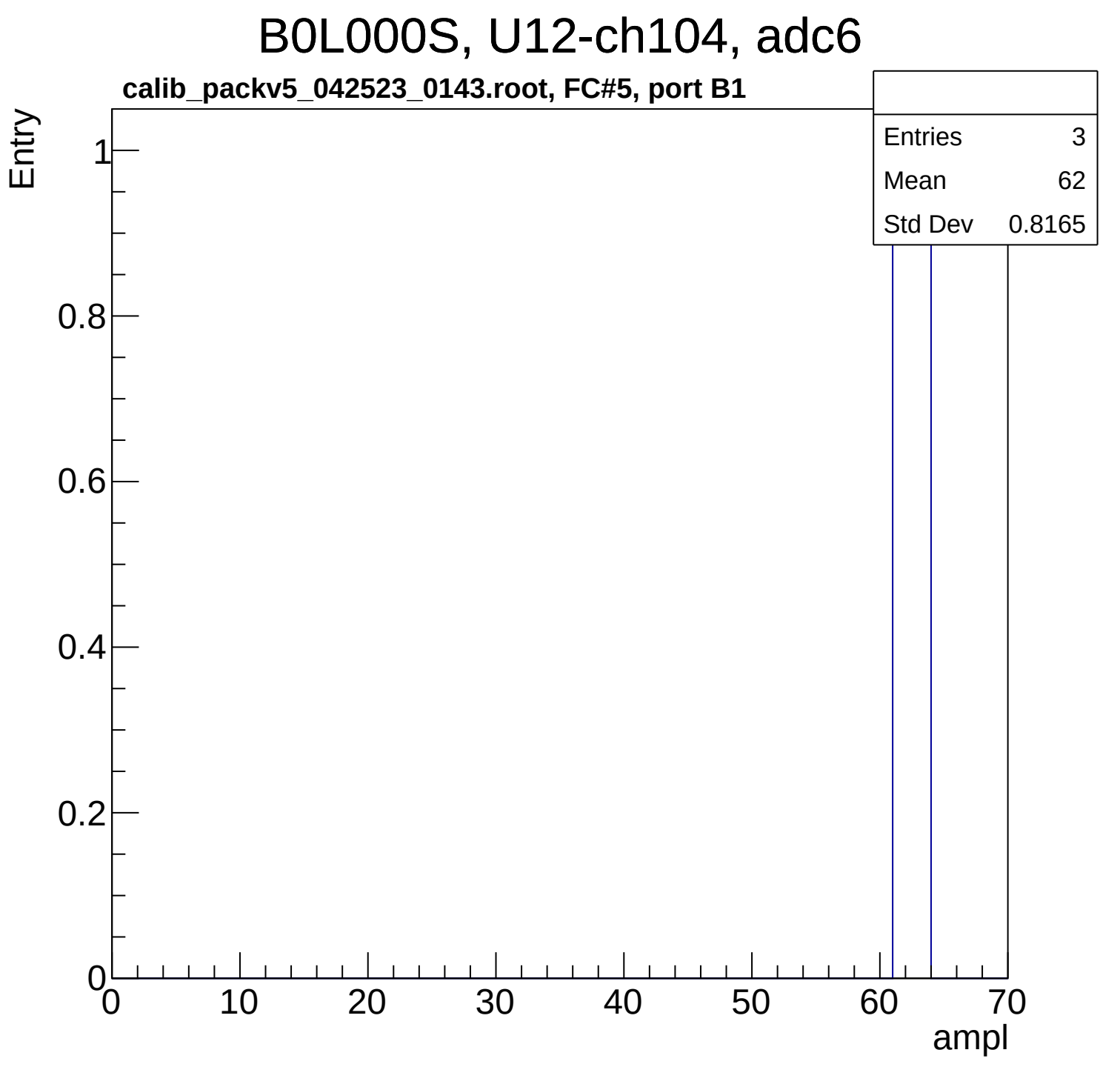
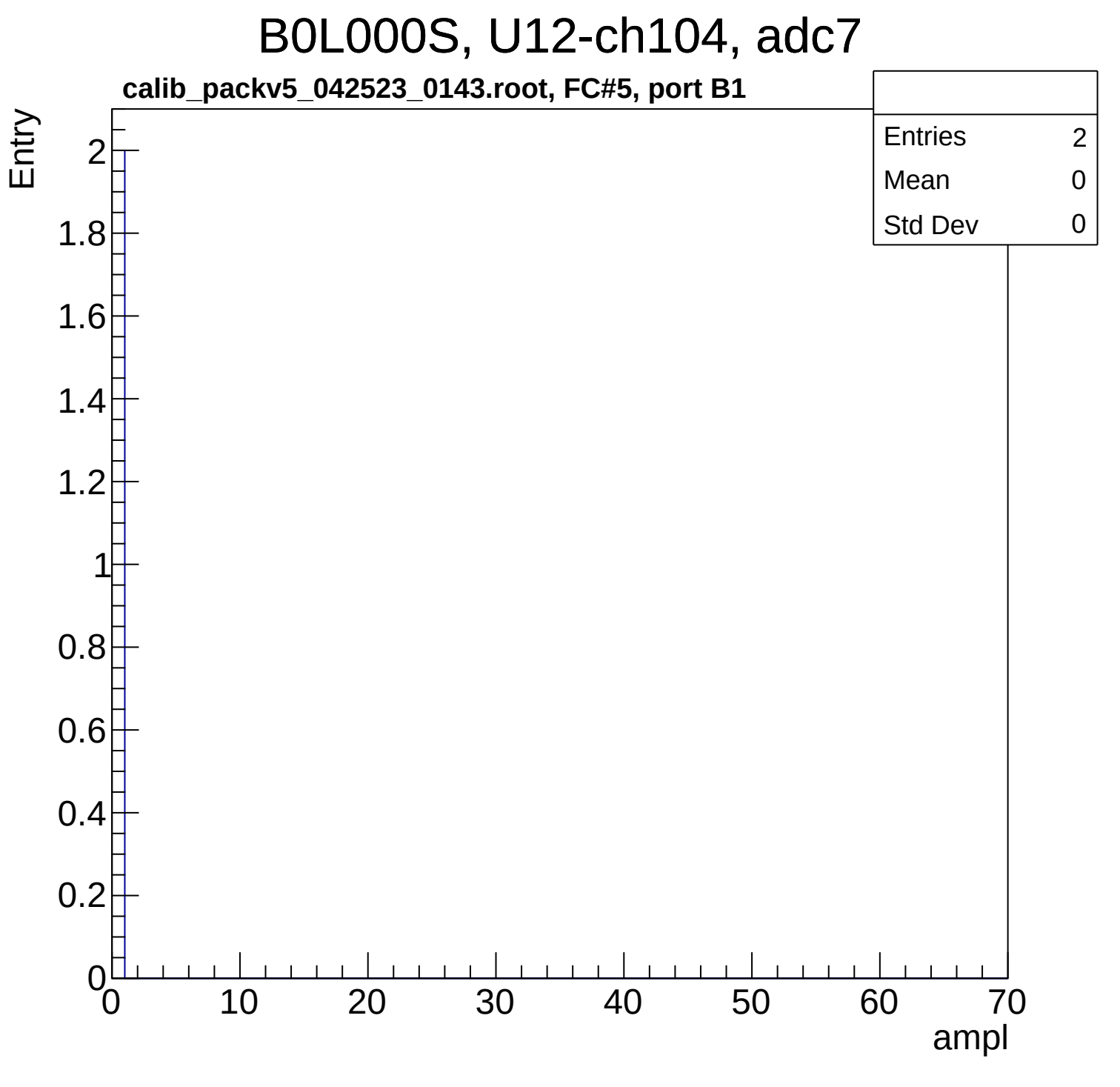

B0L000S, U12-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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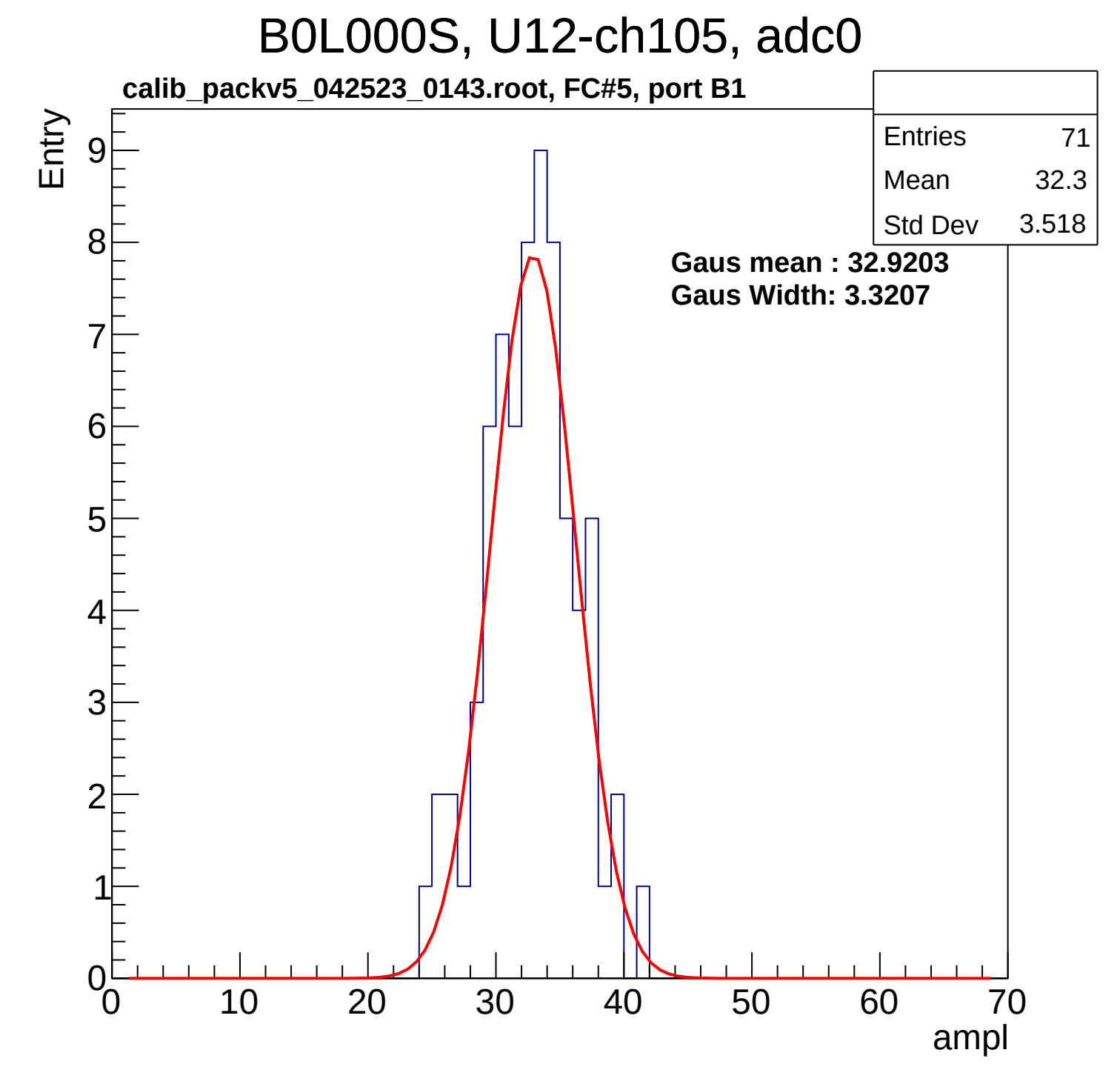
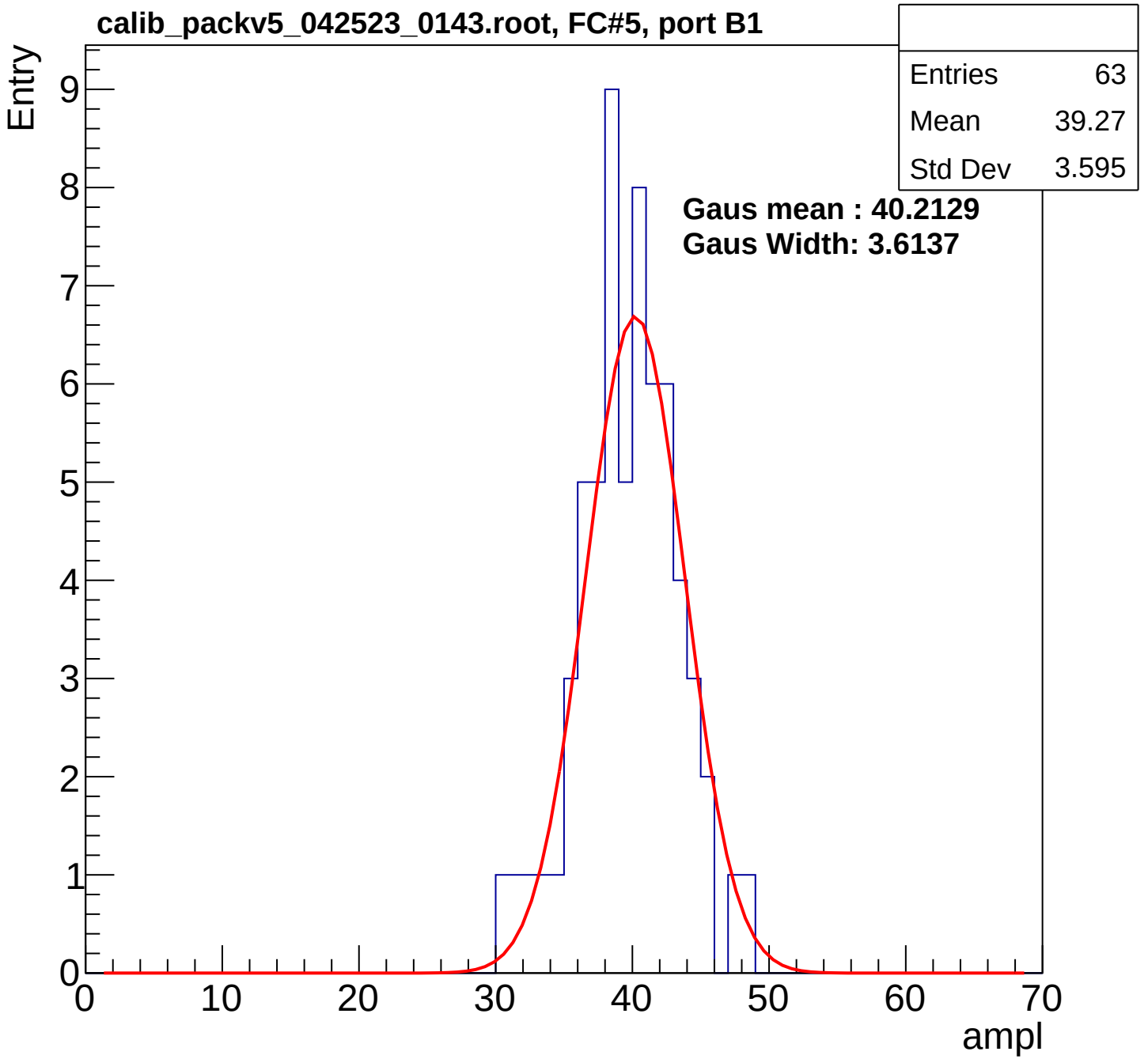

B0L000S, U12-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 32.3
Std Dev 3.518
Gaus mean : 32.9203
Gaus Width: 3.3207
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U12-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 39.27
Std Dev 3.595
Gaus mean : 40.2129
Gaus Width: 3.6137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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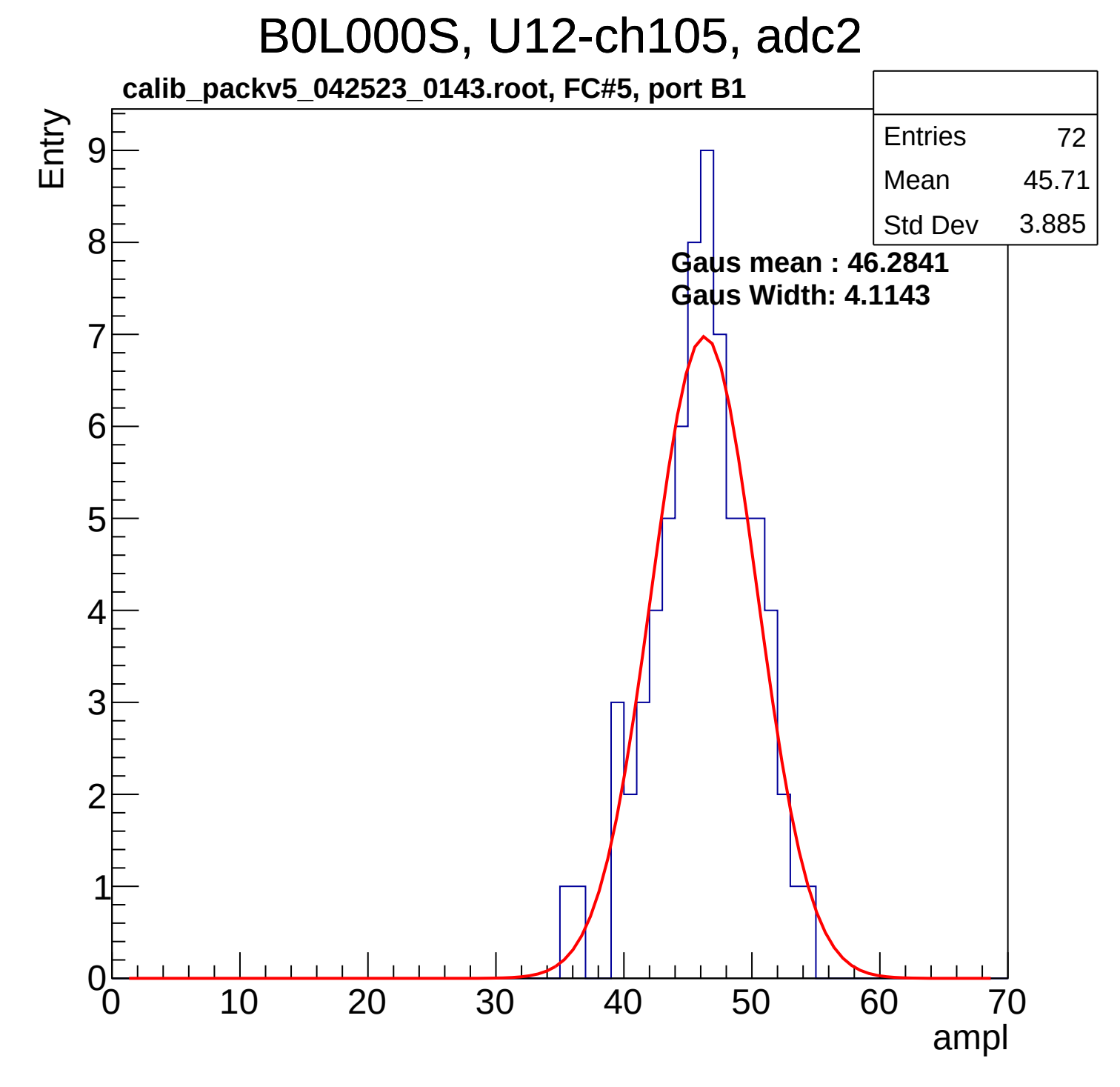
B0L000S, U12-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 45.71
Std Dev 3.885
Gaus mean : 46.2841
Gaus Width: 4.1143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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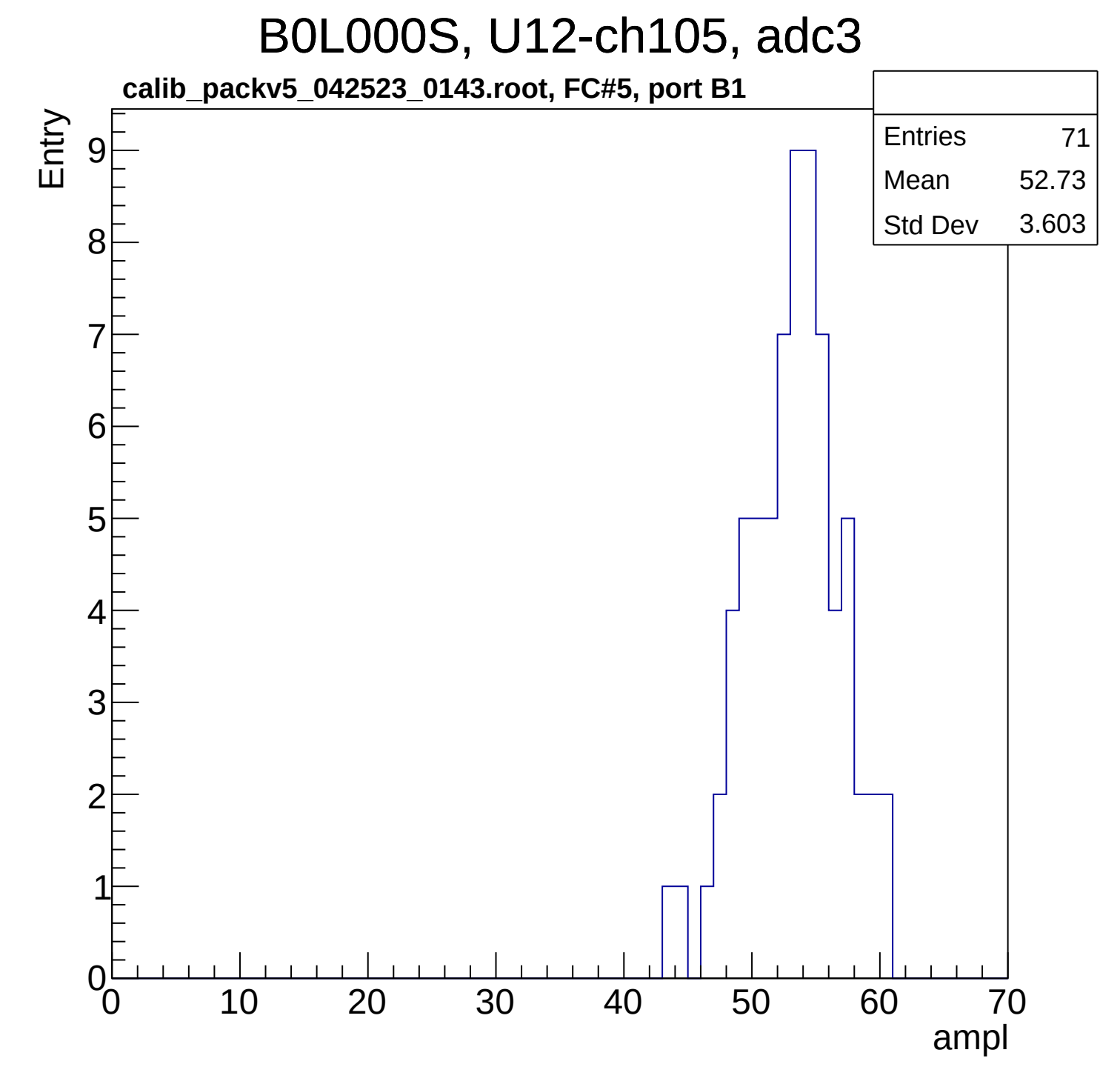
B0L000S, U12-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 52.73
Std Dev 3.603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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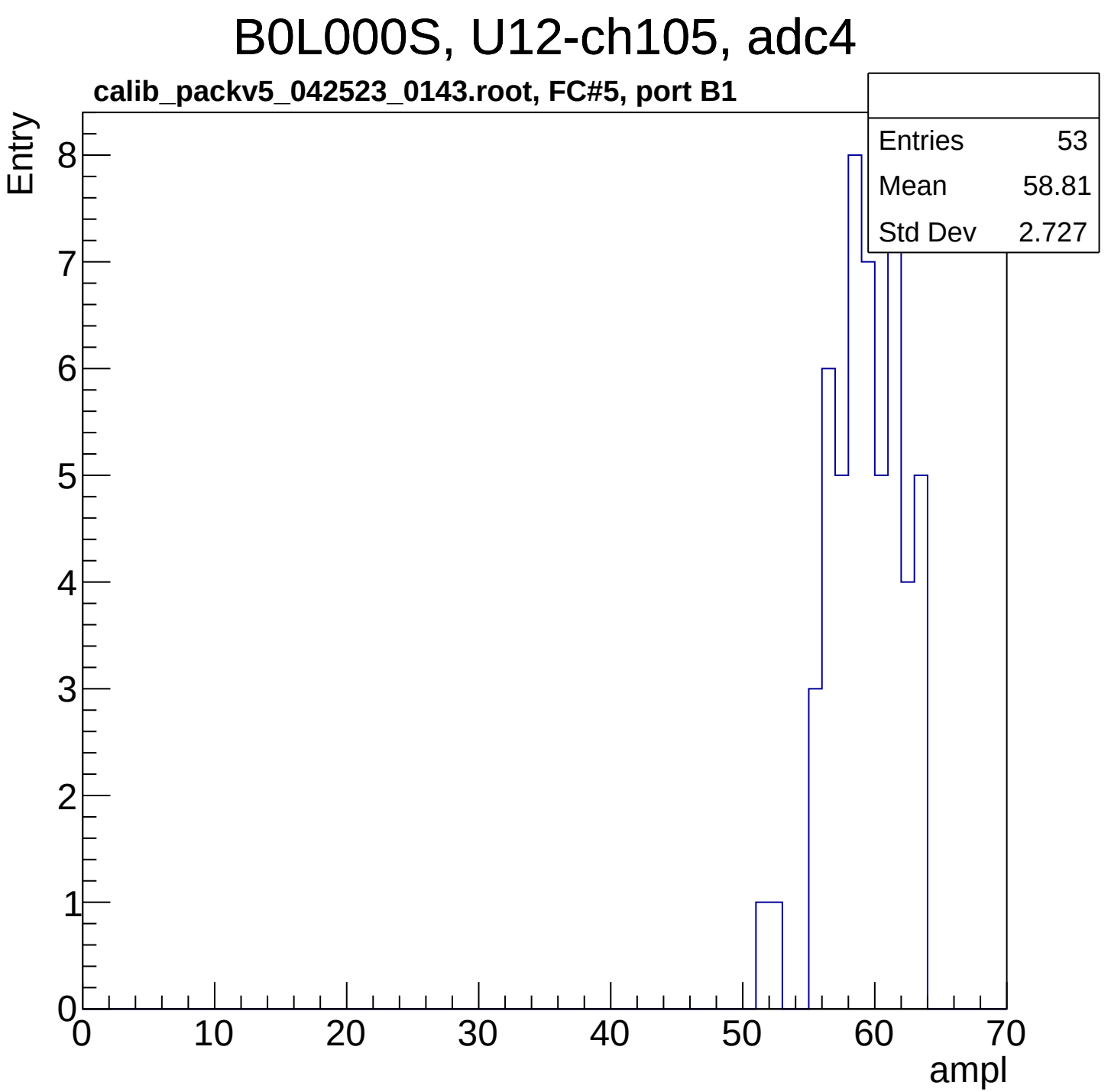
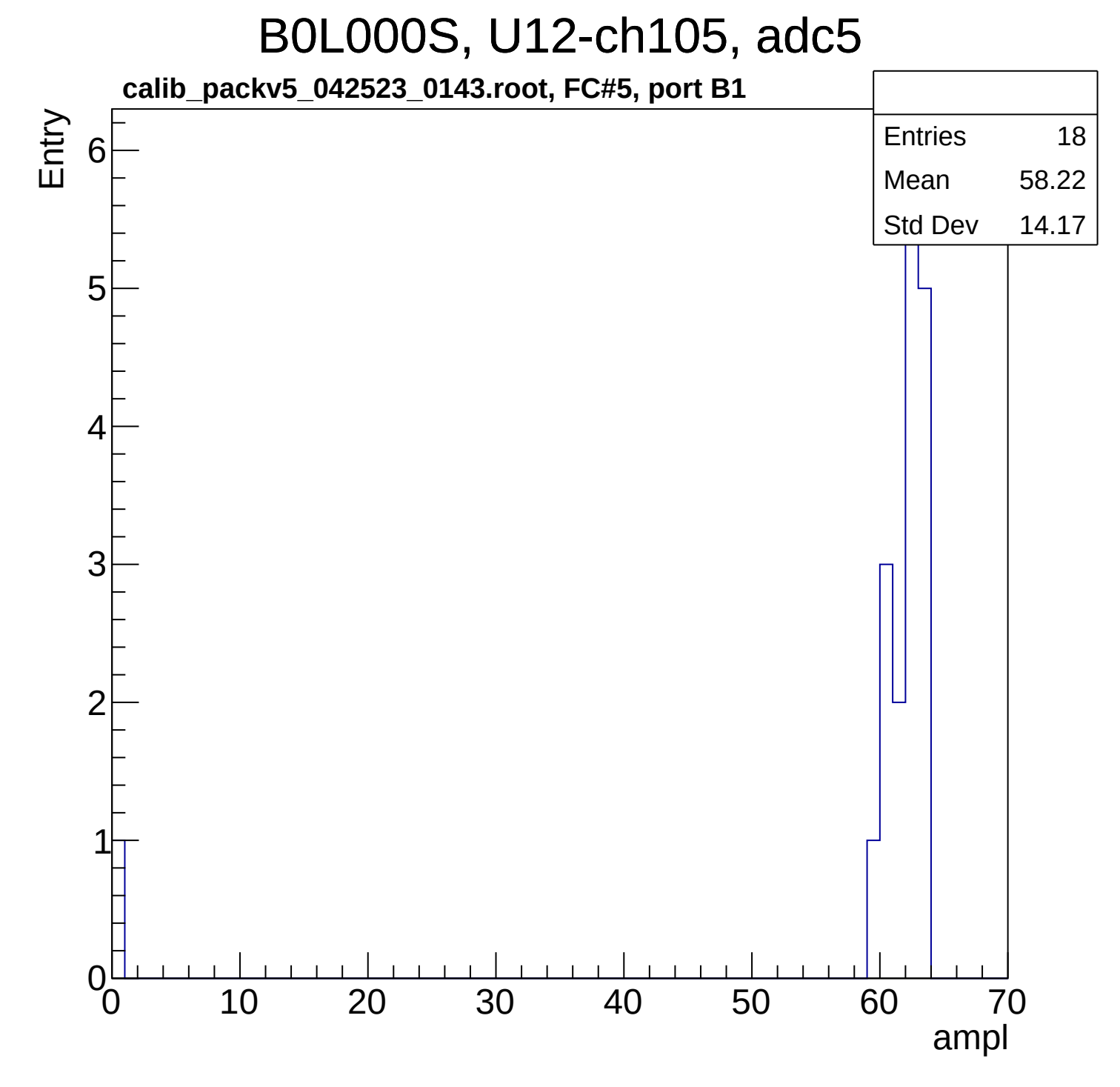
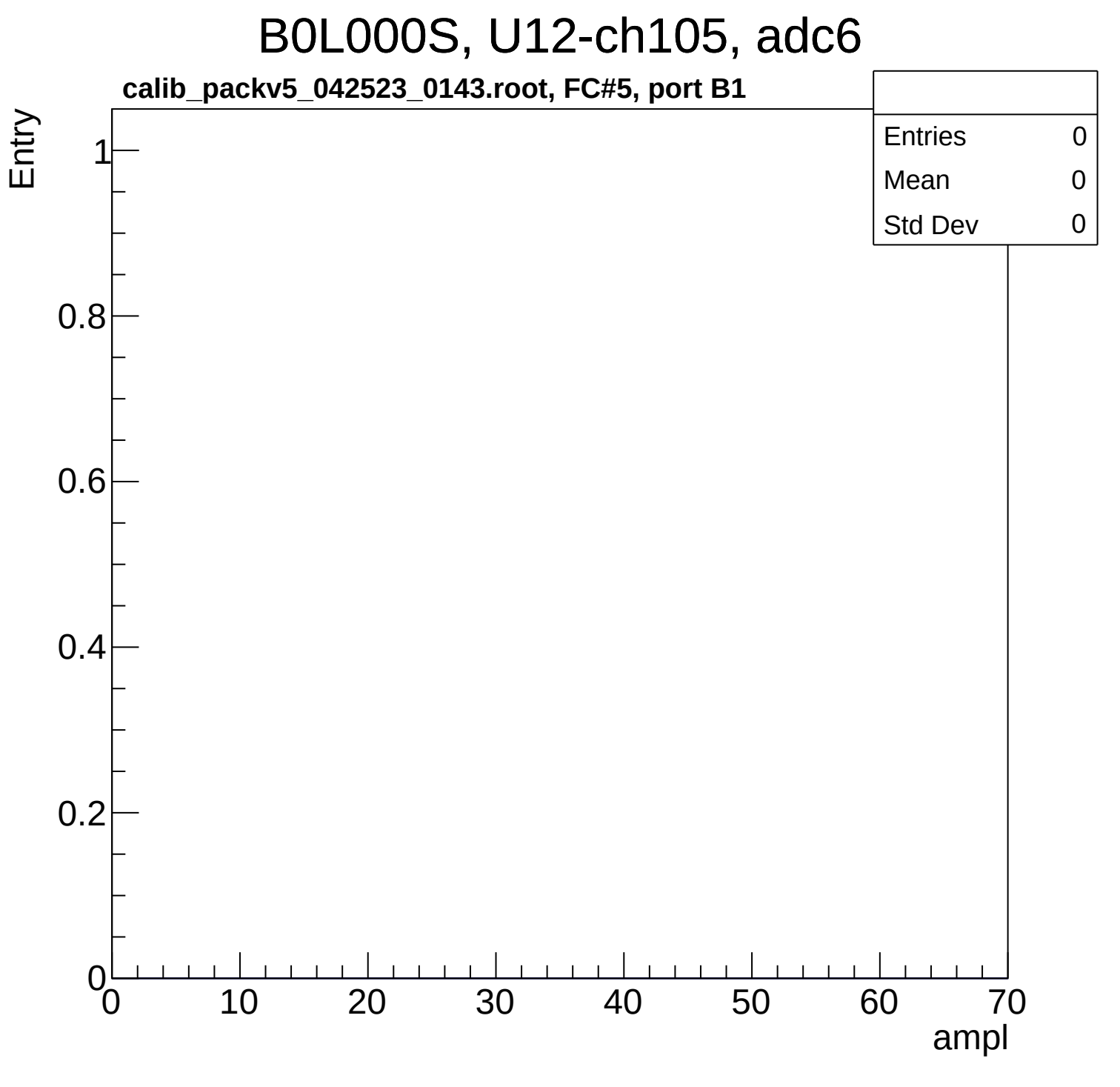
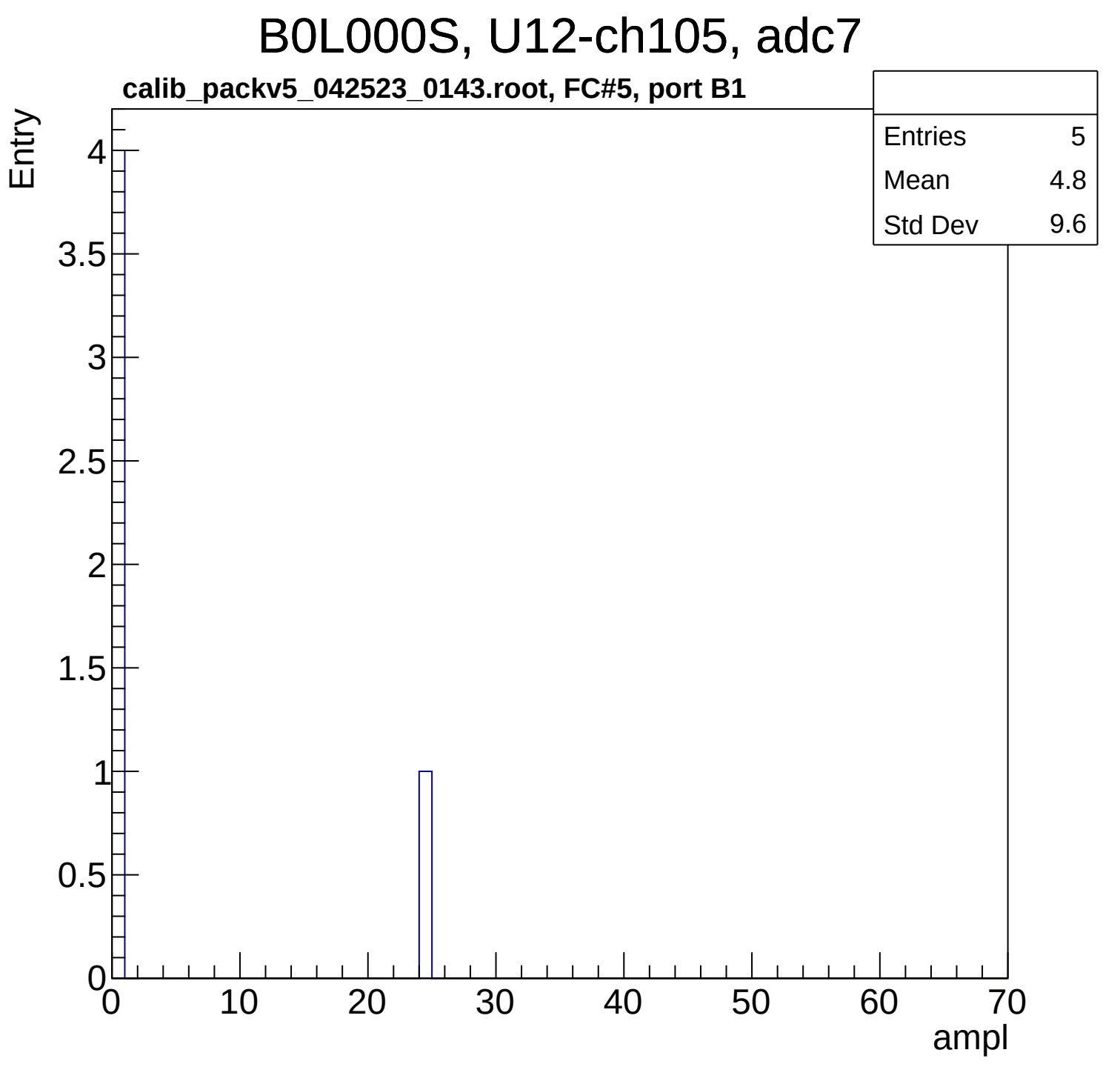
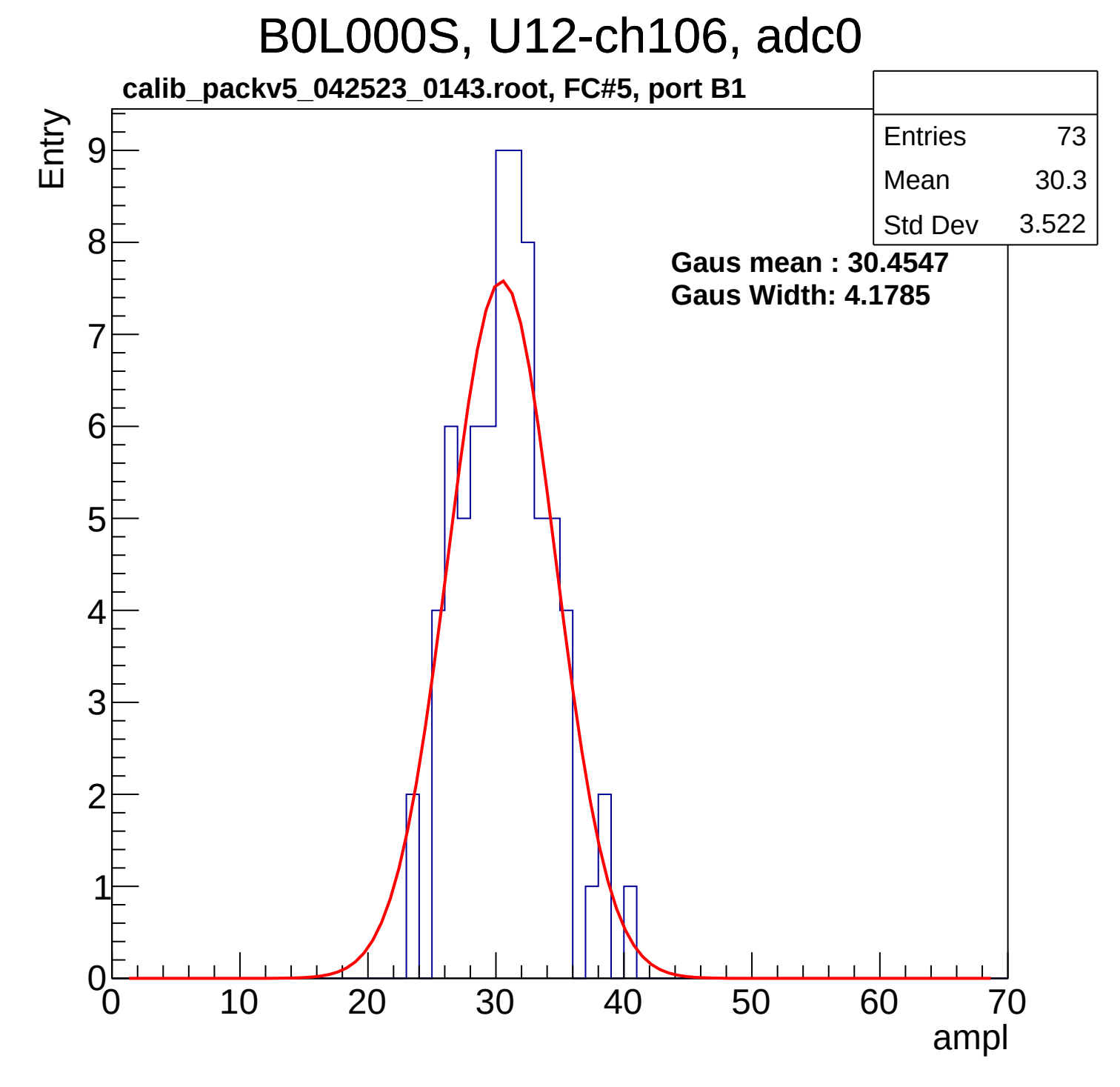
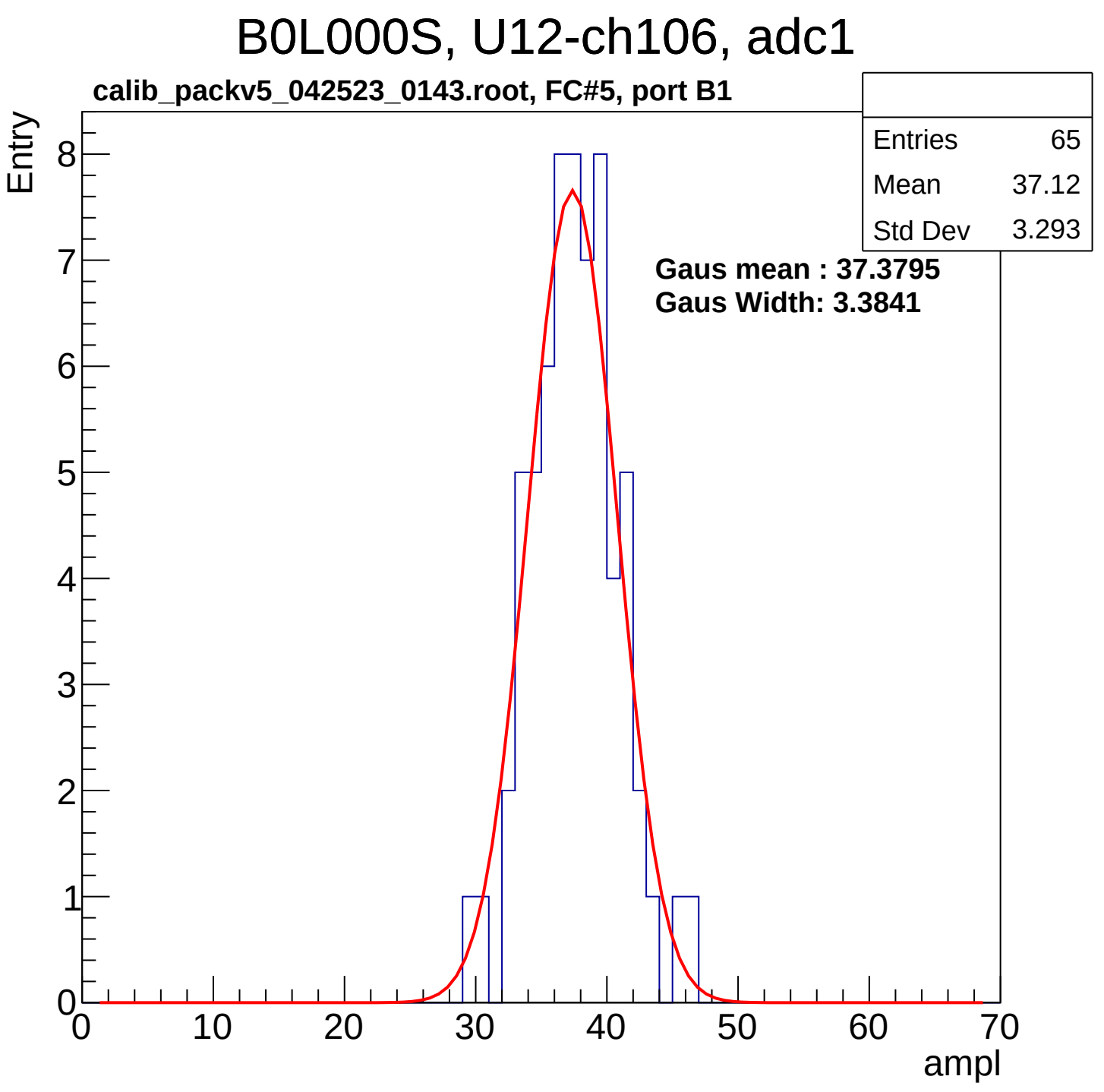

B0L000S, U12-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.81
Std Dev 2.727
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

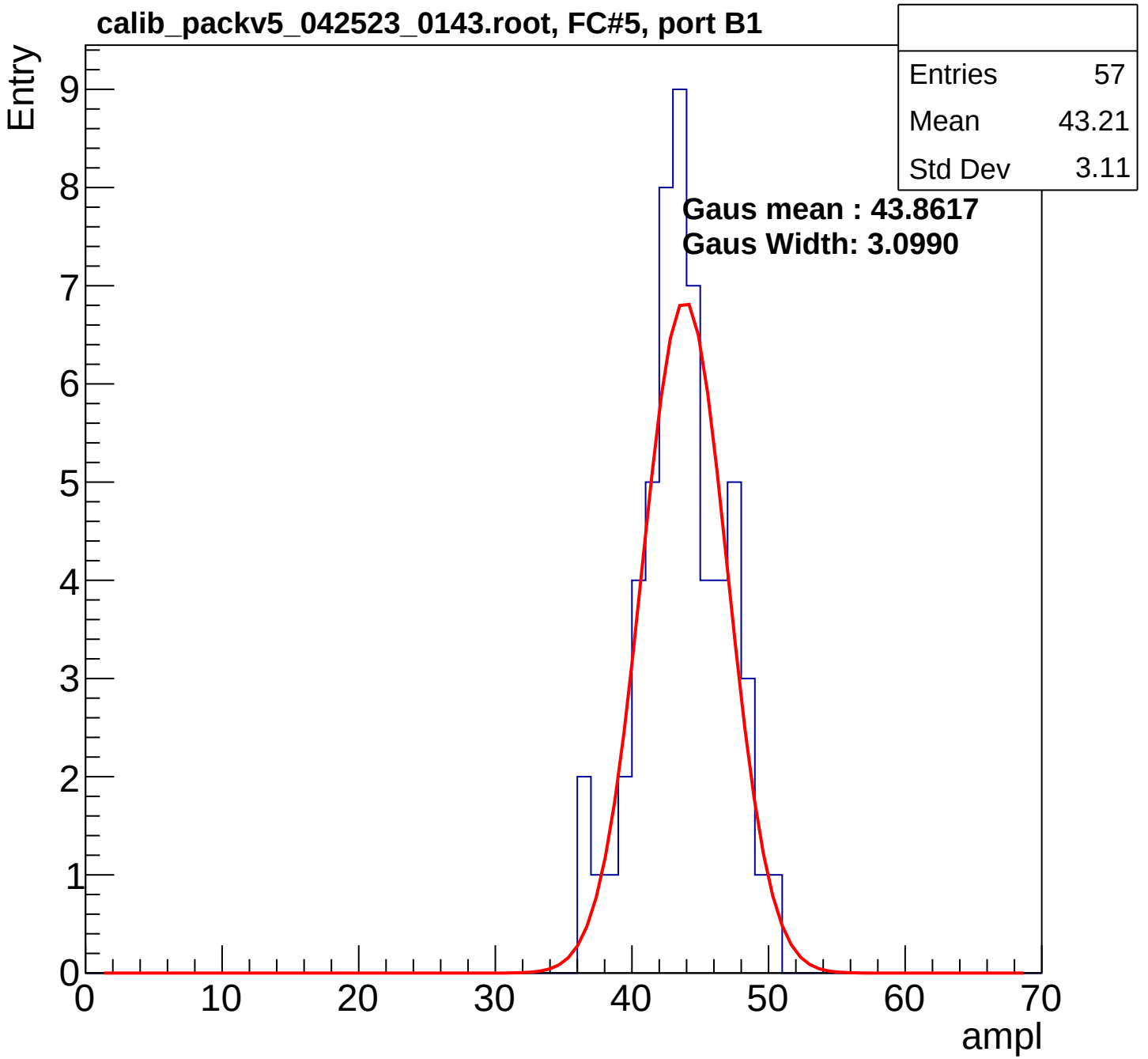

B0L000S, U12-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.22
Std Dev 14.17
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

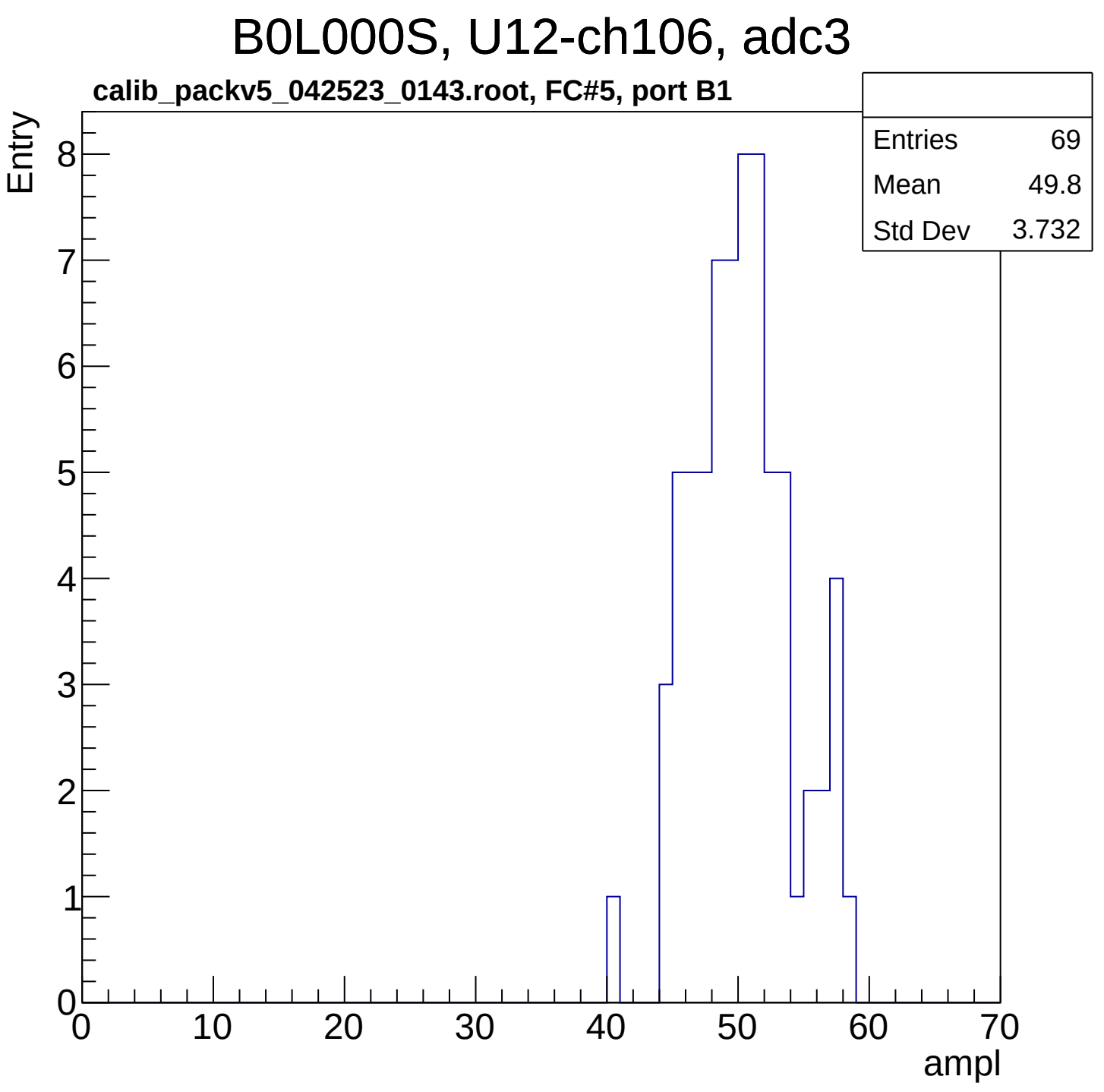

B0L000S, U12-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 4.8
Std Dev 9.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U12-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.3
Std Dev 3.522
Gaus mean : 30.4547
Gaus Width: 4.1785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.12
Std Dev 3.293
Gaus mean : 37.3795
Gaus Width: 3.3841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.21
Std Dev 3.11
Gaus mean : 43.8617
Gaus Width: 3.0990
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U12-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.8
Std Dev 3.732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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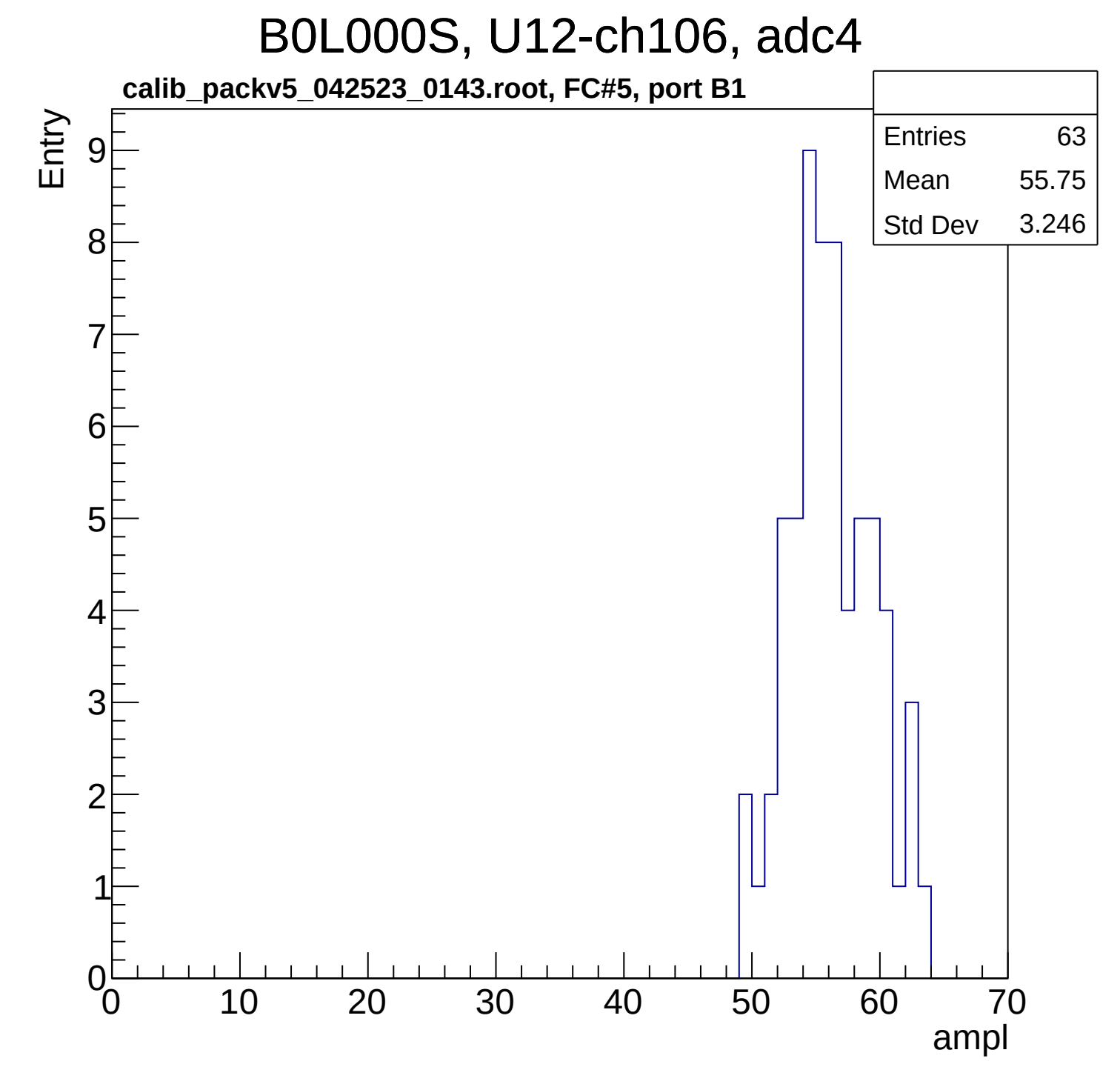
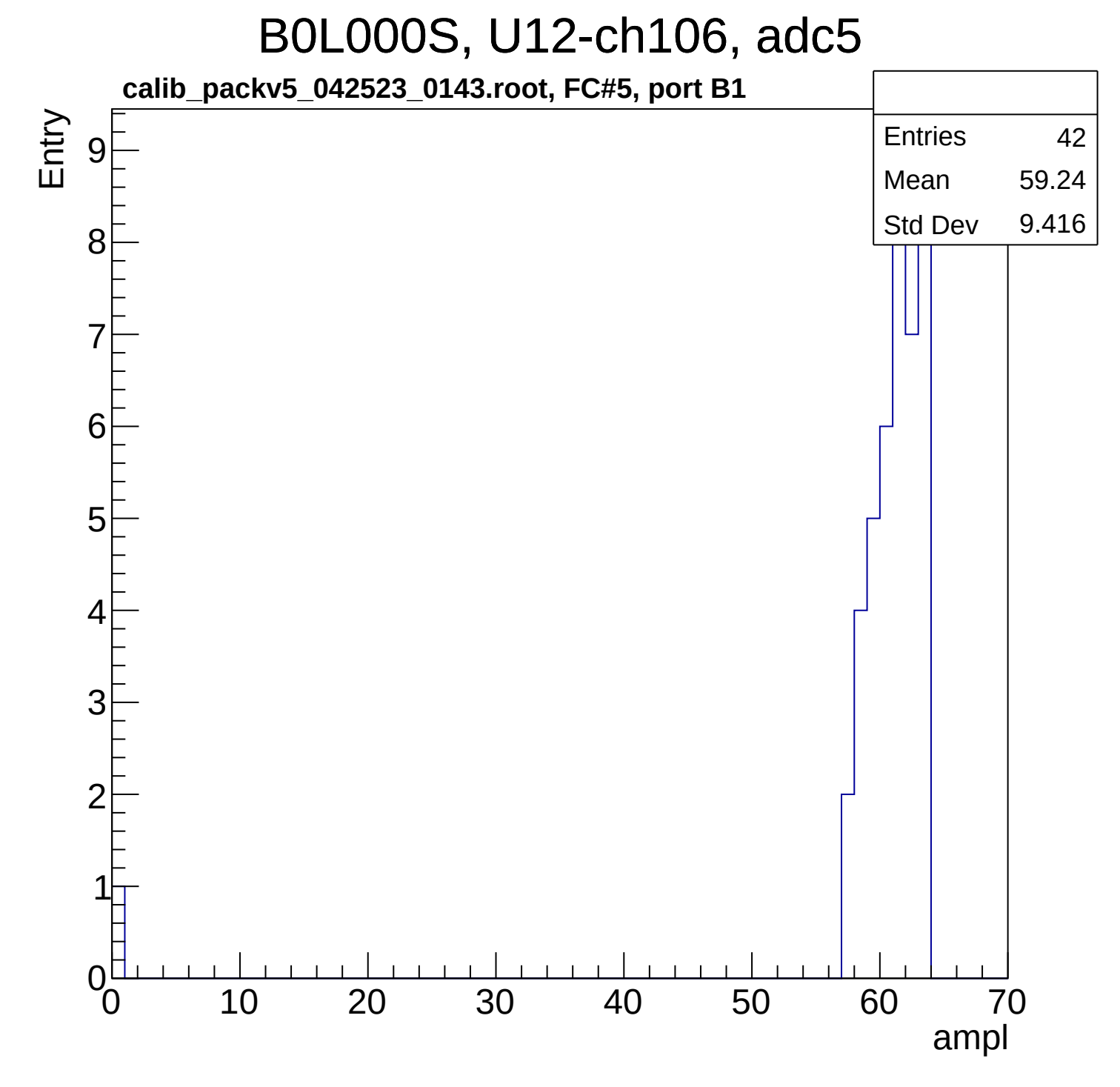
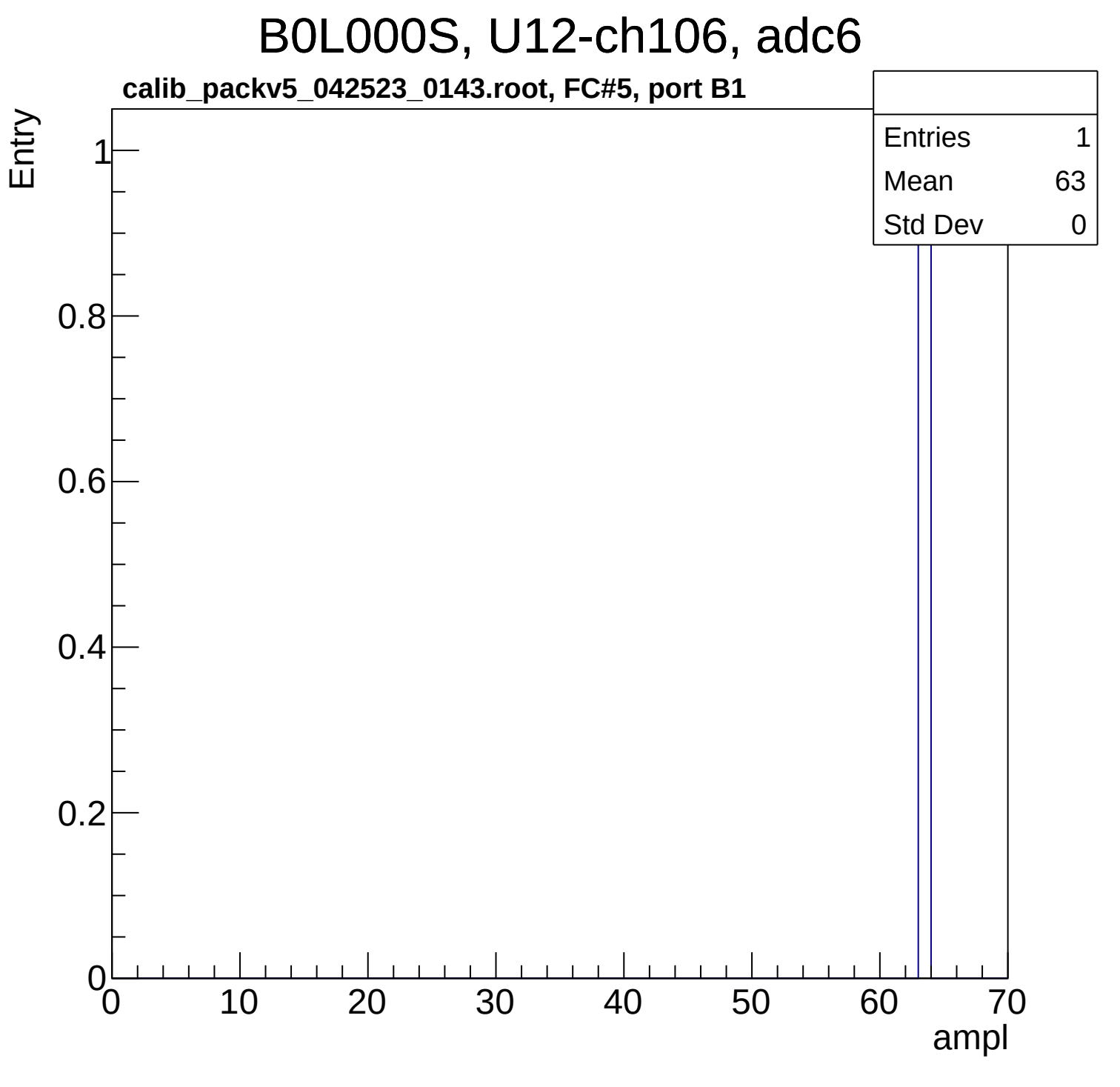
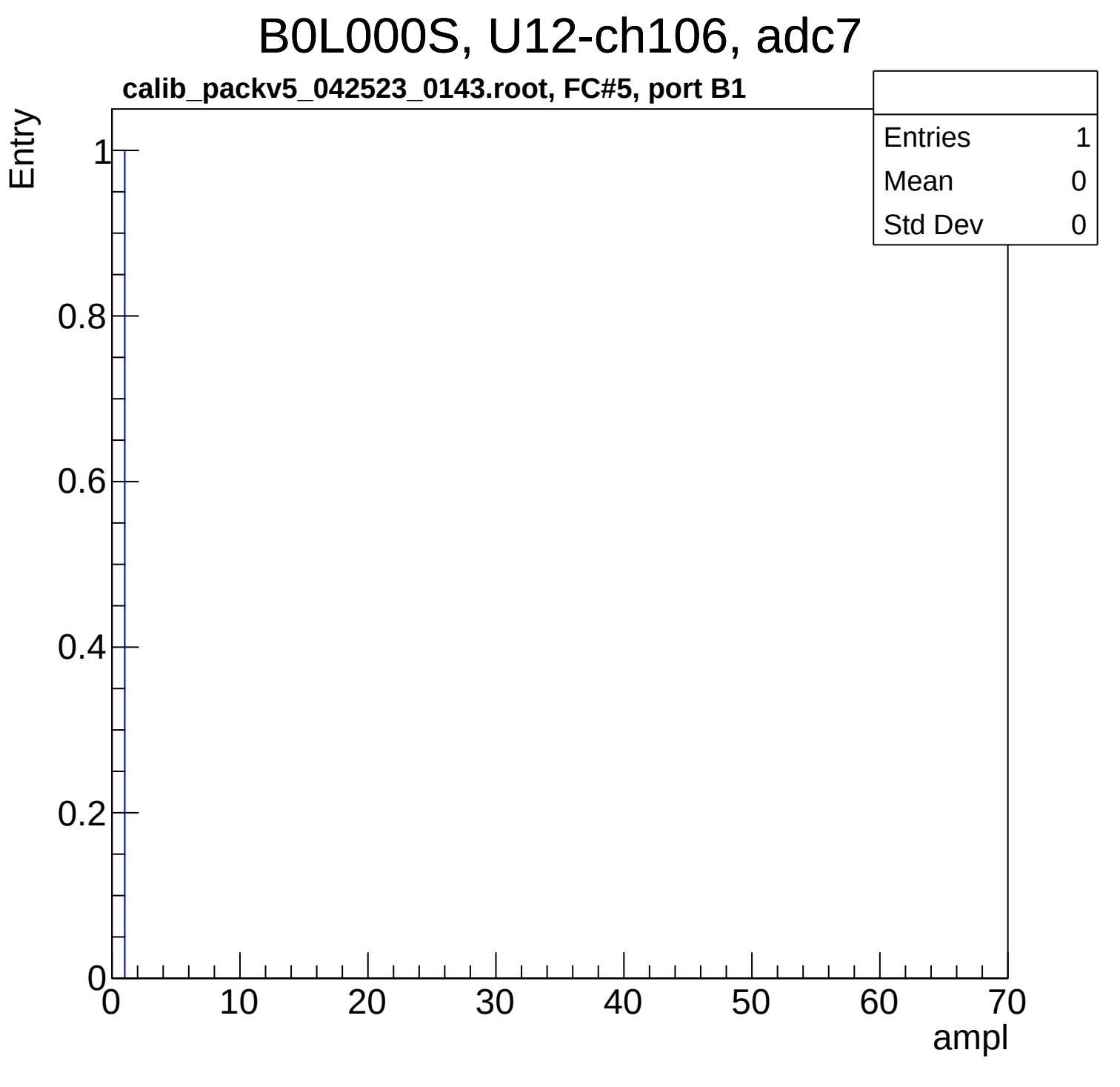
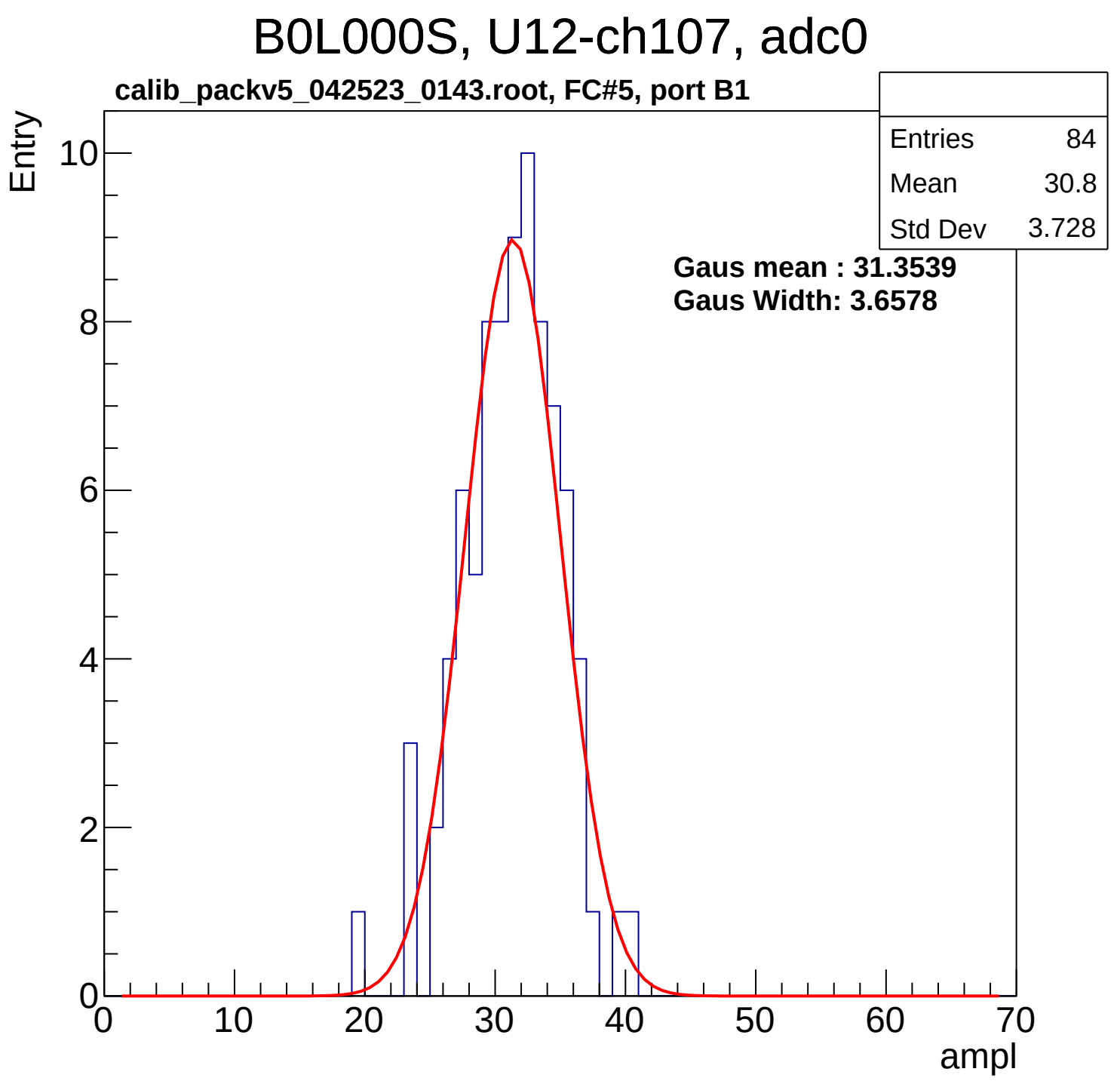
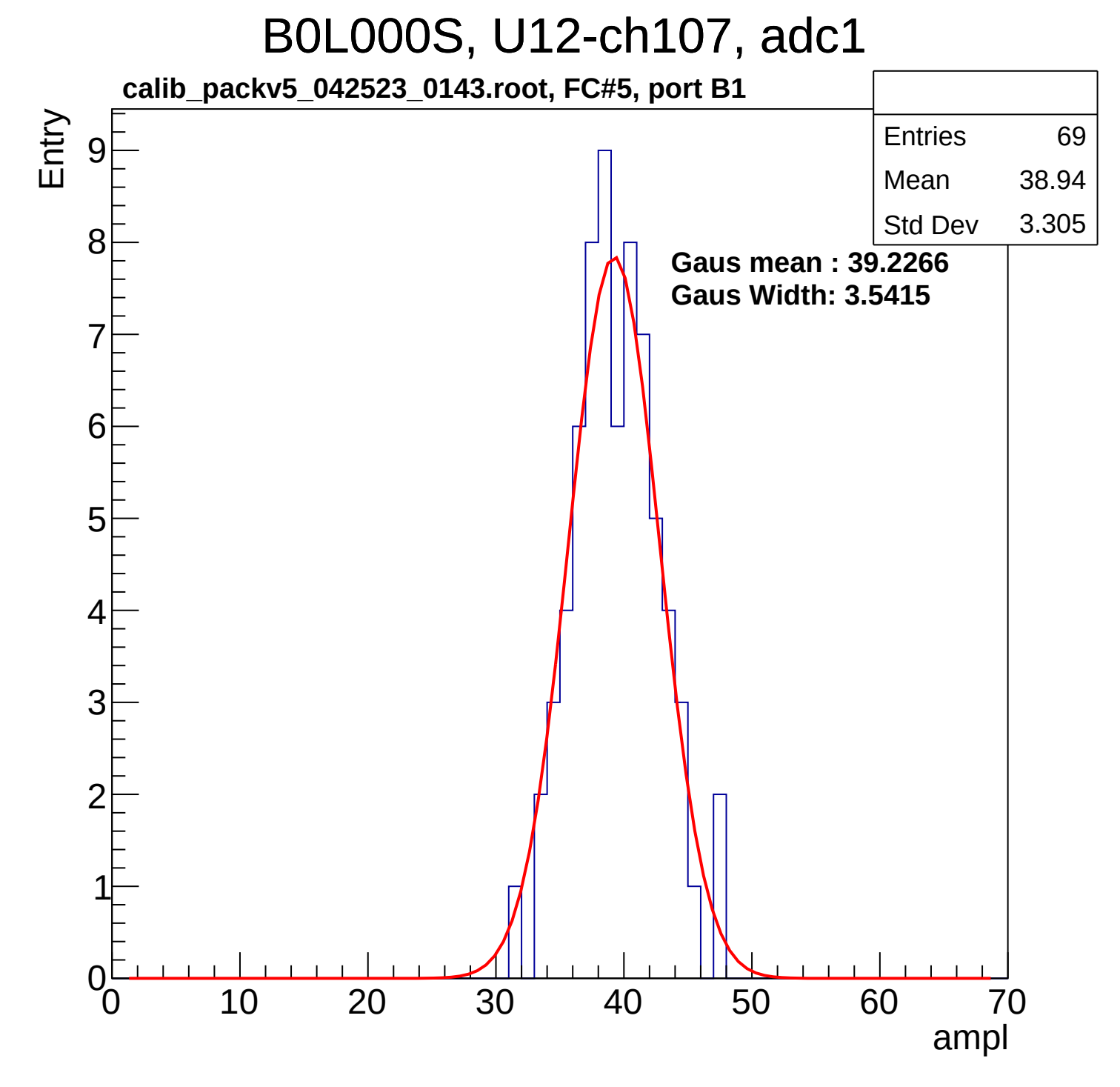

B0L000S, U12-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 55.75
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

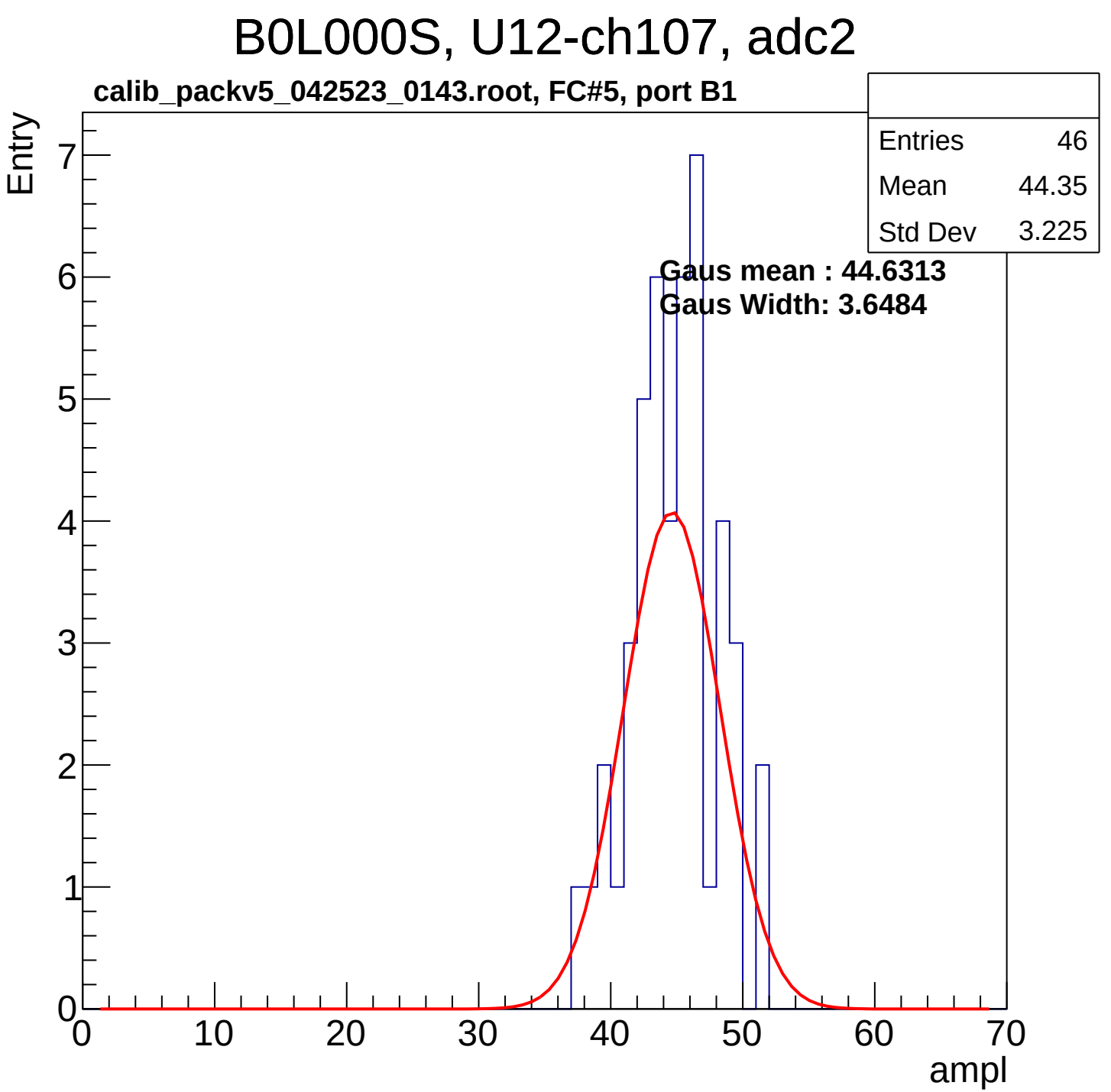

B0L000S, U12-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.24
Std Dev 9.416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 30.8
Std Dev 3.728
Gaus mean : 31.3539
Gaus Width: 3.6578
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 38.94
Std Dev 3.305
Gaus mean : 39.2266
Gaus Width: 3.5415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 44.35
Std Dev 3.225
Gaus mean : 44.6313
Gaus Width: 3.6484
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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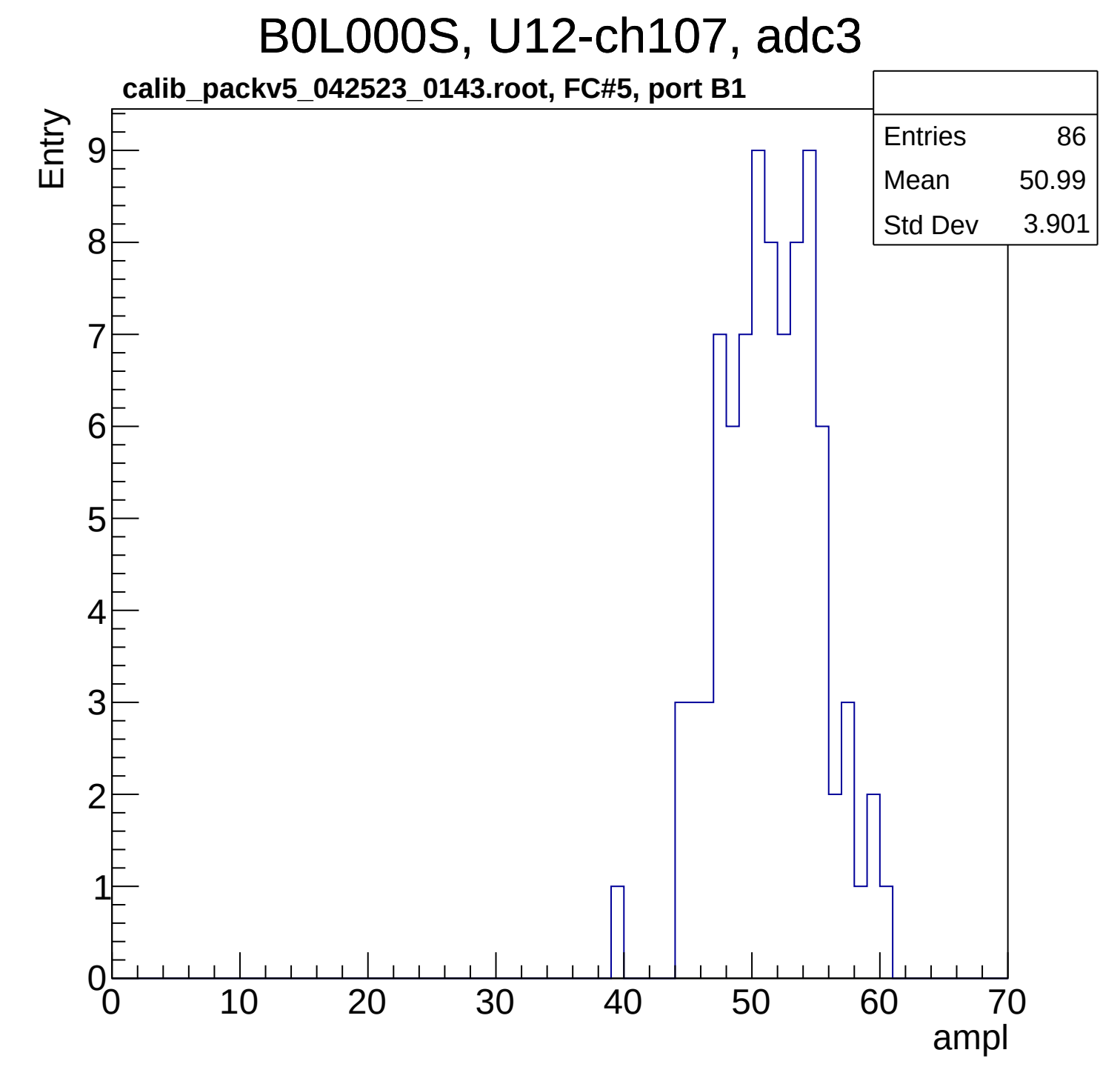
B0L000S, U12-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 50.99
Std Dev 3.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
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50
60
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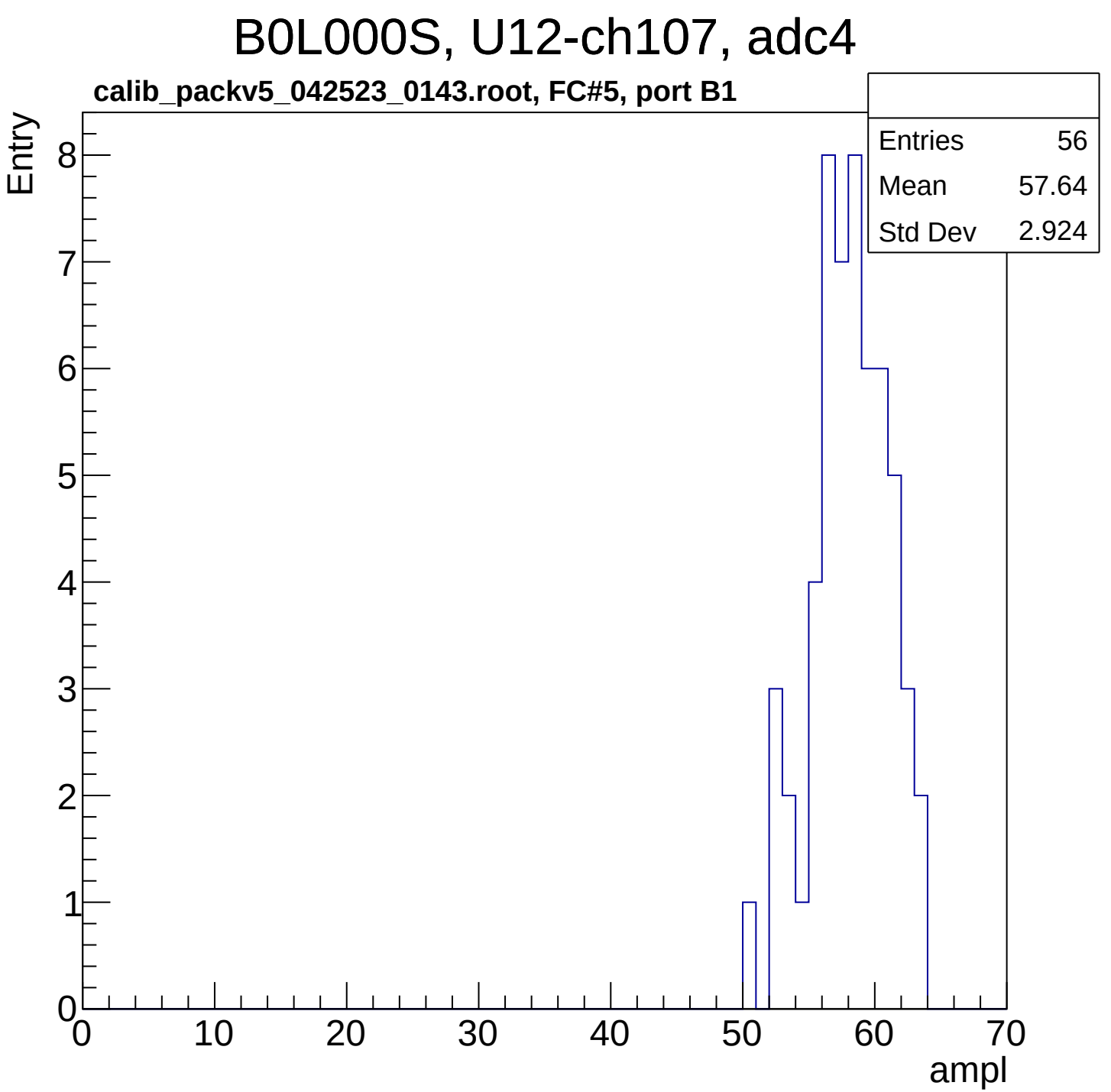
B0L000S, U12-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.64
Std Dev 2.924
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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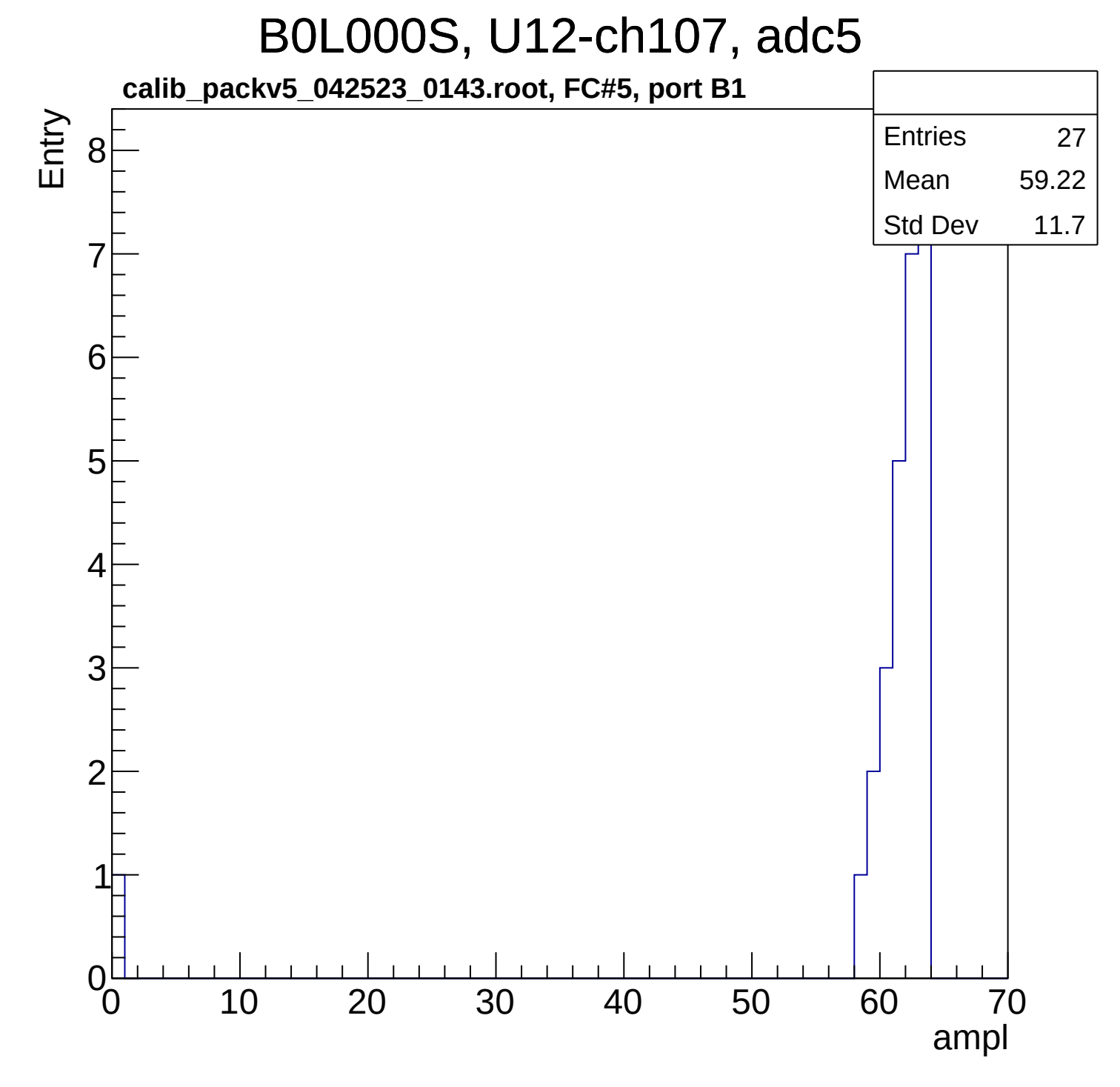
B0L000S, U12-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 59.22
Std Dev 11.7
Entry
ampl
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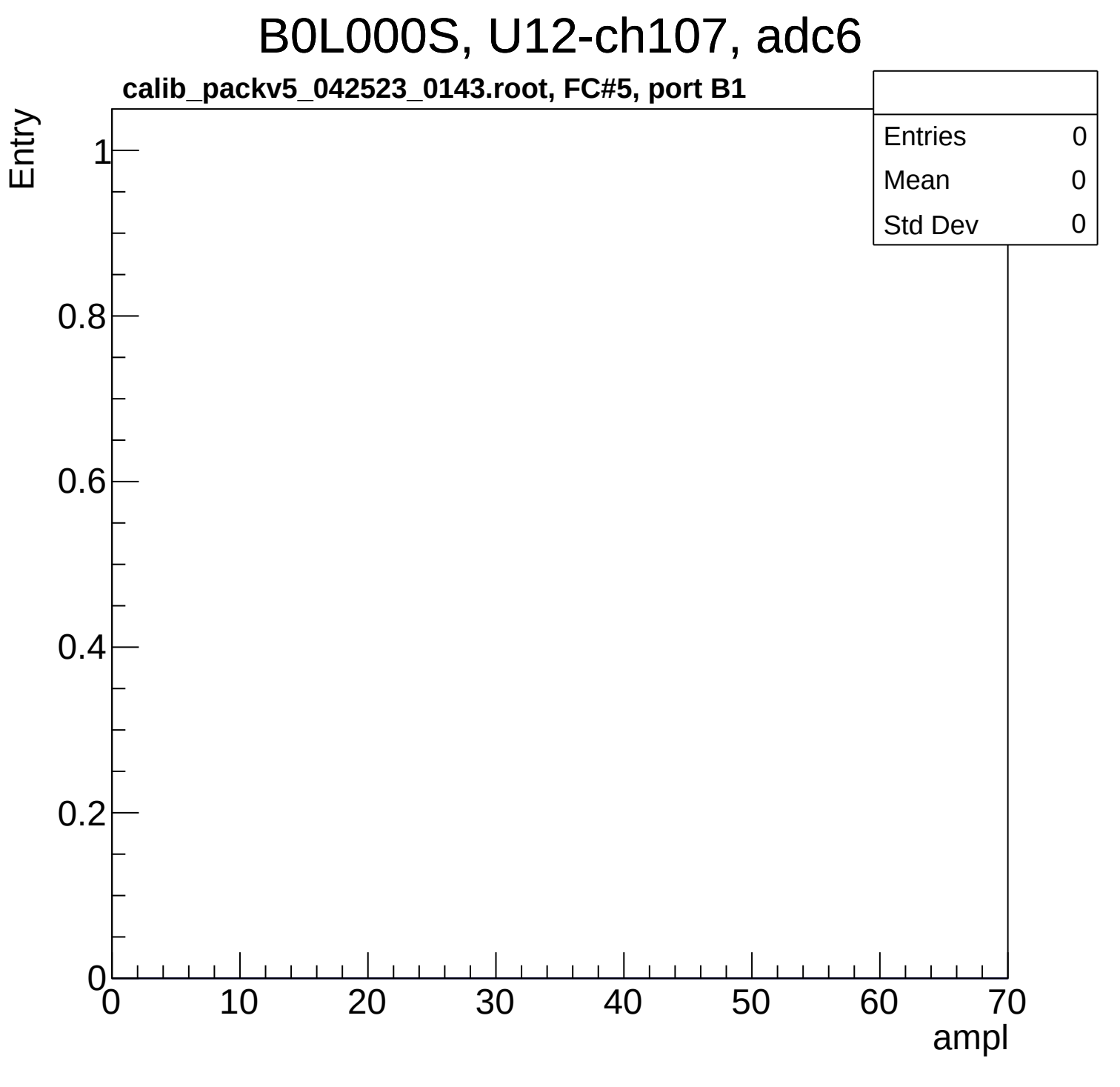
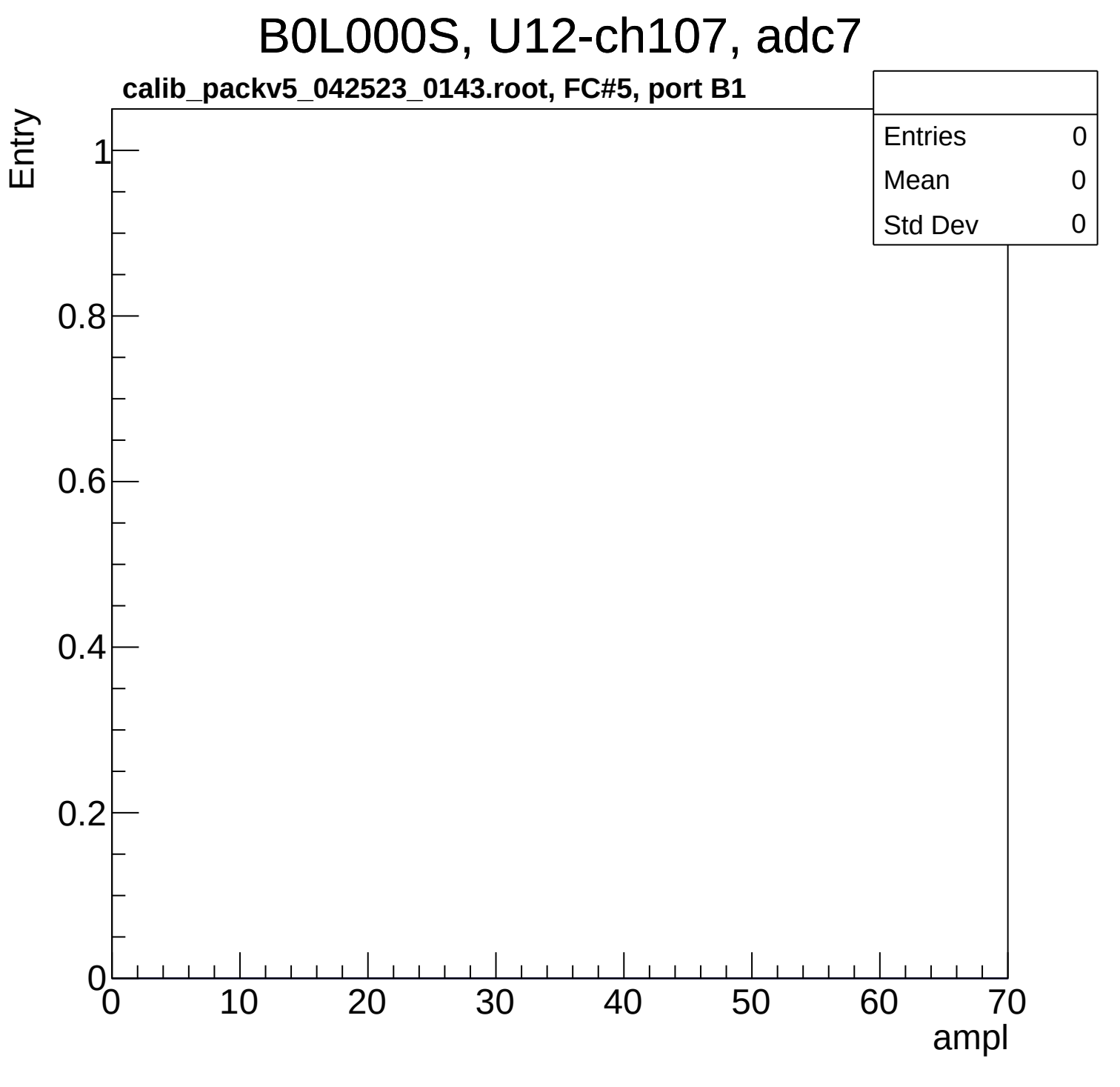
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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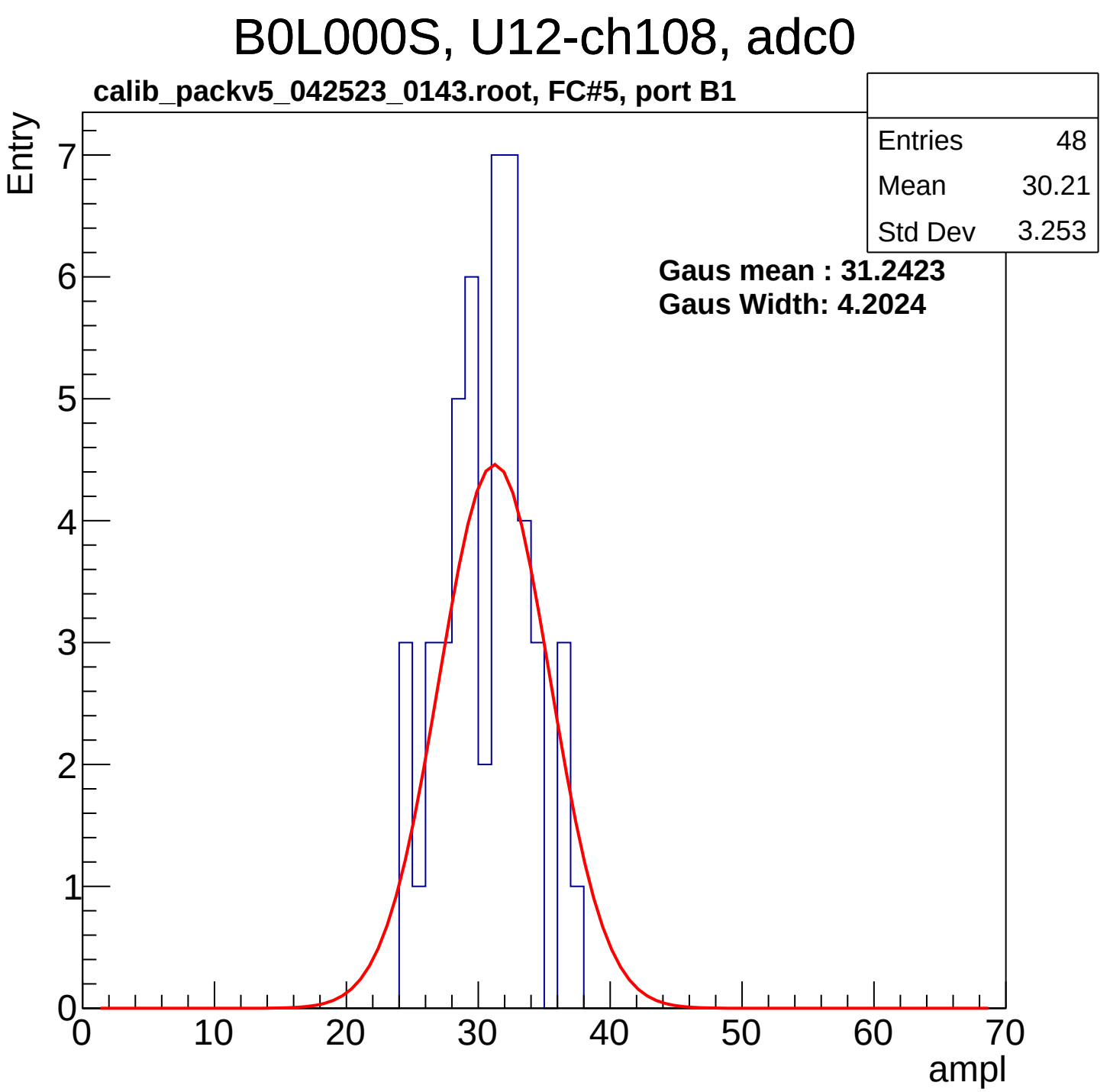
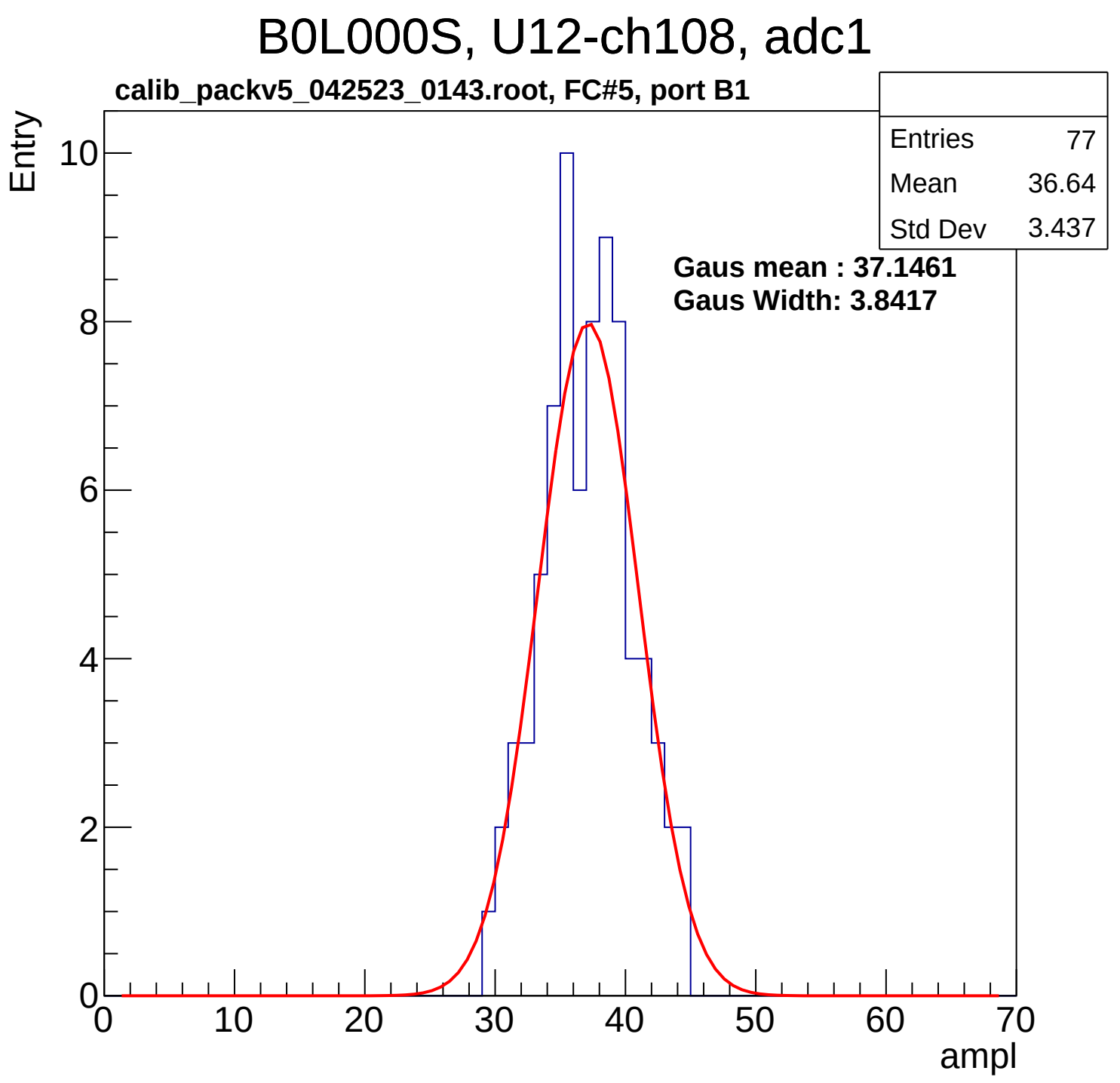
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 30.21
Std Dev 3.253
Gaus mean : 31.2423
Gaus Width: 4.2024
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.64
Std Dev 3.437
Gaus mean : 37.1461
Gaus Width: 3.8417
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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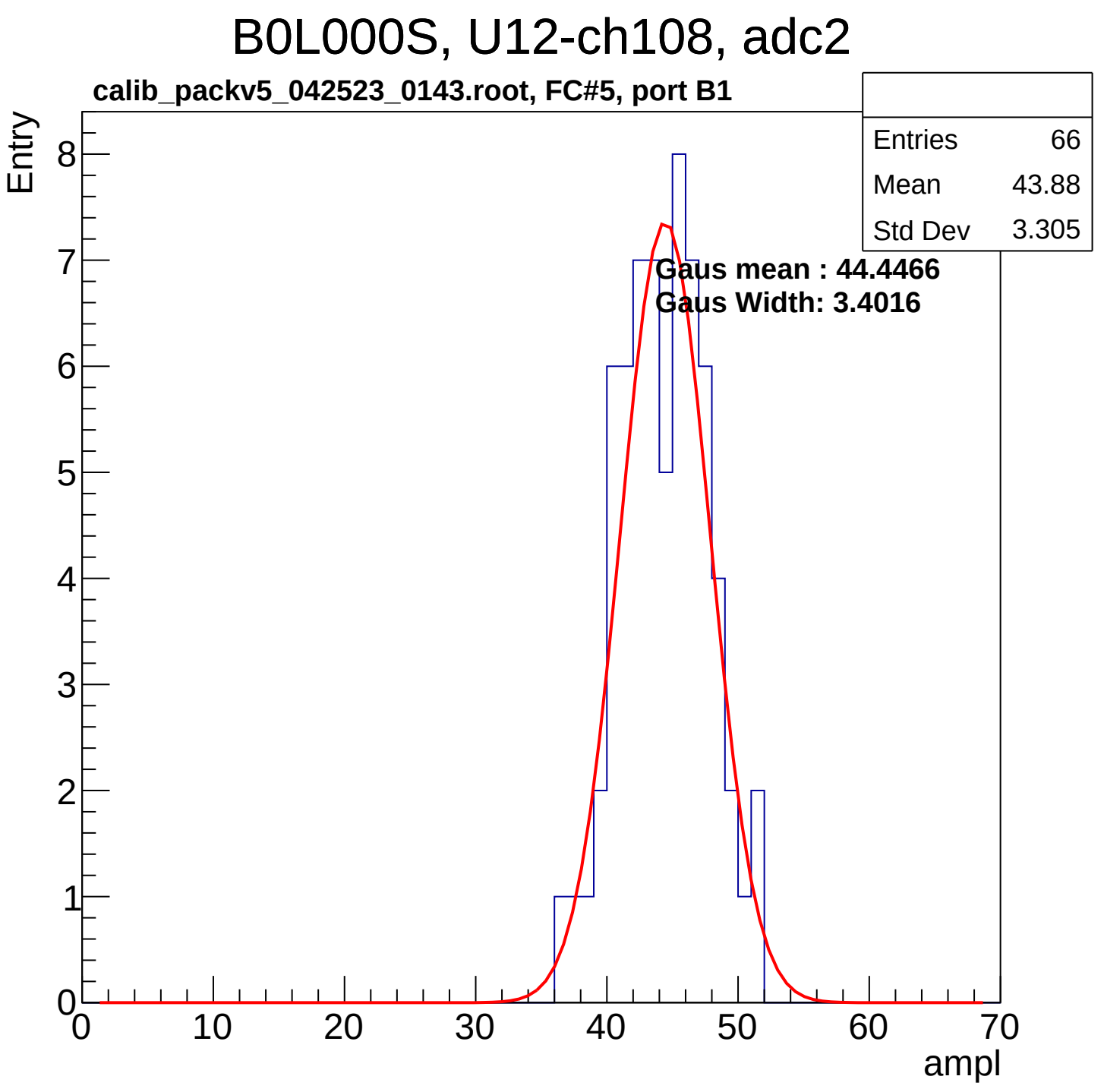
B0L000S, U12-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.88
Std Dev 3.305
Gaus mean : 44.4466
Gaus Width: 3.4016
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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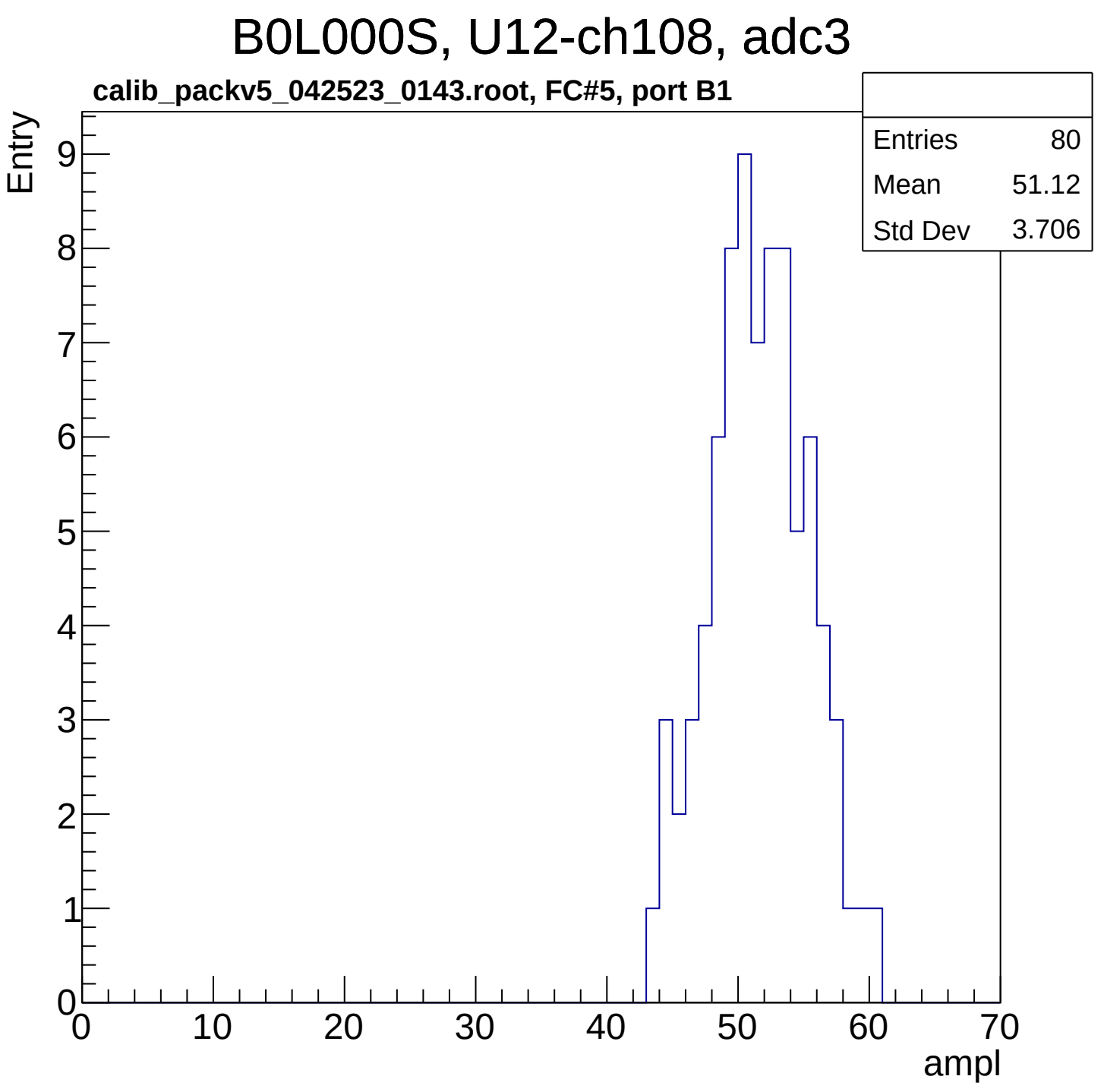
B0L000S, U12-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 51.12
Std Dev 3.706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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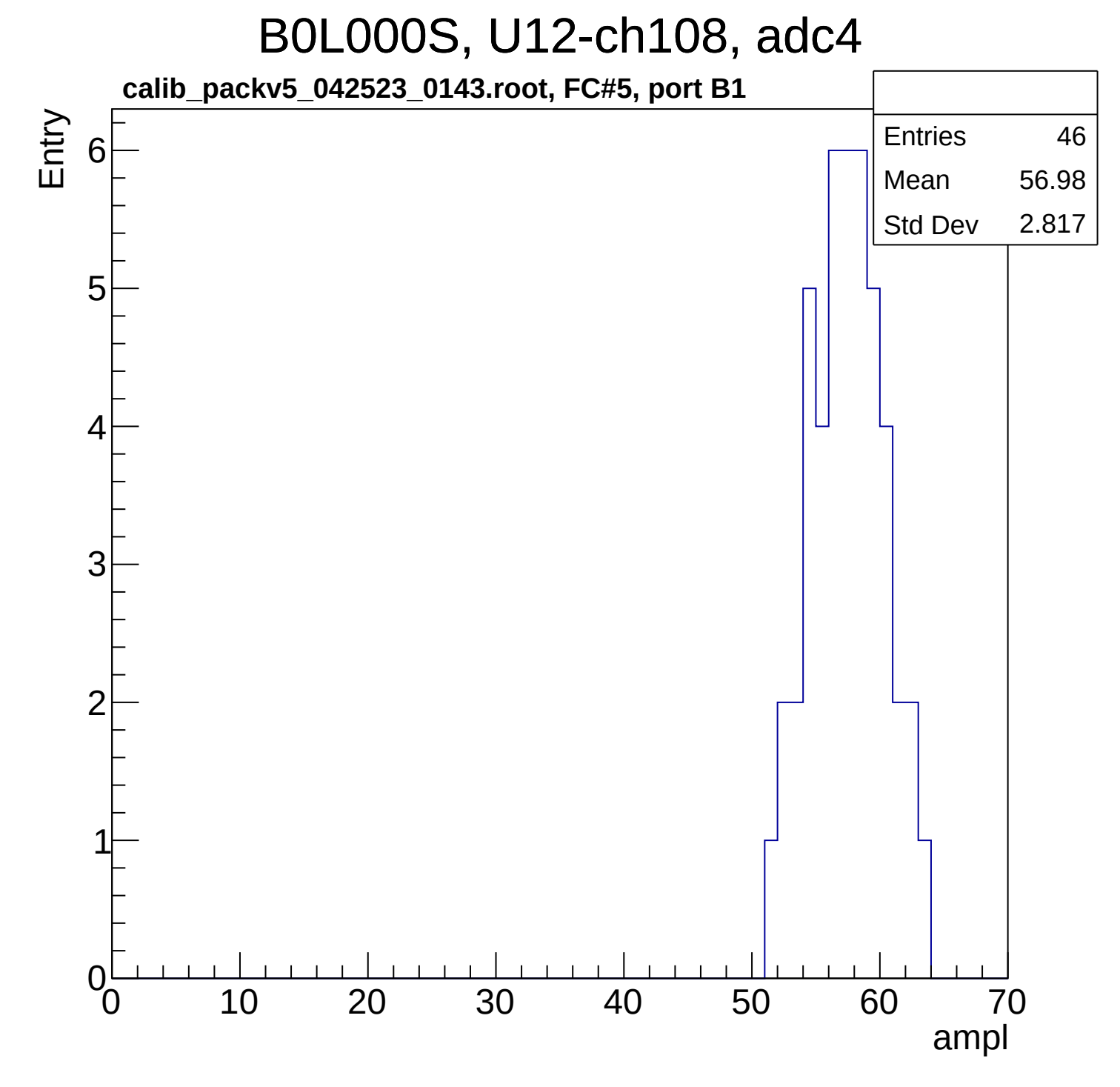
B0L000S, U12-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.98
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
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0
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60
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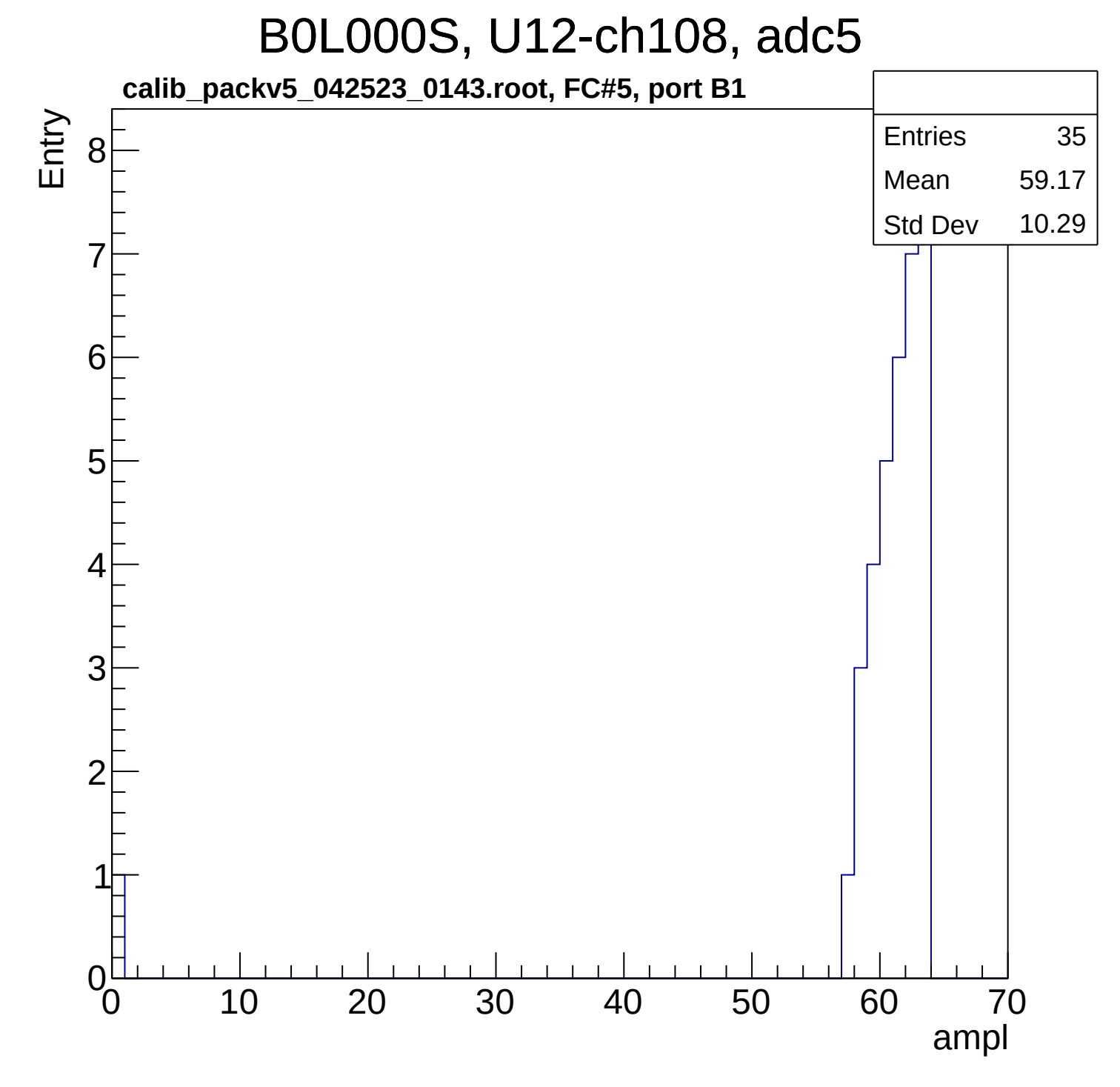
B0L000S, U12-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.17
Std Dev 10.29
Entry
ampl
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1
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3
4
5
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7
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0
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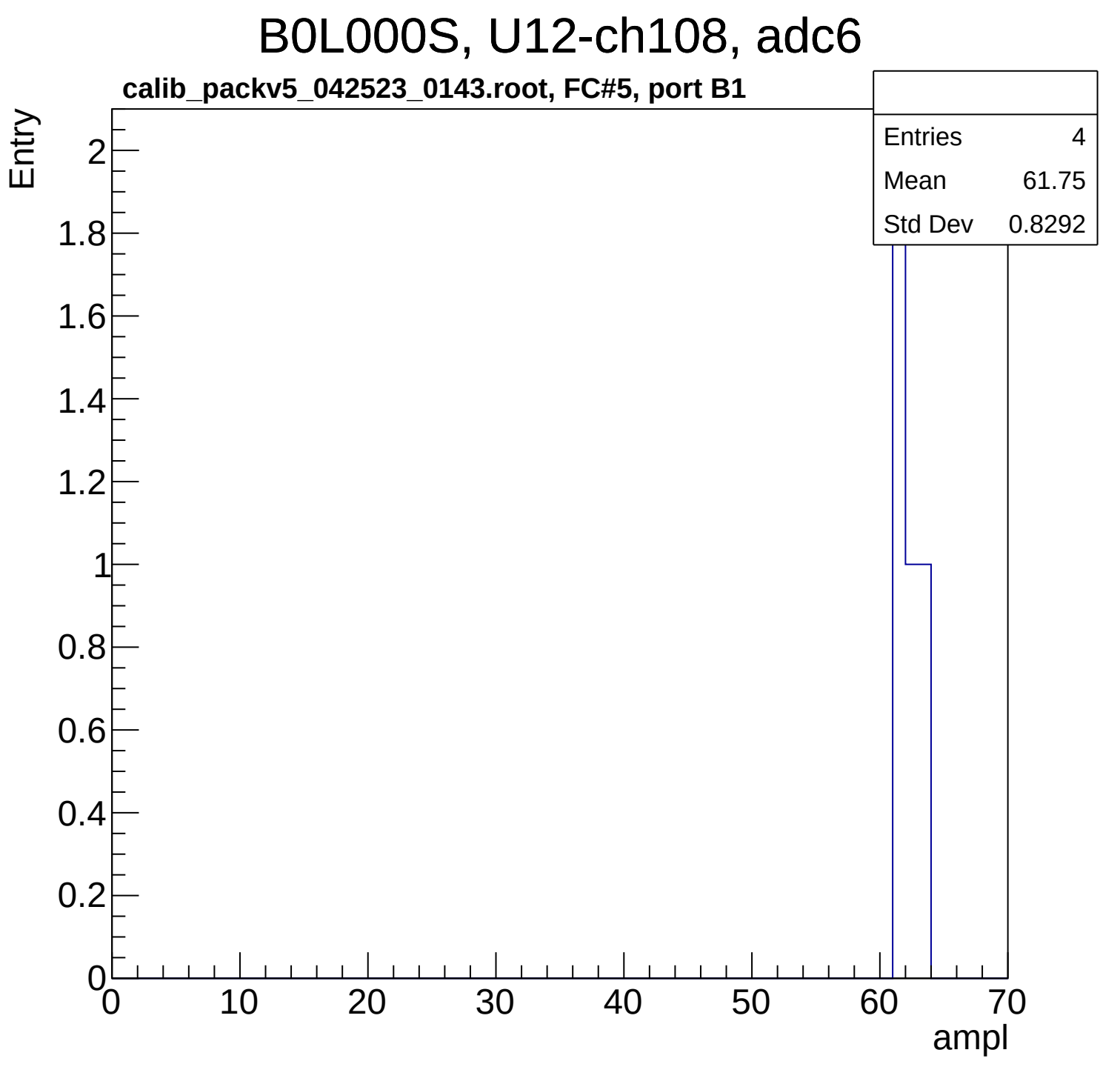
B0L000S, U12-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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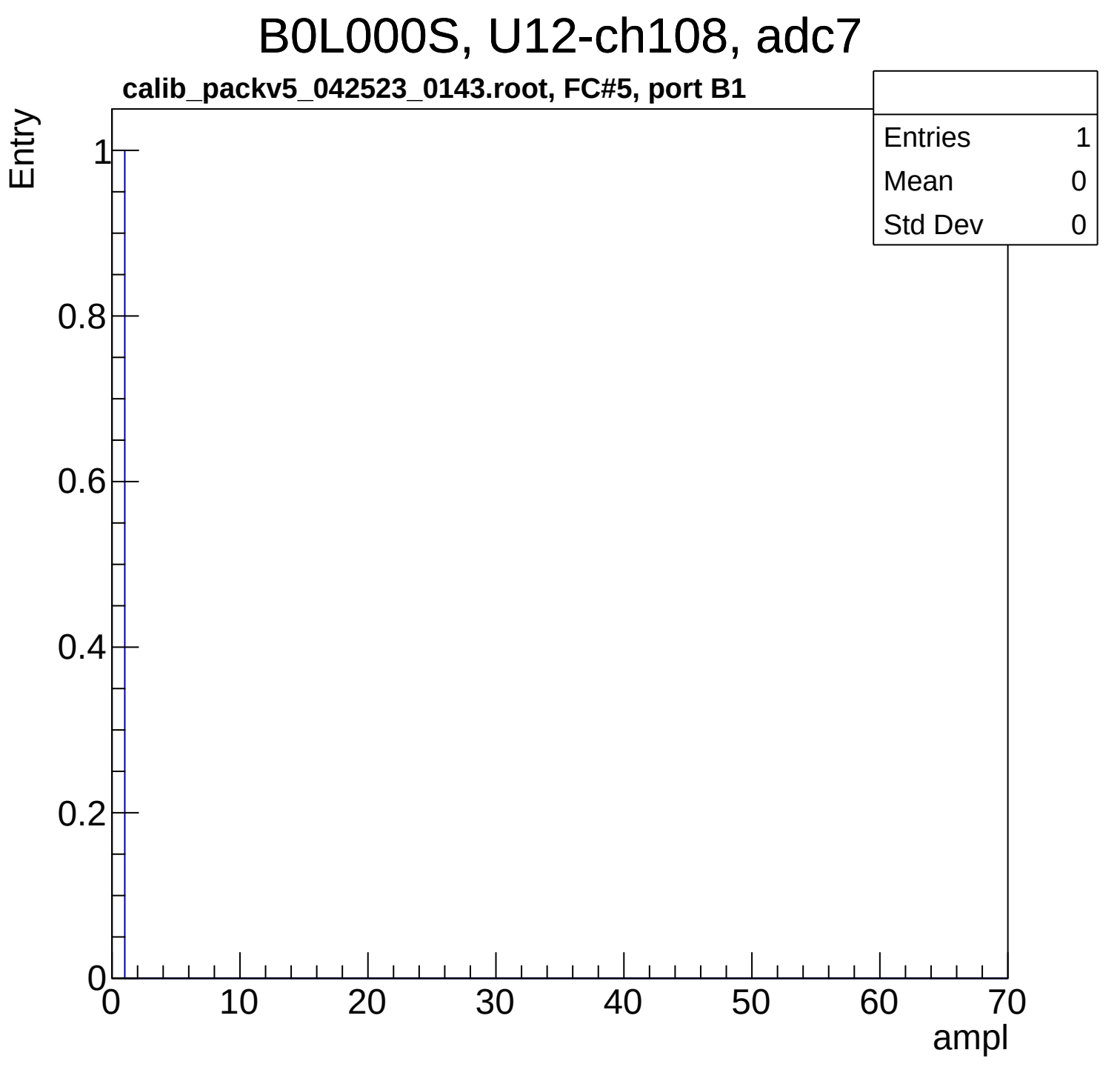
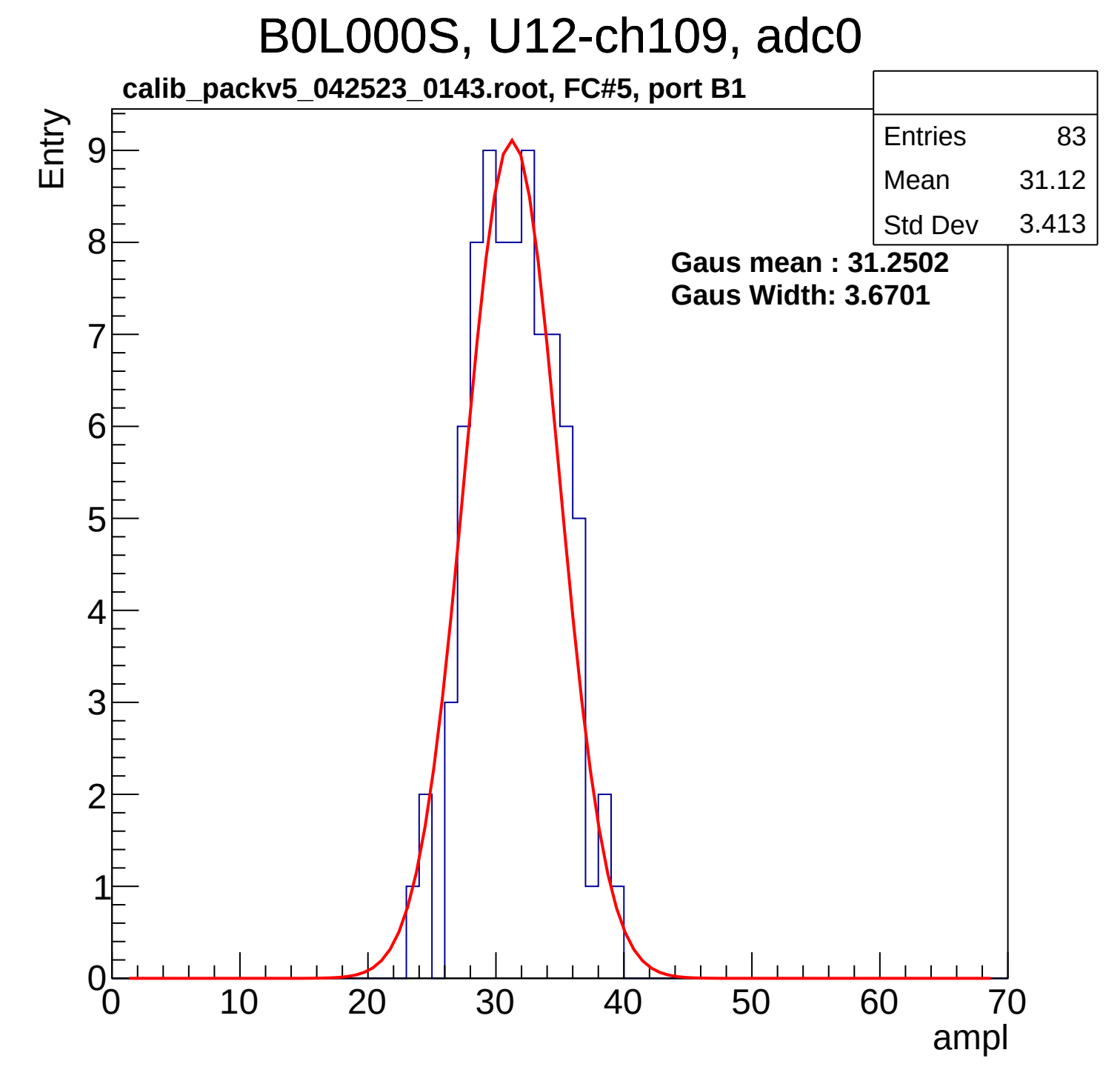

B0L000S, U12-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 31.12
Std Dev 3.413
Gaus mean : 31.2502
Gaus Width: 3.6701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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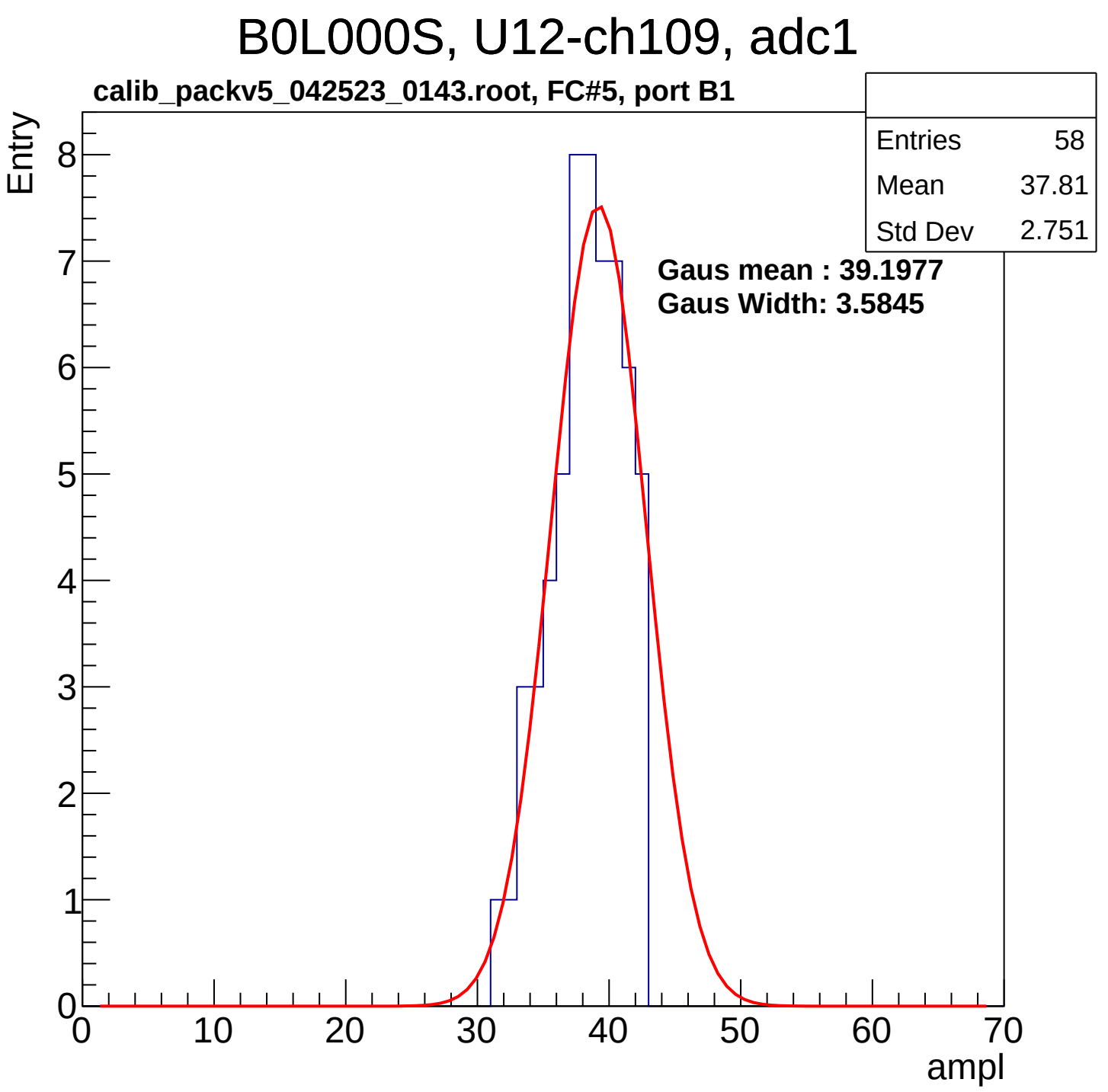
B0L000S, U12-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 37.81
Std Dev 2.751
Gaus mean : 39.1977
Gaus Width: 3.5845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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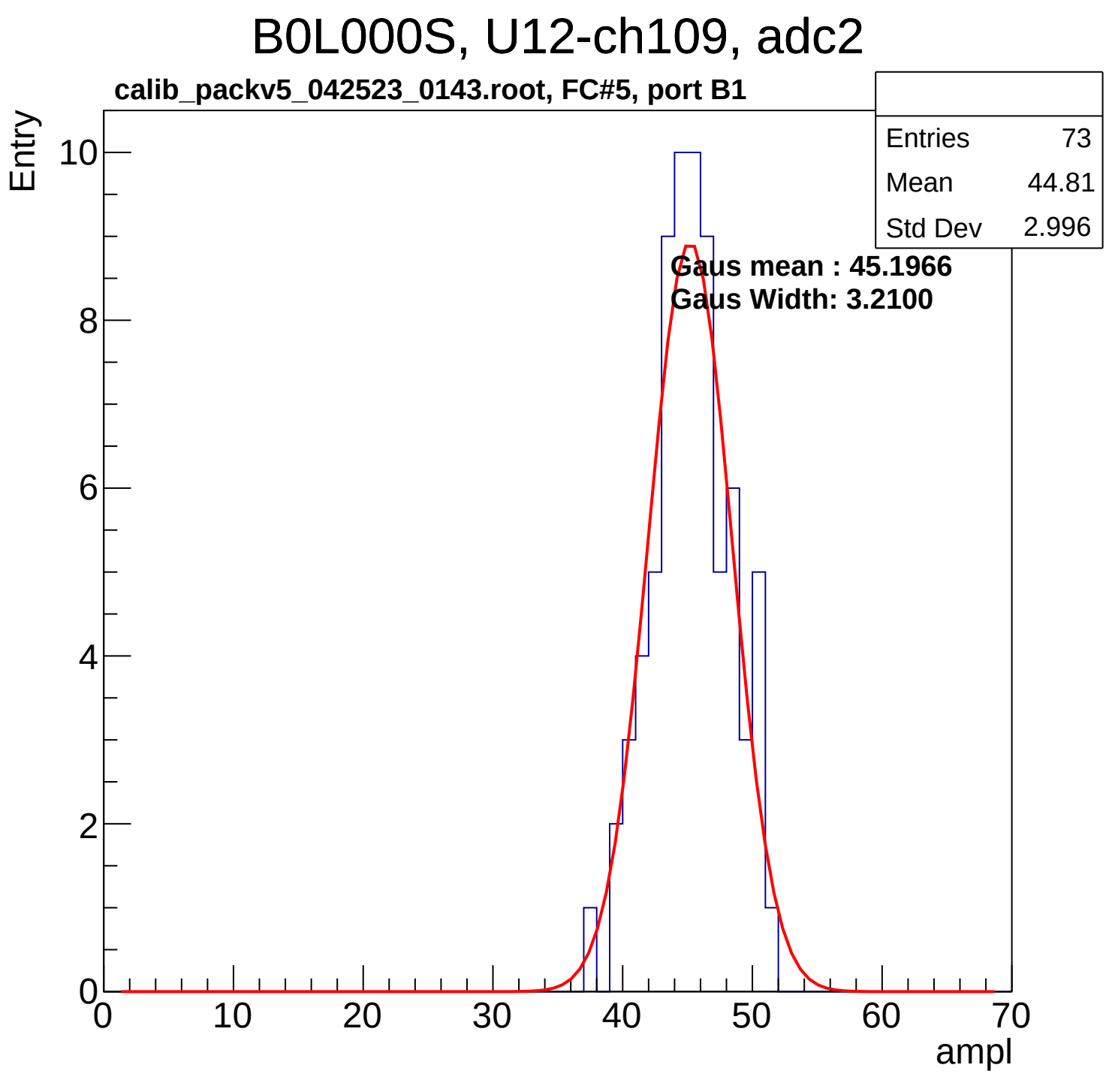
B0L000S, U12-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.81
Std Dev 2.996
Gaus mean : 45.1966
Gaus Width: 3.2100
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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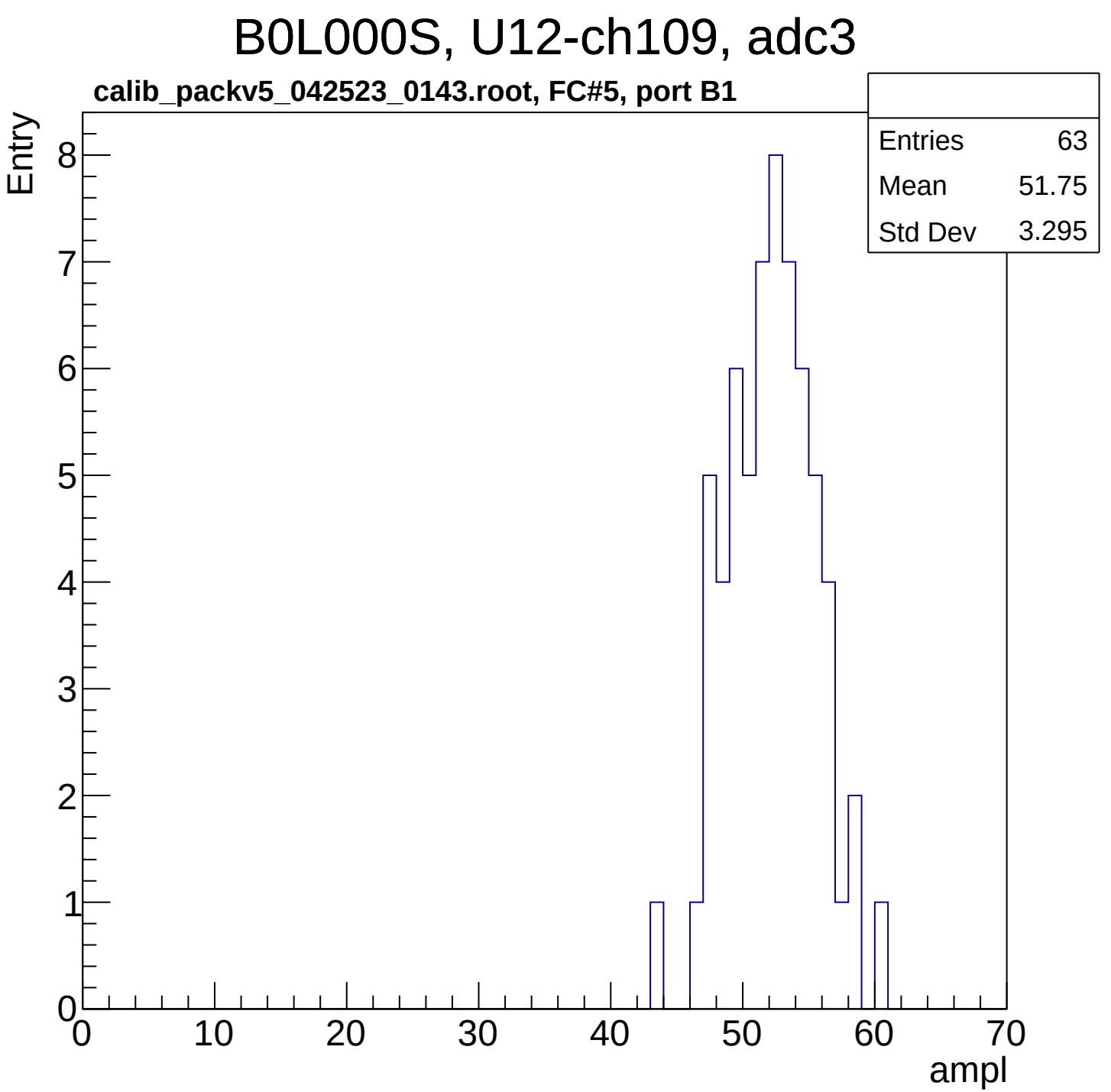
B0L000S, U12-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.75
Std Dev 3.295
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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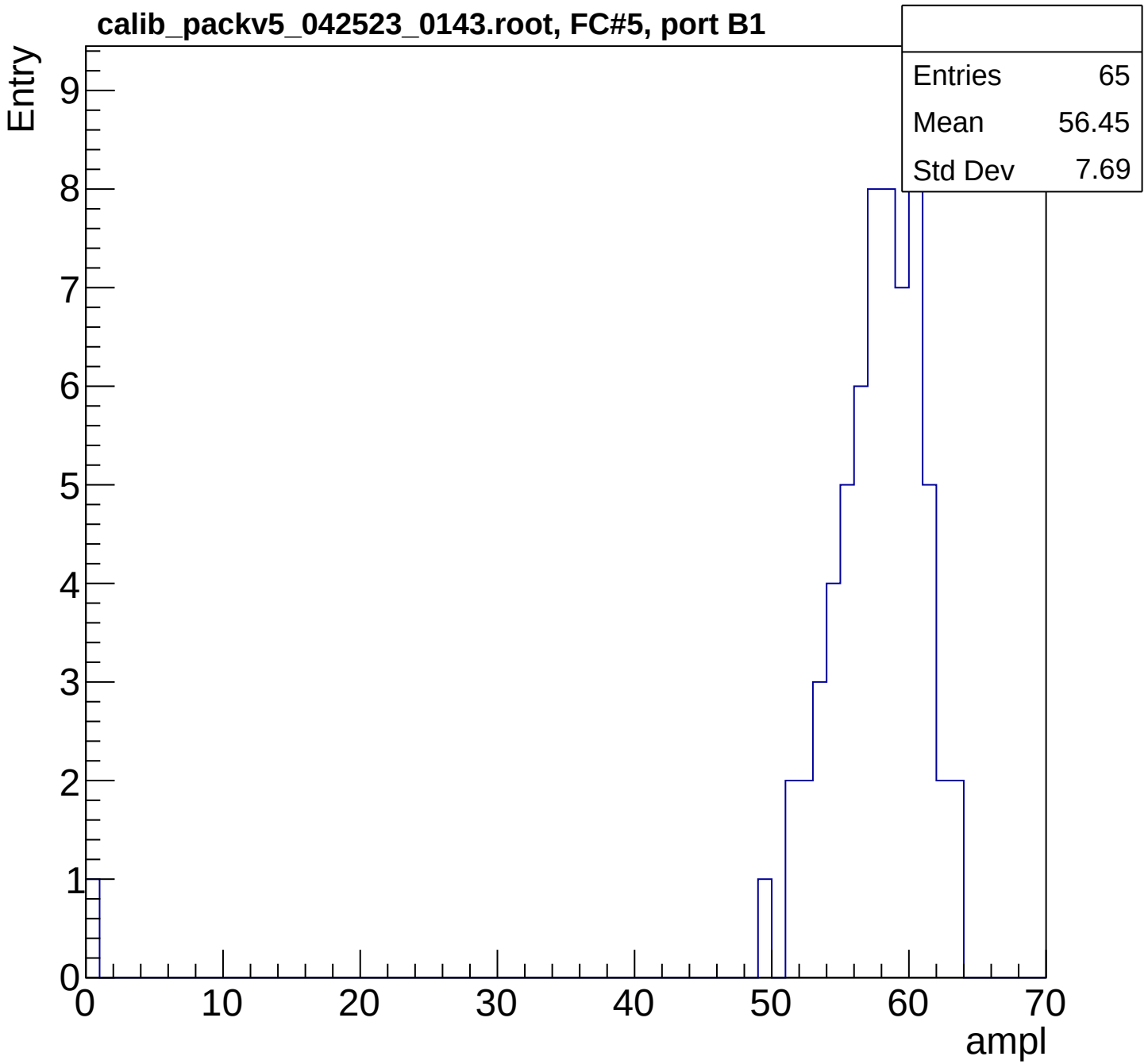
B0L000S, U12-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 56.45
Std Dev 7.69
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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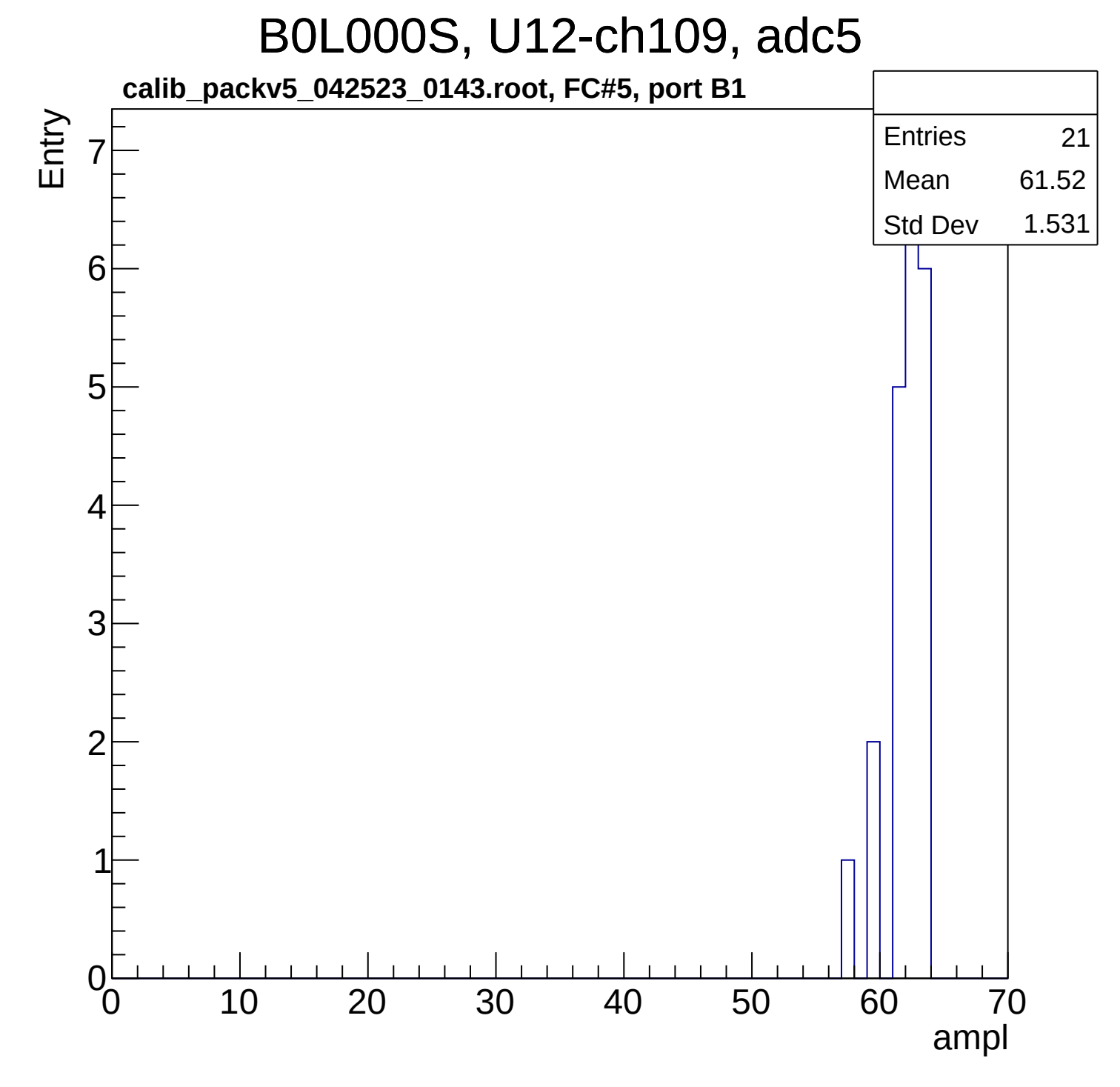
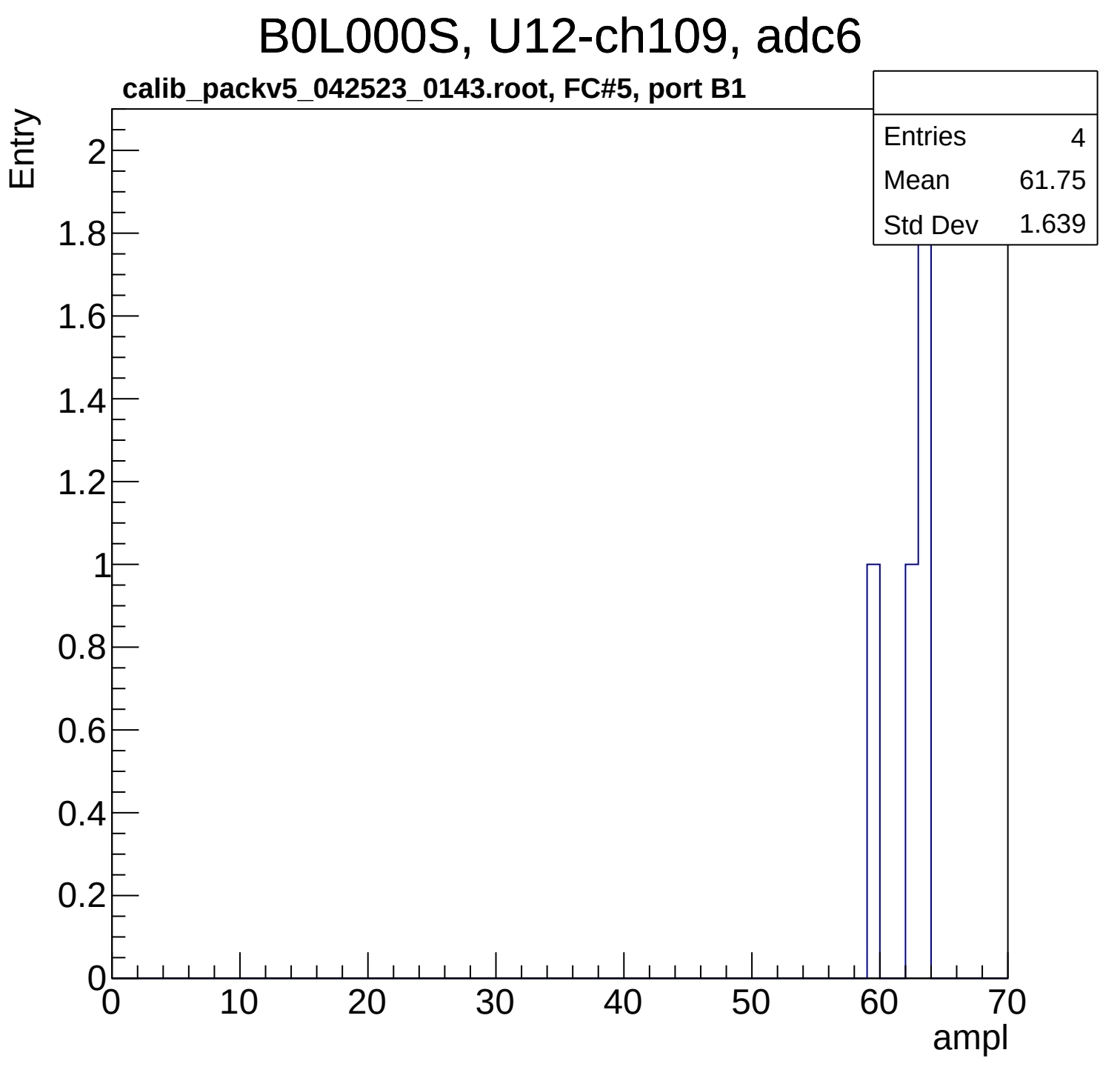
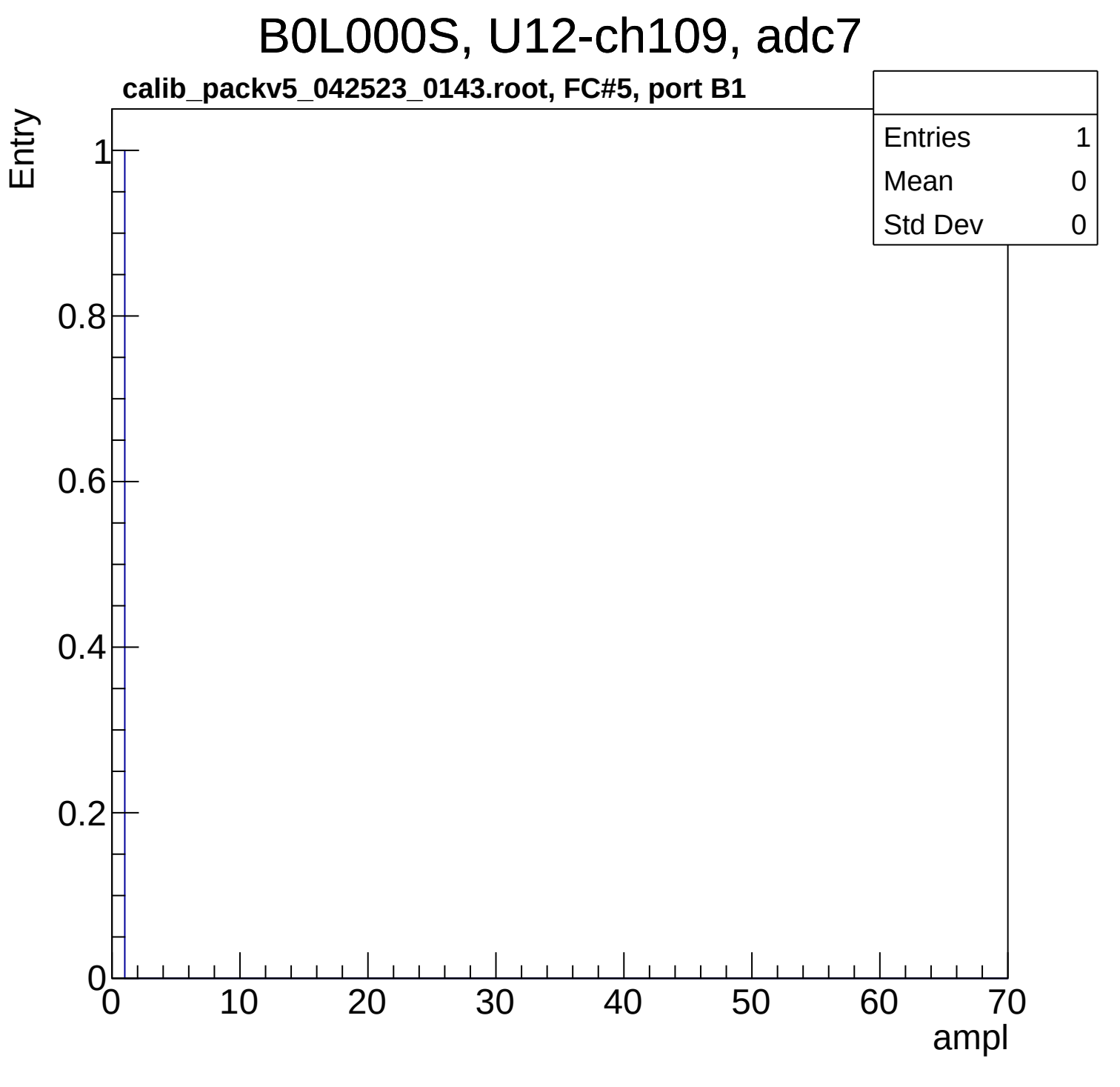
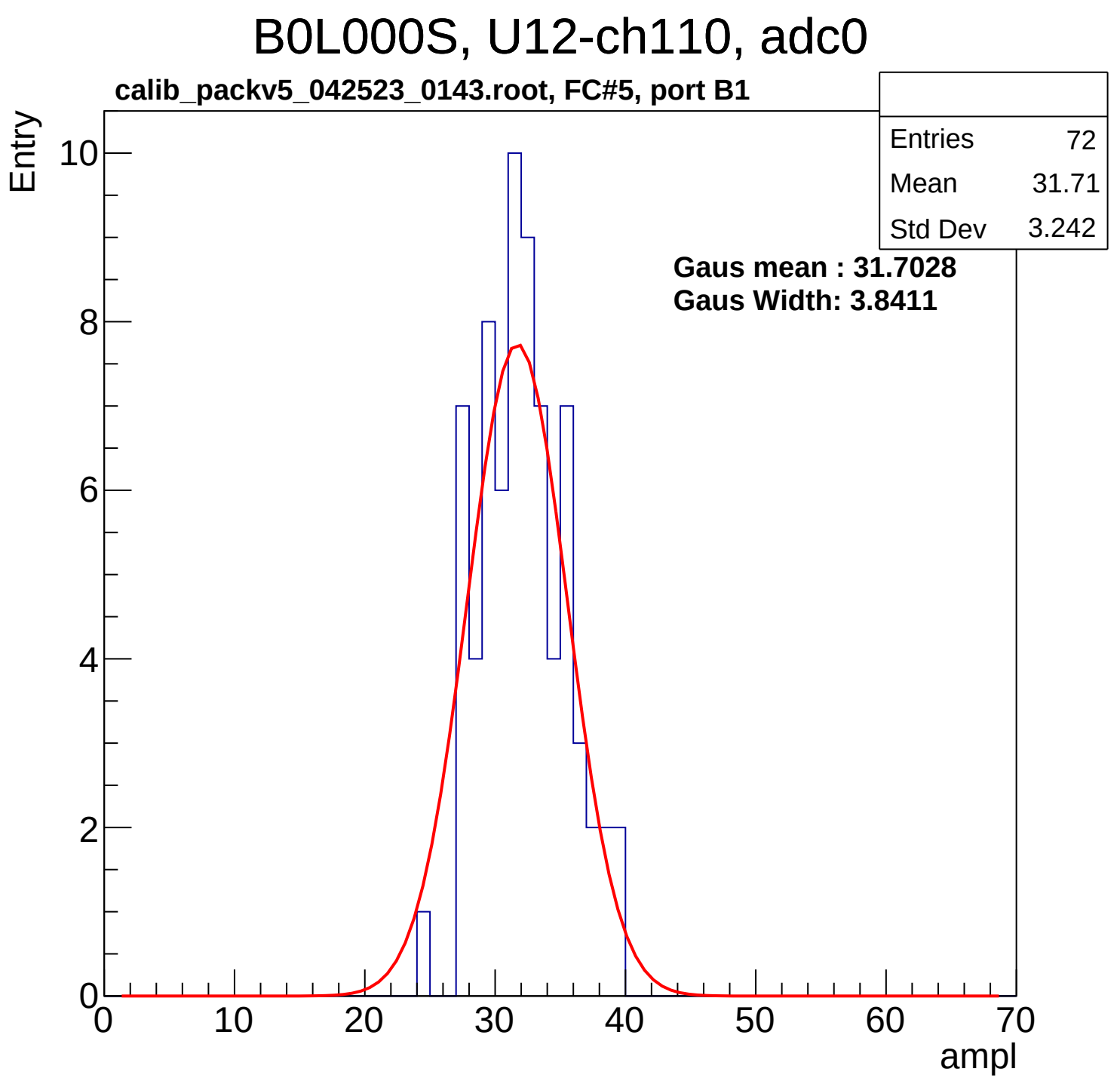
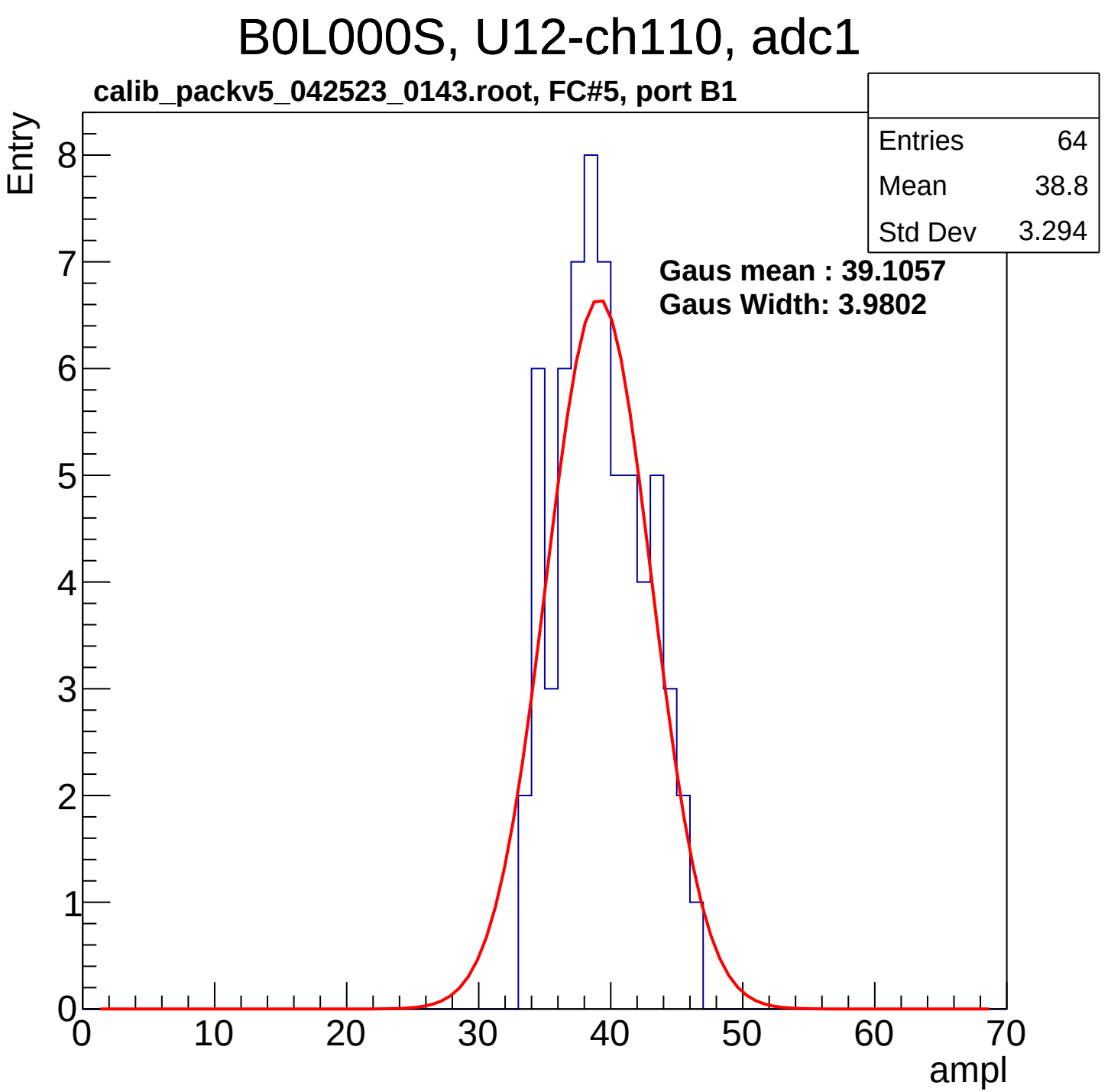

B0L000S, U12-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.52
Std Dev 1.531
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 31.71
Std Dev 3.242
Gaus mean : 31.7028
Gaus Width: 3.8411
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.8
Std Dev 3.294
Gaus mean : 39.1057
Gaus Width: 3.9802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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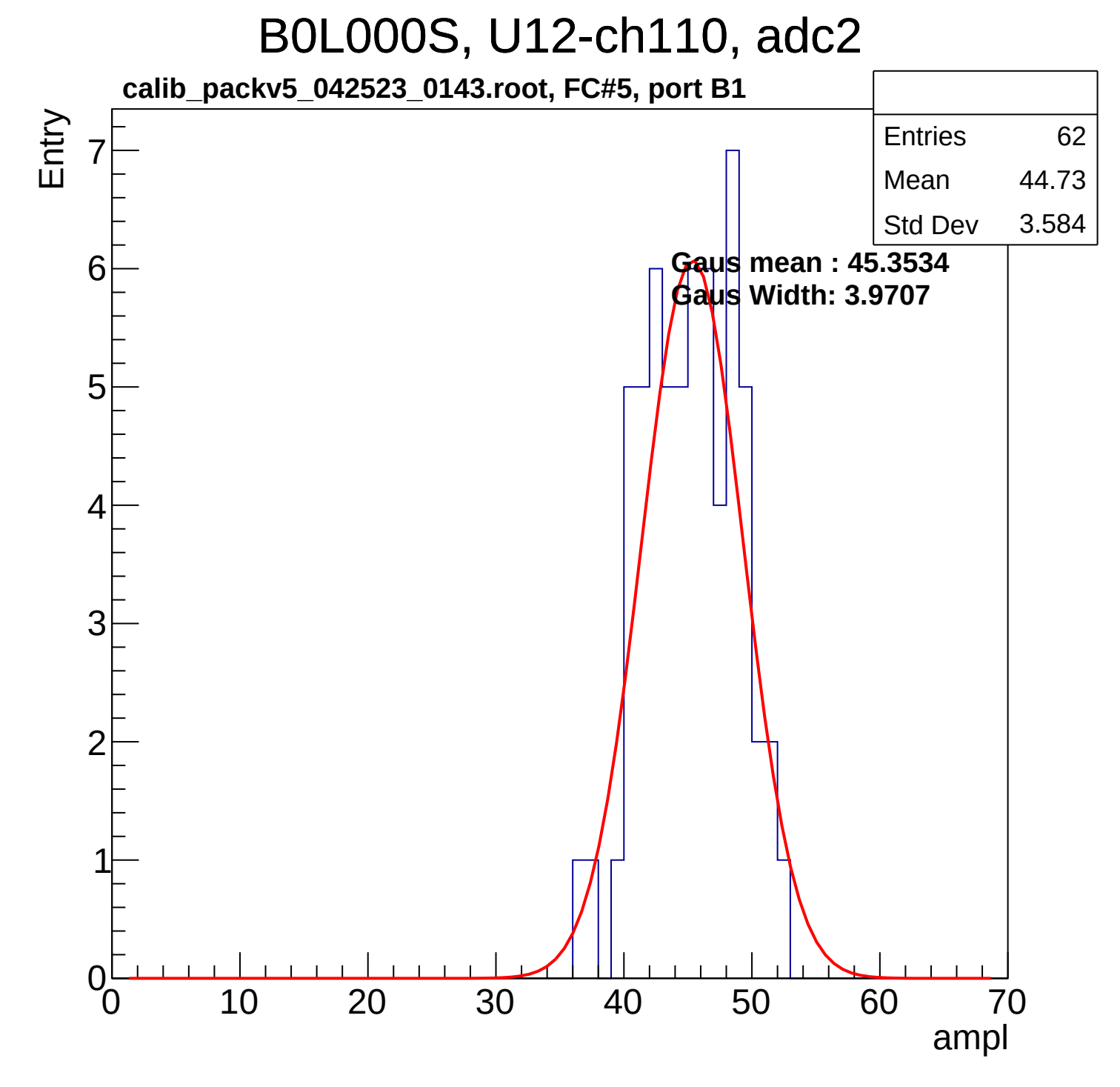
B0L000S, U12-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 44.73
Std Dev 3.584
Gaus mean : 45.3534
Gaus Width: 3.9707
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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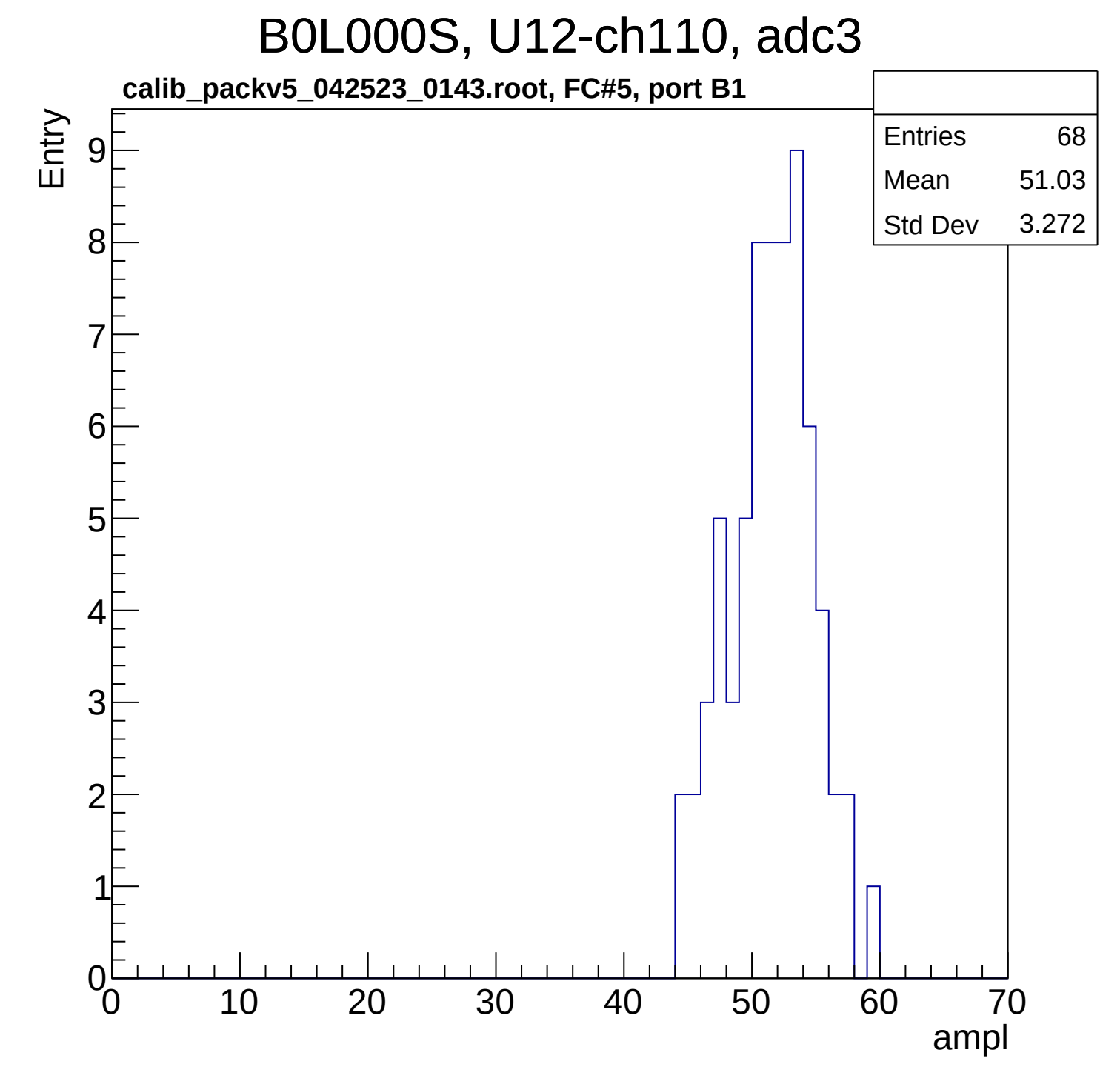
B0L000S, U12-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.03
Std Dev 3.272
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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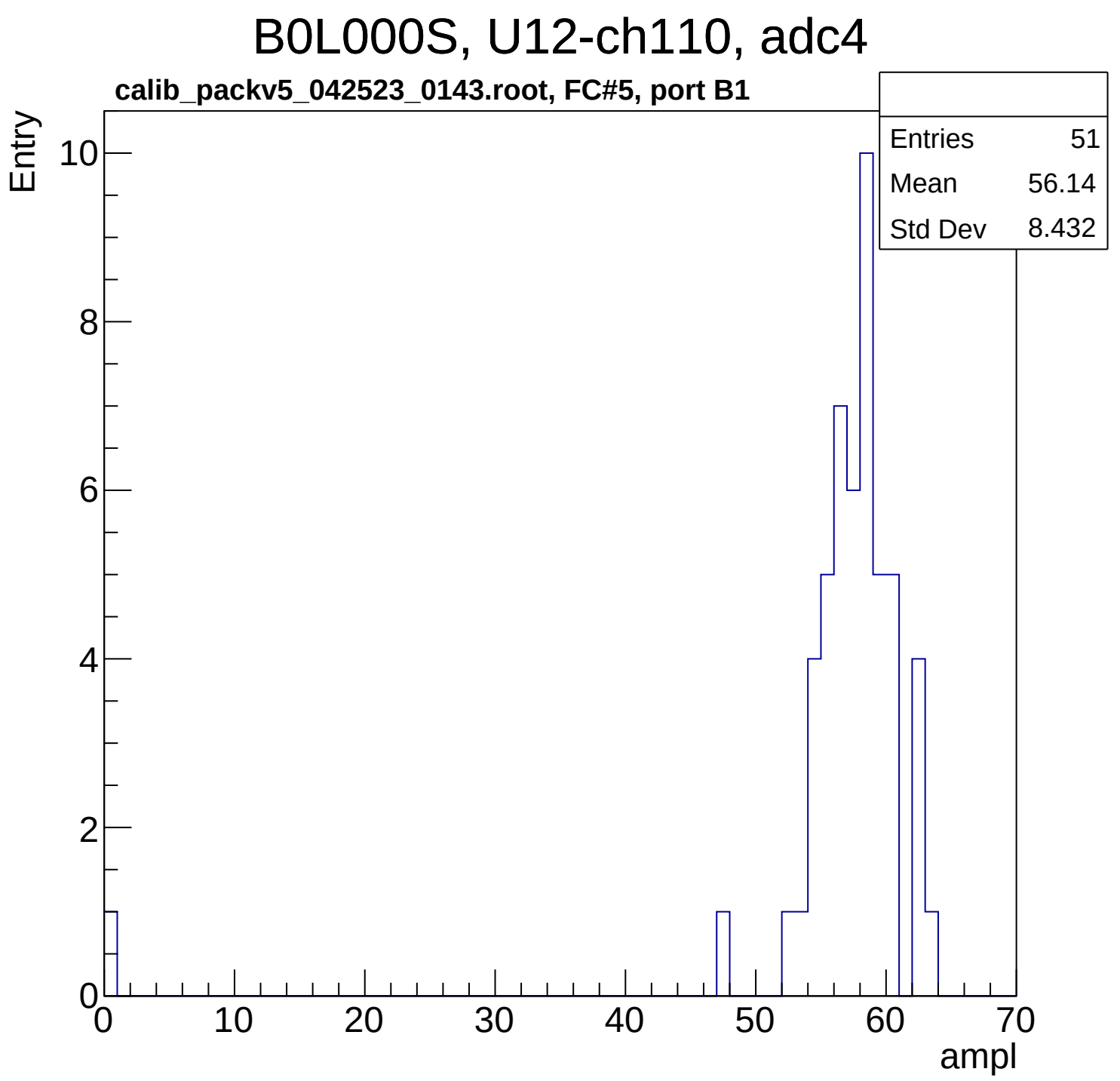
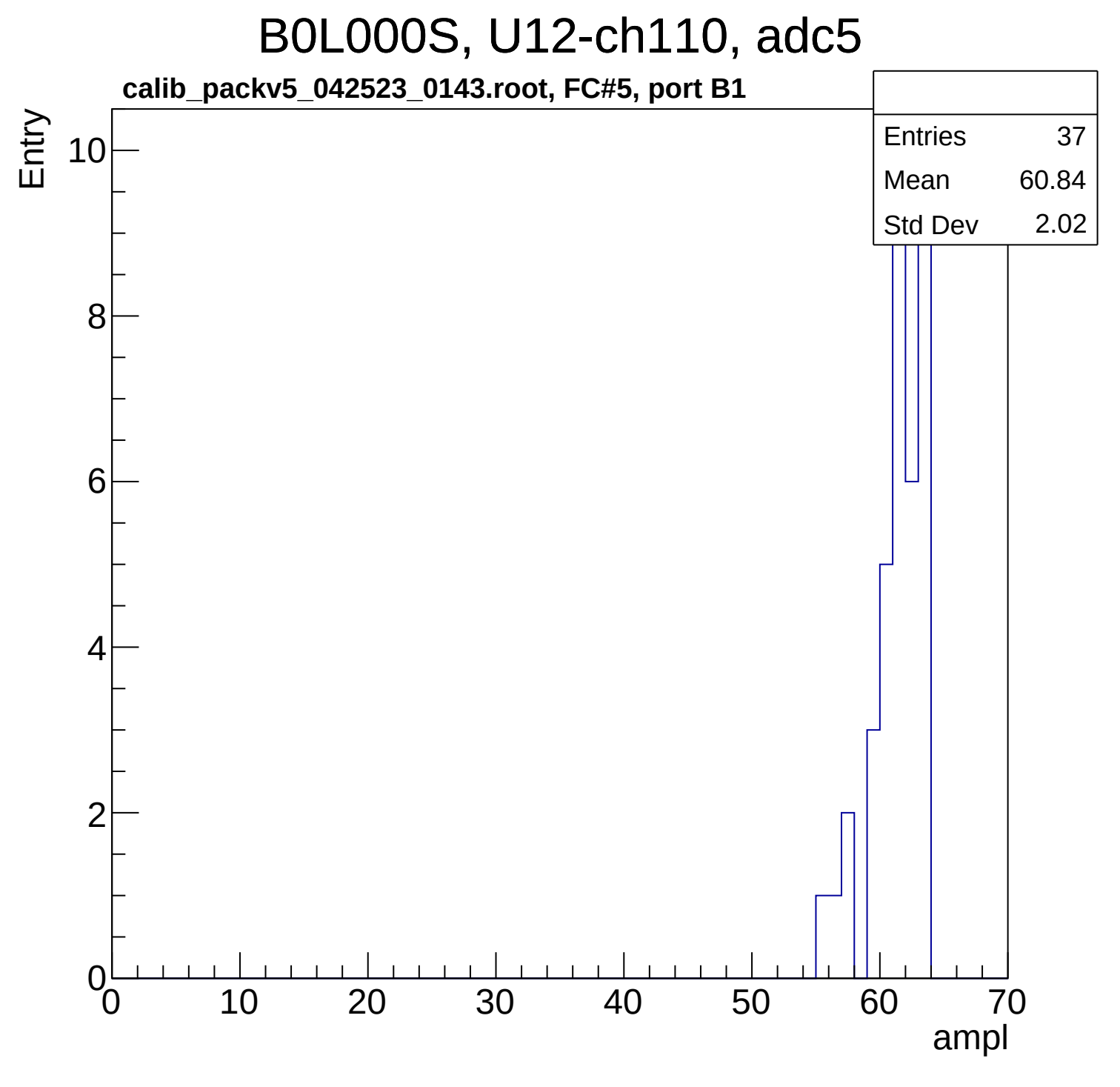
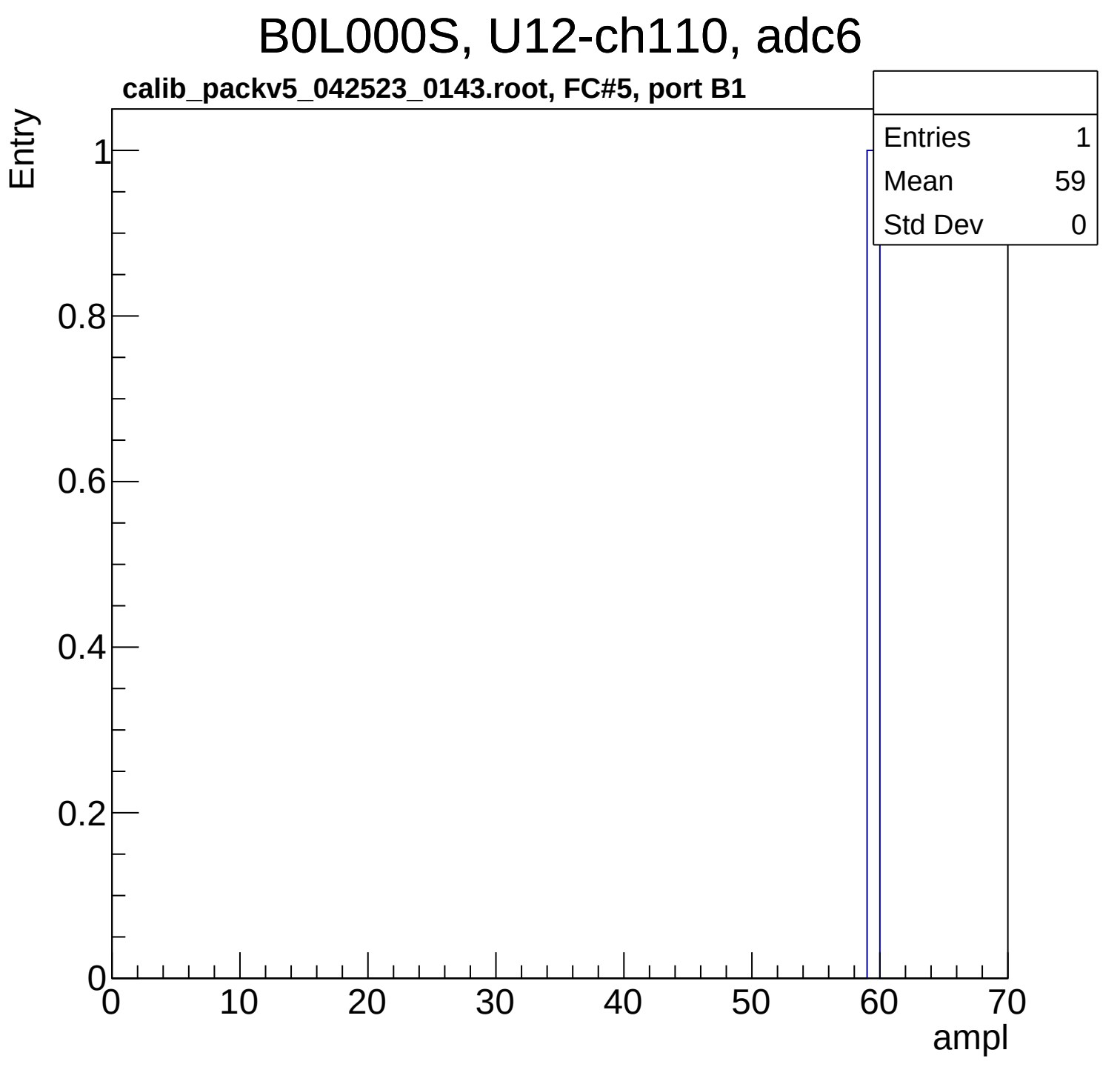
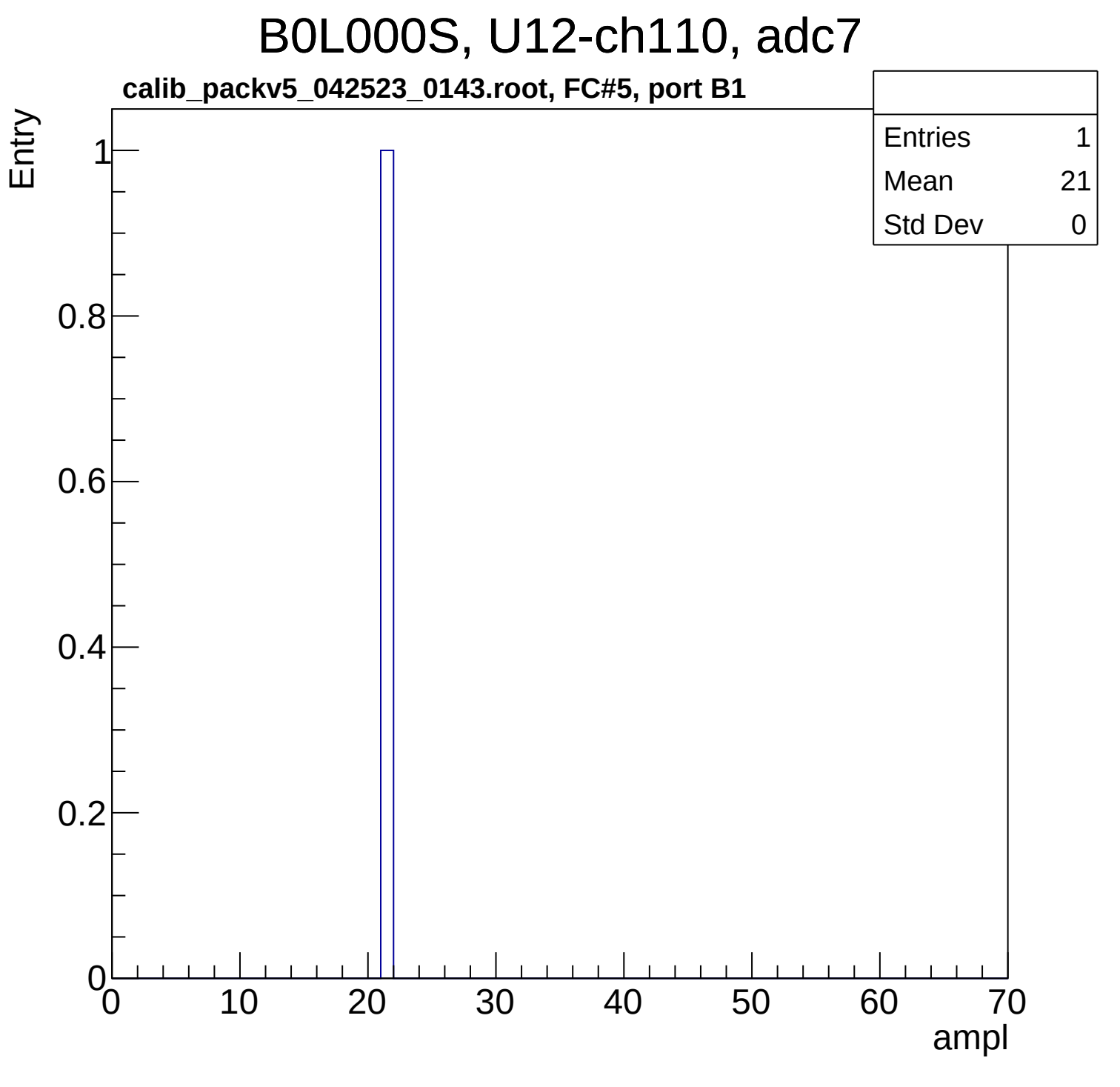
40
50
60
70


B0L000S, U12-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.14
Std Dev 8.432
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.84
Std Dev 2.02
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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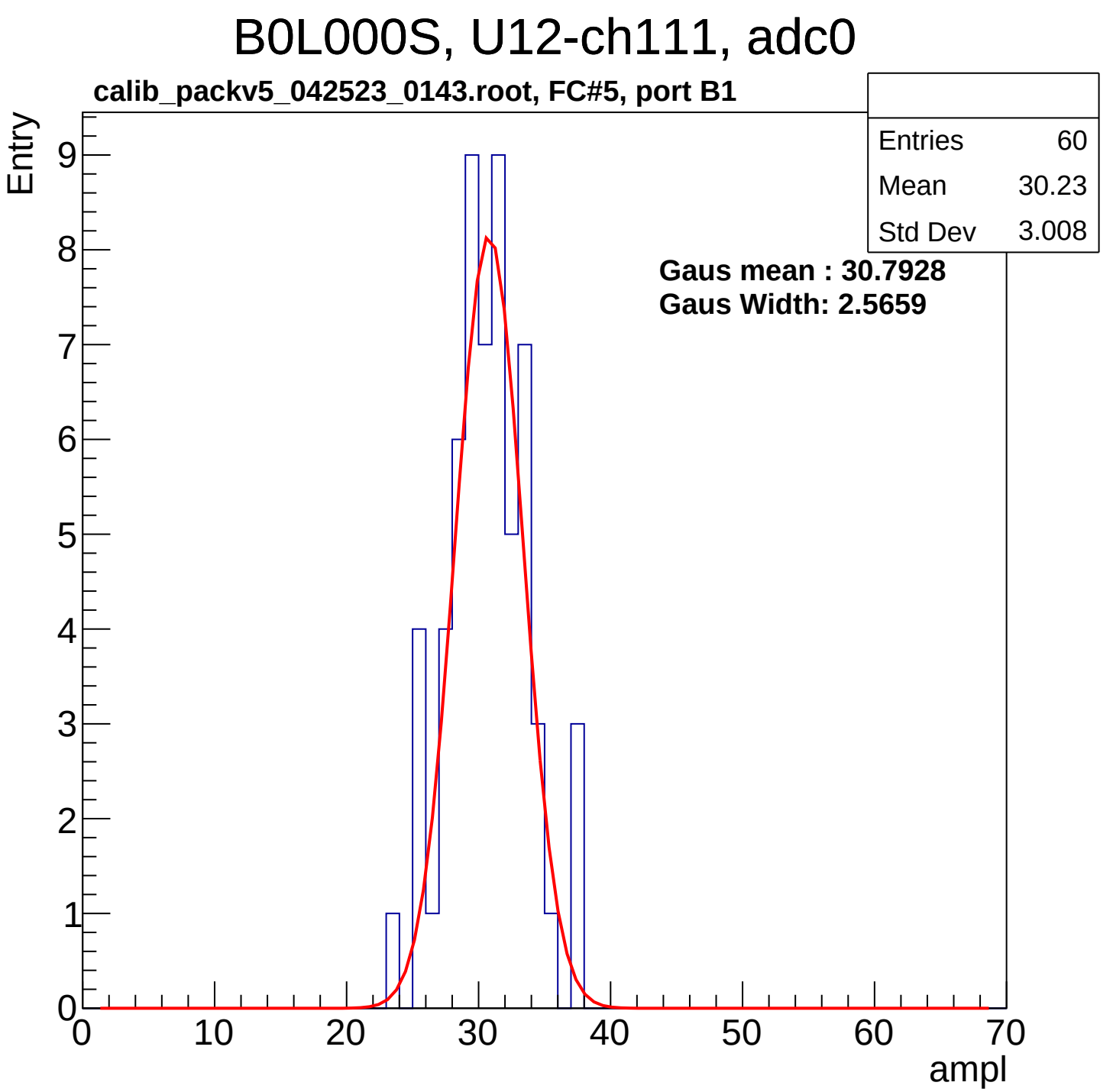
50
60
70
ampl


B0L000S, U12-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.23
Std Dev 3.008
Gaus mean : 30.7928
Gaus Width: 2.5659
Entry
ampl
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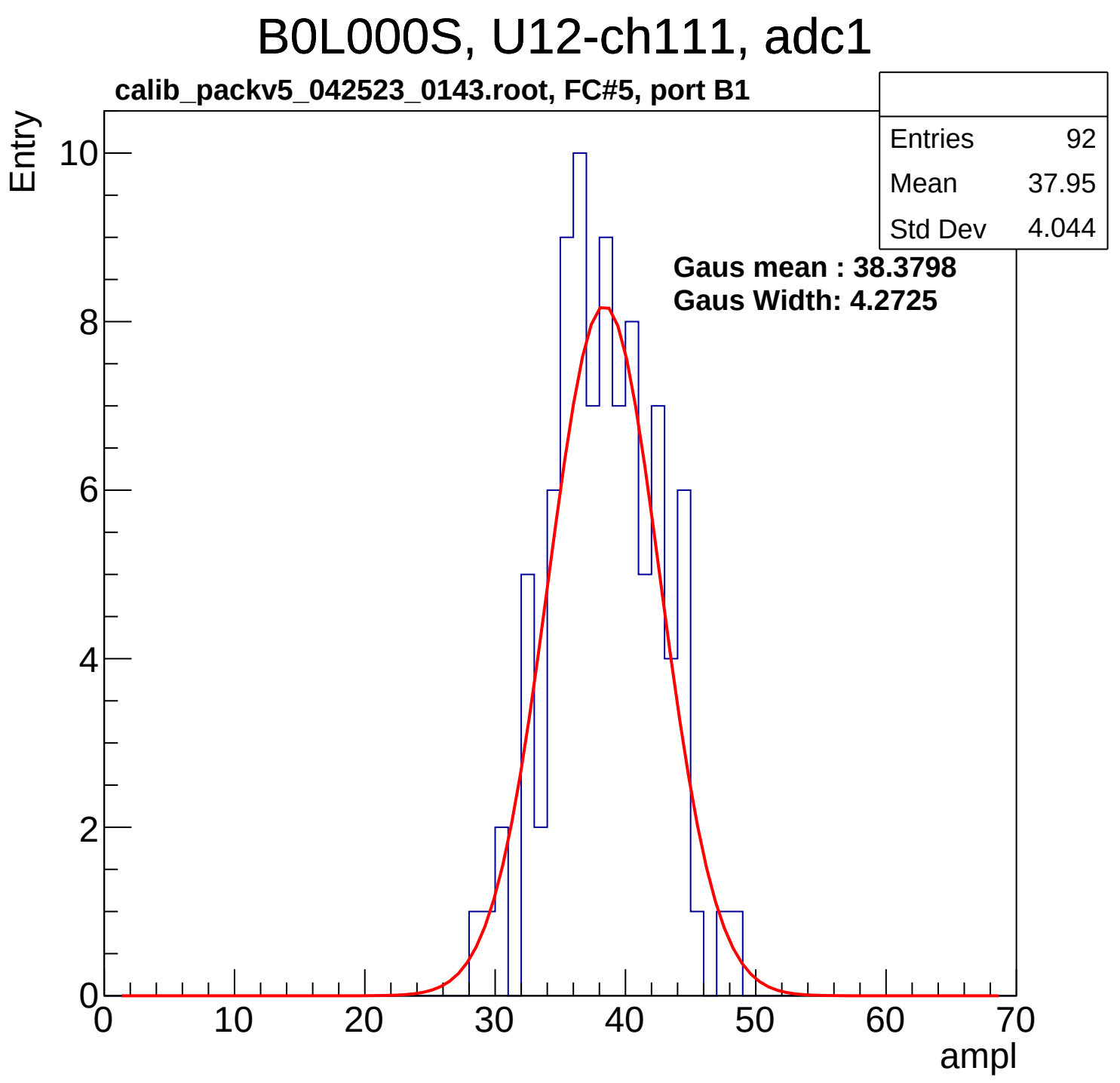
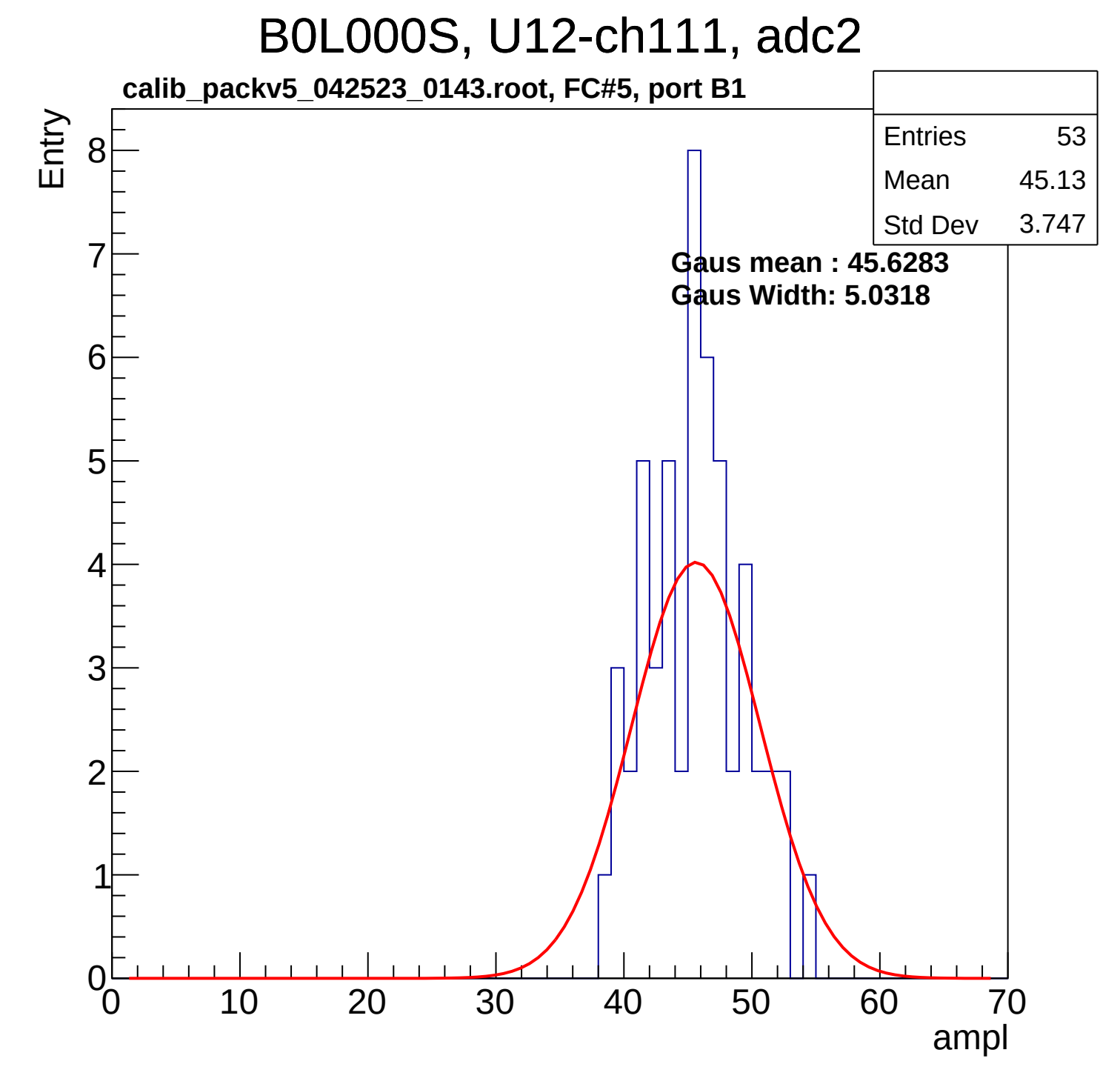
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 92
Mean 37.95
Std Dev 4.044
Gaus mean : 38.3798
Gaus Width: 4.2725
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 45.13
Std Dev 3.747
Gaus mean : 45.6283
Gaus Width: 5.0318
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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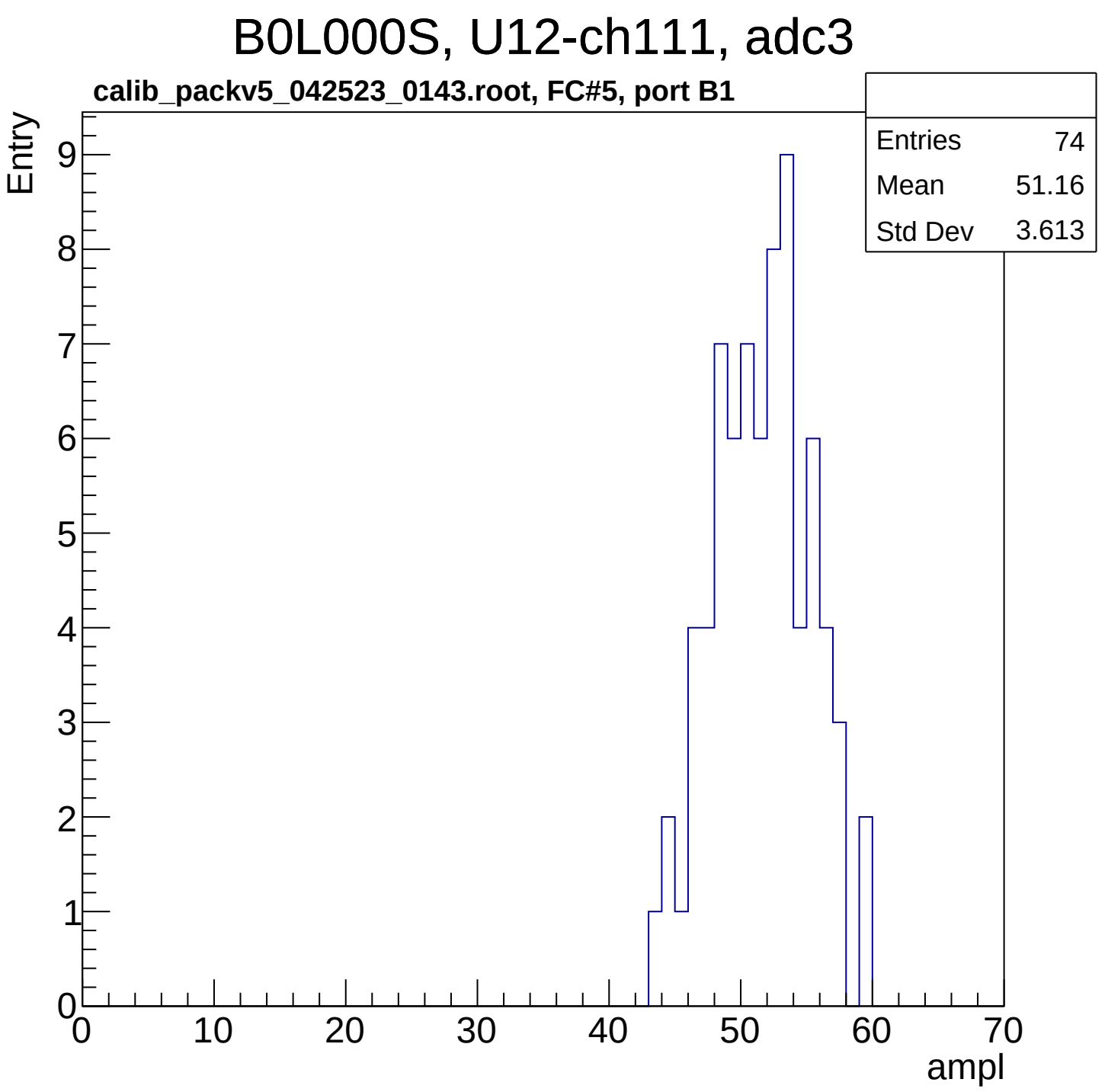
B0L000S, U12-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 51.16
Std Dev 3.613
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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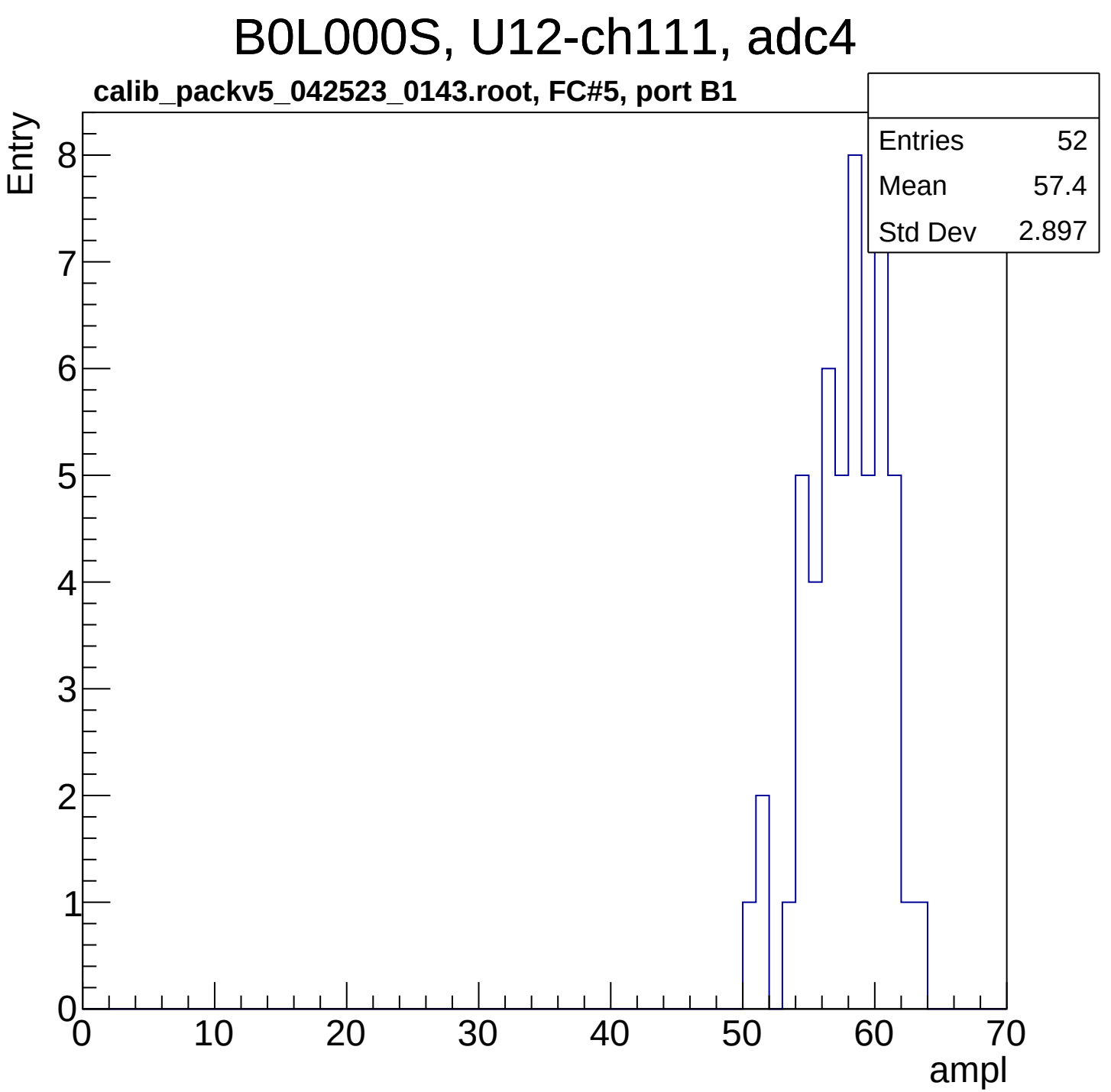
B0L000S, U12-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.4
Std Dev 2.897
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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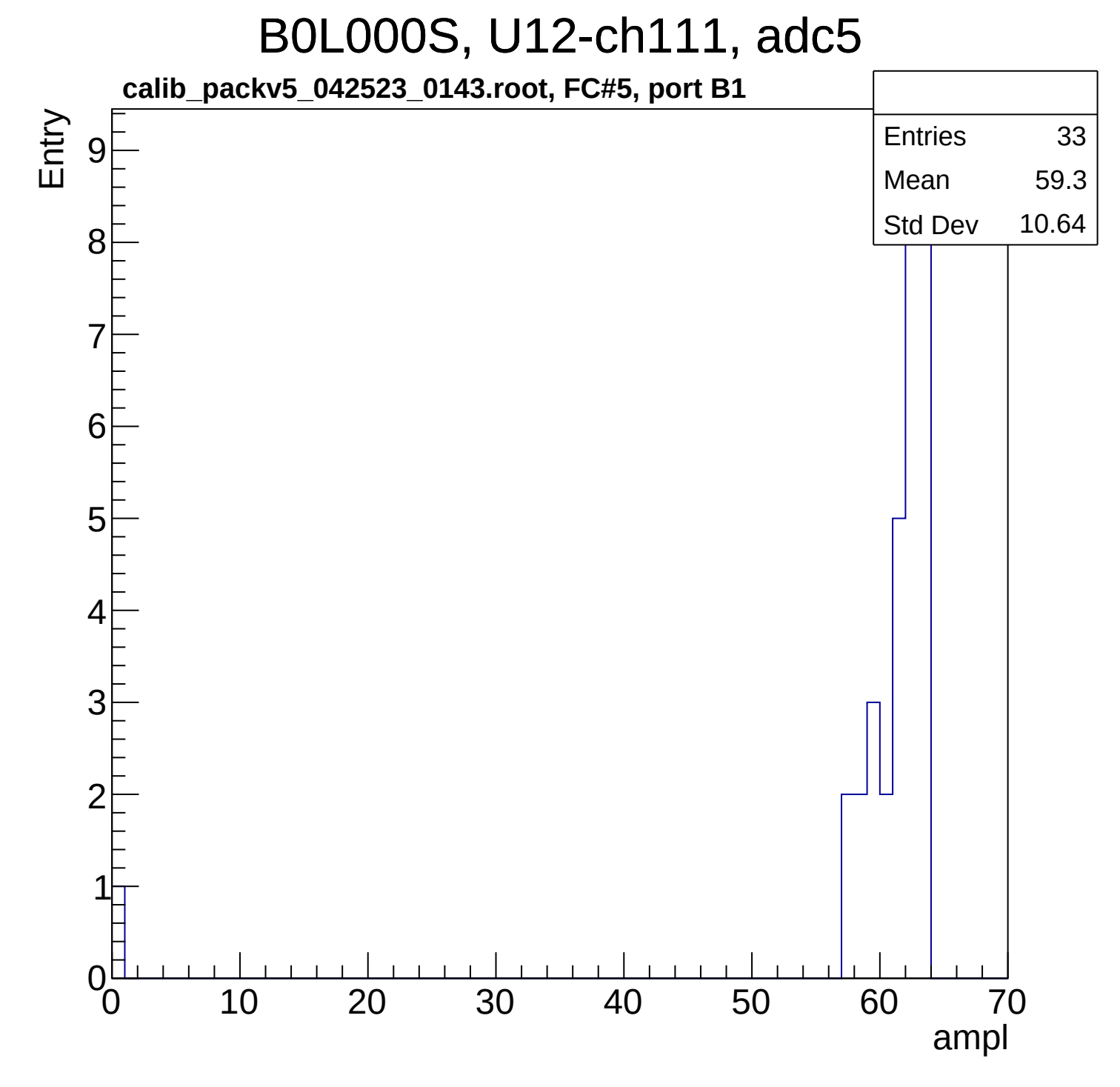
B0L000S, U12-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 59.3
Std Dev 10.64
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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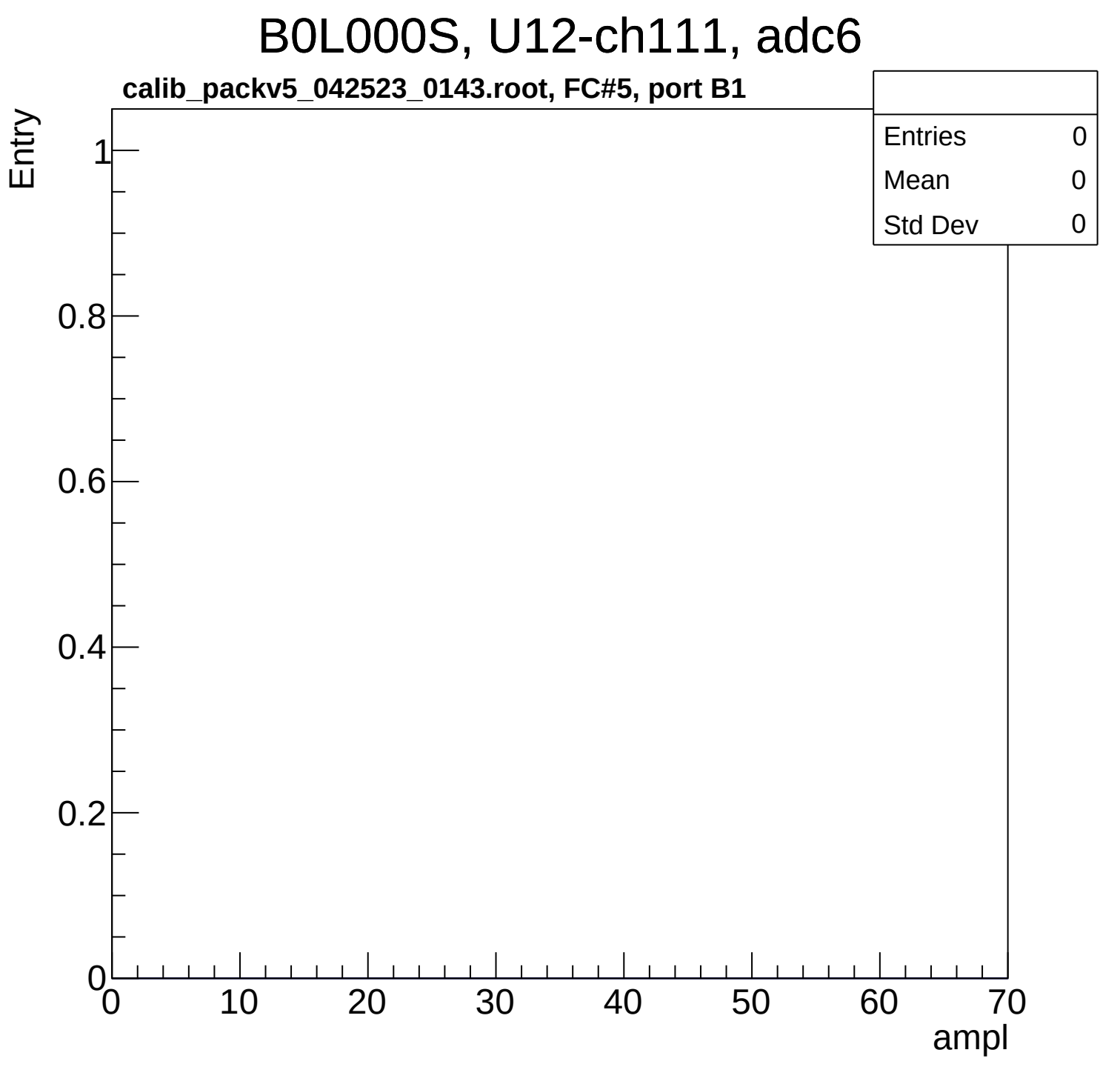
B0L000S, U12-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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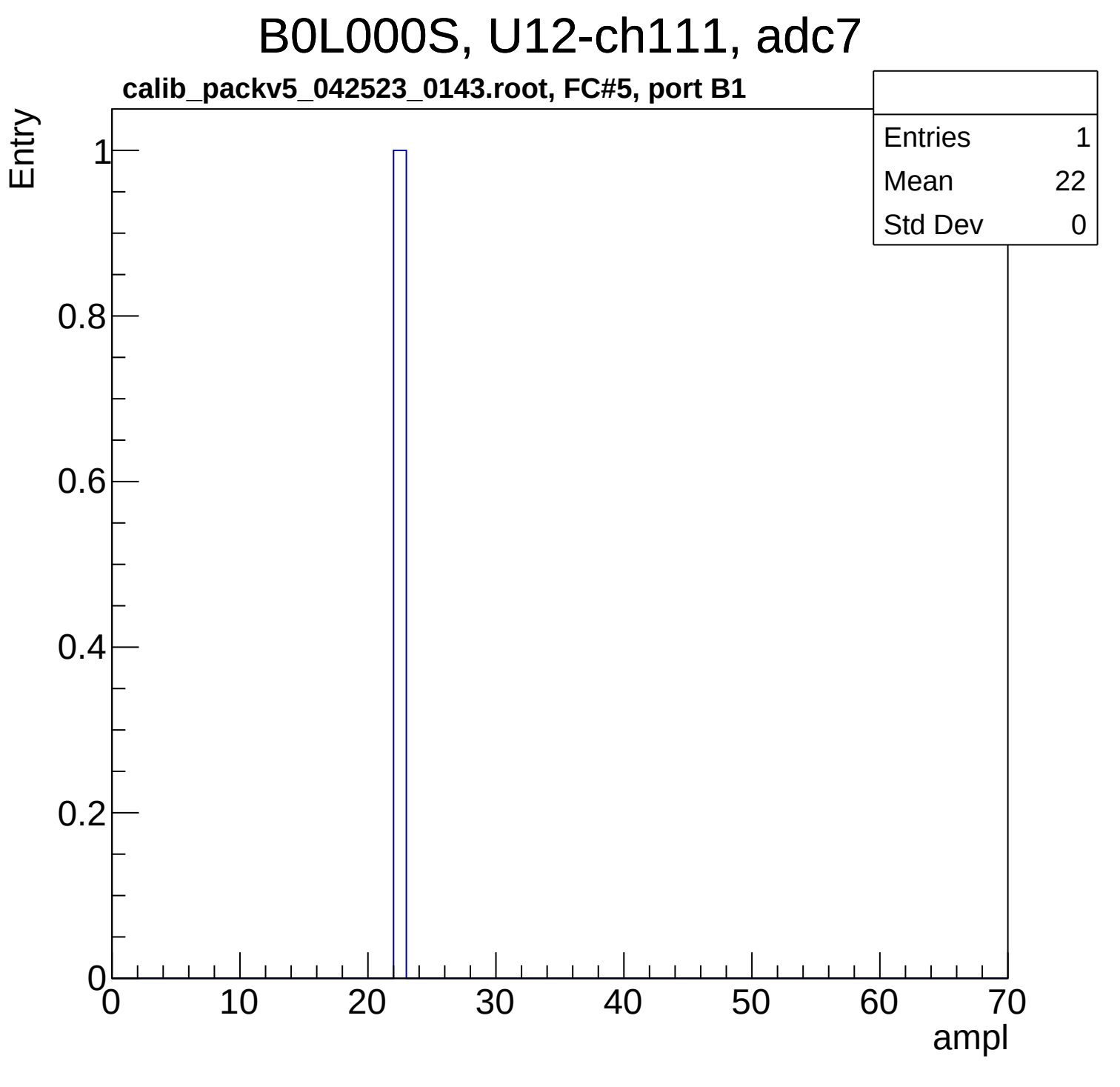
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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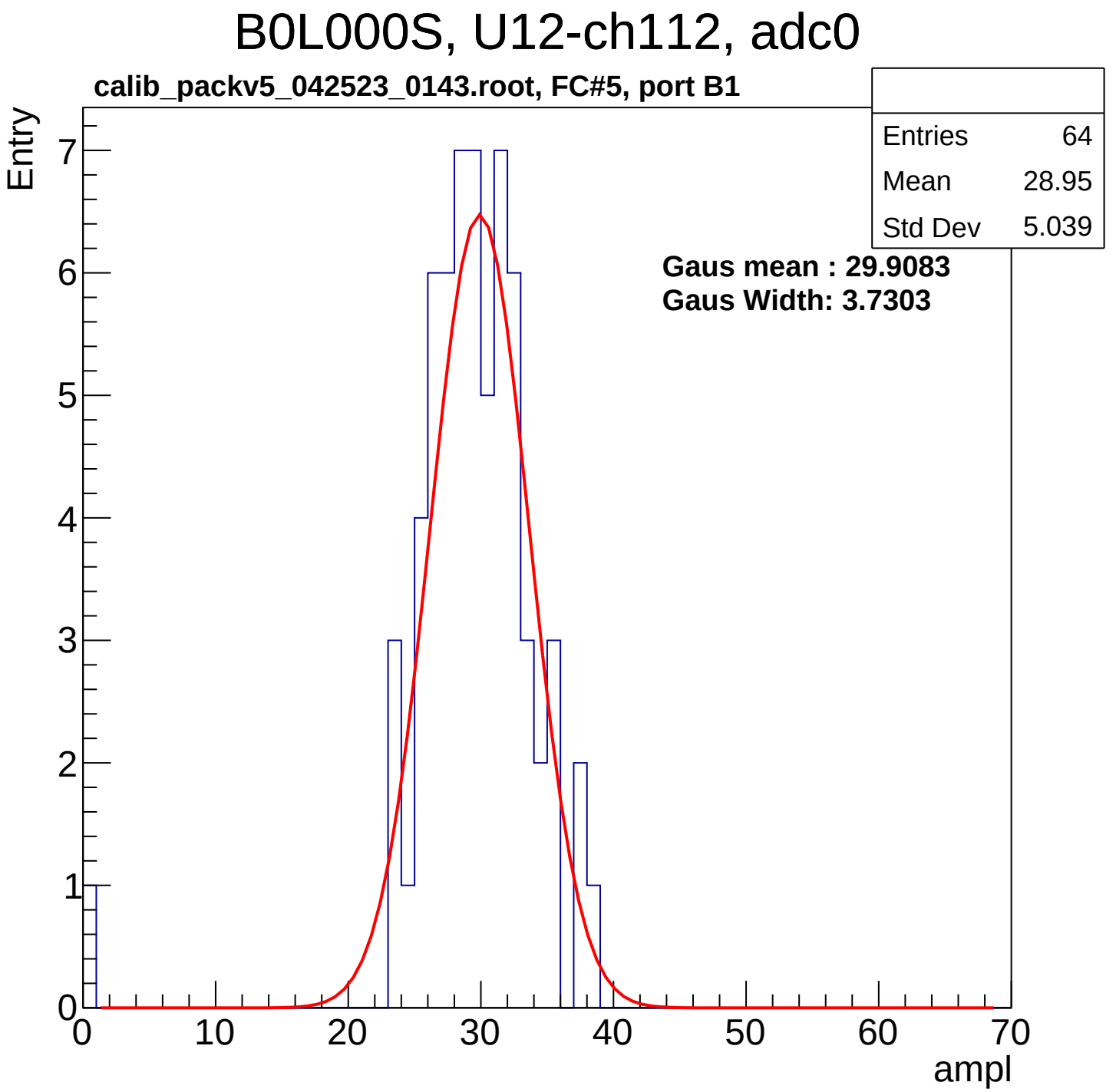
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.95
Std Dev 5.039
Gaus mean : 29.9083
Gaus Width: 3.7303
Entry
ampl
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3
4
5
6
7
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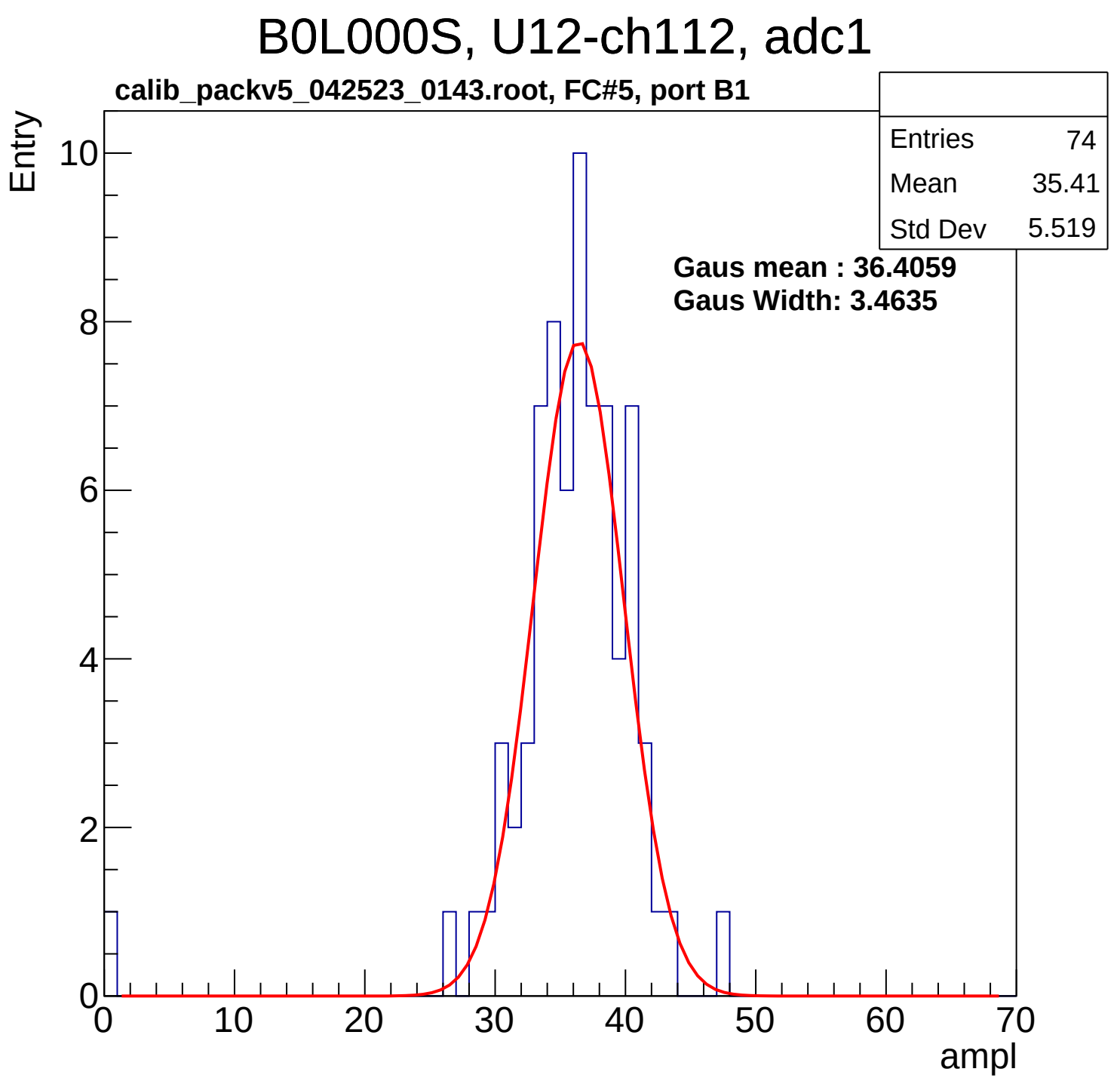
B0L000S, U12-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.41
Std Dev 5.519
Gaus mean : 36.4059
Gaus Width: 3.4635
Entry
ampl
0
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4
6
8
10
0
10
20
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40
50
60
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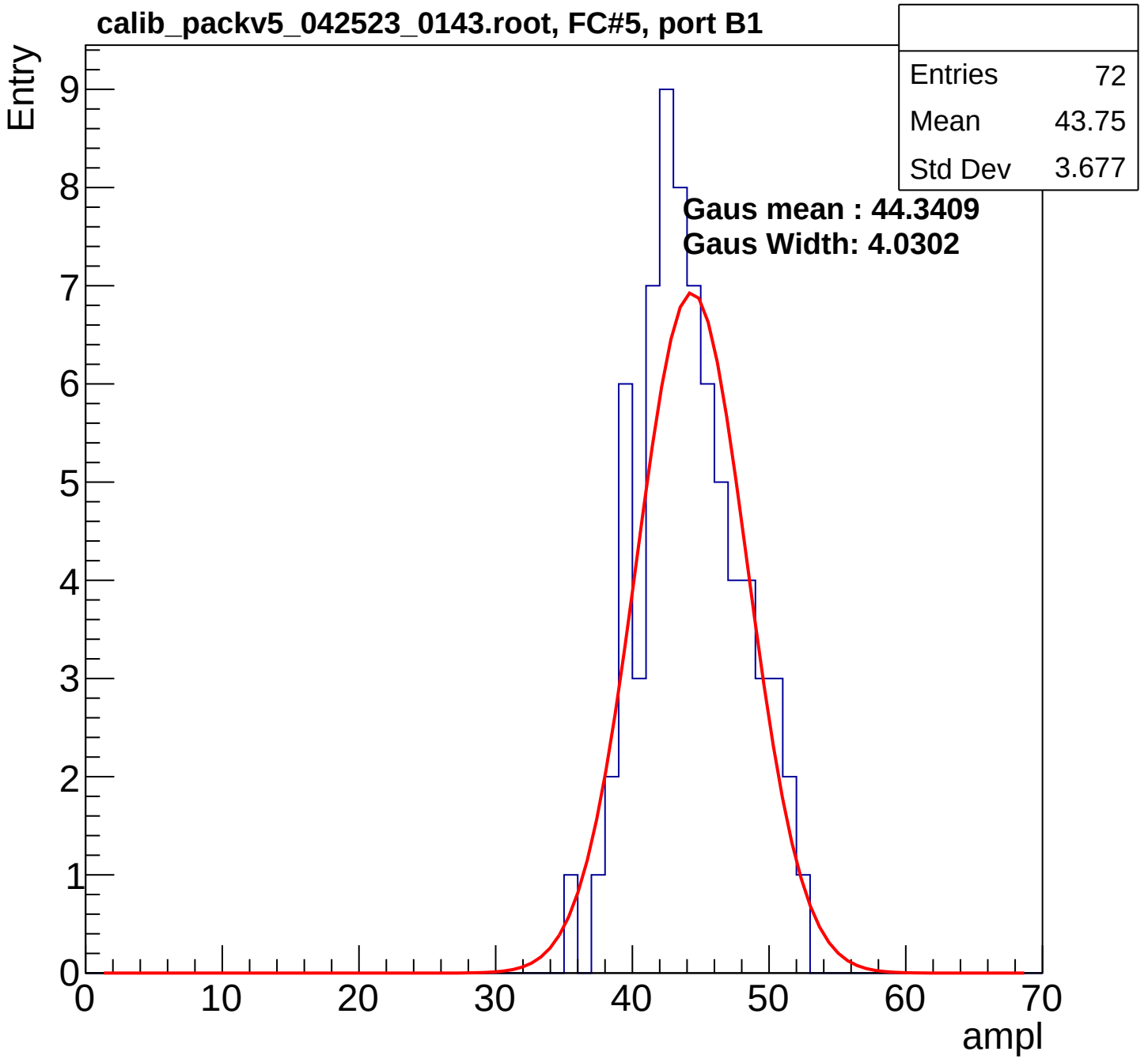
B0L000S, U12-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.75
Std Dev 3.677
Gaus mean : 44.3409
Gaus Width: 4.0302
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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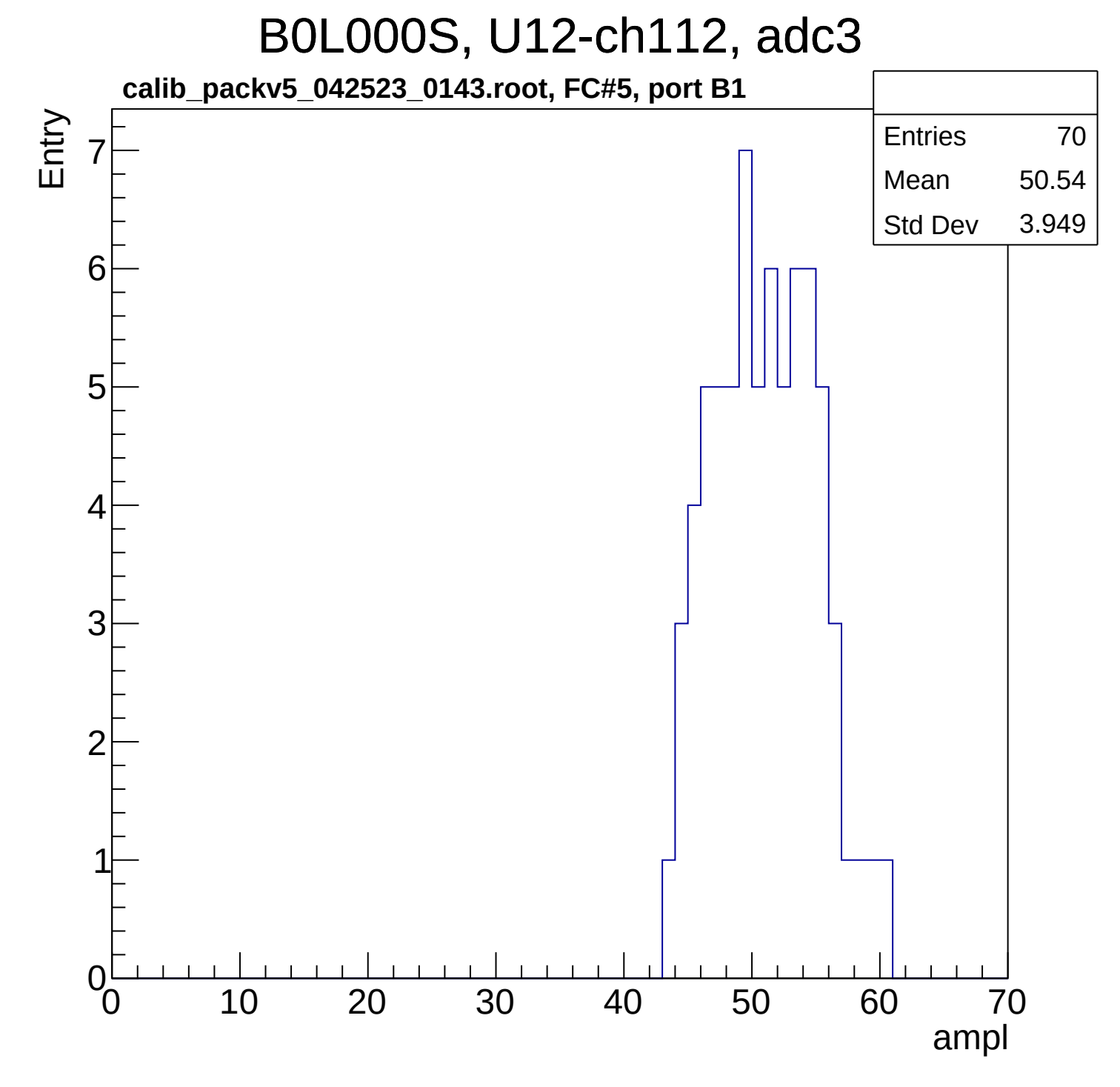
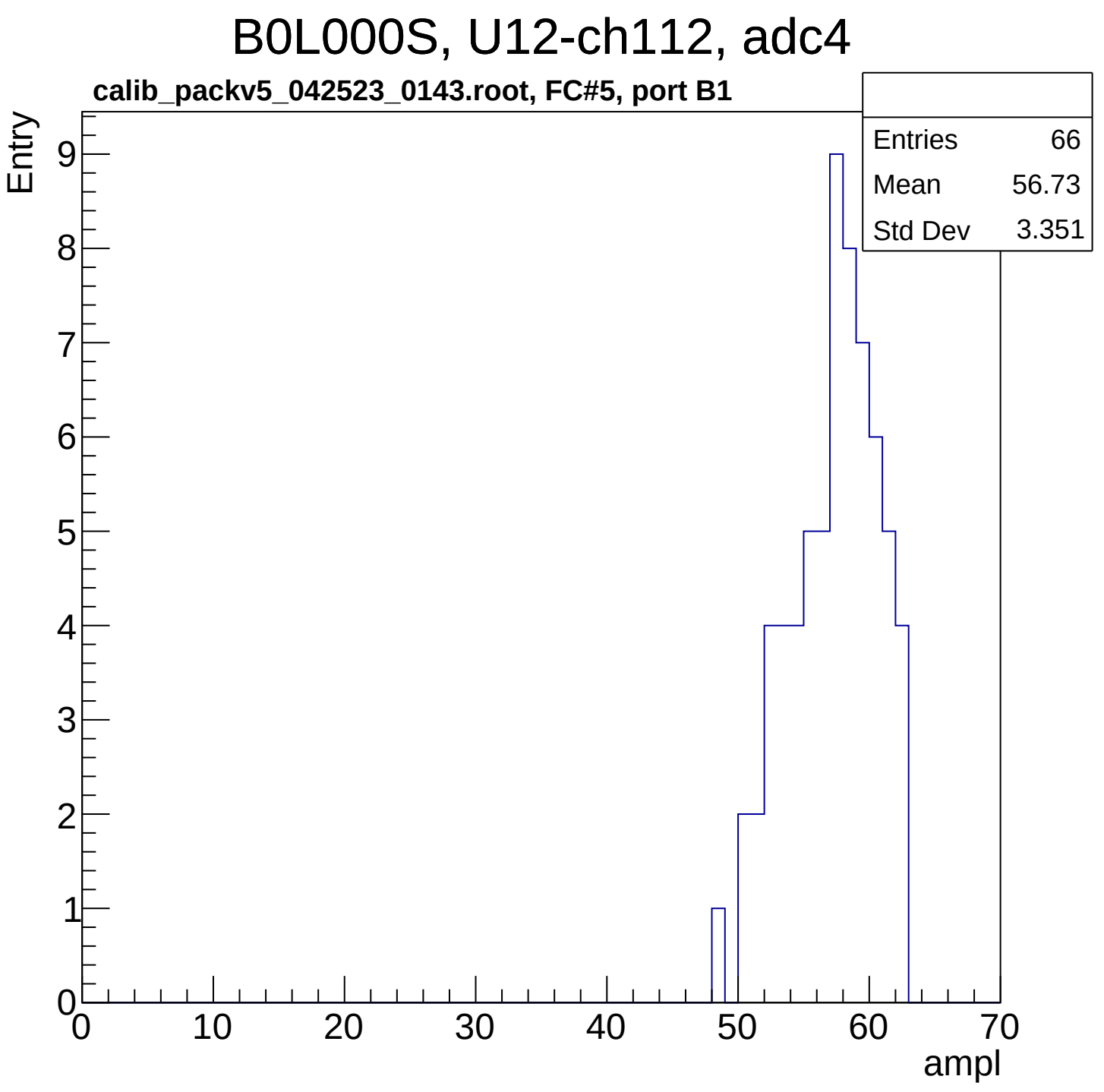
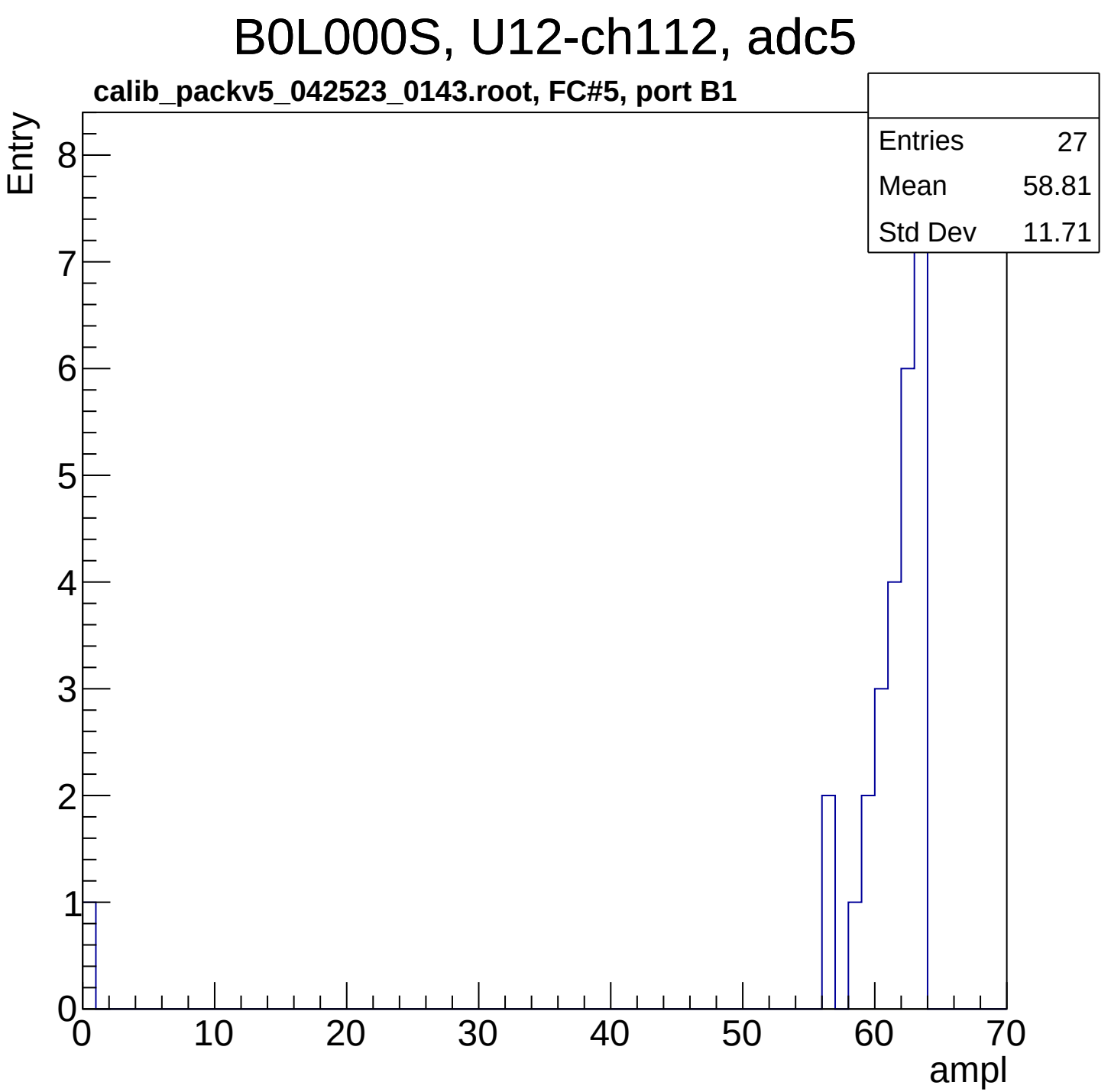

B0L000S, U12-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 50.54
Std Dev 3.949
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.73
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.81
Std Dev 11.71
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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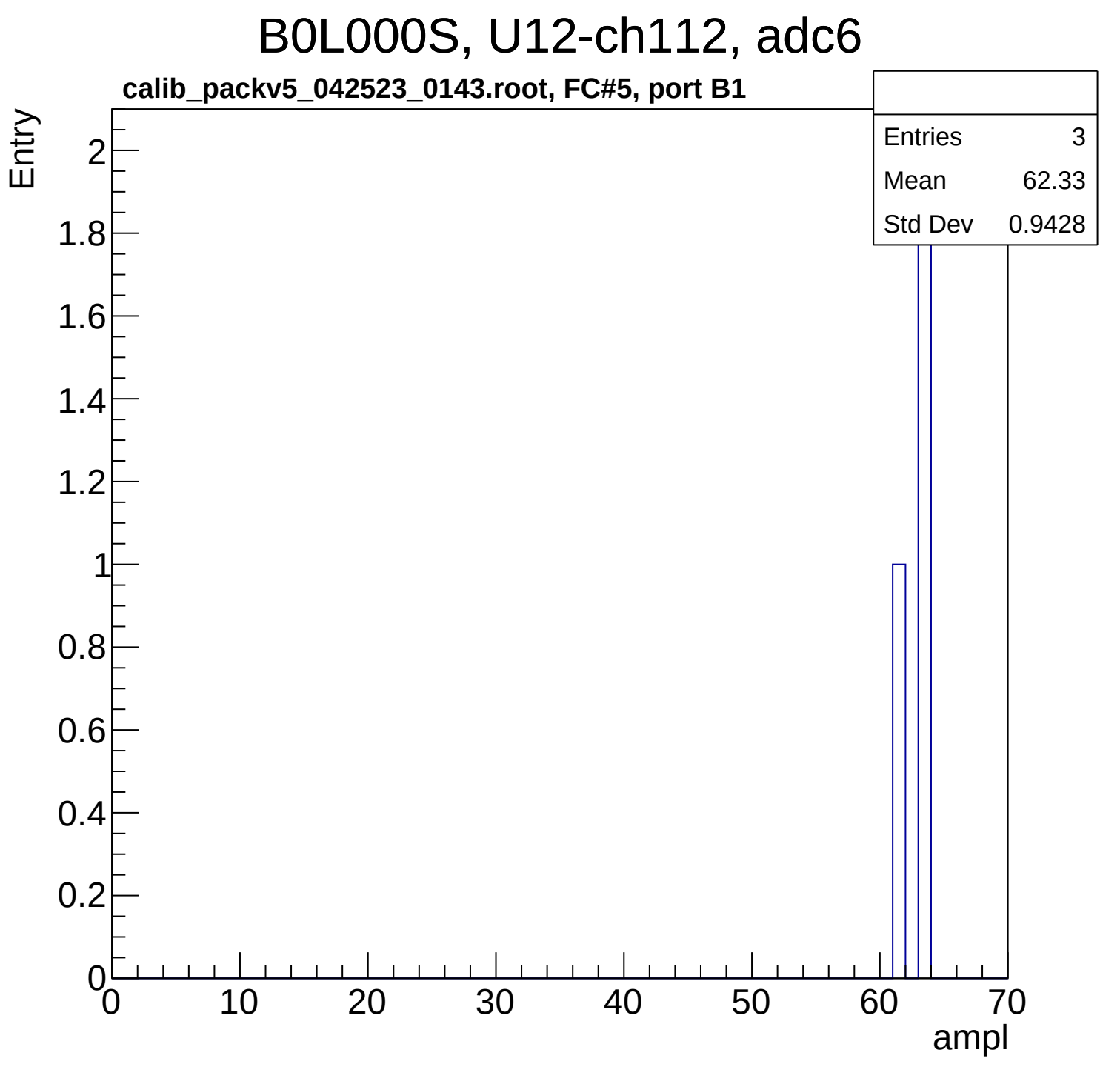
B0L000S, U12-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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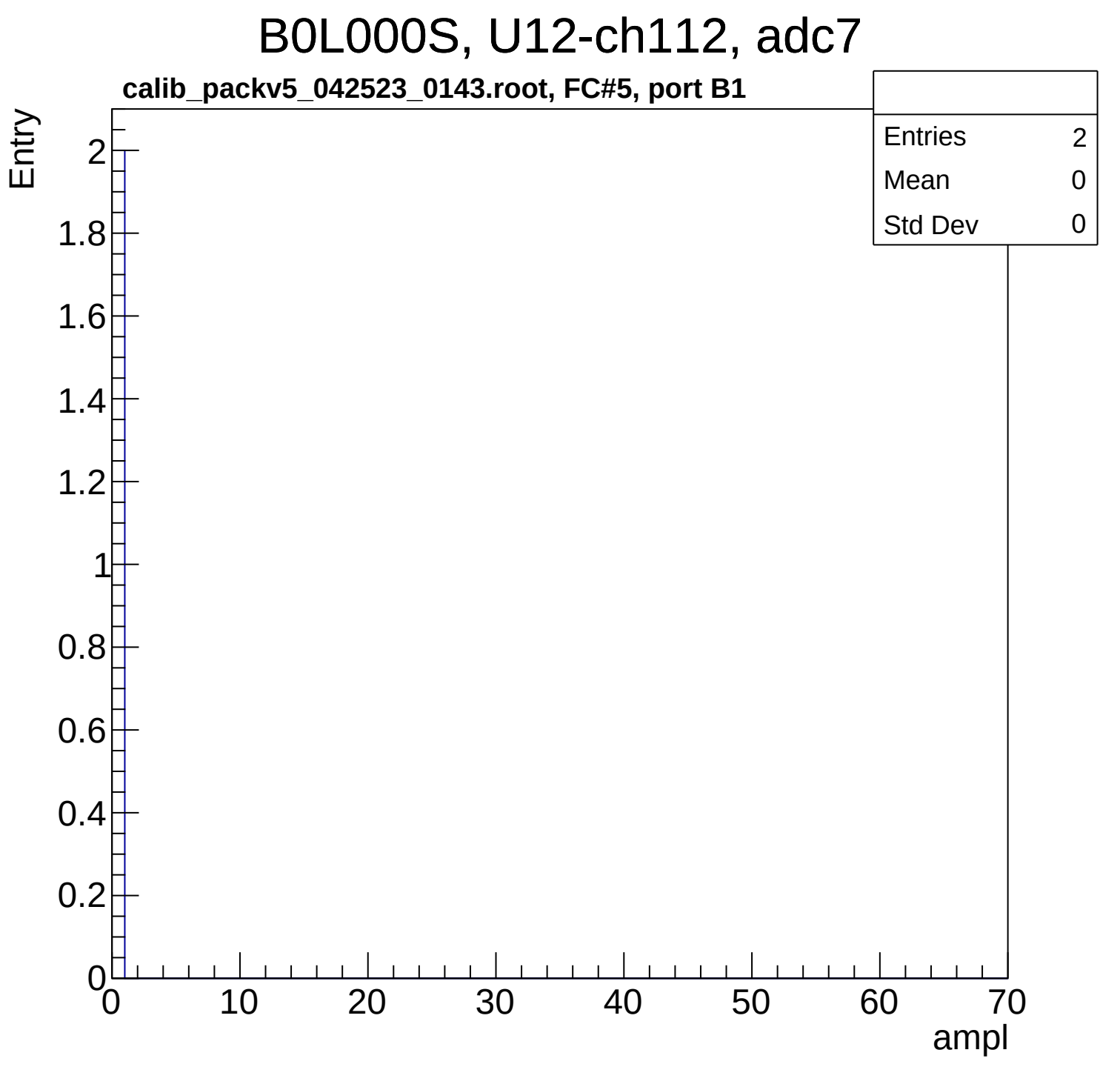
B0L000S, U12-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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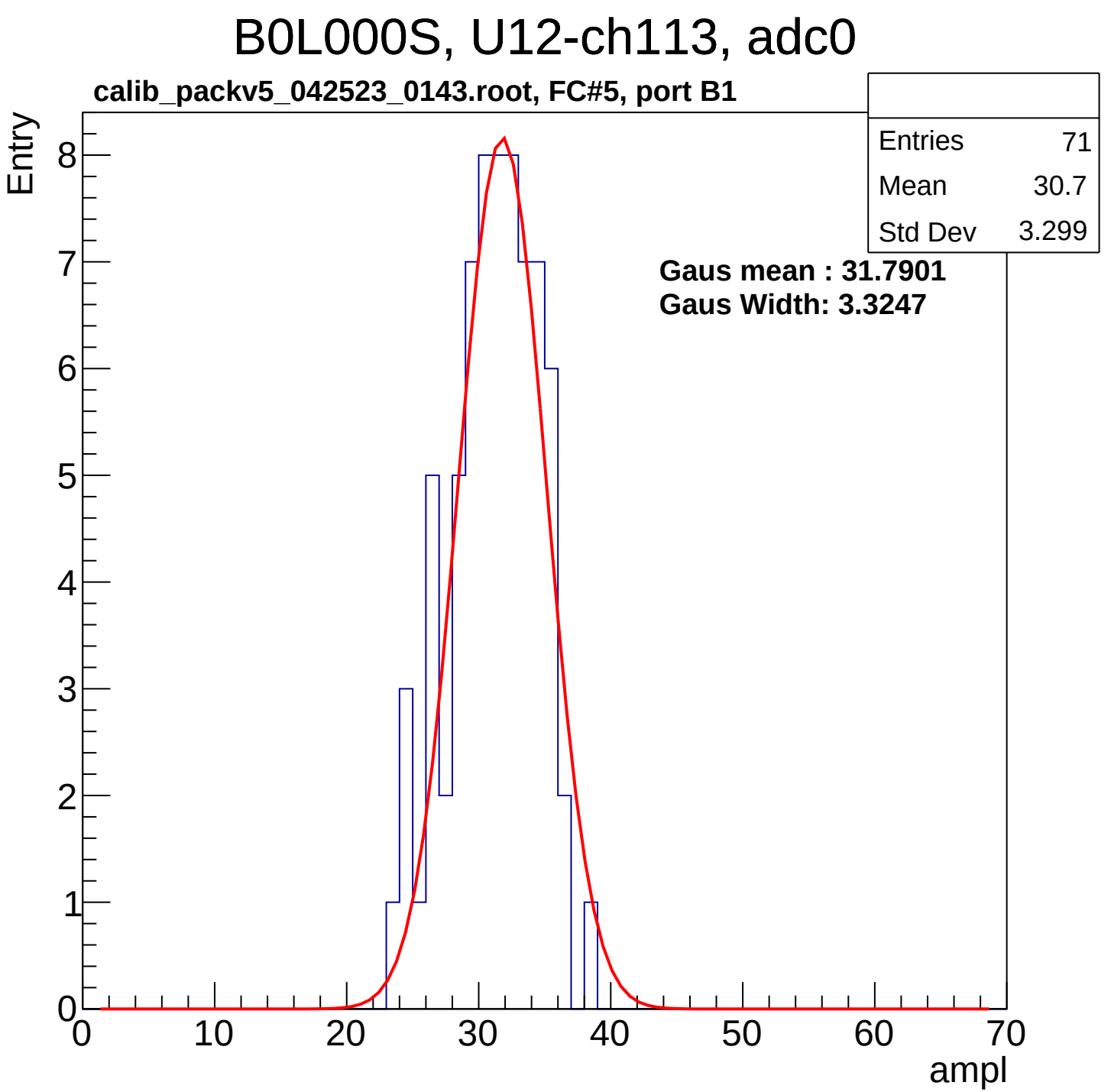
B0L000S, U12-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.7
Std Dev 3.299
Gaus mean : 31.7901
Gaus Width: 3.3247
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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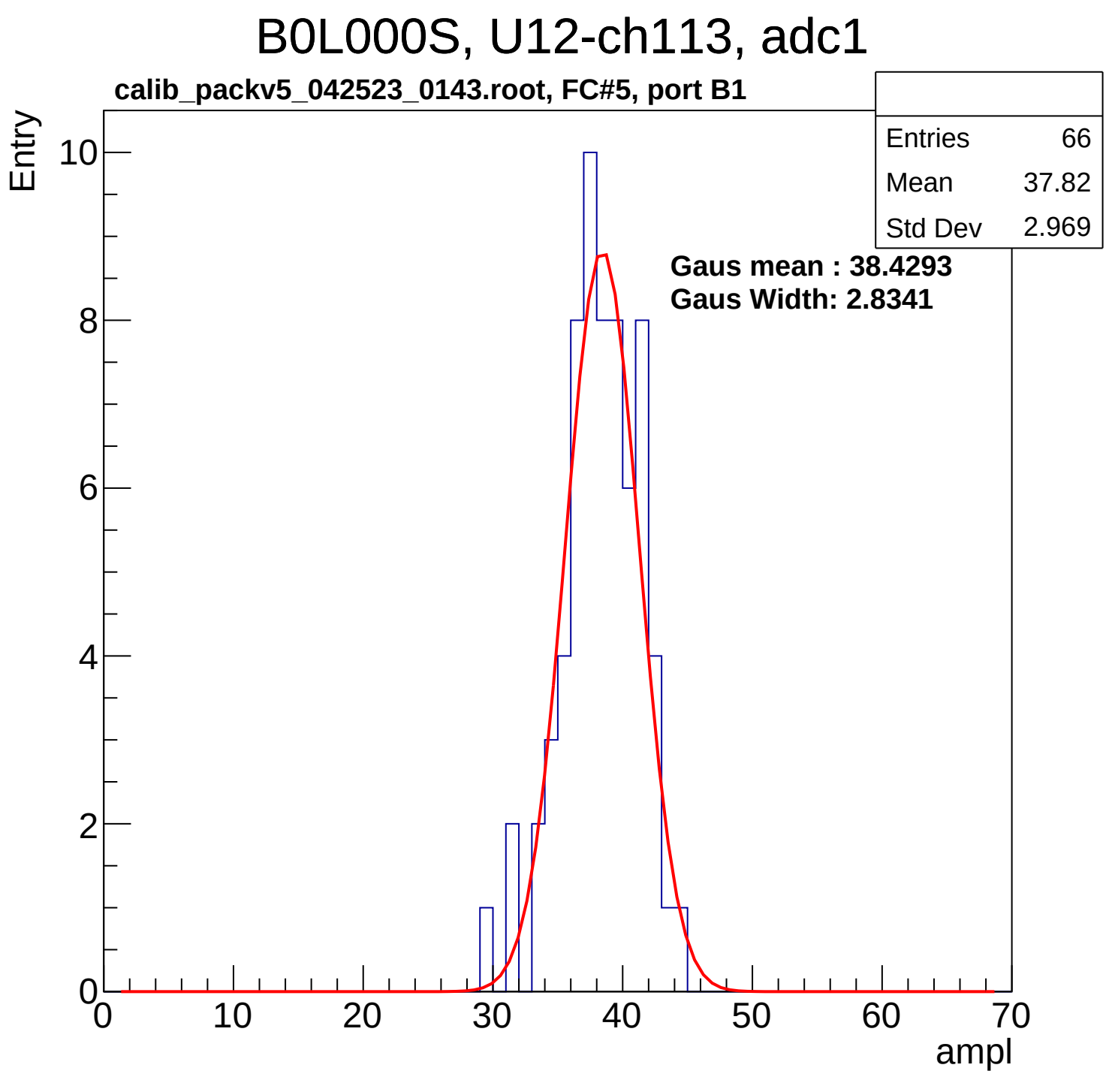
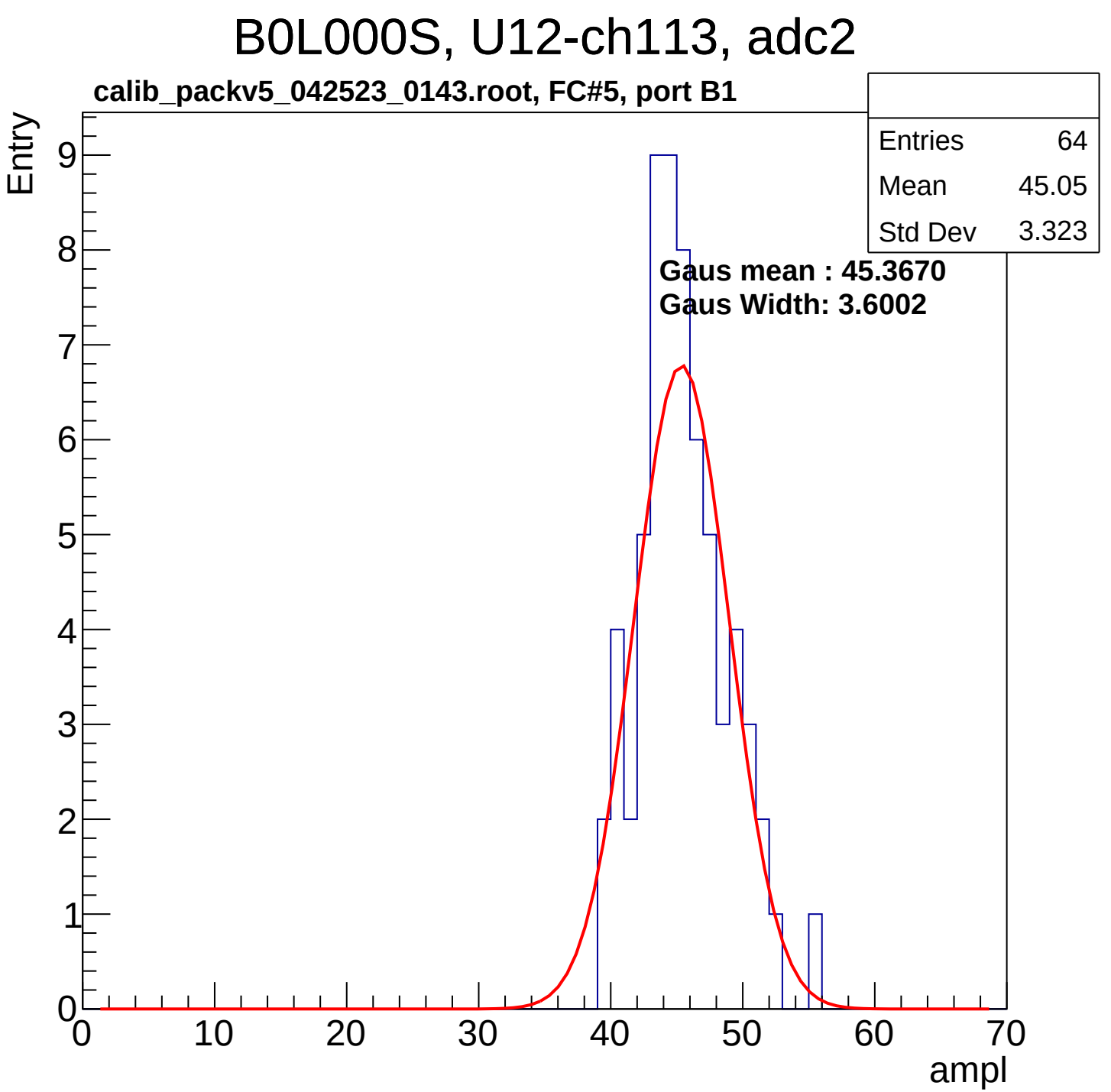

B0L000S, U12-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.82
Std Dev 2.969
Gaus mean : 38.4293
Gaus Width: 2.8341
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 45.05
Std Dev 3.323
Gaus mean : 45.3670
Gaus Width: 3.6002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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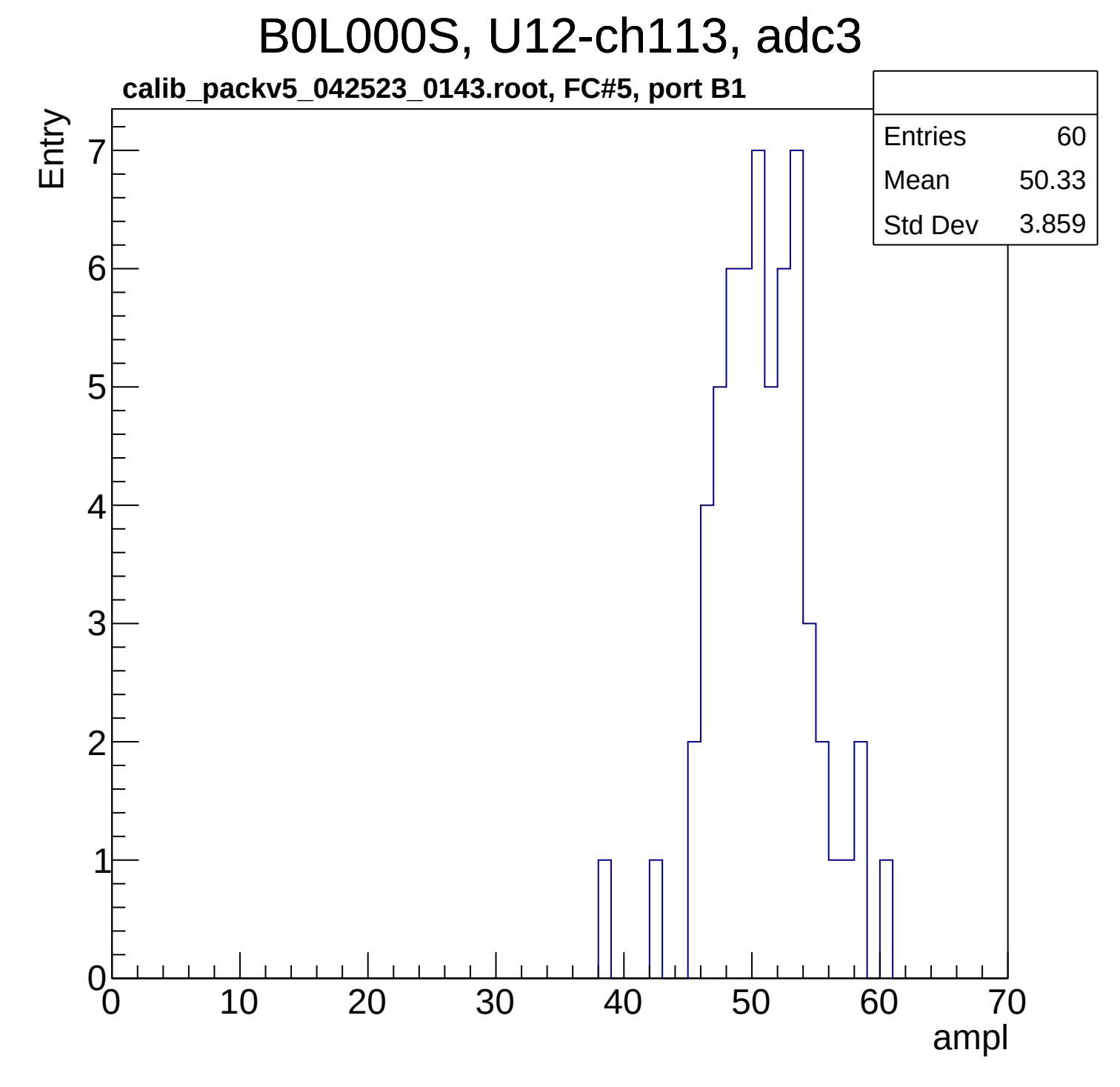
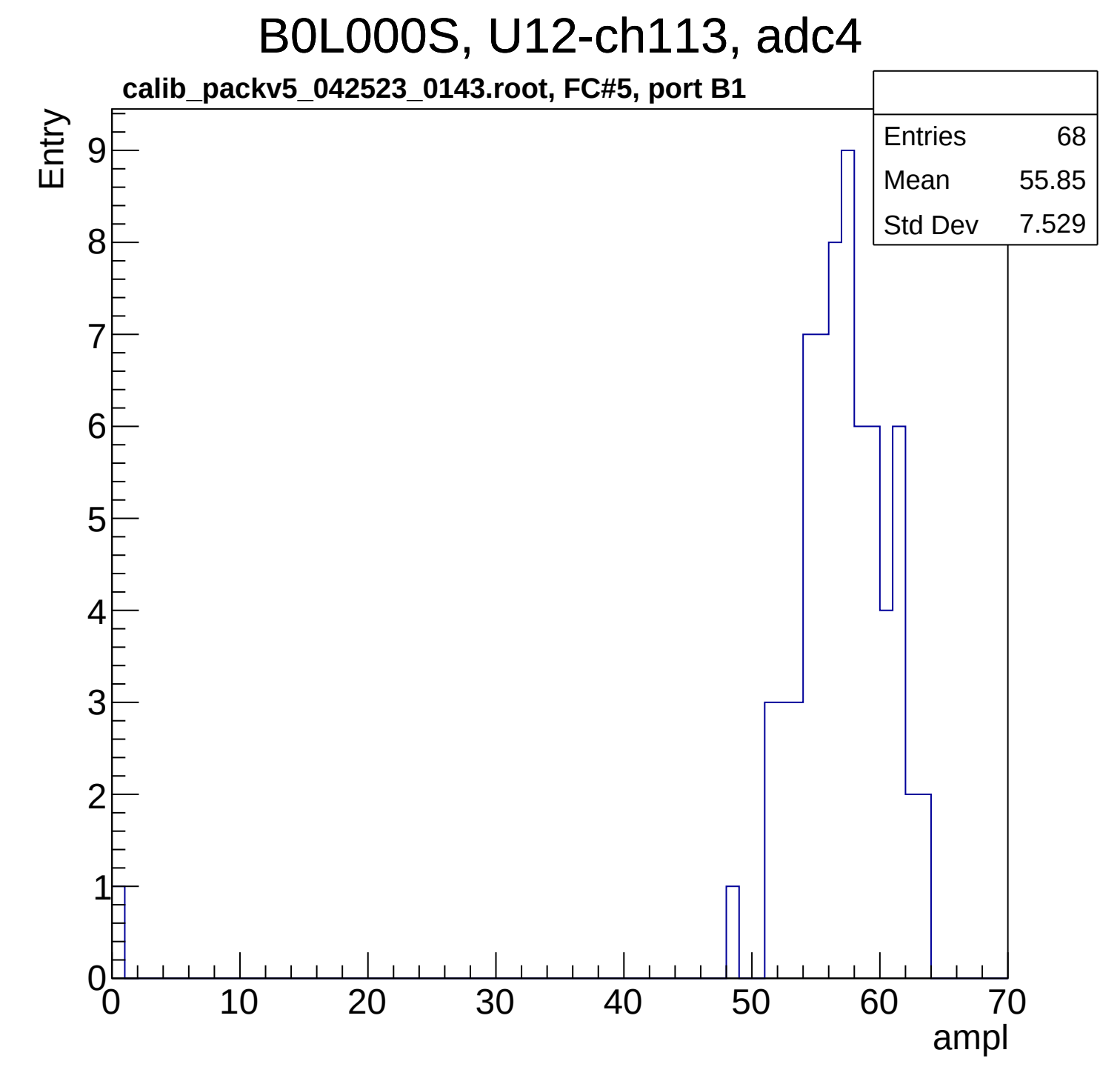
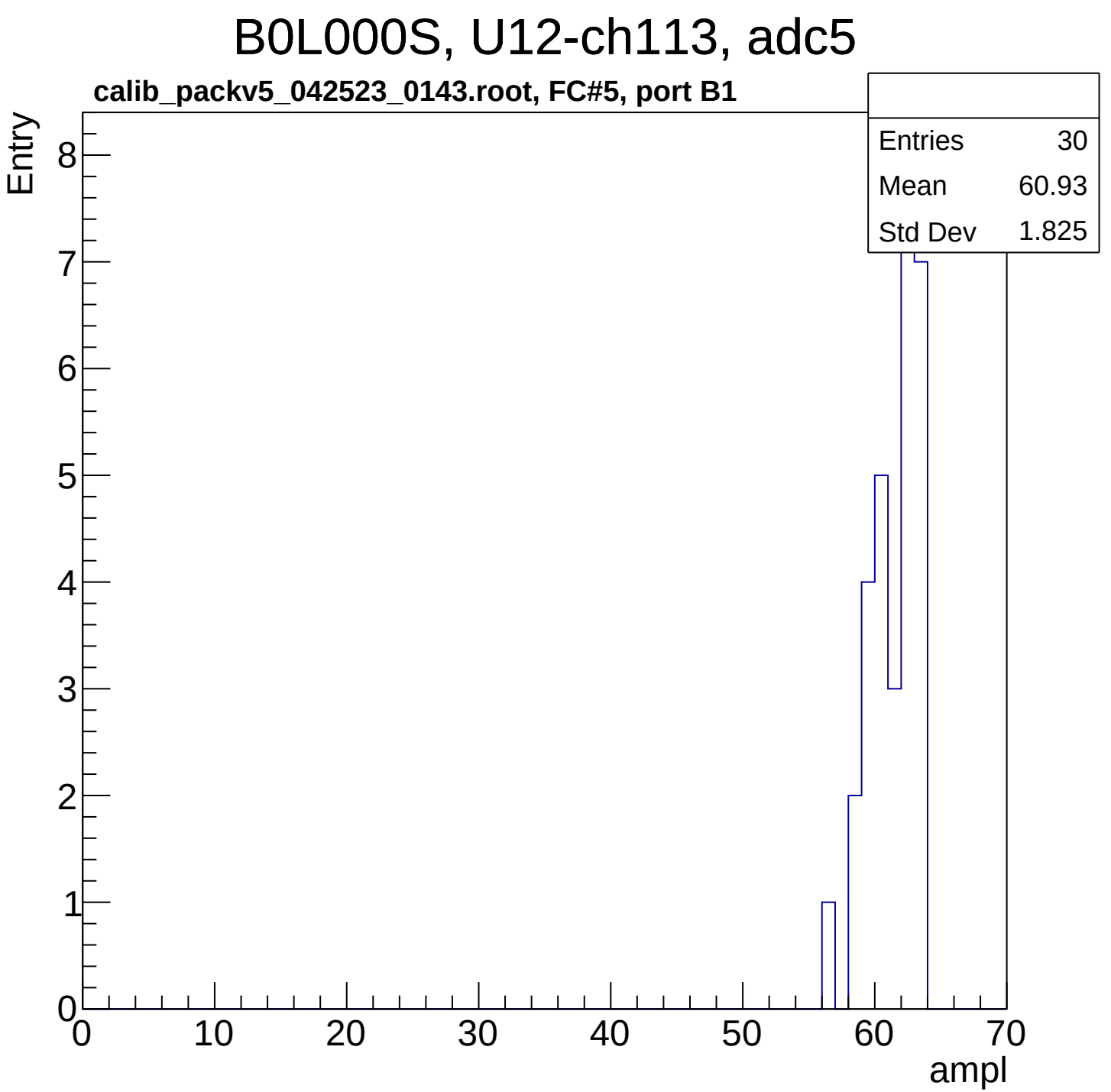

B0L000S, U12-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 50.33
Std Dev 3.859
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 55.85
Std Dev 7.529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.93
Std Dev 1.825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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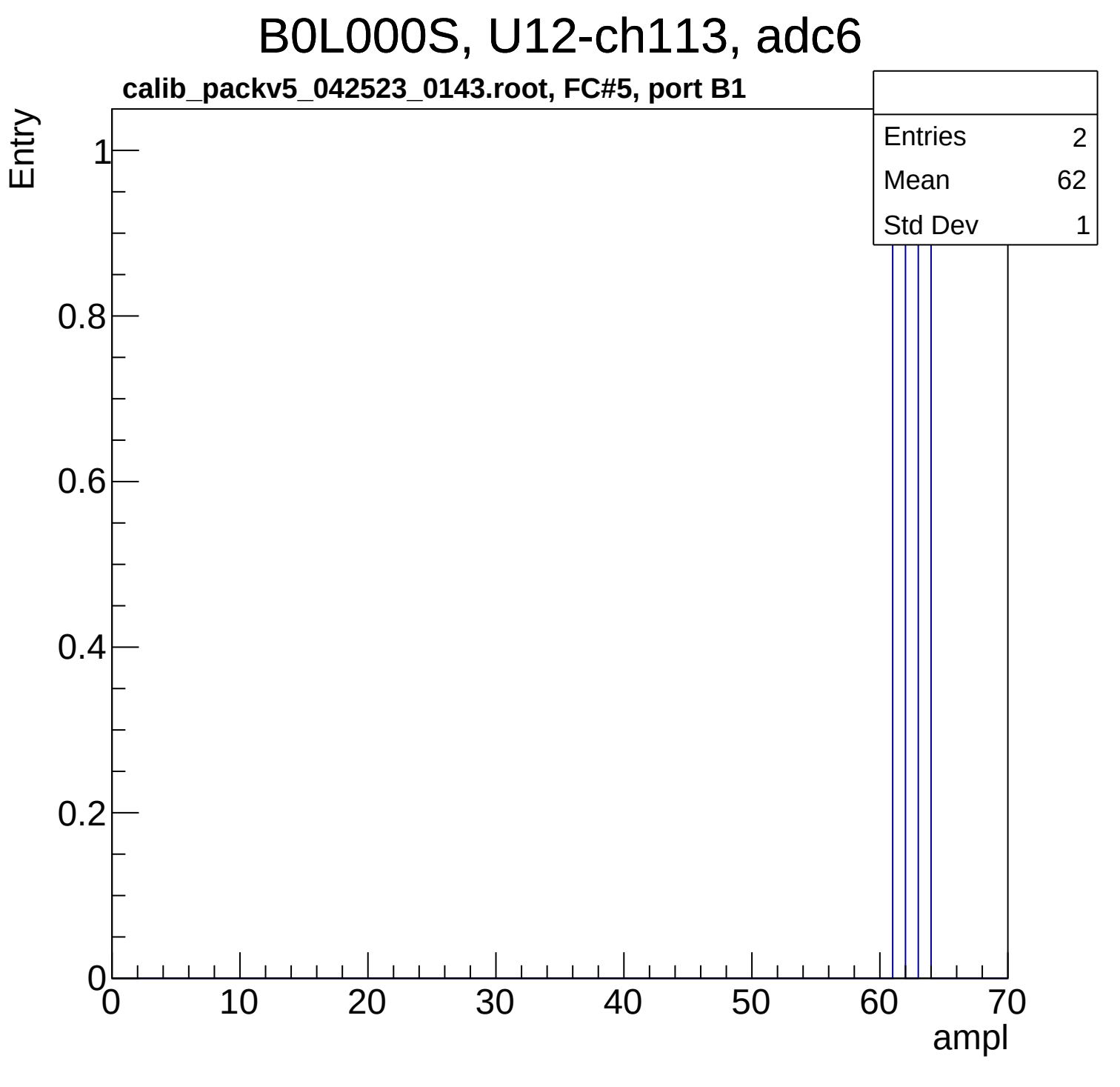
B0L000S, U12-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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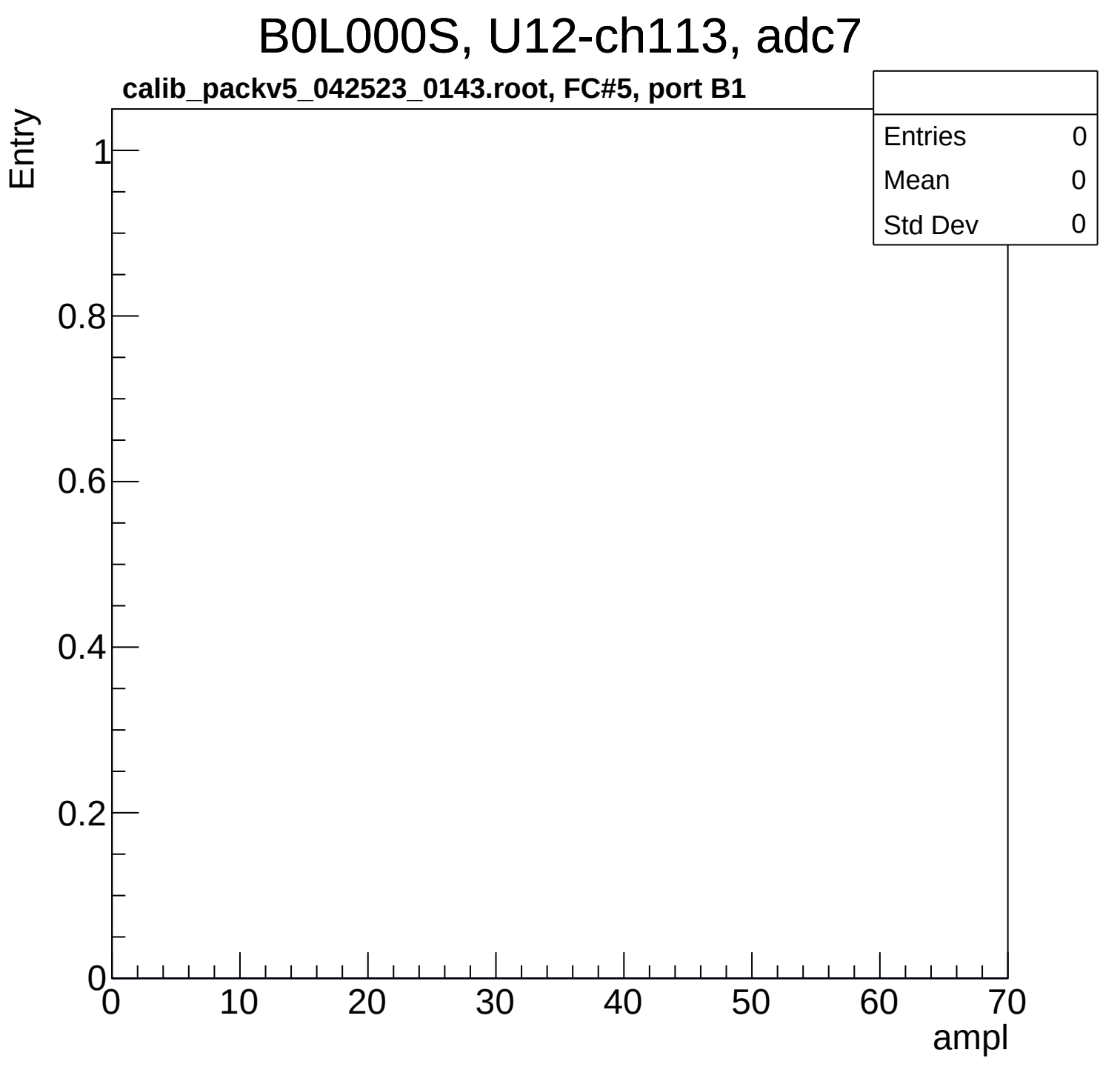
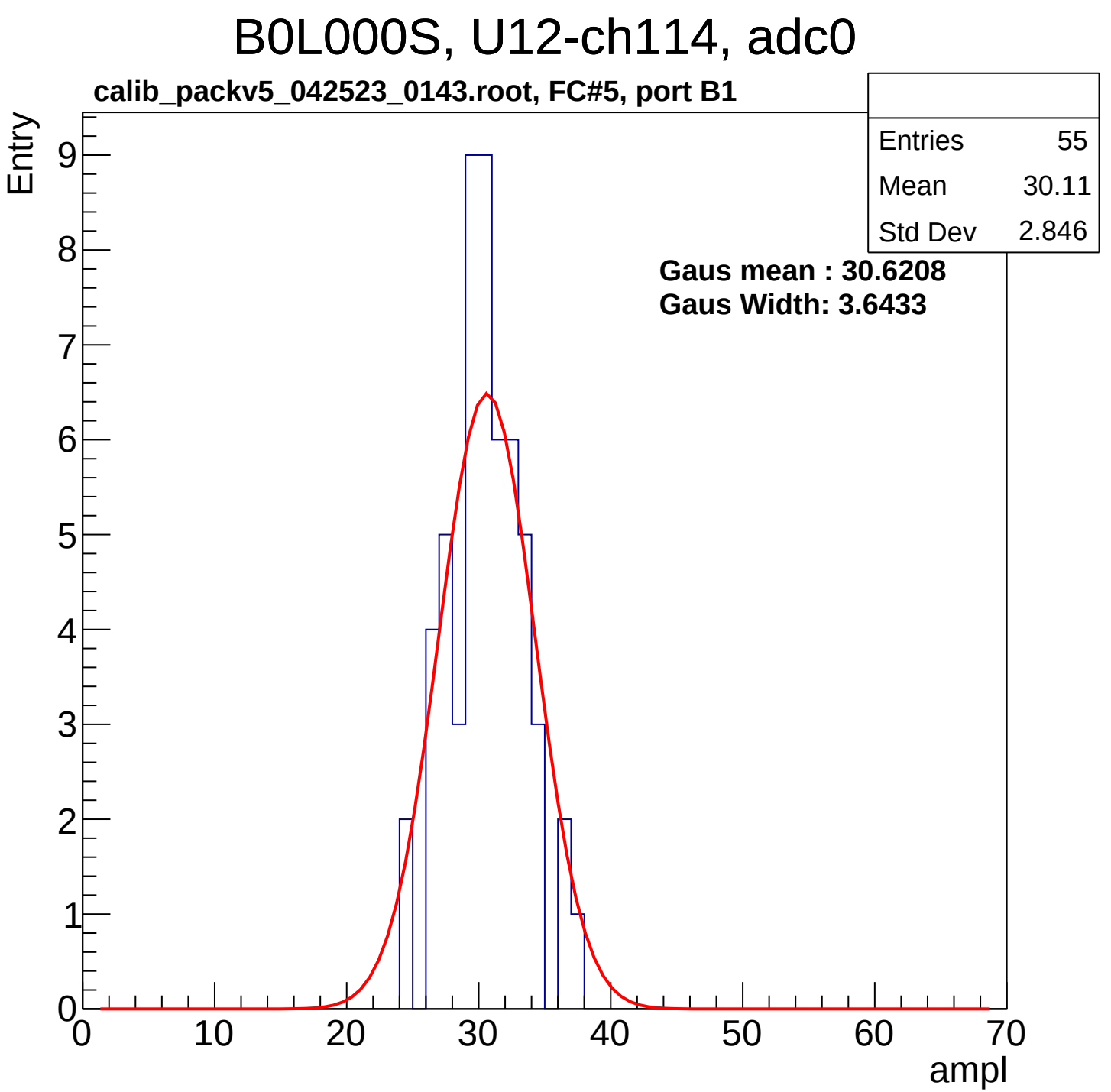
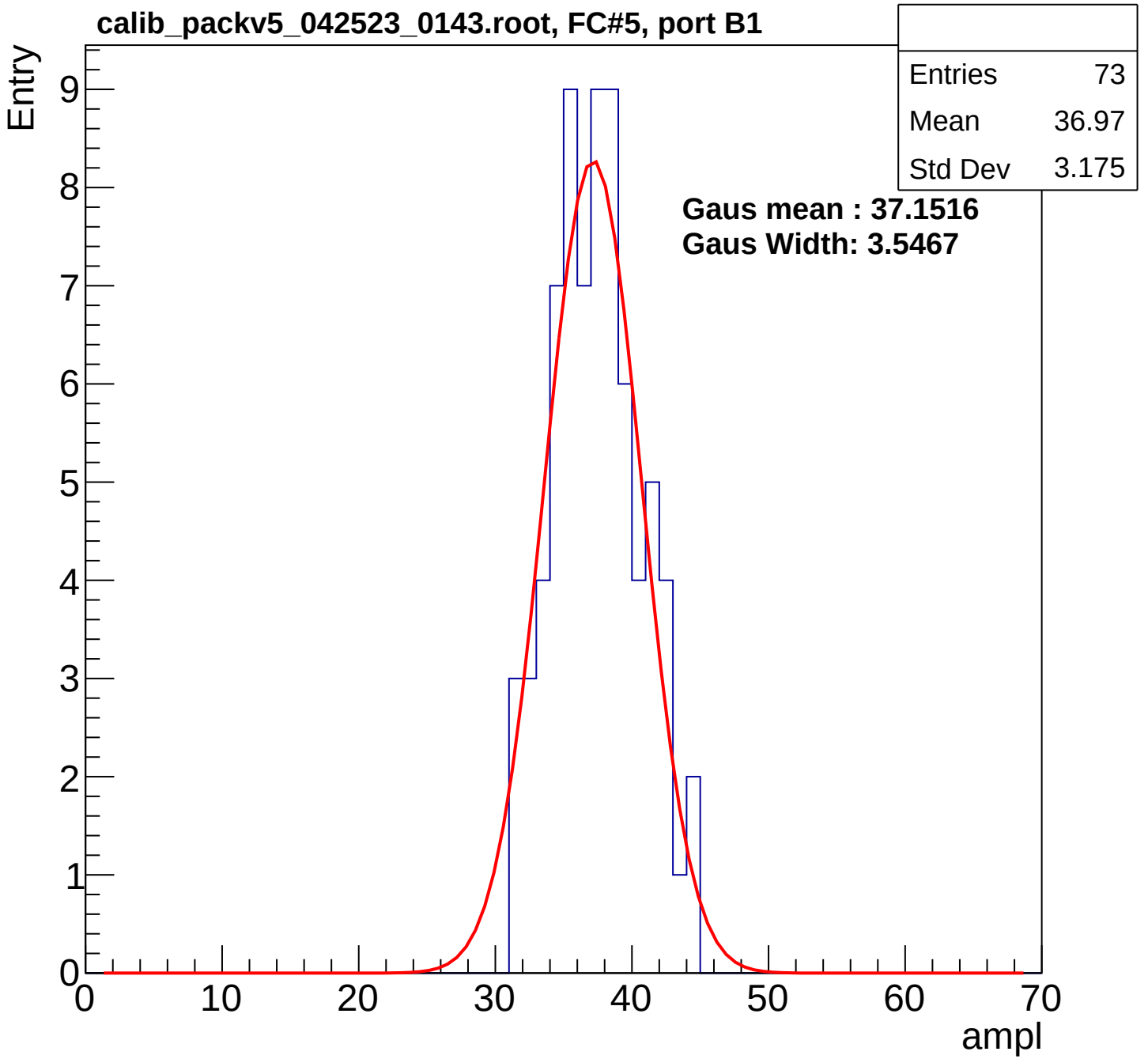
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.11
Std Dev 2.846
Gaus mean : 30.6208
Gaus Width: 3.6433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.97
Std Dev 3.175
Gaus mean : 37.1516
Gaus Width: 3.5467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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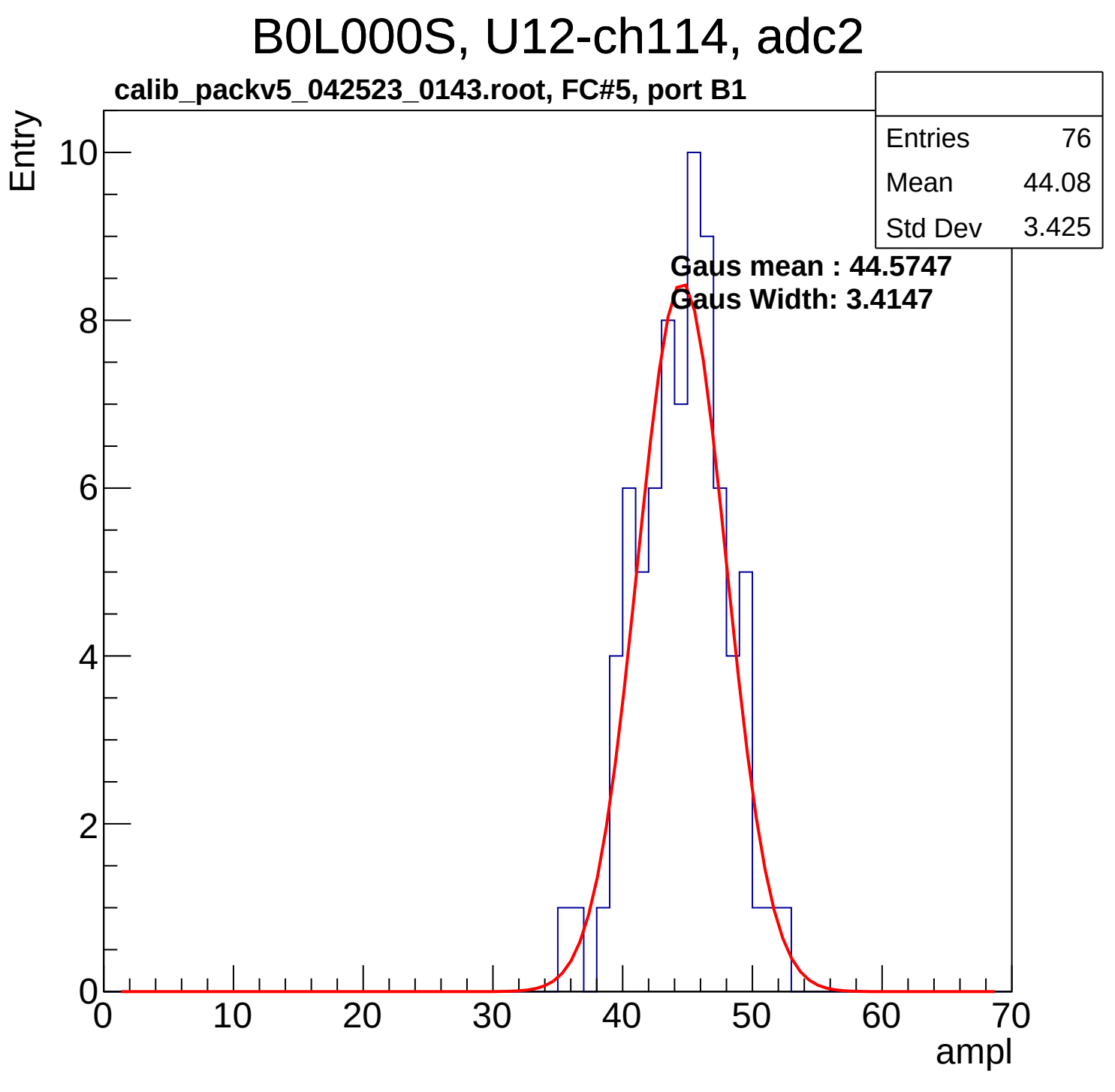
B0L000S, U12-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 44.08
Std Dev 3.425
Gaus mean : 44.5747
Gaus Width: 3.4147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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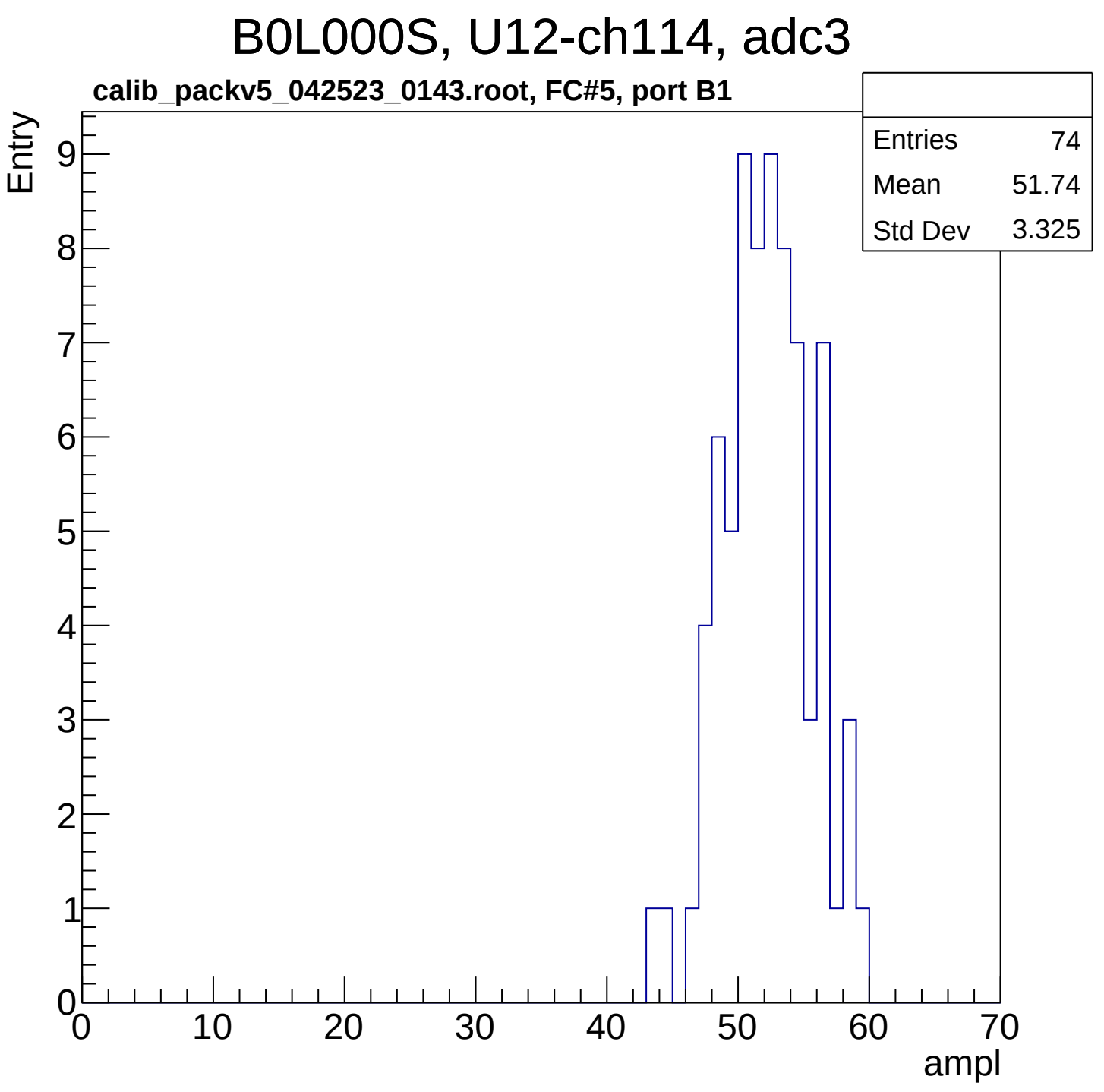
B0L000S, U12-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 51.74
Std Dev 3.325
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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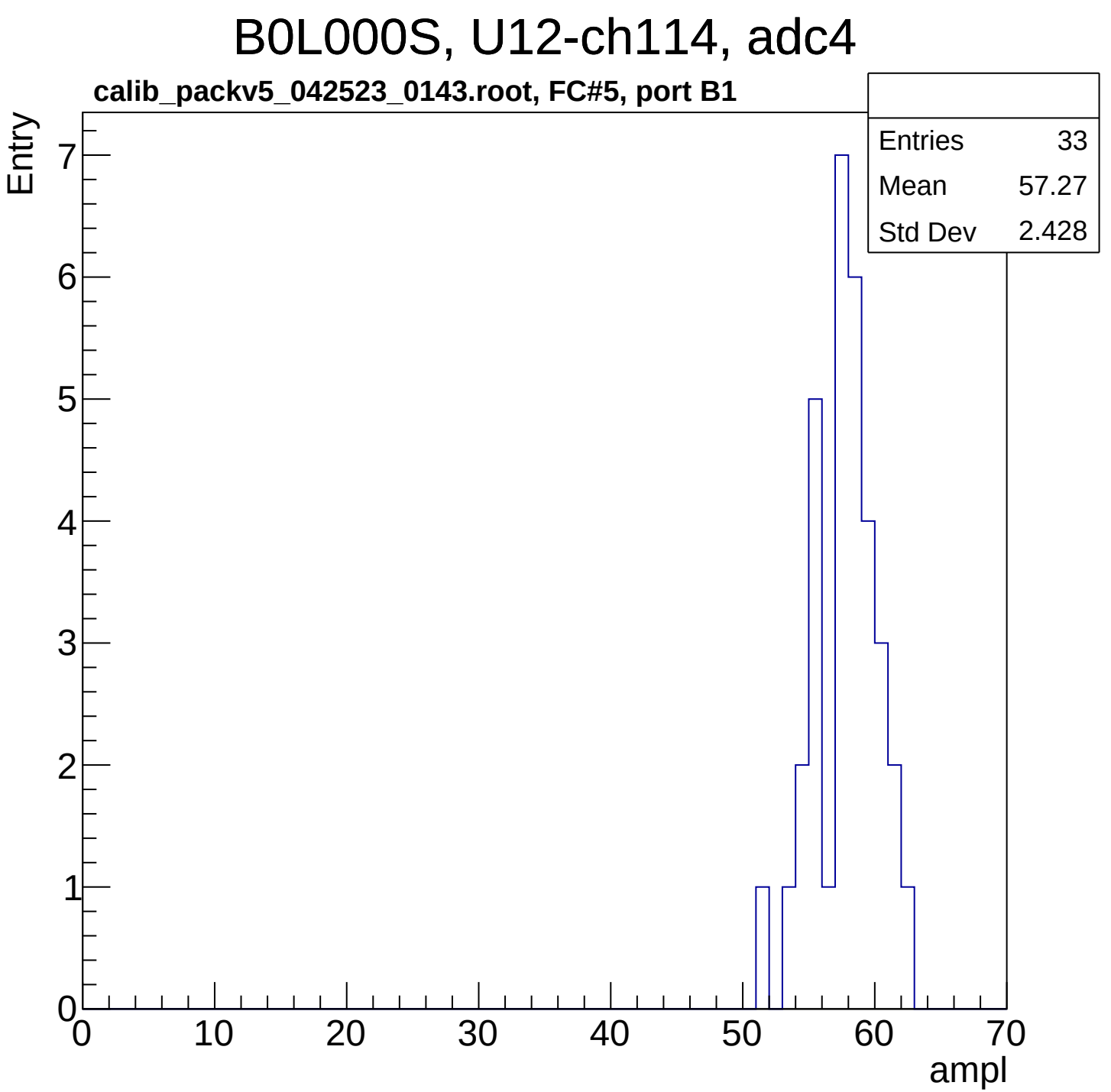
60
70


B0L000S, U12-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 57.27
Std Dev 2.428
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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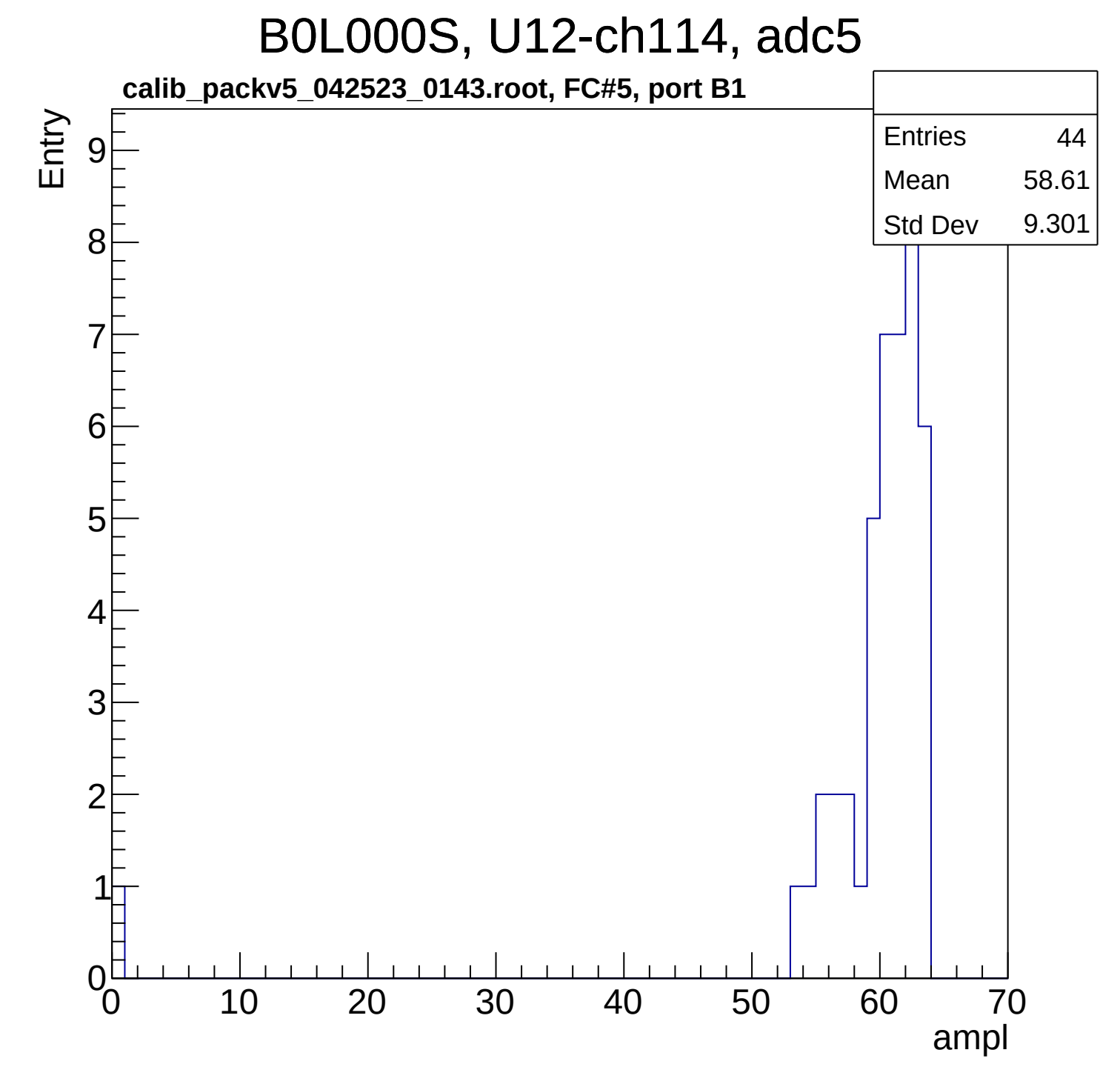
40
50
60
70


B0L000S, U12-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.61
Std Dev 9.301
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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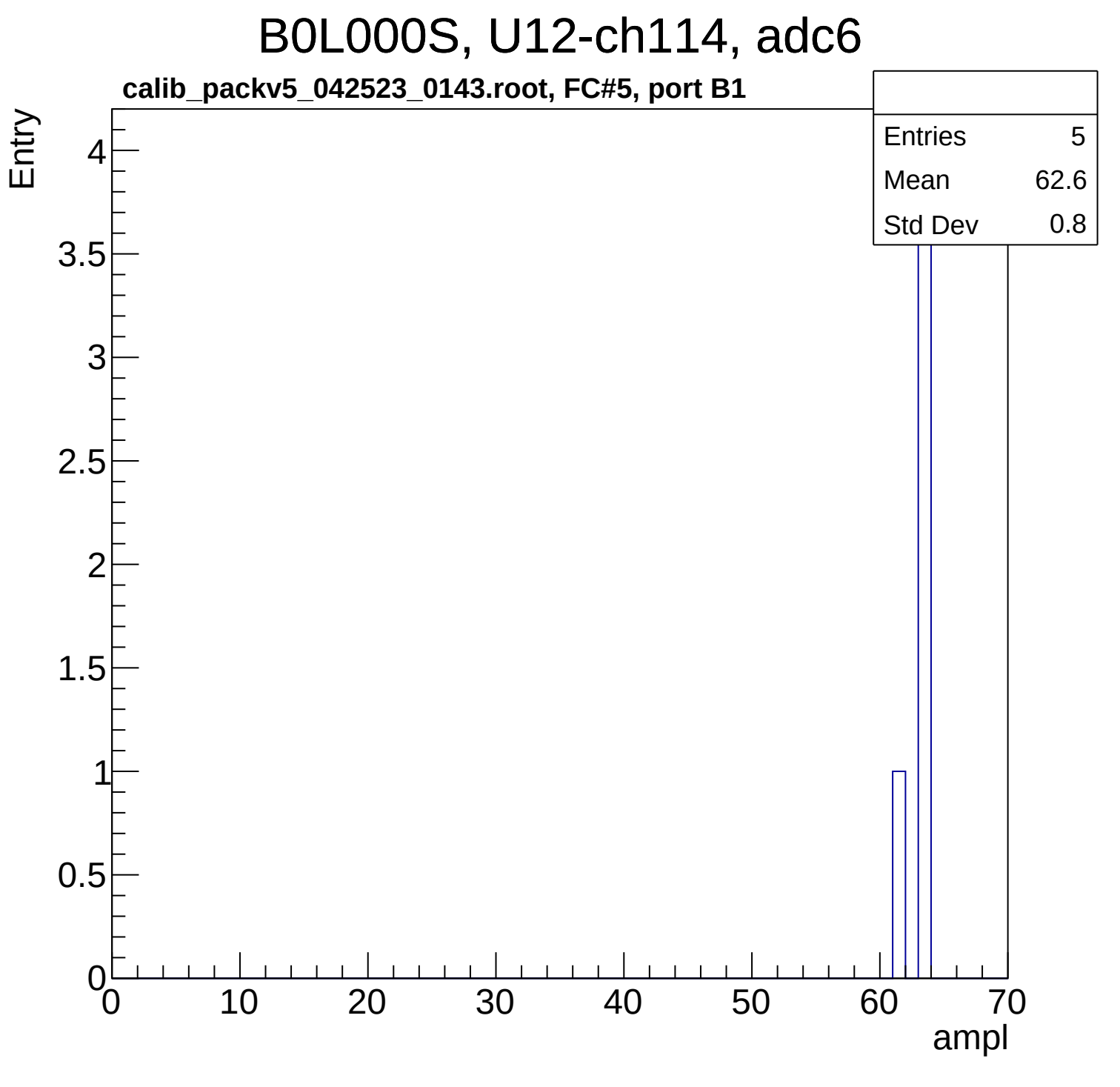
B0L000S, U12-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.6
Std Dev 0.8
Entry
ampl
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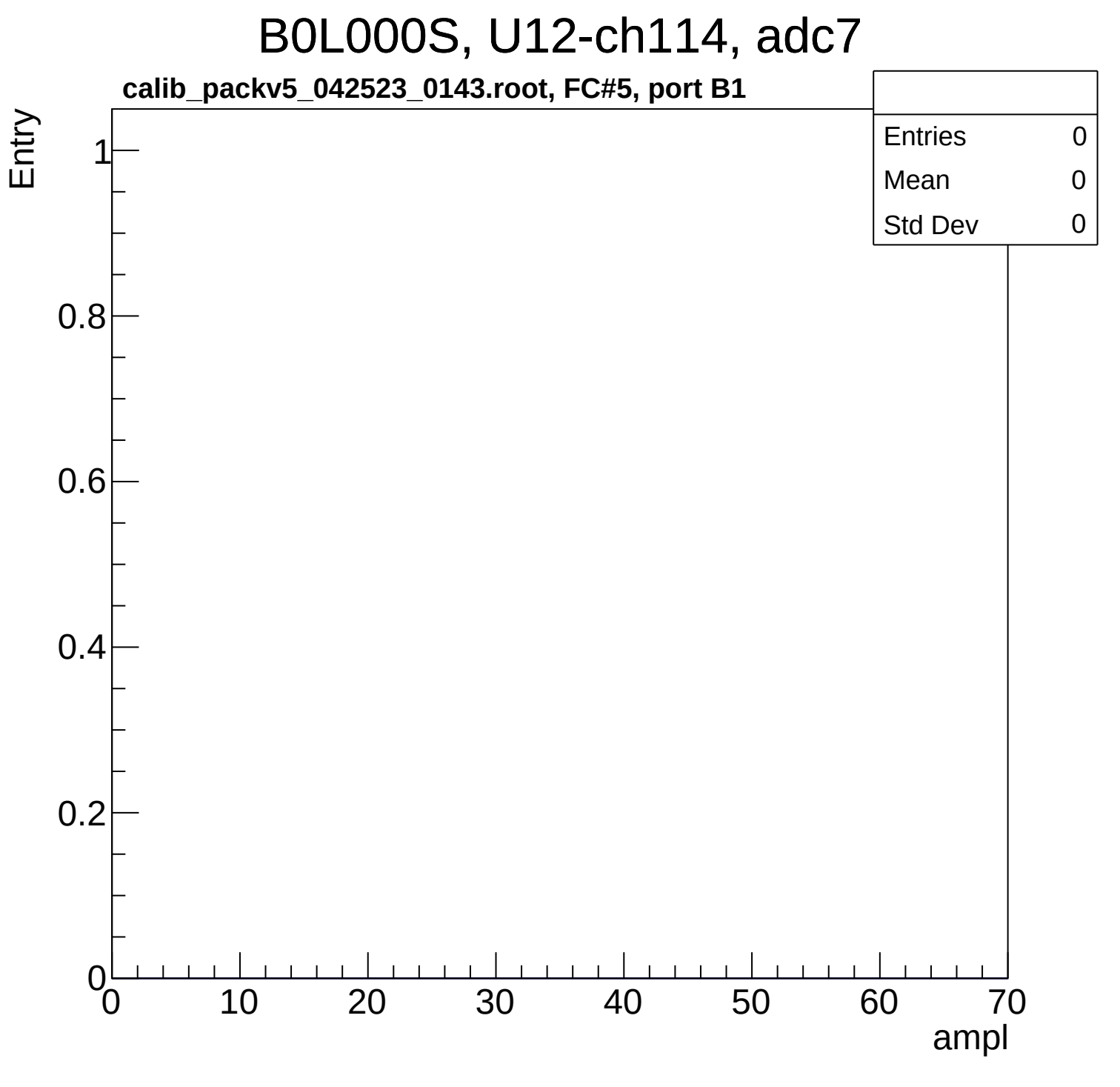
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U12-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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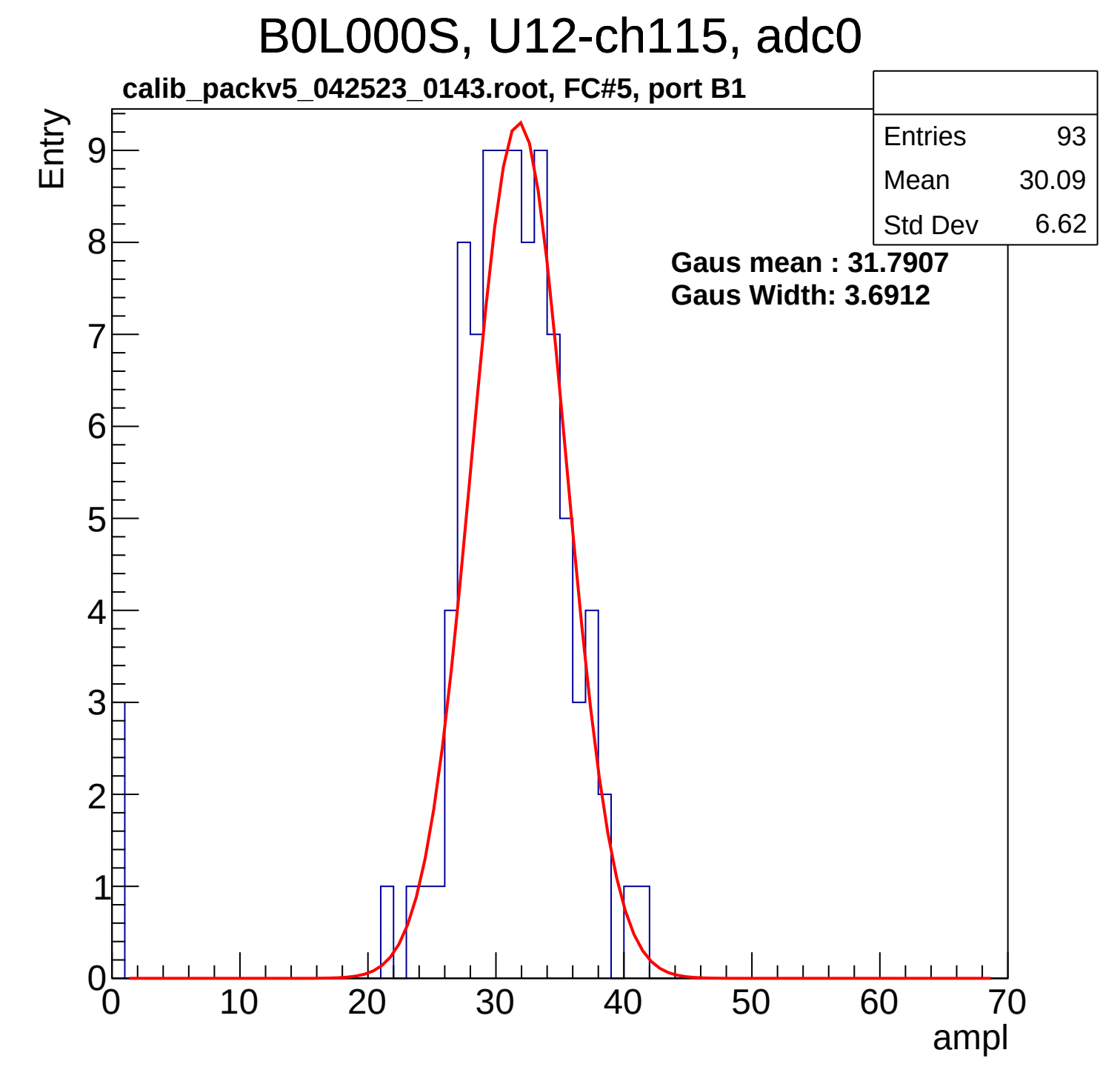
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 93
Mean 30.09
Std Dev 6.62
Gaus mean : 31.7907
Gaus Width: 3.6912
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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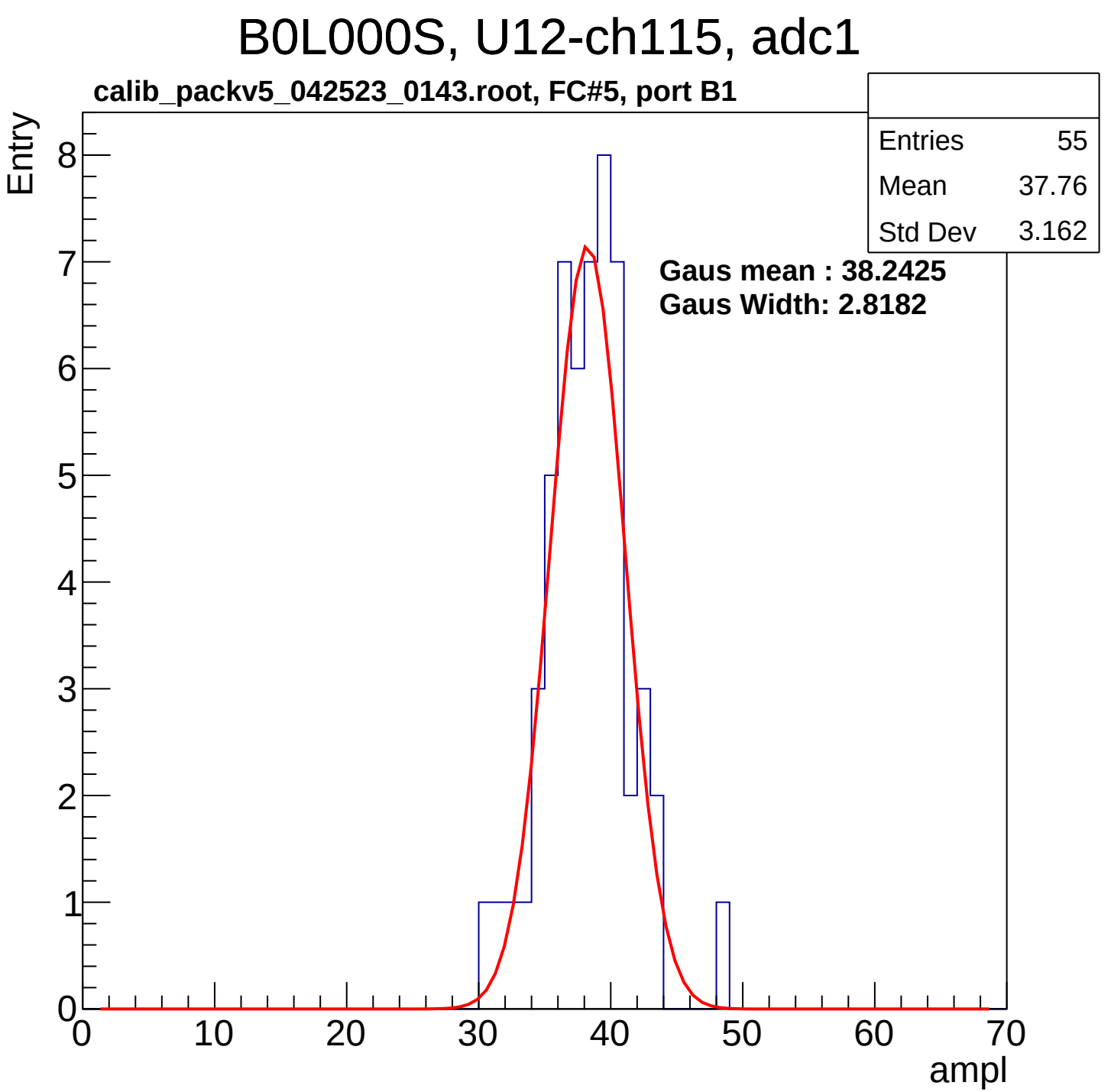
60
70


B0L000S, U12-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 37.76
Std Dev 3.162
Gaus mean : 38.2425
Gaus Width: 2.8182
Entry
ampl
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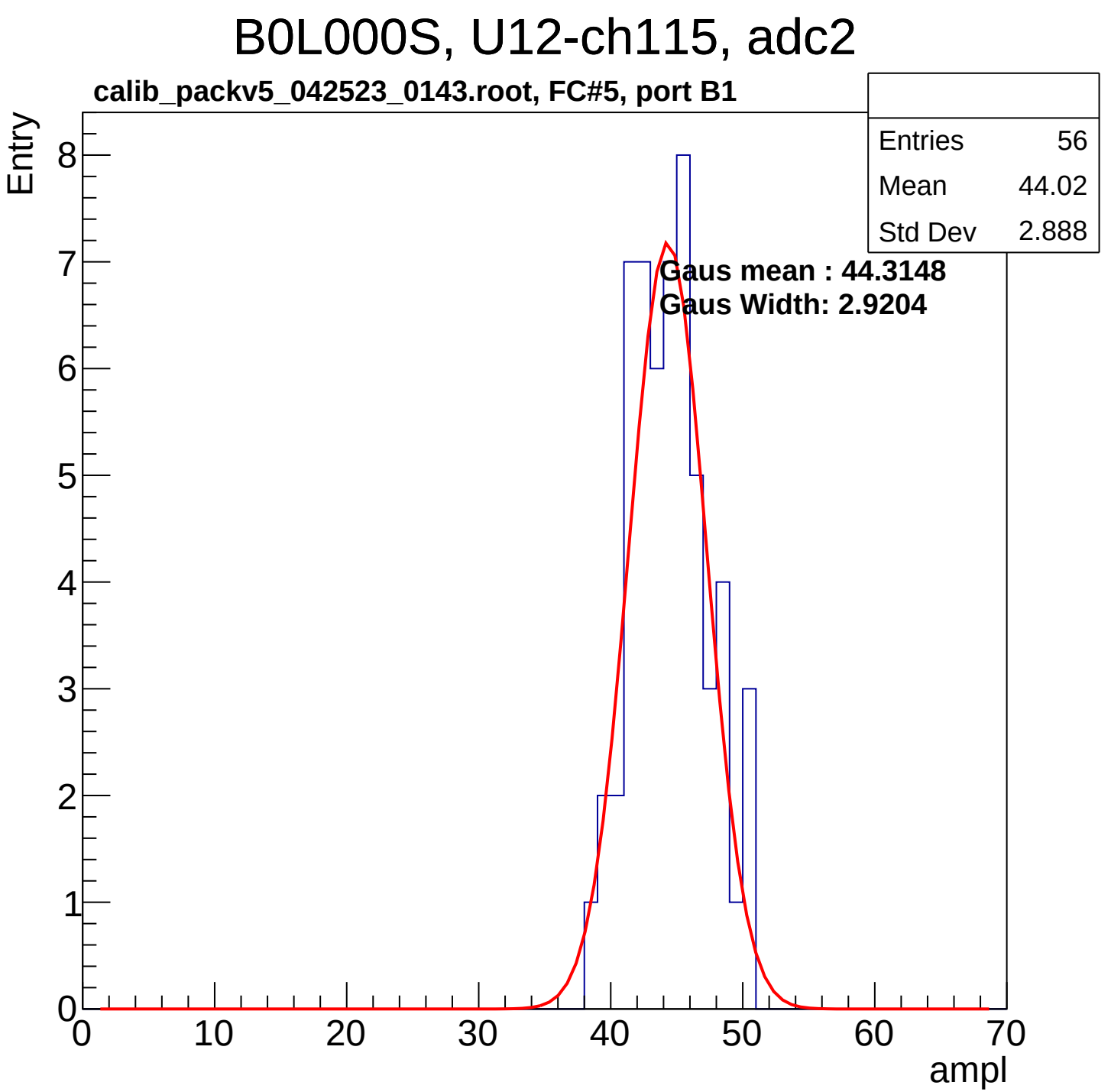
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.02
Std Dev 2.888
Gaus mean : 44.3148
Gaus Width: 2.9204
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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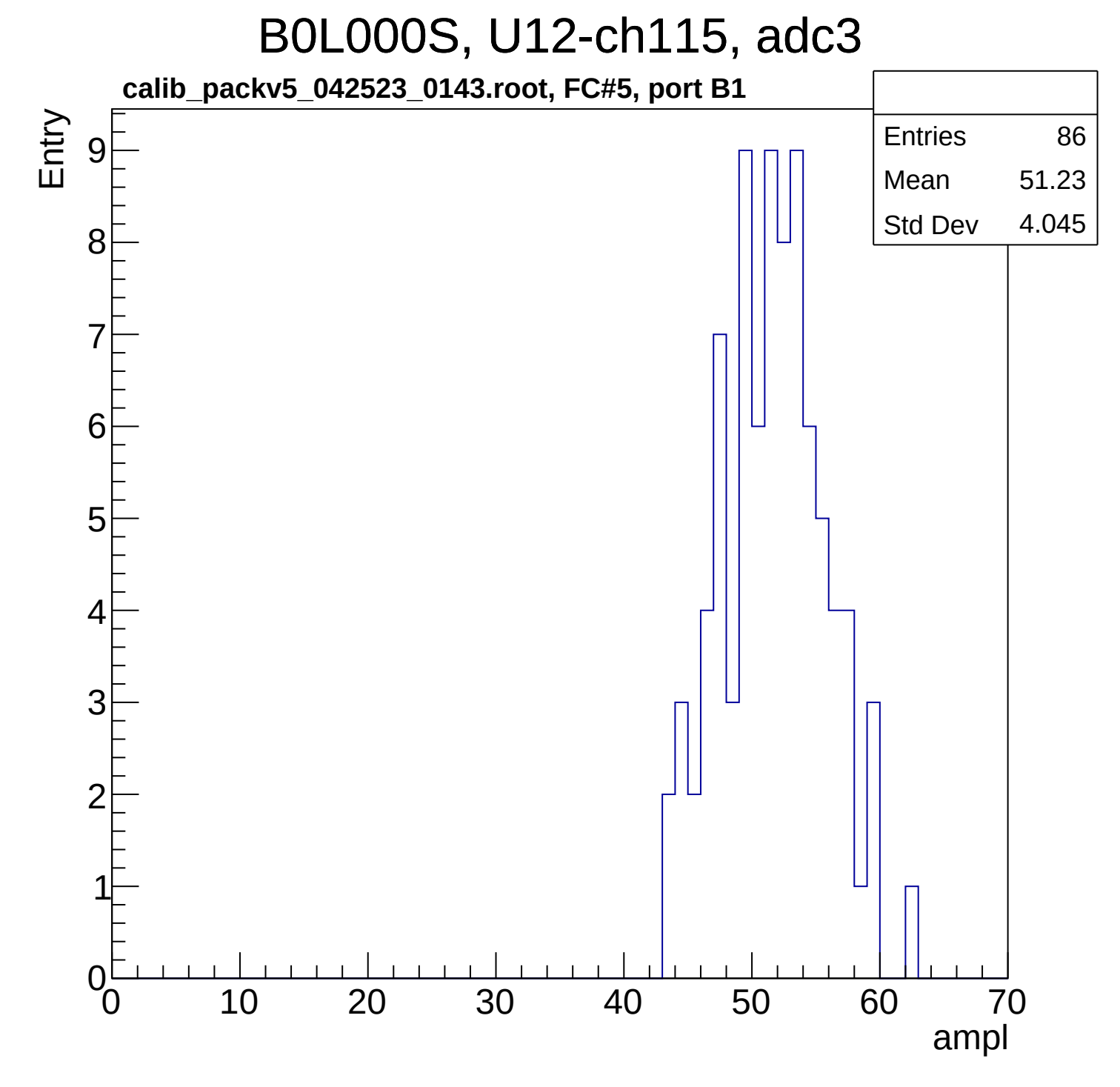
B0L000S, U12-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 51.23
Std Dev 4.045
Entry
ampl
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2
3
4
5
6
7
8
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0
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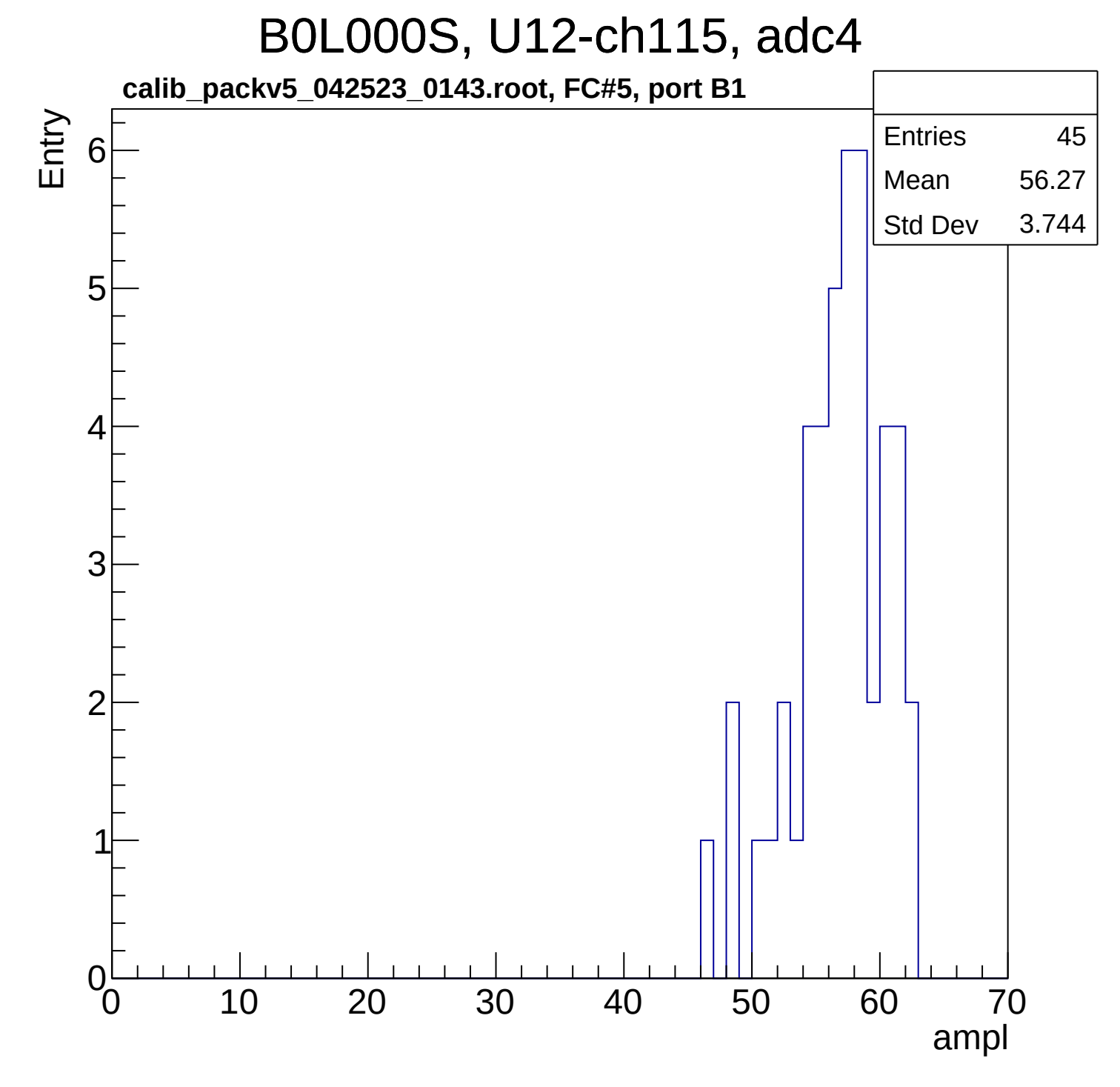
B0L000S, U12-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.27
Std Dev 3.744
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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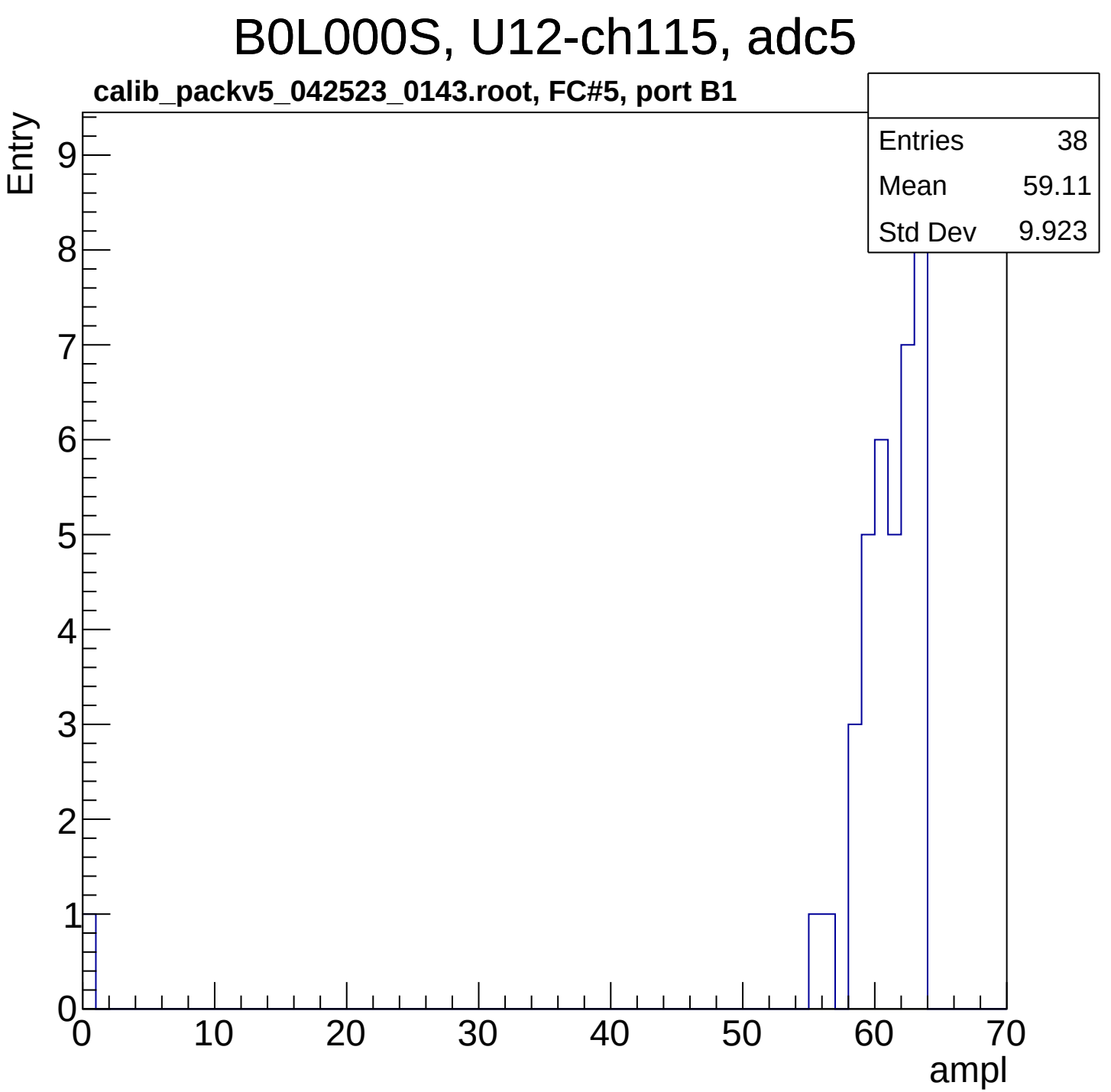
B0L000S, U12-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.11
Std Dev 9.923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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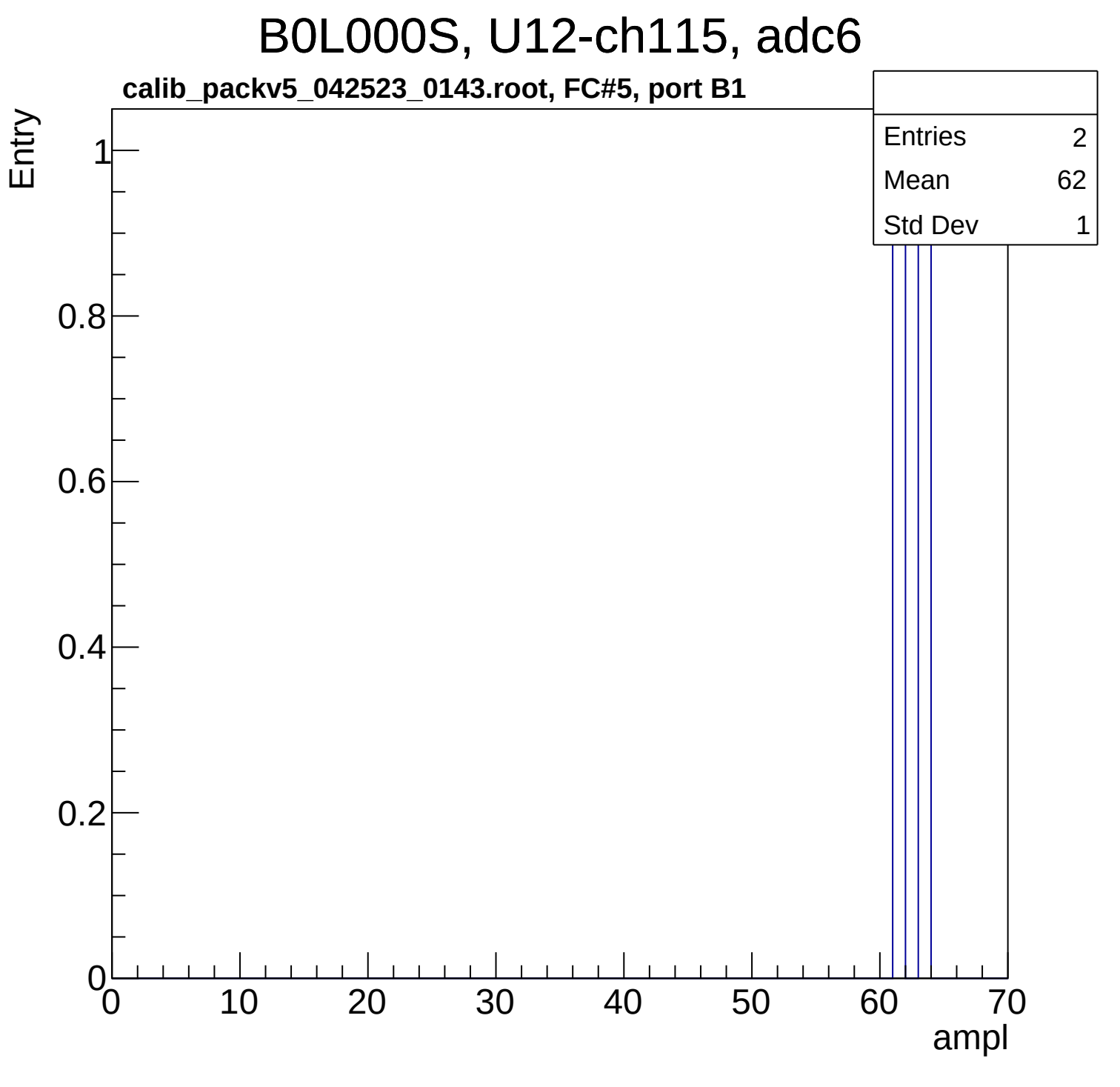
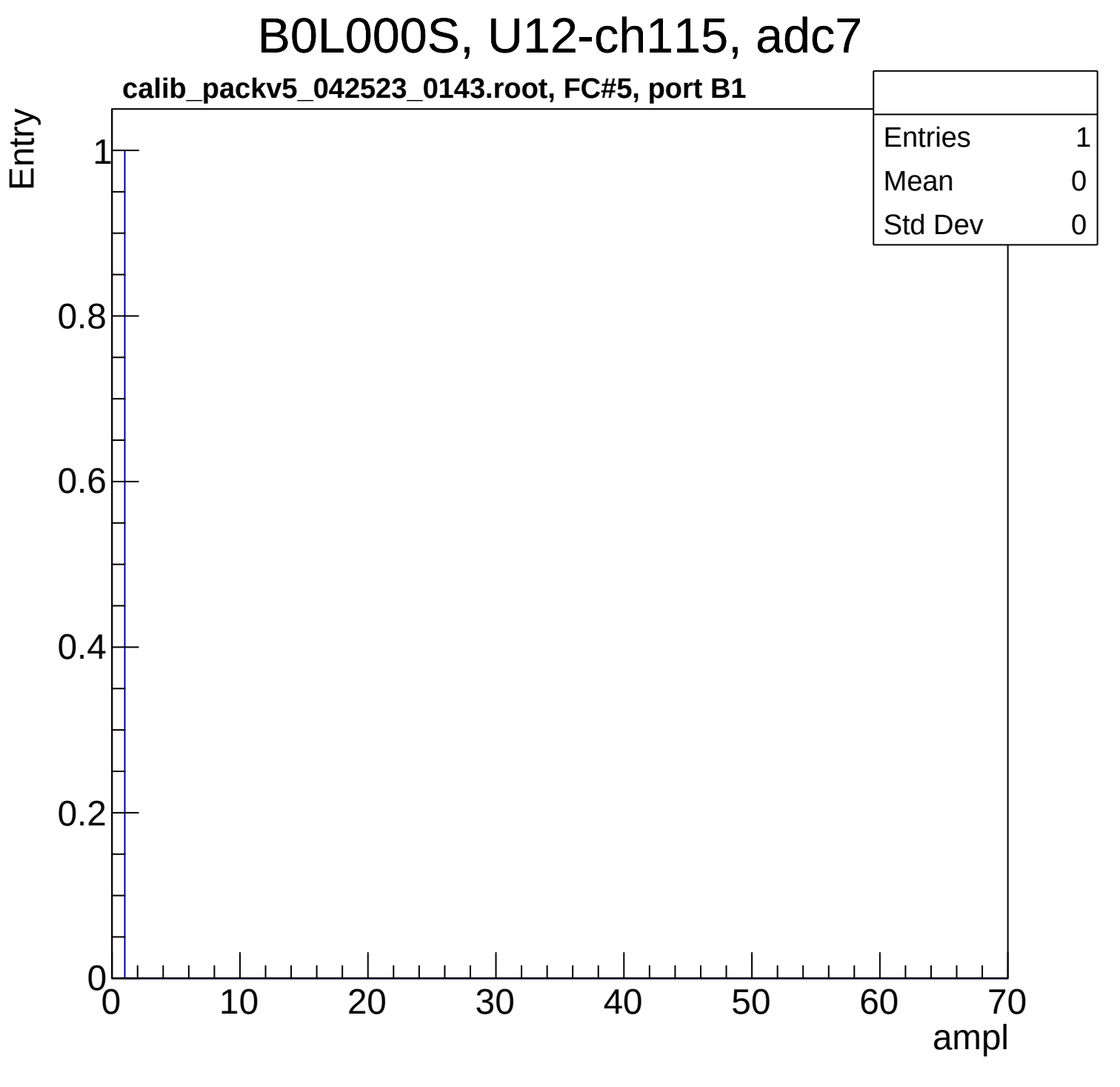

B0L000S, U12-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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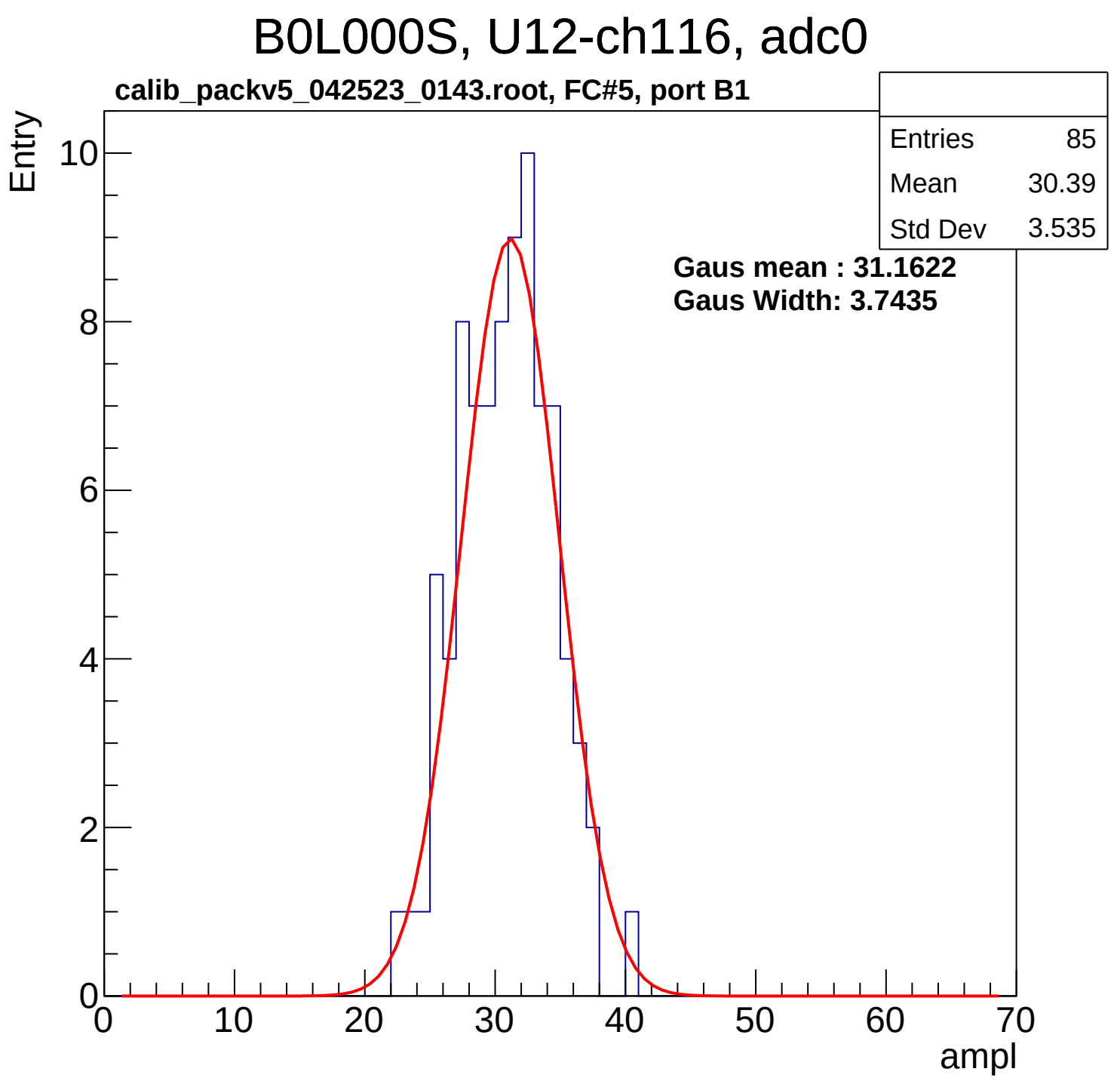
50
60
70
ampl


B0L000S, U12-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.39
Std Dev 3.535
Gaus mean : 31.1622
Gaus Width: 3.7435
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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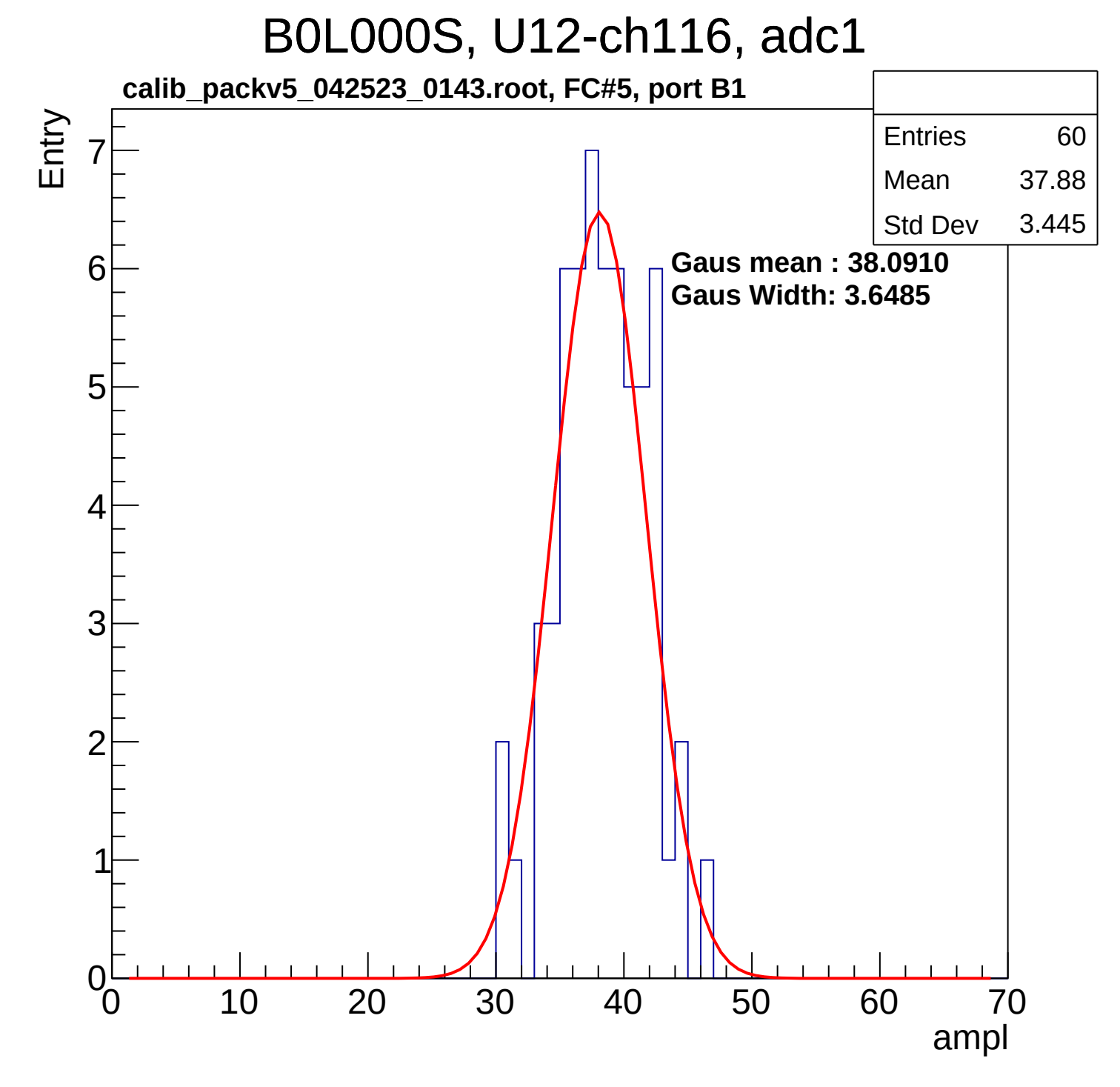
B0L000S, U12-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.88
Std Dev 3.445
Gaus mean : 38.0910
Gaus Width: 3.6485
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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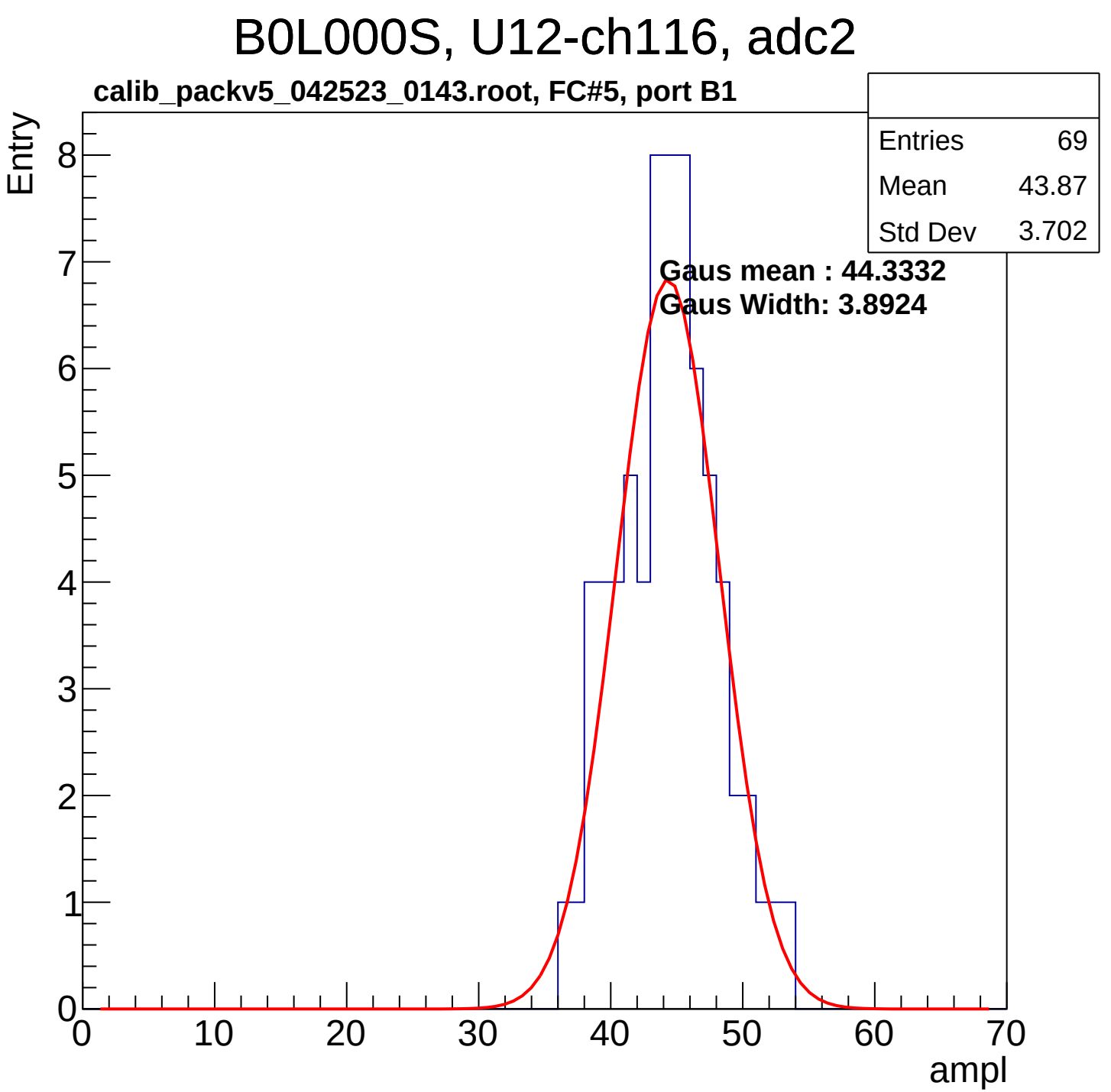
B0L000S, U12-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.87
Std Dev 3.702
Gaus mean : 44.3332
Gaus Width: 3.8924
Entry
ampl
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1
2
3
4
5
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0
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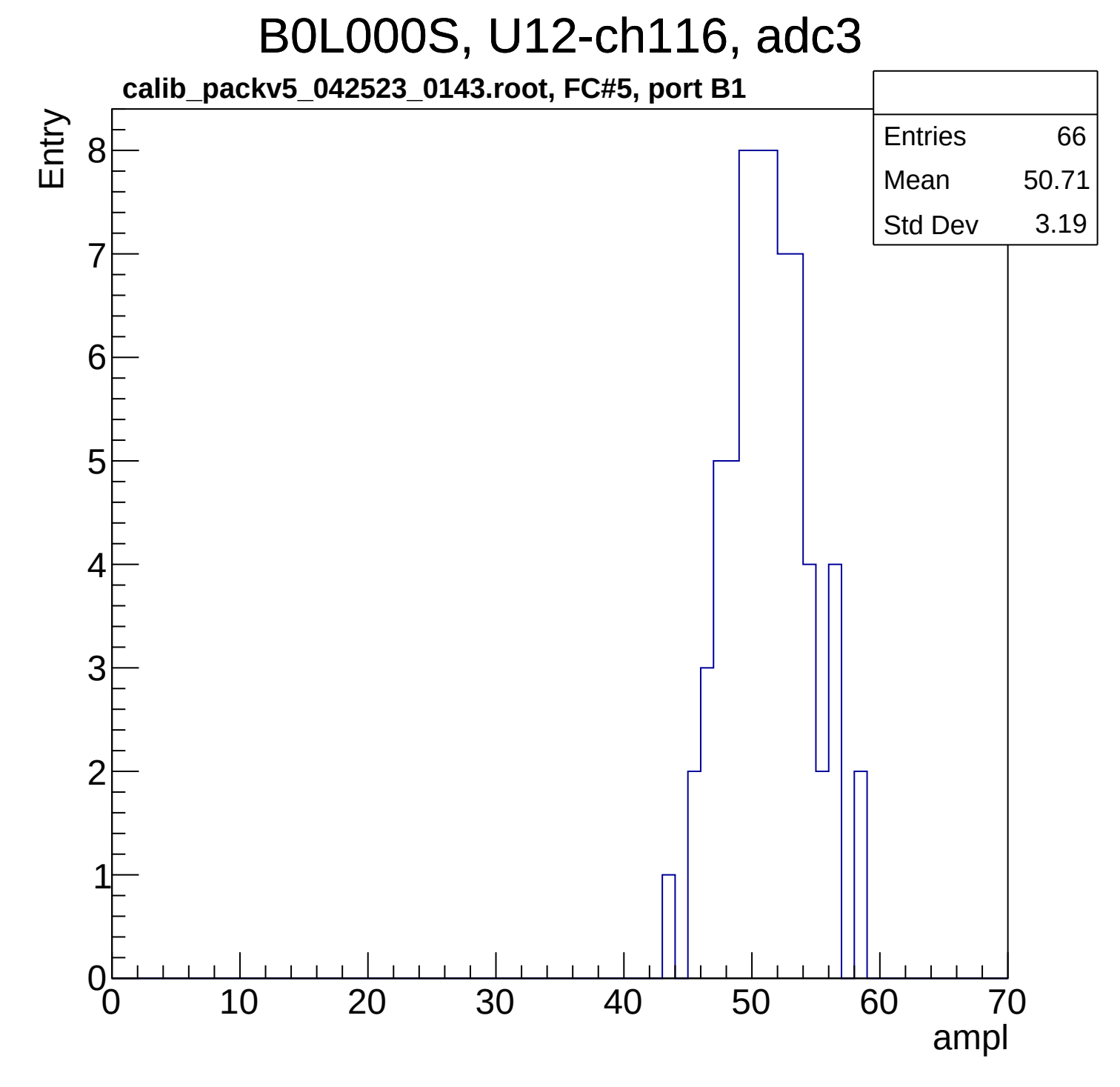
B0L000S, U12-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.71
Std Dev 3.19
Entry
ampl
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5
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0
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60
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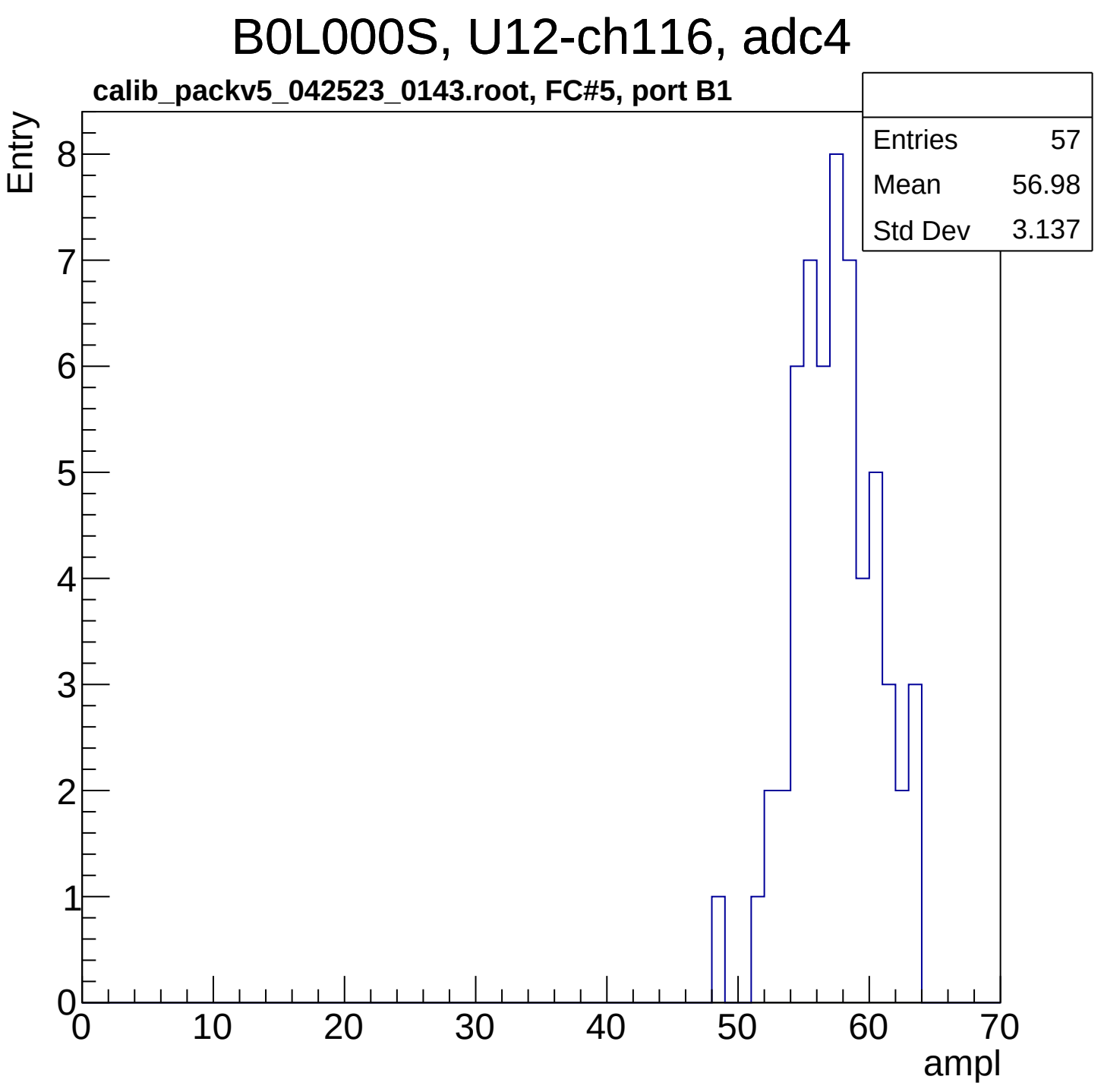
B0L000S, U12-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 56.98
Std Dev 3.137
Entry
ampl
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2
3
4
5
6
7
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0
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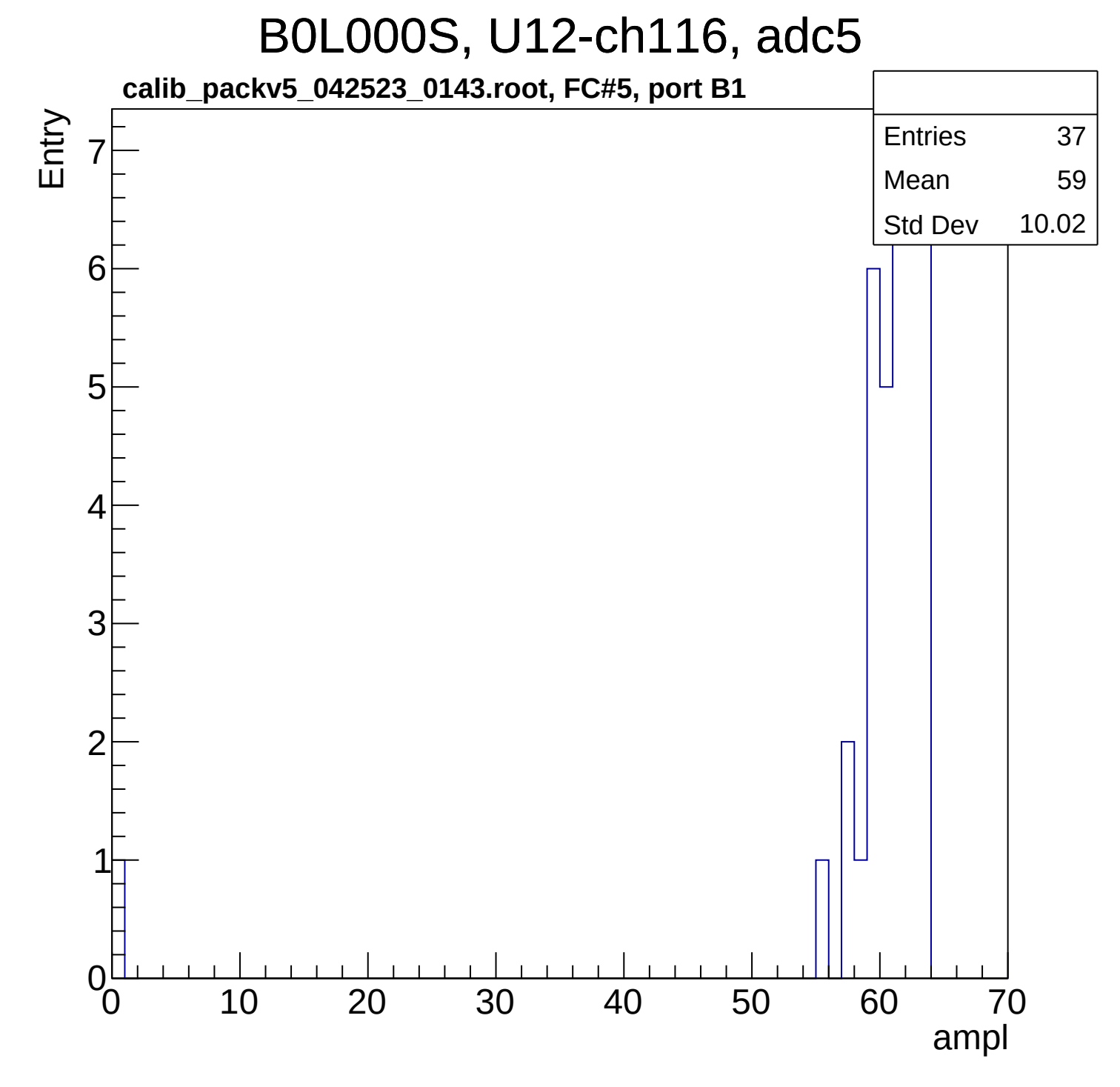
B0L000S, U12-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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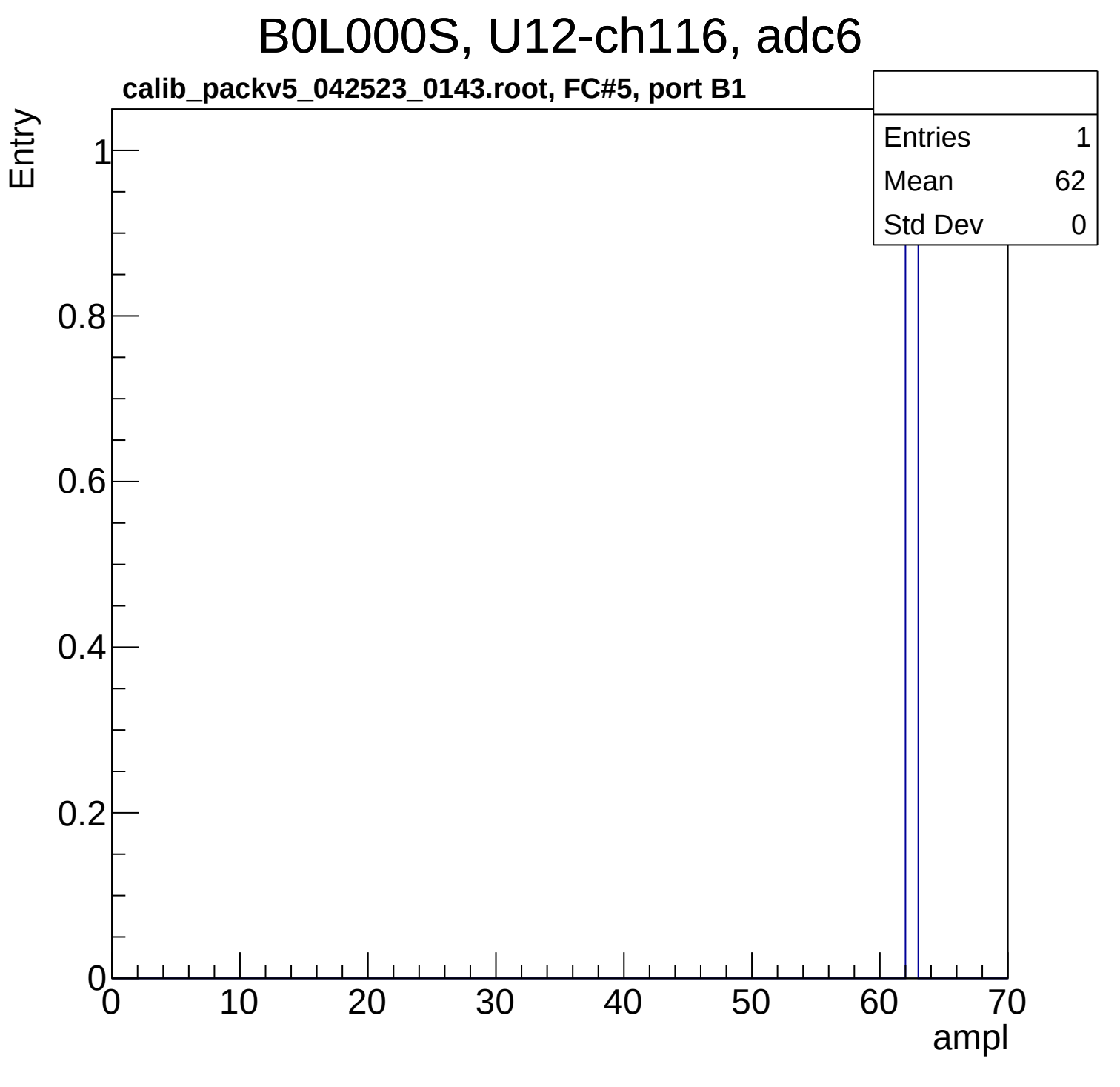
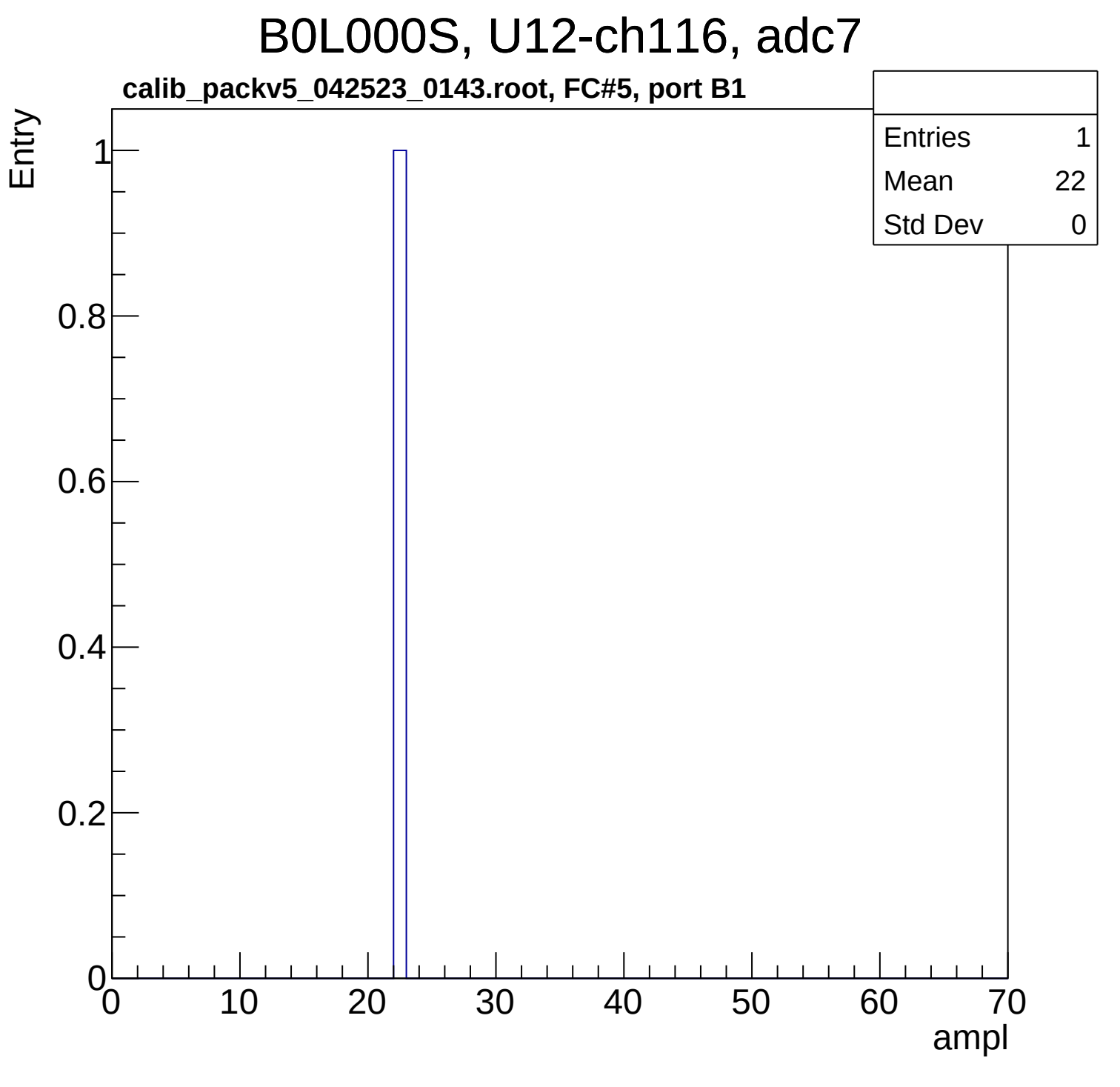
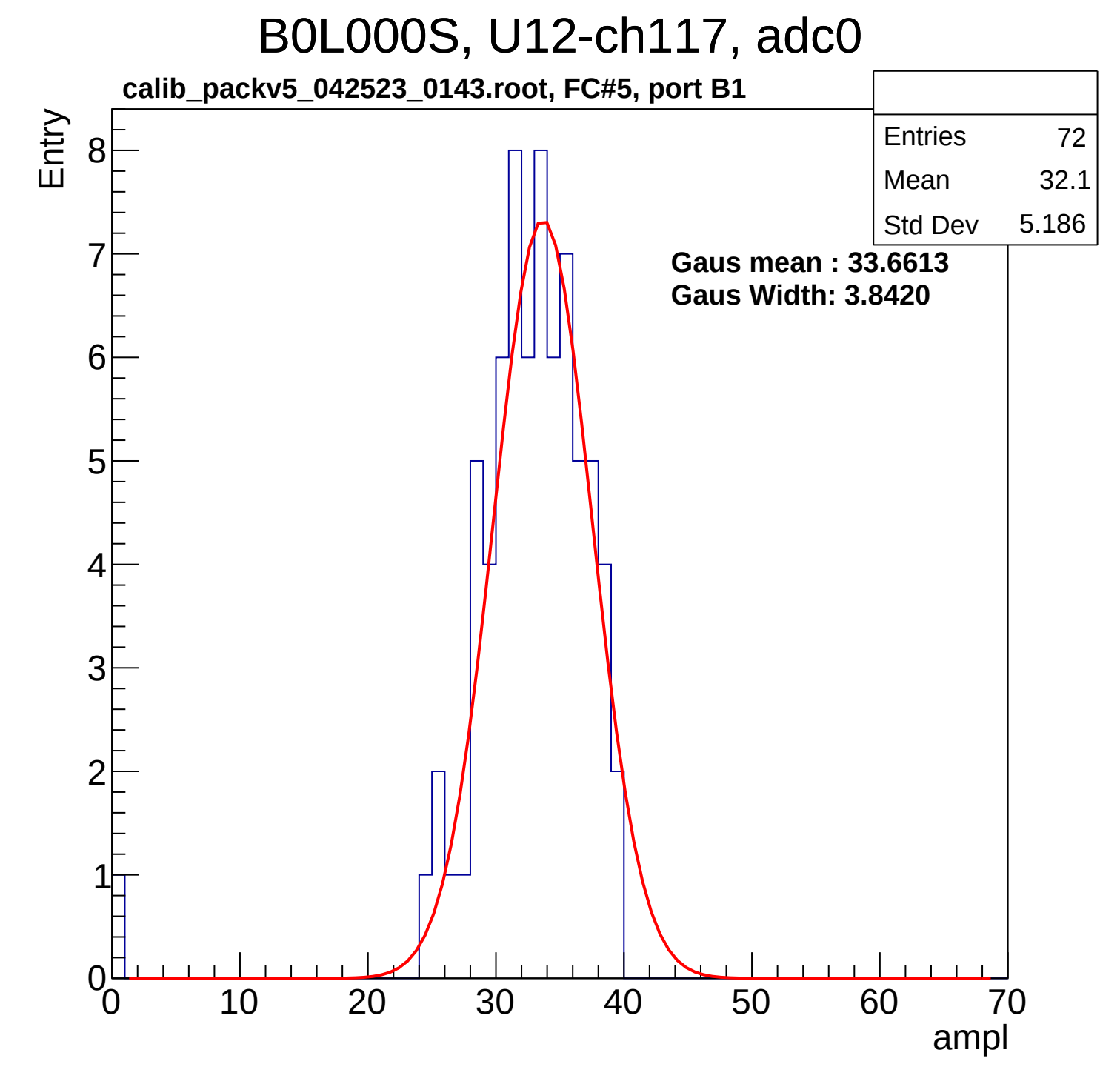
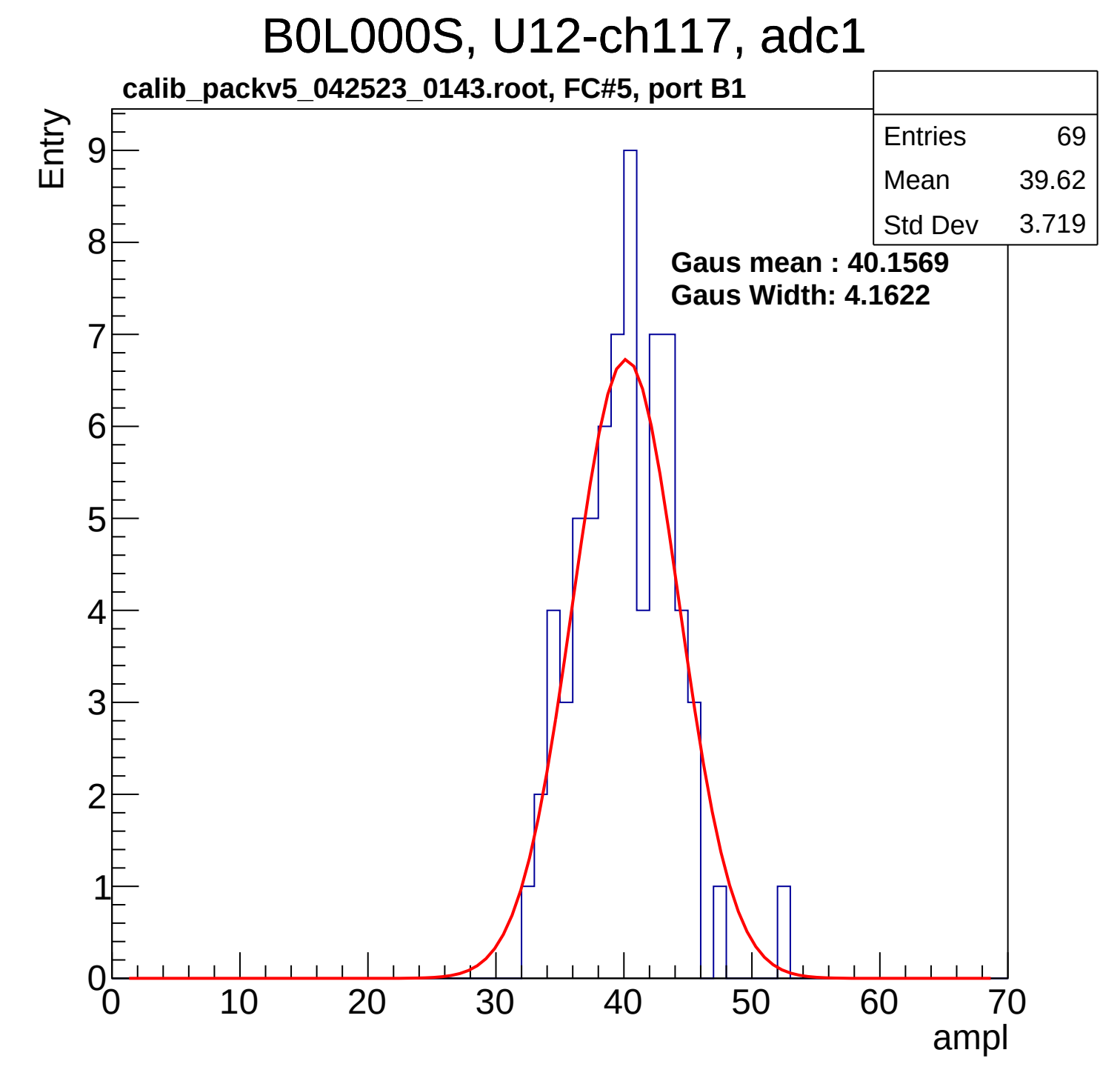

B0L000S, U12-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 32.1
Std Dev 5.186
Gaus mean : 33.6613
Gaus Width: 3.8420
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 39.62
Std Dev 3.719
Gaus mean : 40.1569
Gaus Width: 4.1622
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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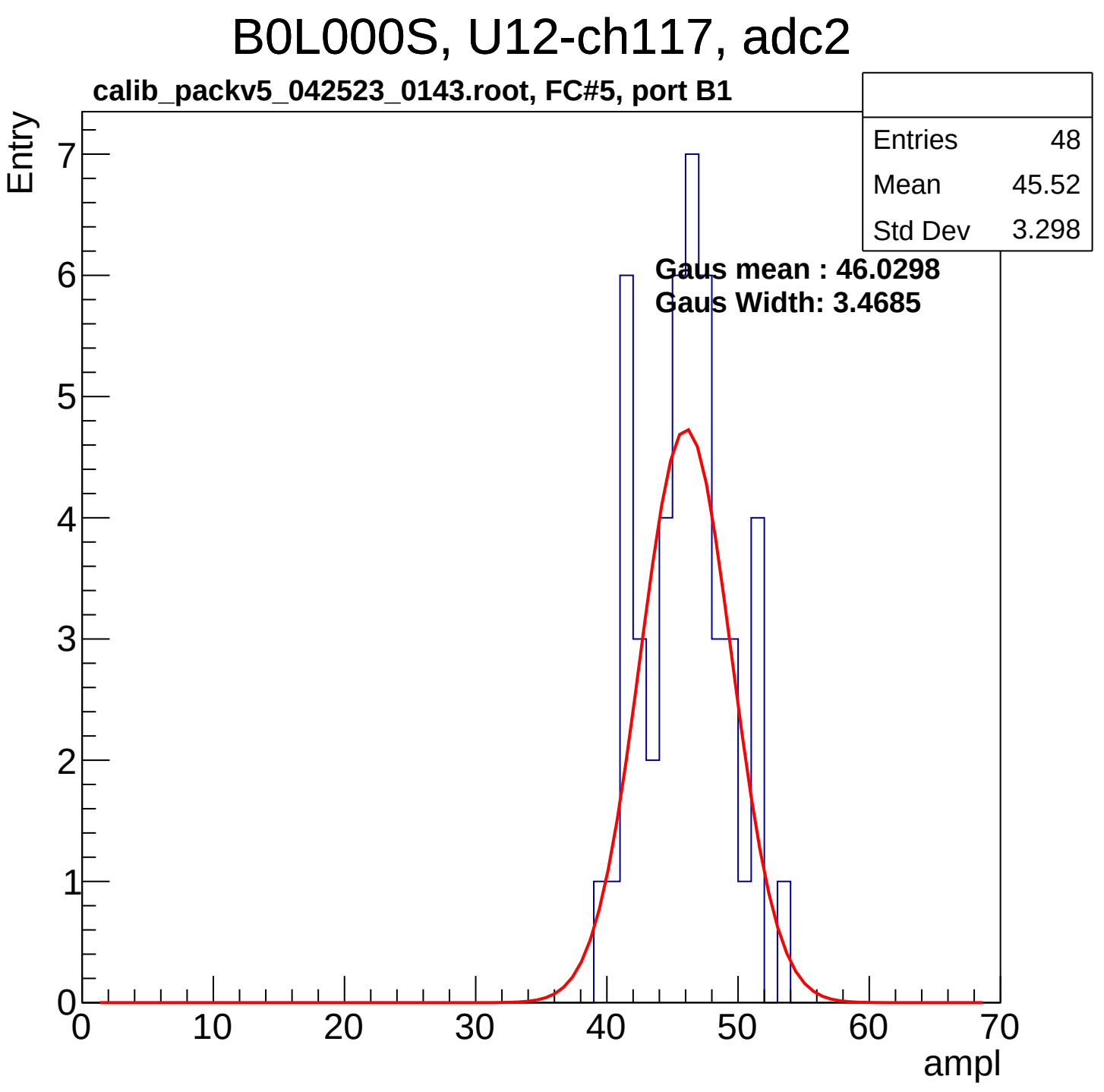
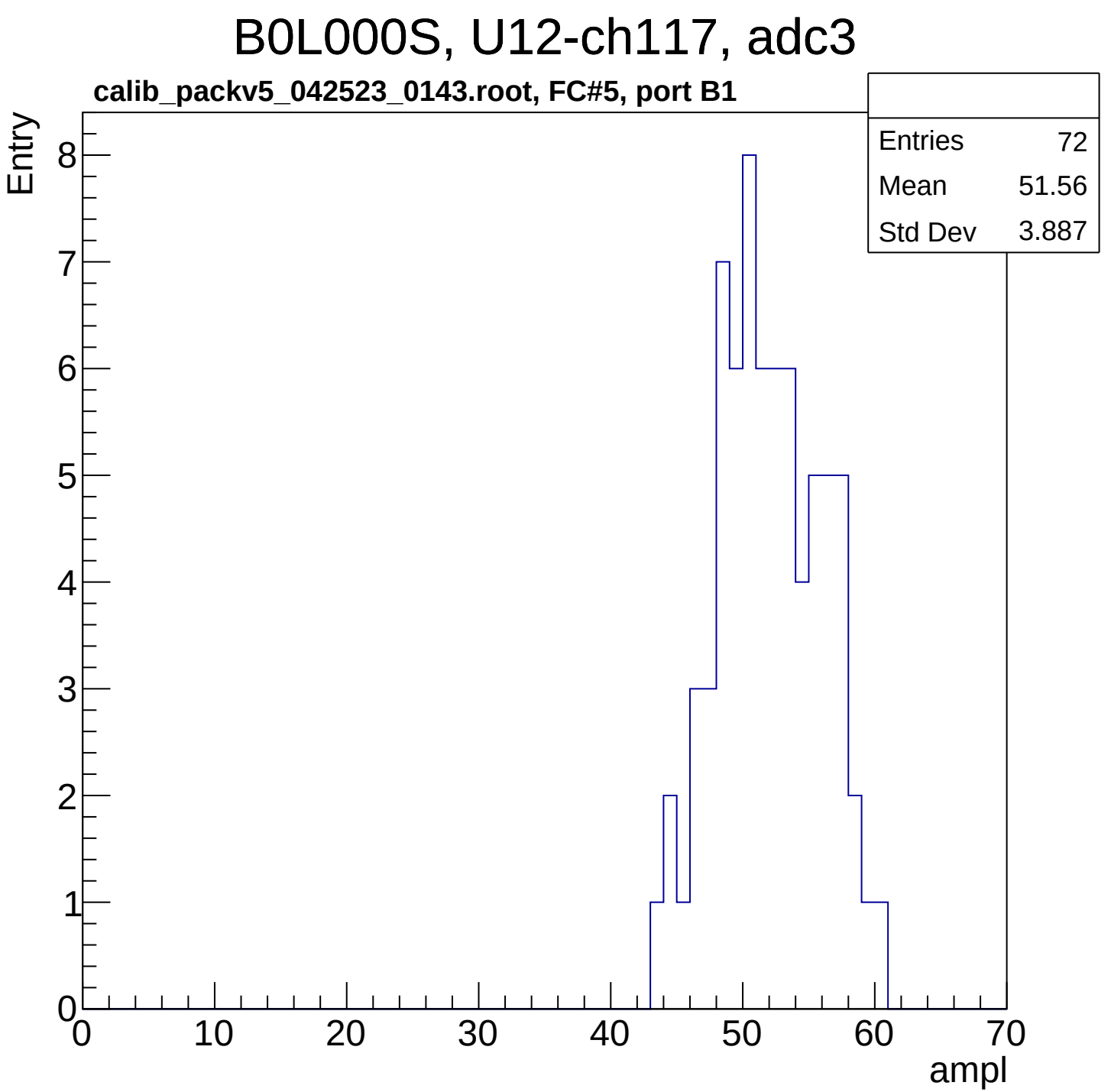
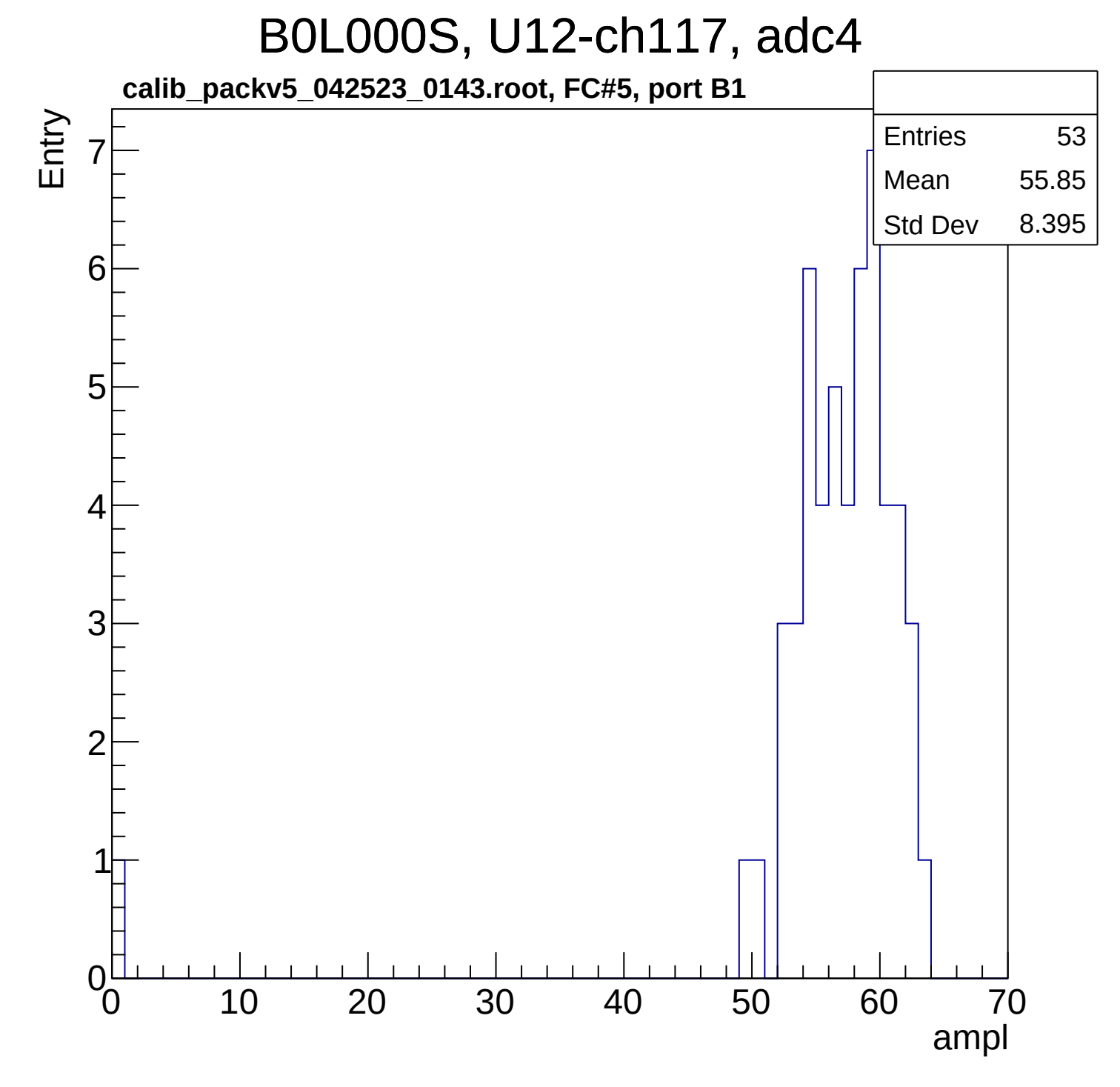

B0L000S, U12-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 45.52
Std Dev 3.298
Gaus mean : 46.0298
Gaus Width: 3.4685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 51.56
Std Dev 3.887
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.85
Std Dev 8.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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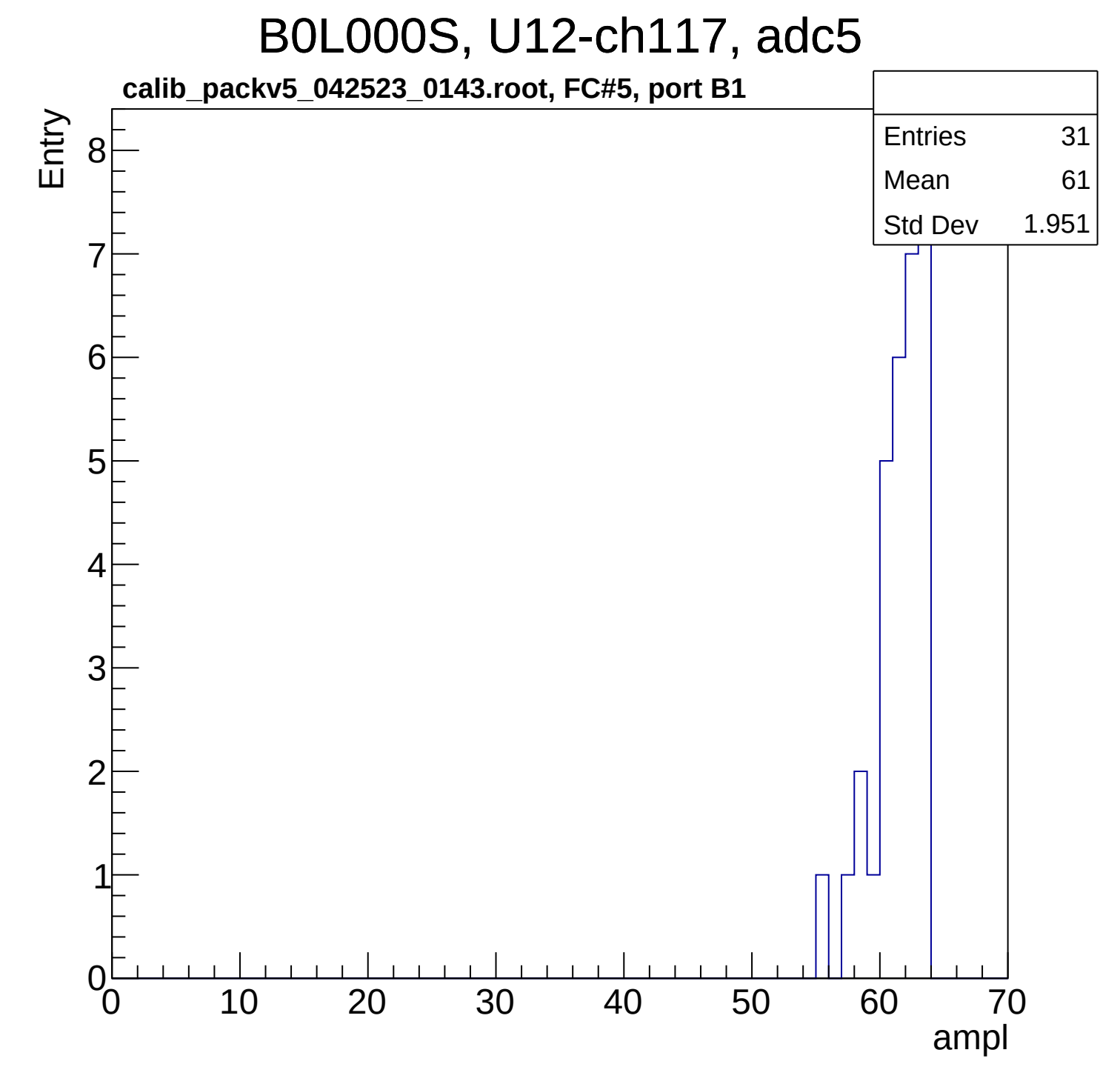
B0L000S, U12-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 61
Std Dev 1.951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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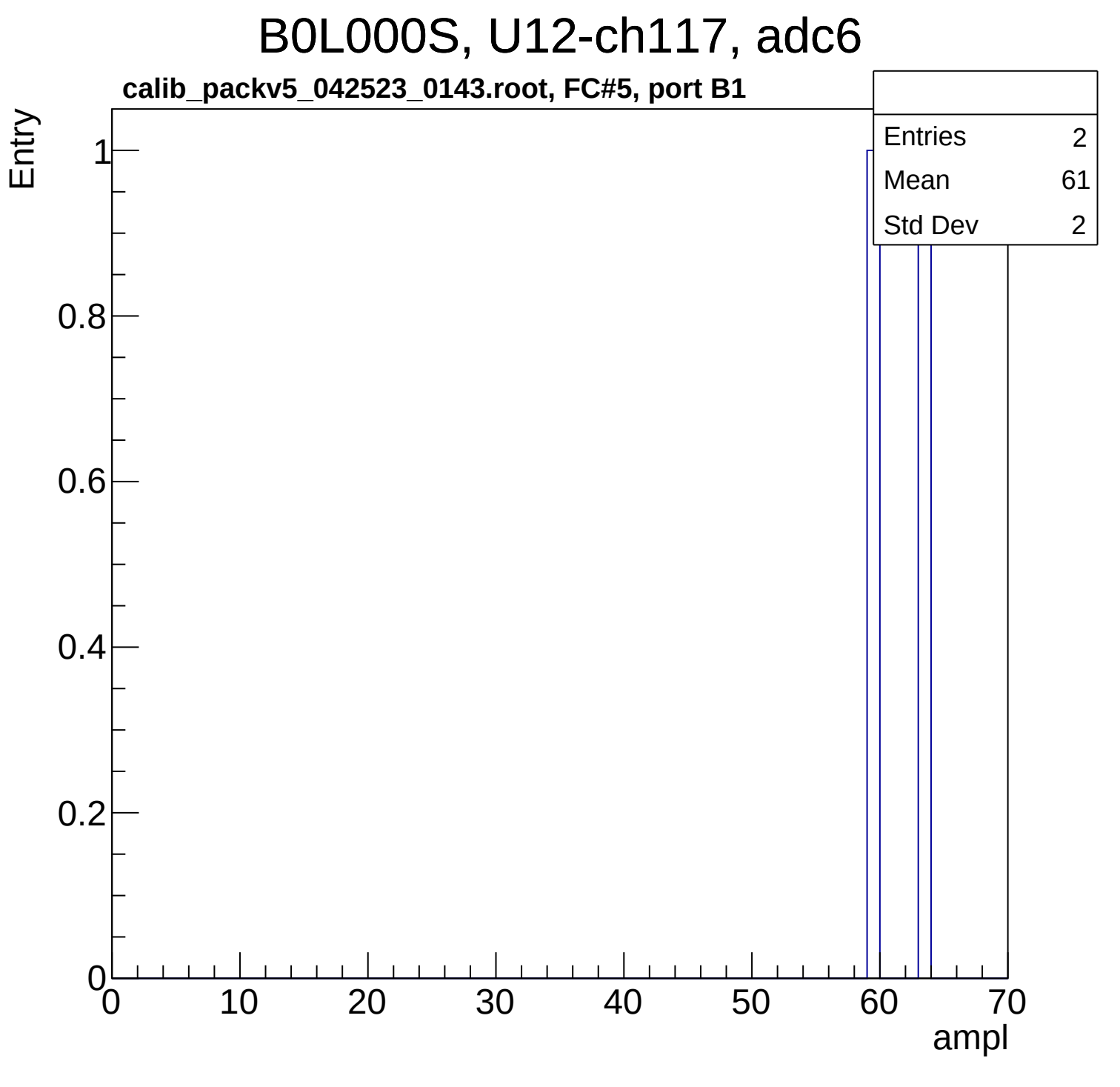
B0L000S, U12-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61
Std Dev 2
Entry
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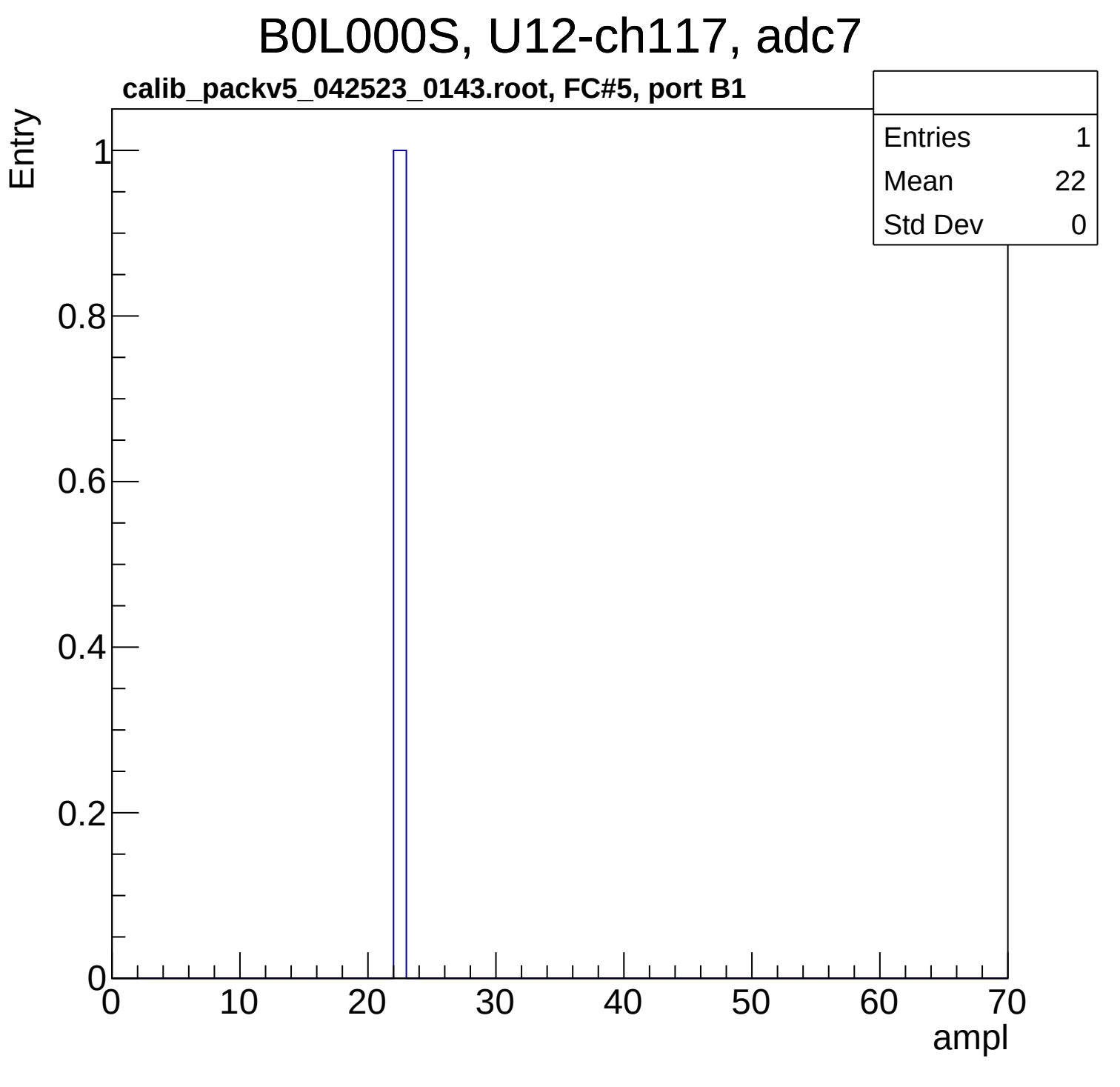
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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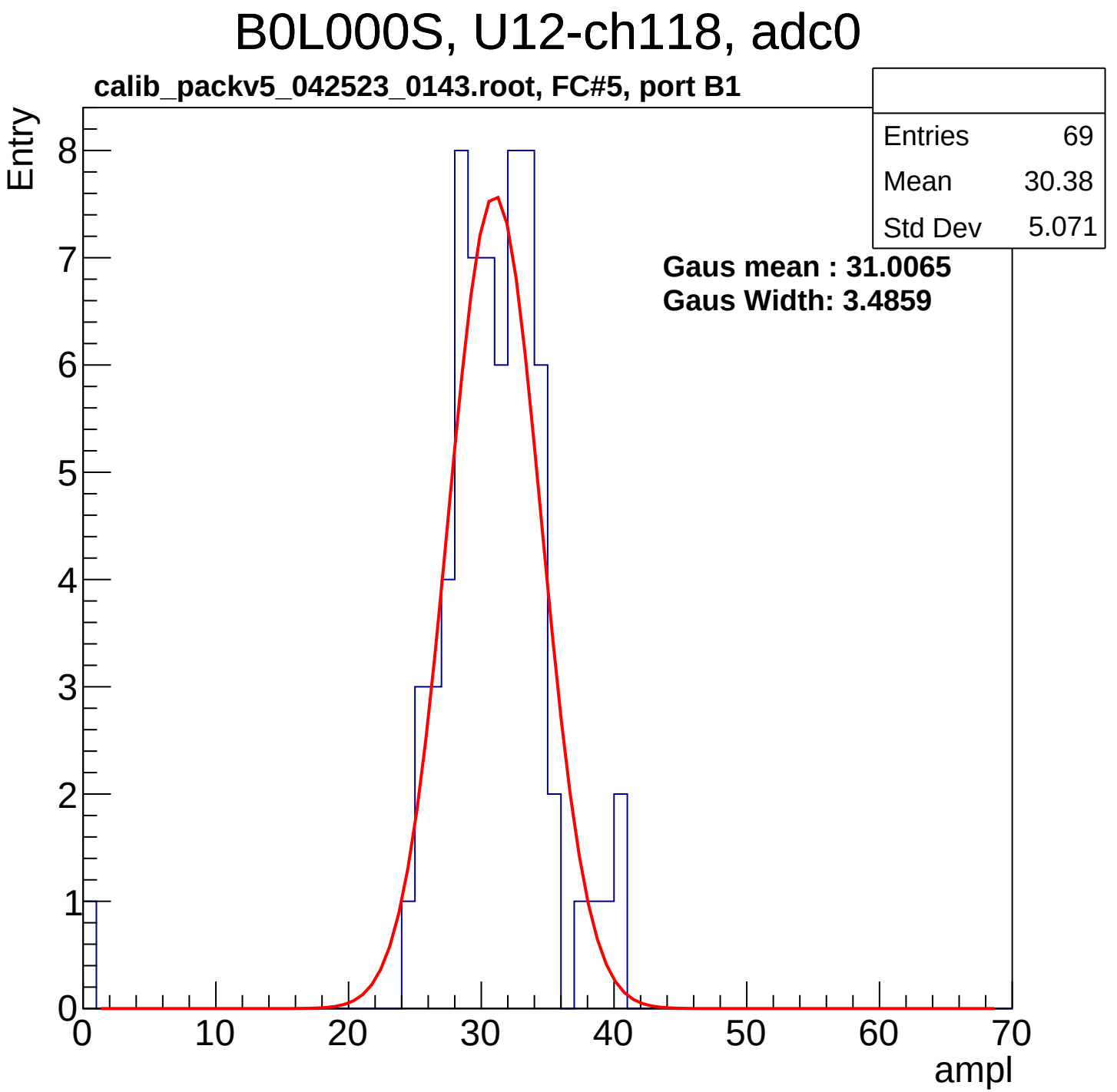
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.38
Std Dev 5.071
Gaus mean : 31.0065
Gaus Width: 3.4859
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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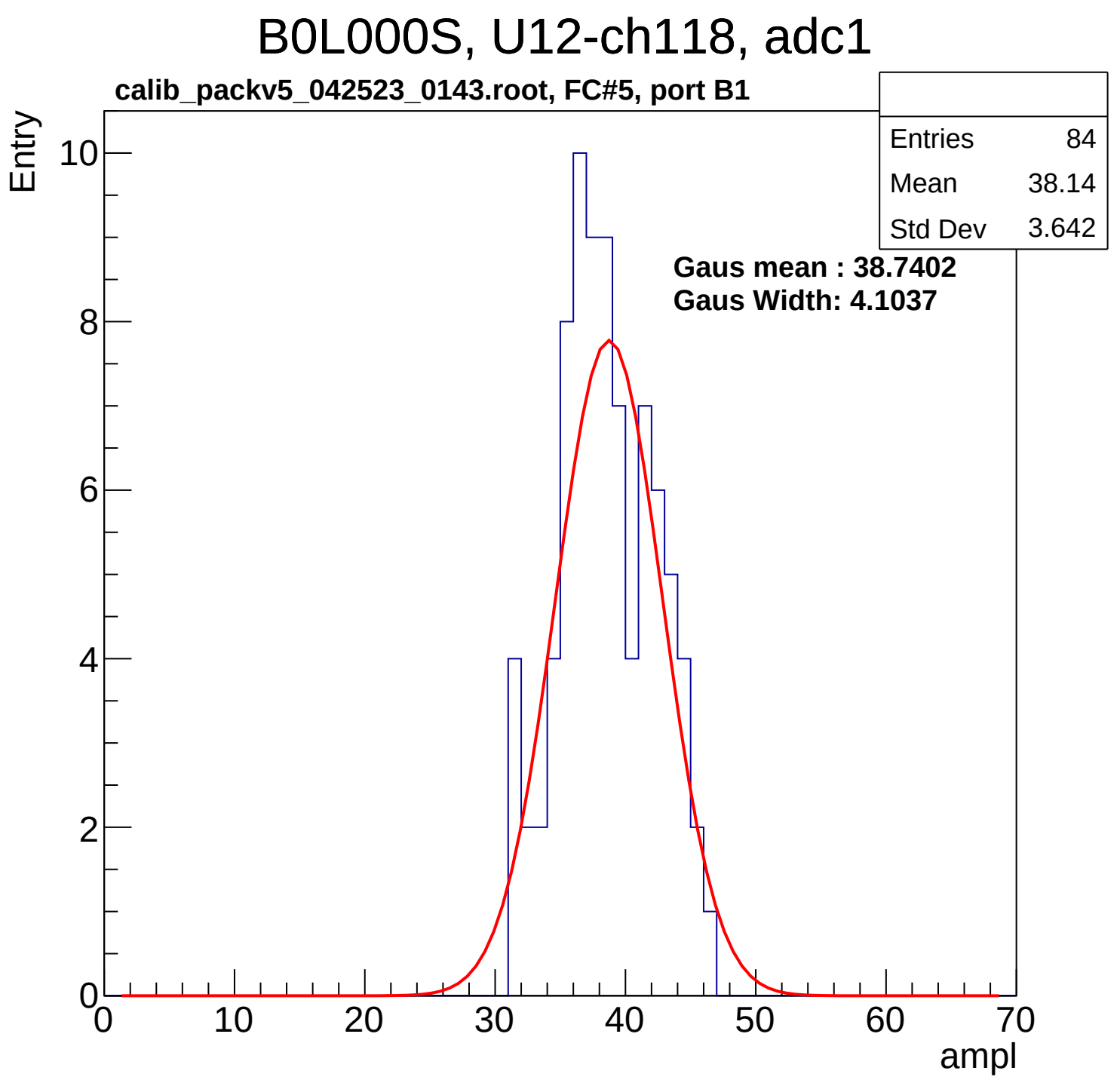
40
50
60
70


B0L000S, U12-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 38.14
Std Dev 3.642
Gaus mean : 38.7402
Gaus Width: 4.1037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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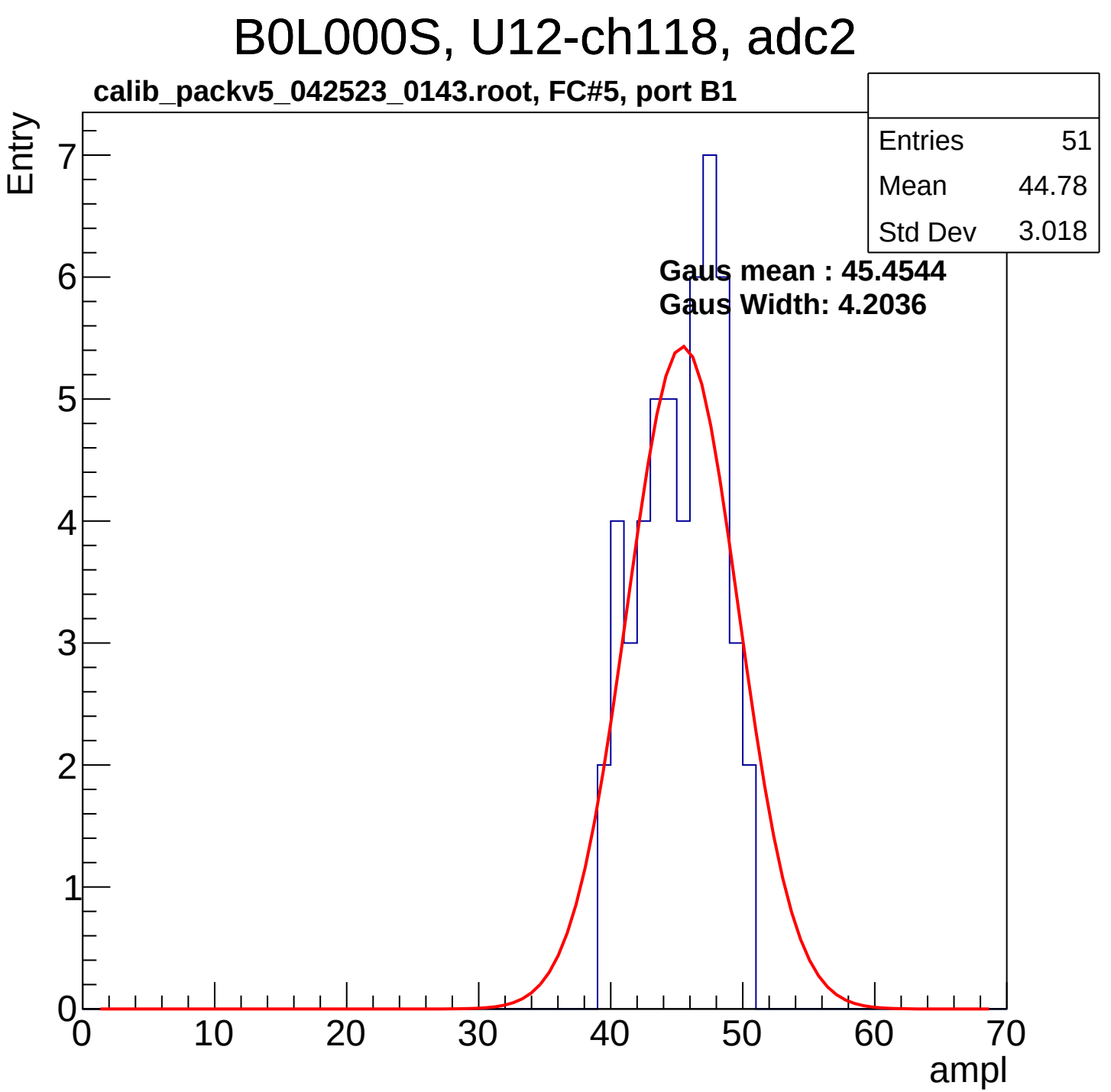
B0L000S, U12-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 44.78
Std Dev 3.018
Gaus mean : 45.4544
Gaus Width: 4.2036
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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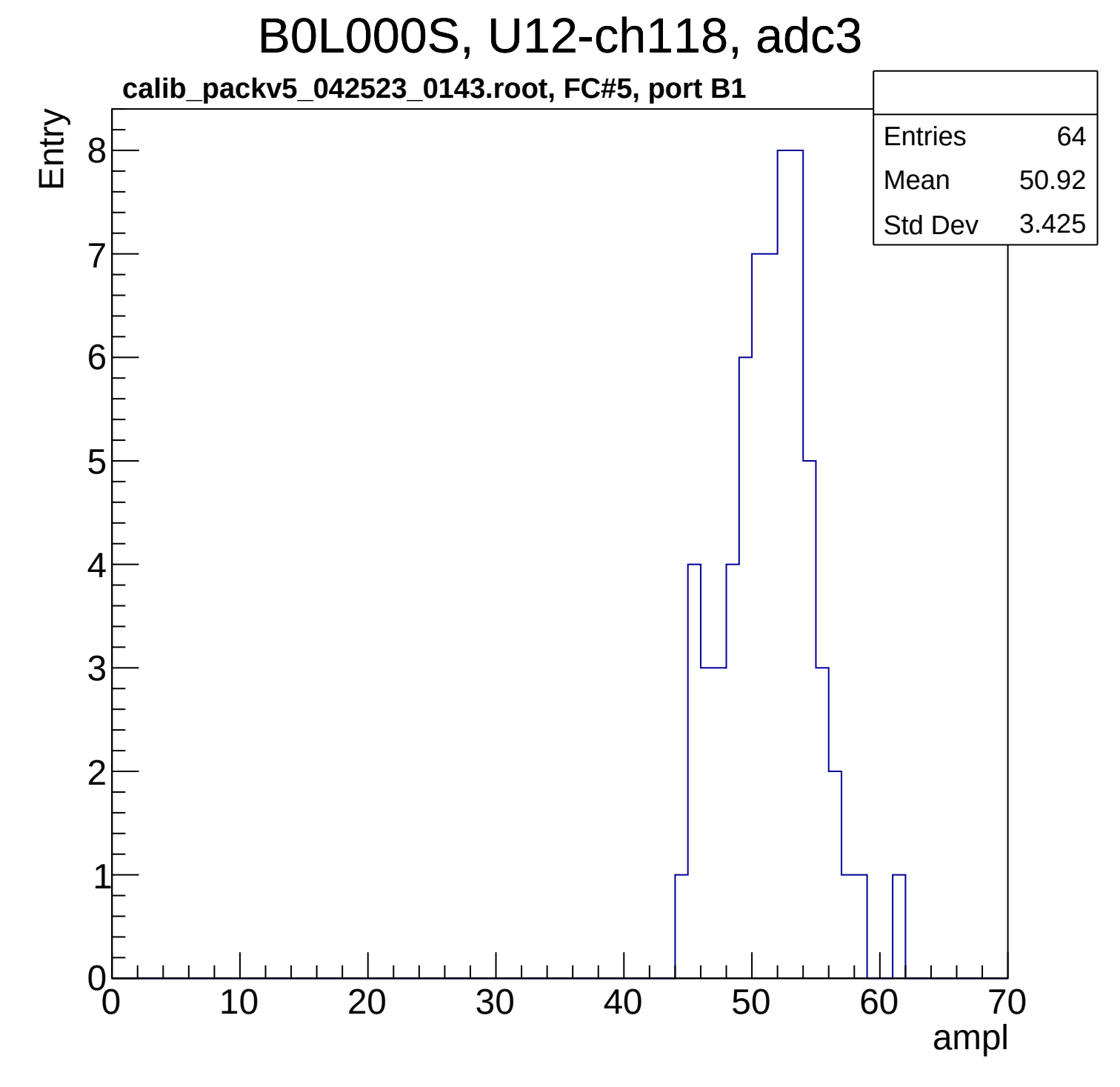
B0L000S, U12-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50.92
Std Dev 3.425
Entry
ampl
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2
3
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5
6
7
8
0
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40
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60
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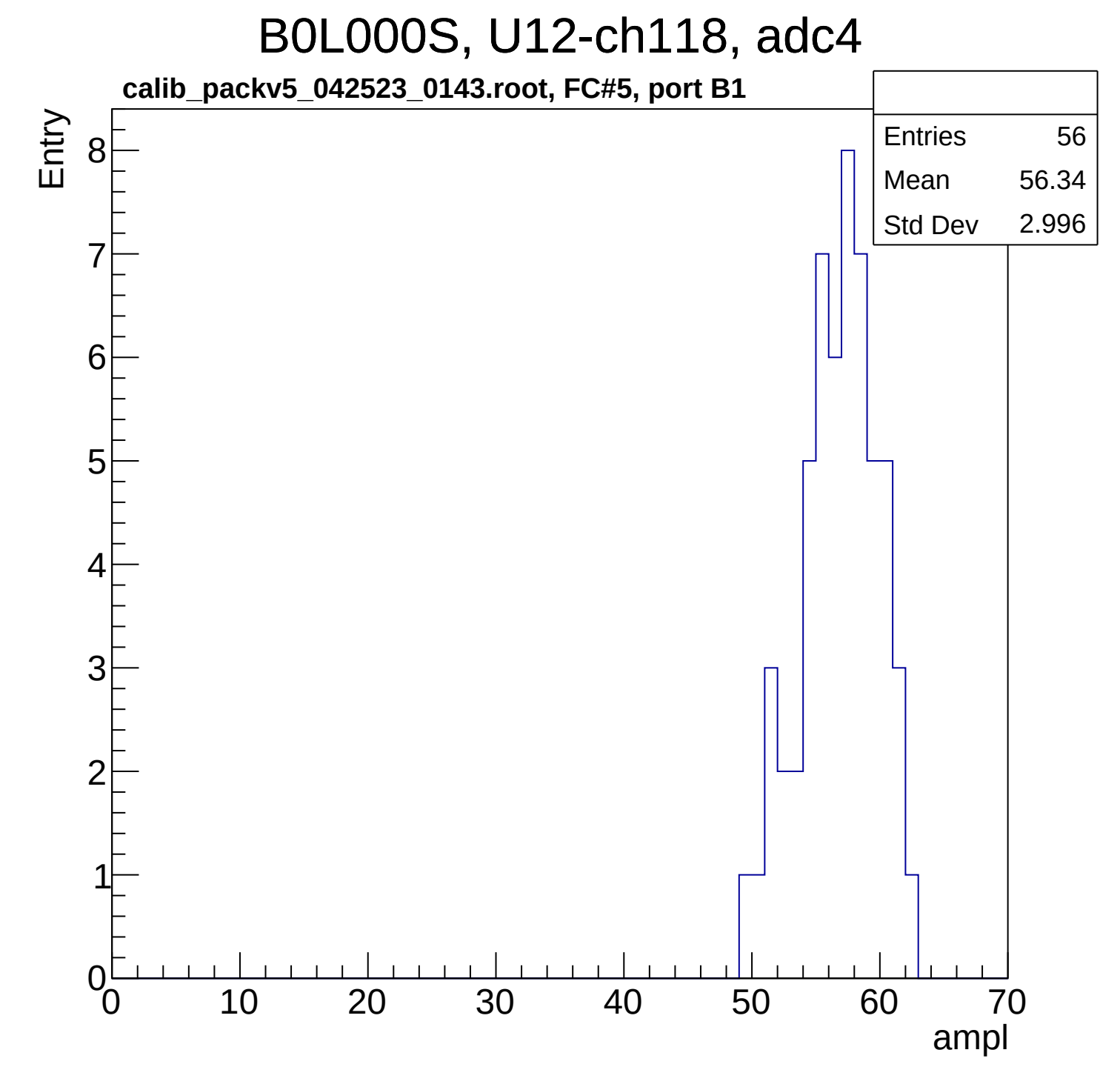
B0L000S, U12-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.34
Std Dev 2.996
Entry
ampl
0
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2
3
4
5
6
7
8
0
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20
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60
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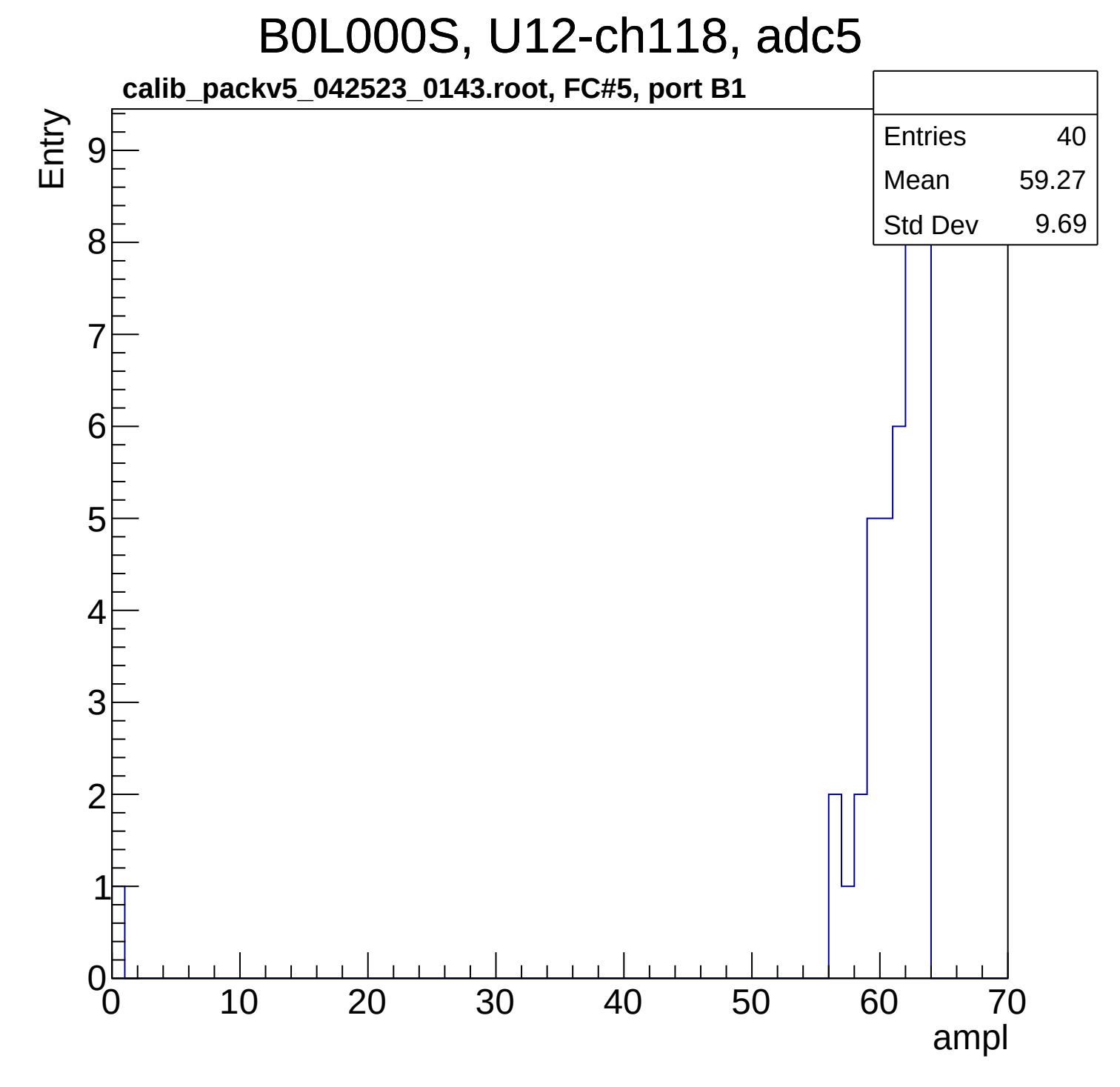
B0L000S, U12-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.27
Std Dev 9.69
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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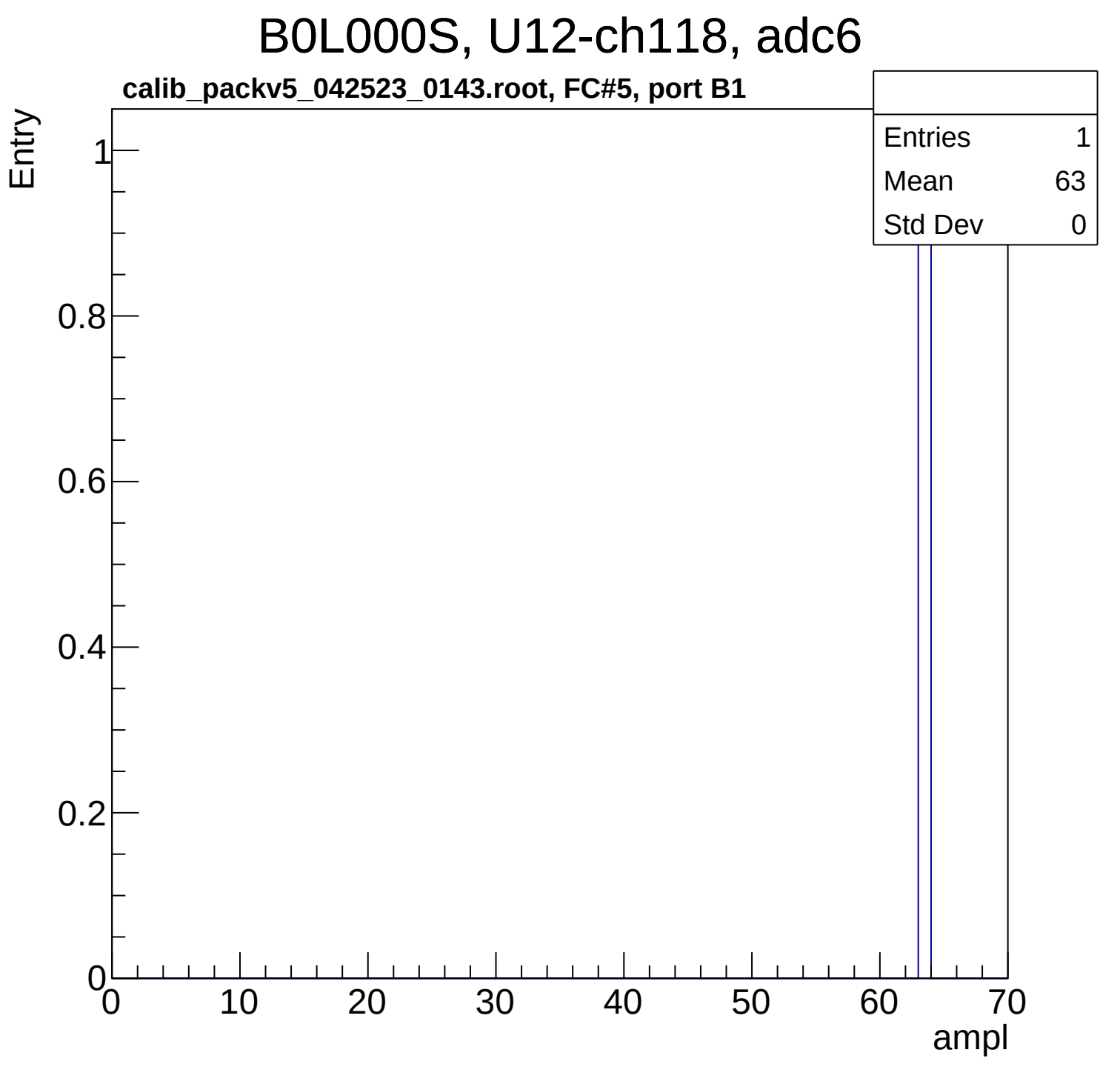
B0L000S, U12-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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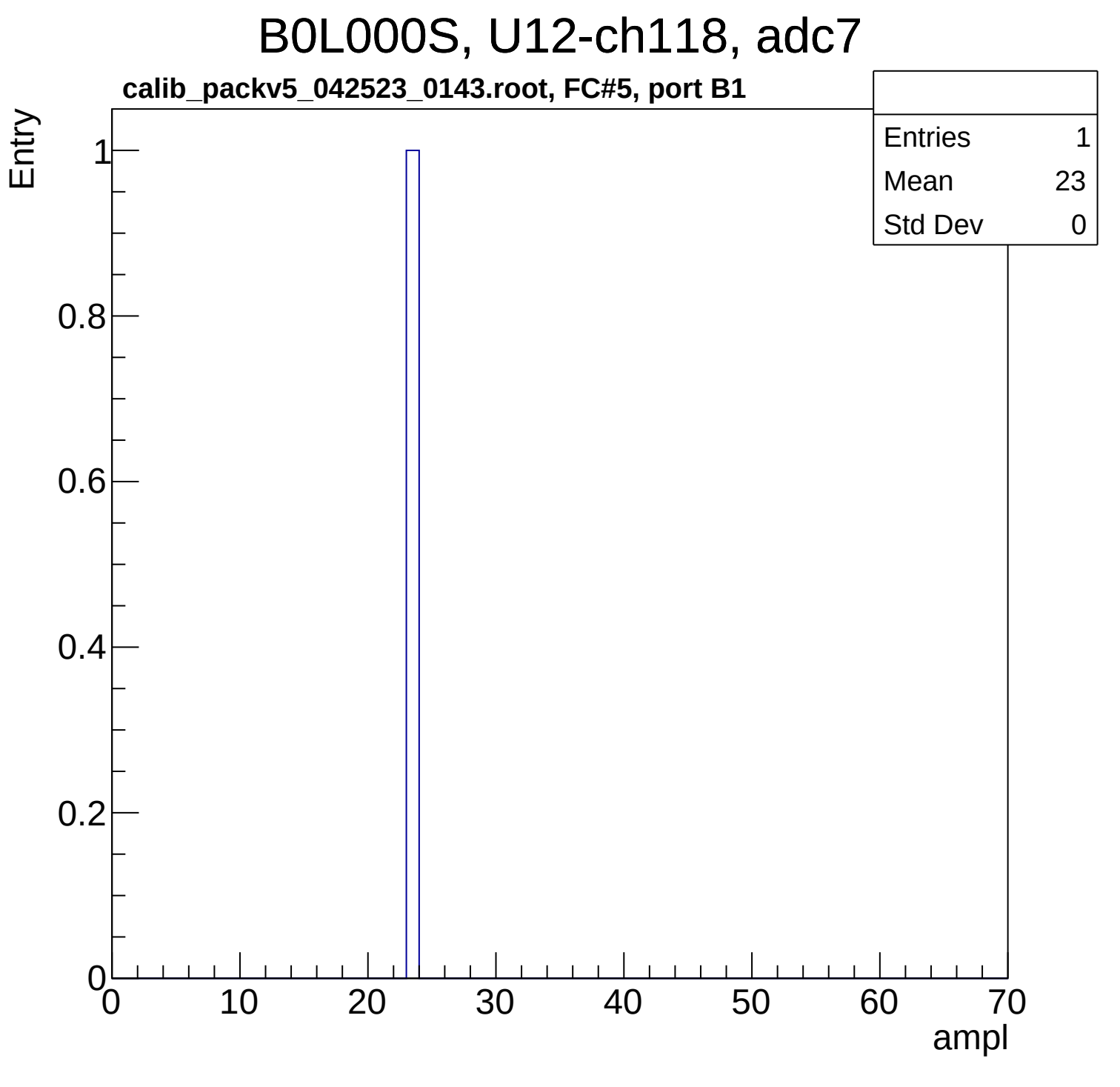
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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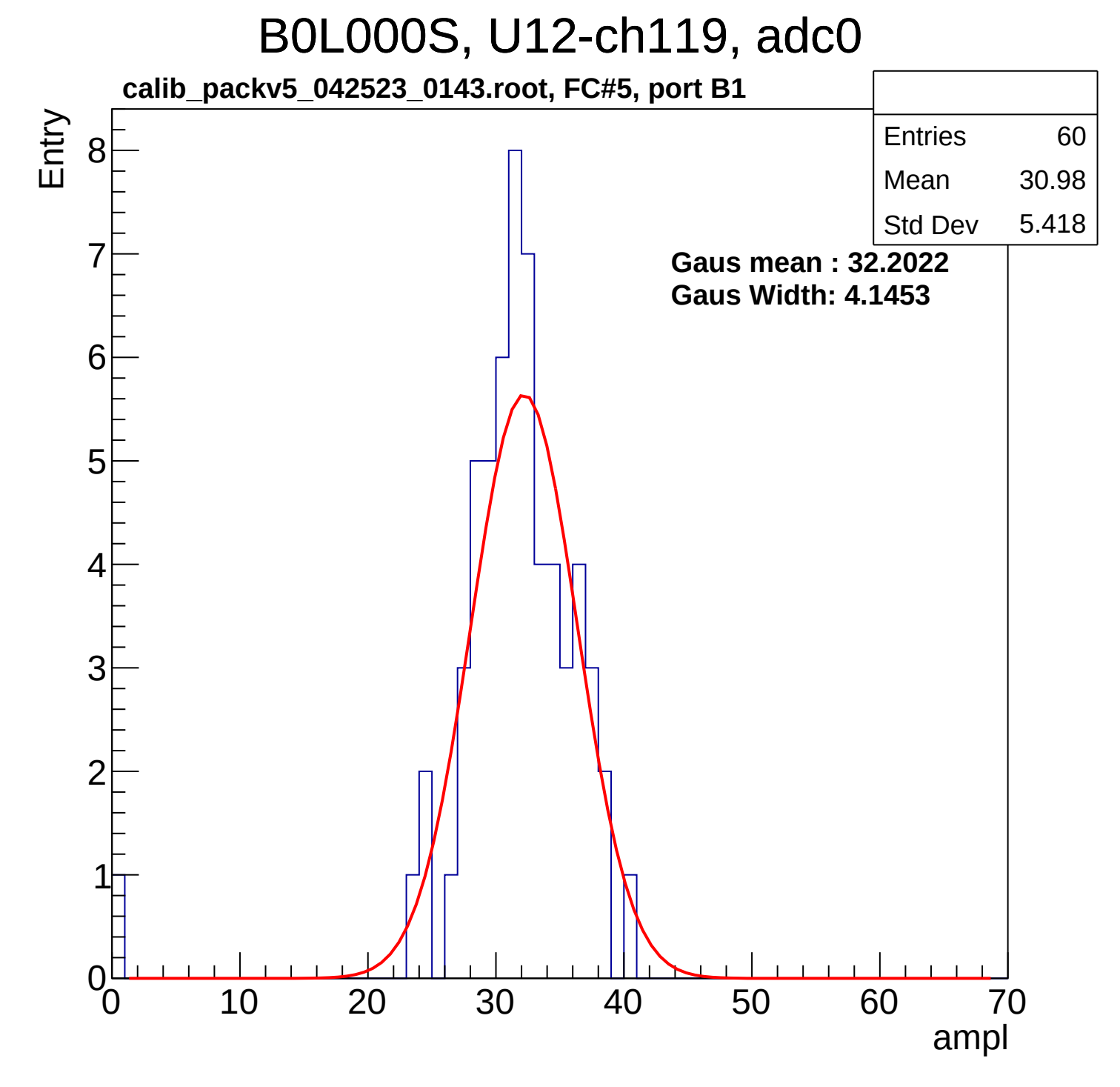
50
60
70
ampl


B0L000S, U12-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.98
Std Dev 5.418
Gaus mean : 32.2022
Gaus Width: 4.1453
Entry
ampl
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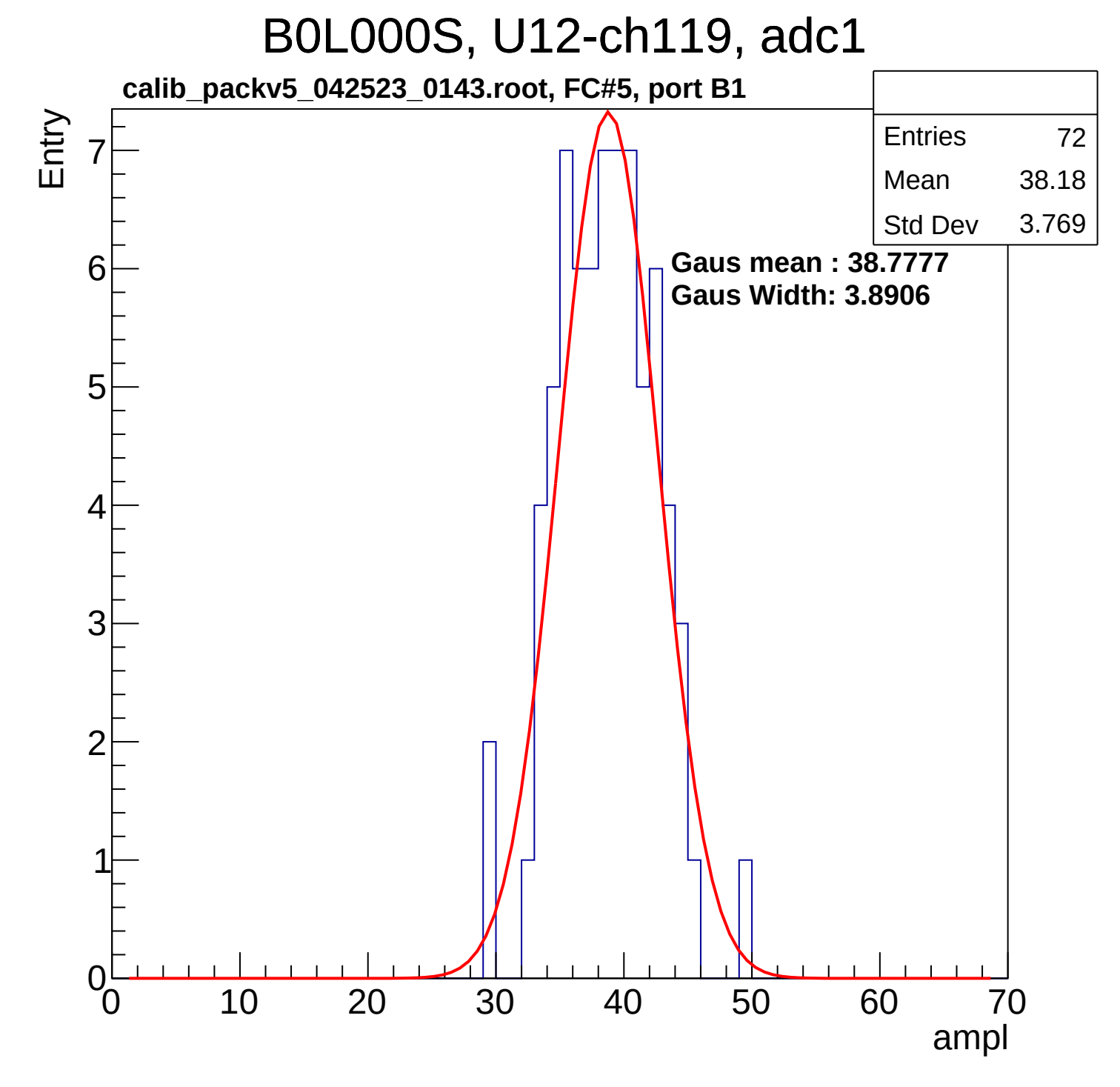
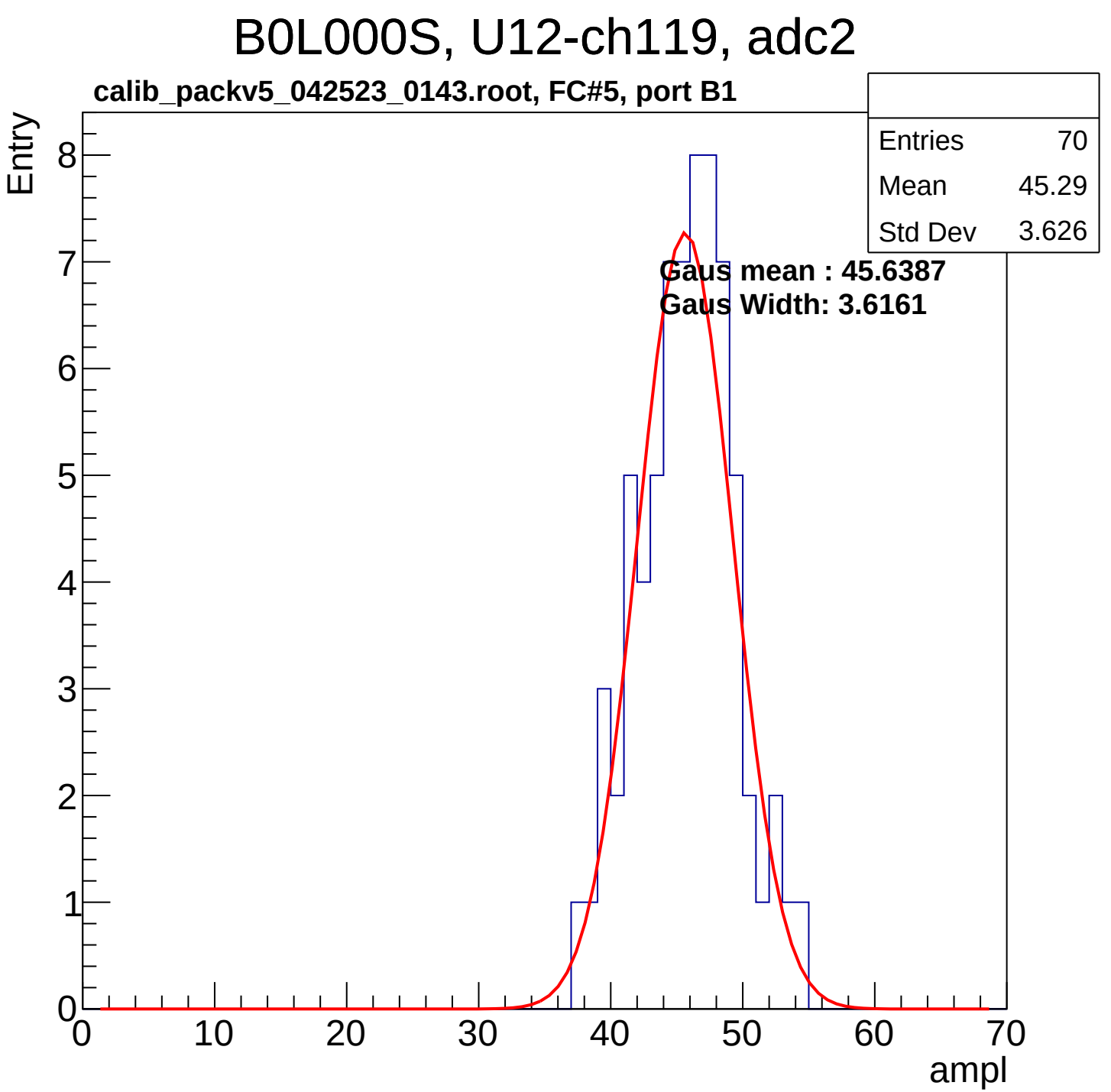
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 38.18
Std Dev 3.769
Gaus mean : 38.7777
Gaus Width: 3.8906
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.29
Std Dev 3.626
Gaus mean : 45.6387
Gaus Width: 3.6161
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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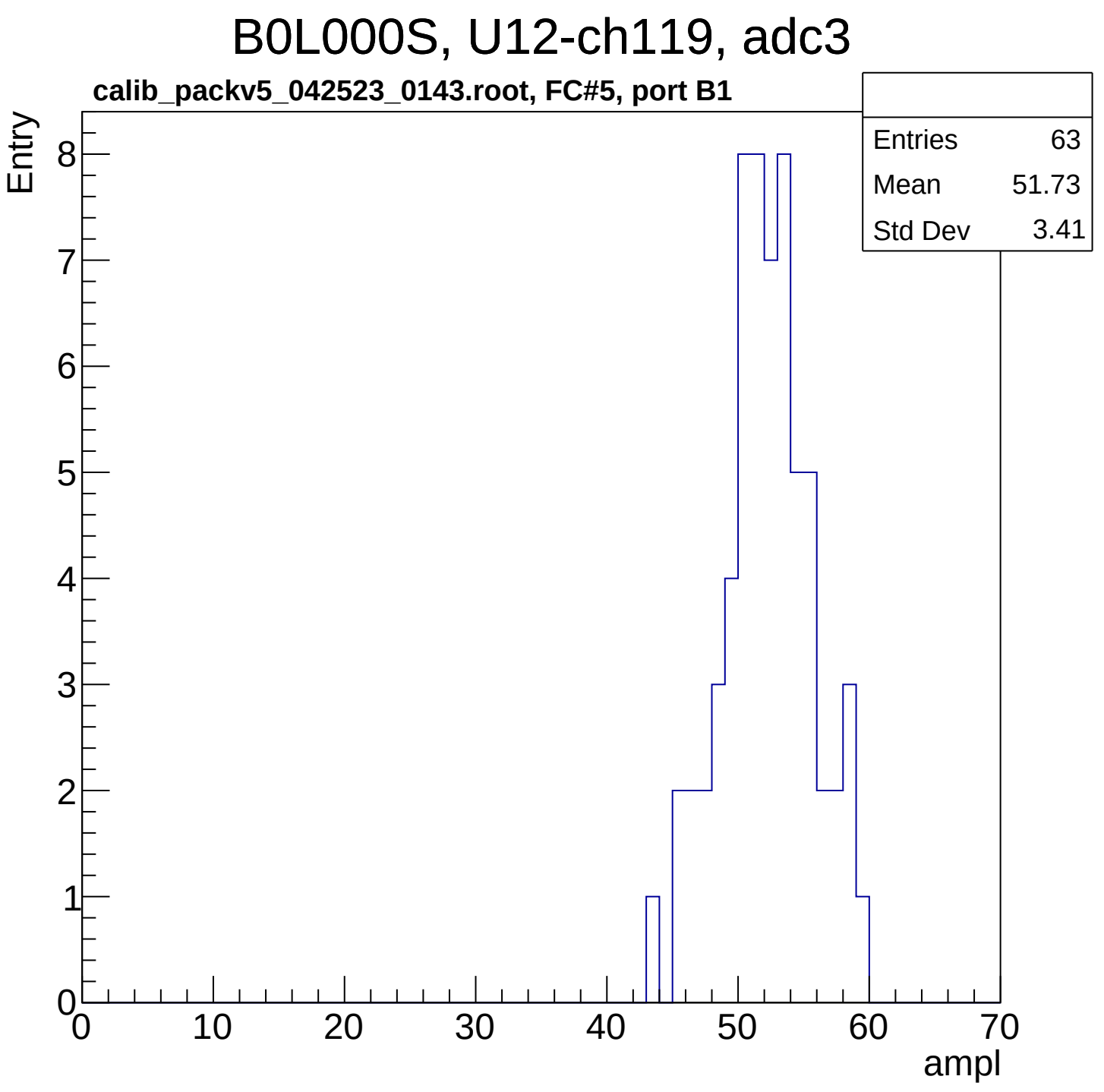
B0L000S, U12-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.73
Std Dev 3.41
Entry
ampl
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2
3
4
5
6
7
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0
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60
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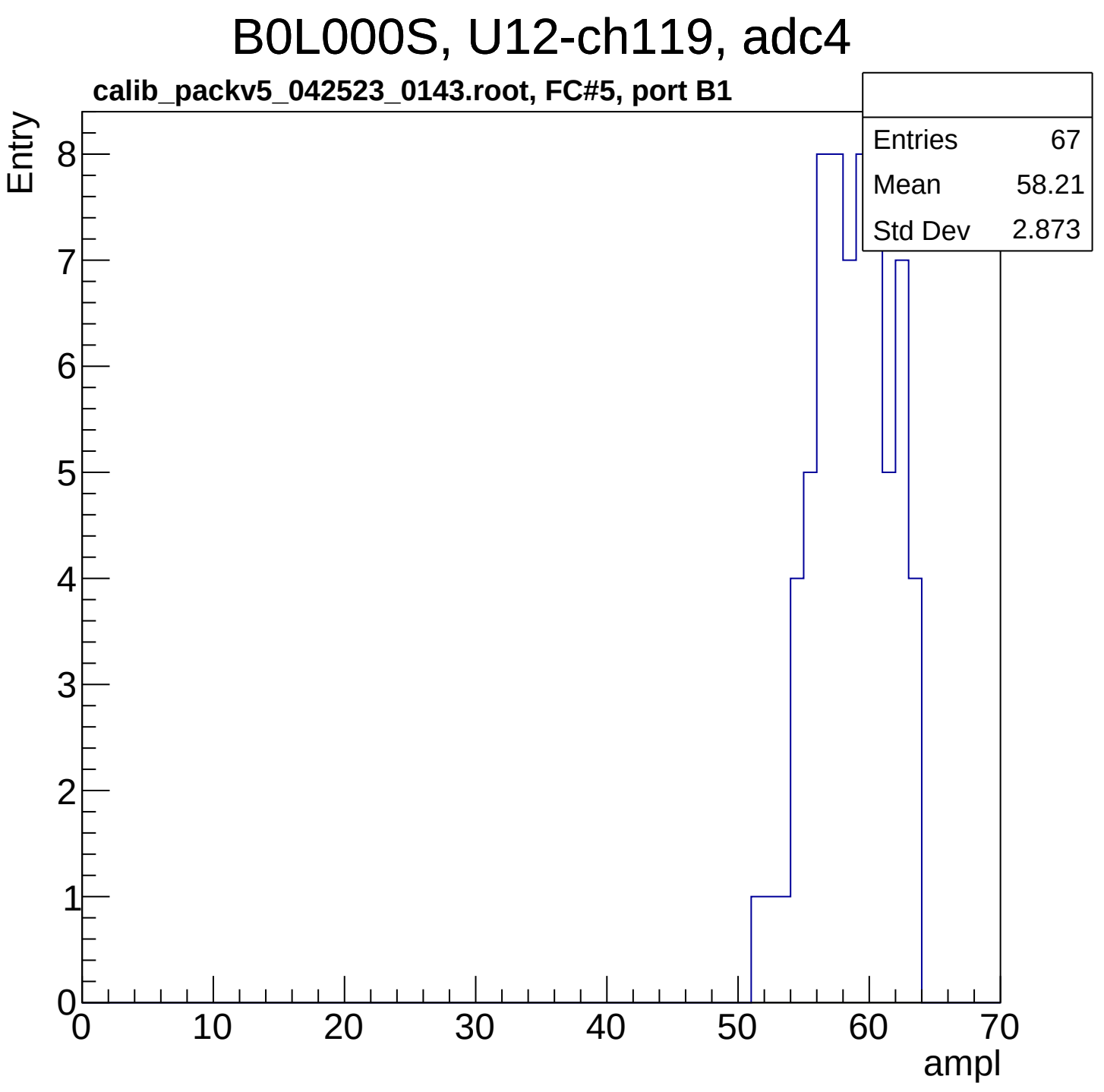
B0L000S, U12-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 58.21
Std Dev 2.873
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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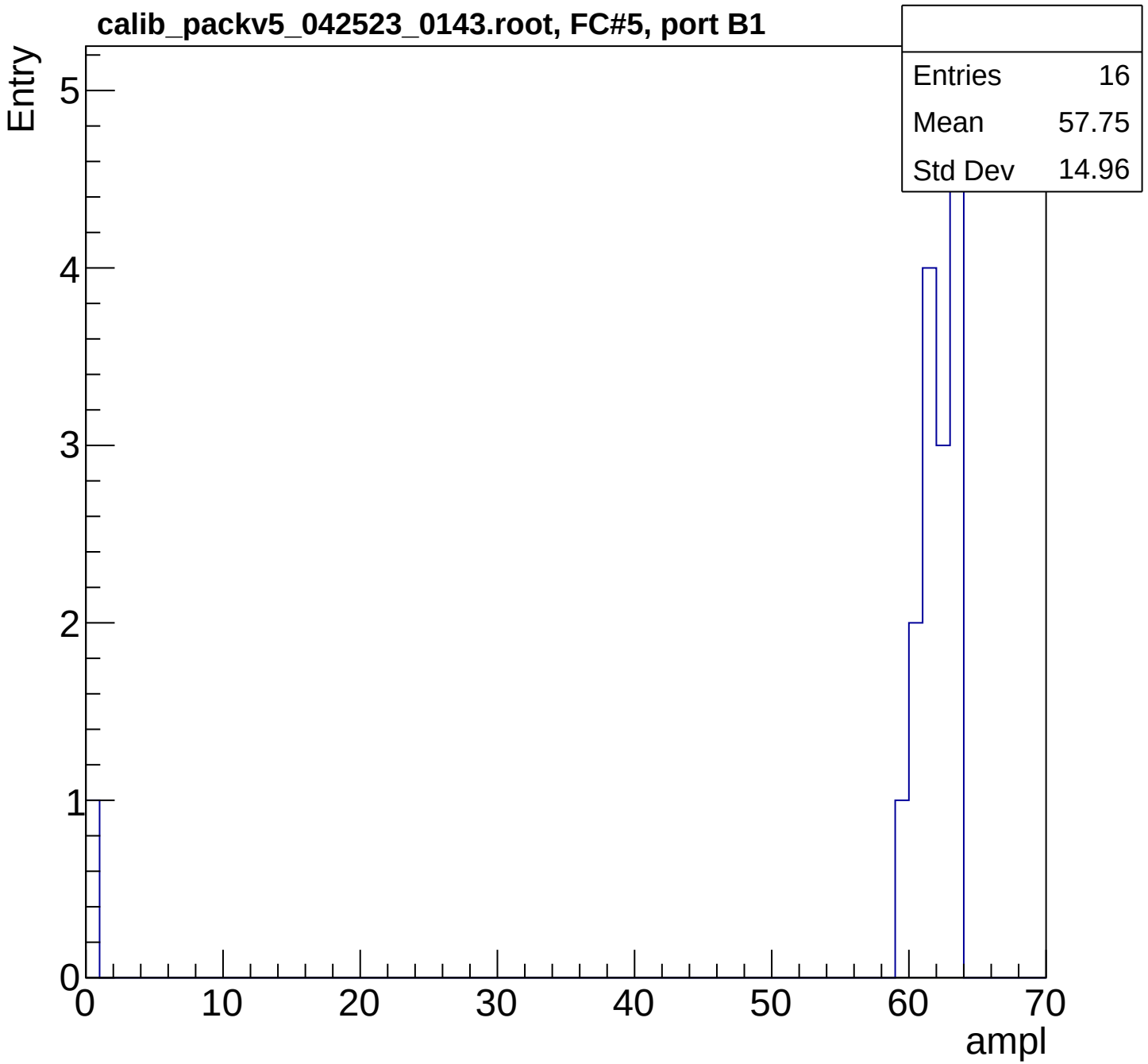
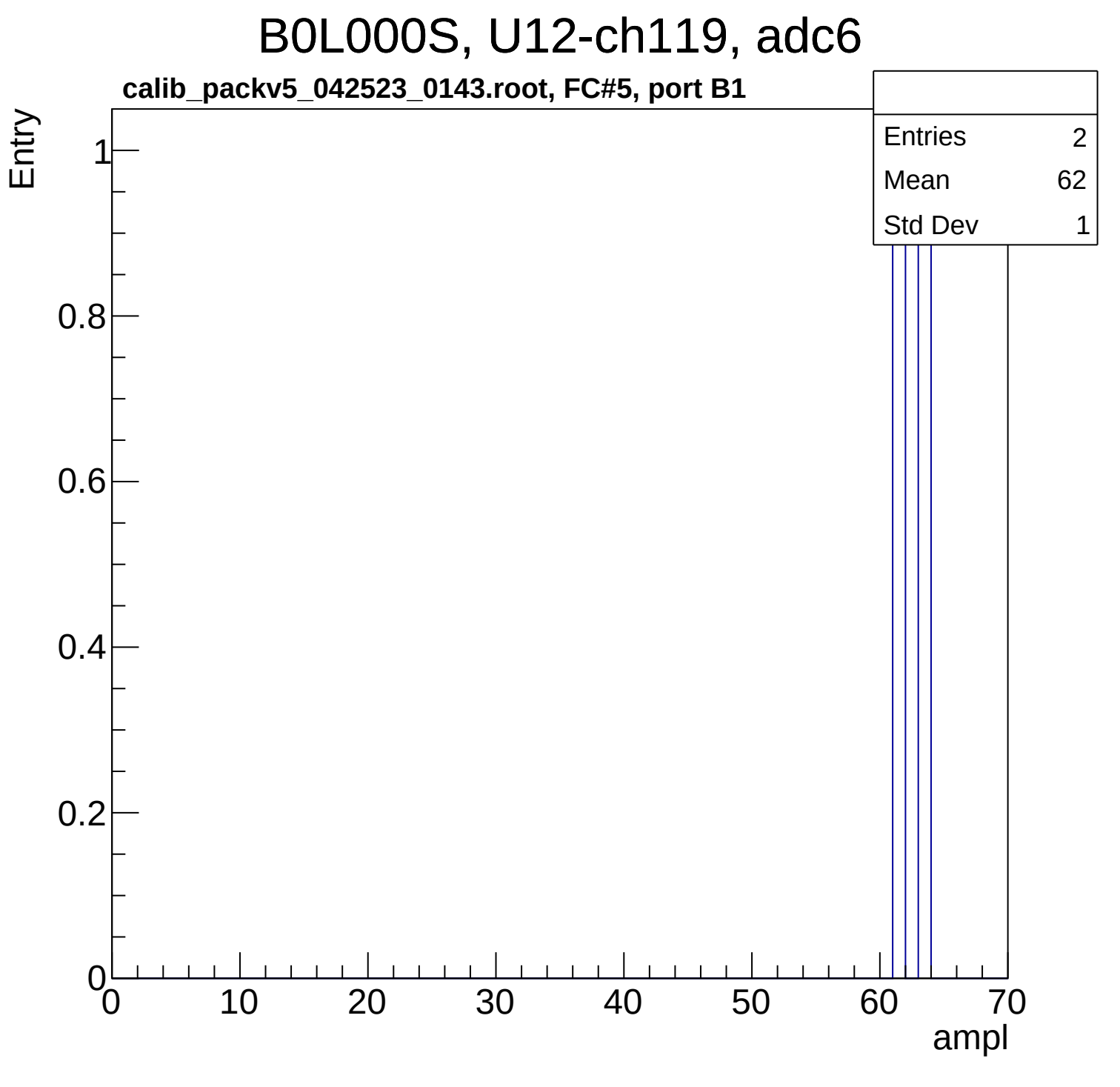

B0L000S, U12-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.75
Std Dev 14.96
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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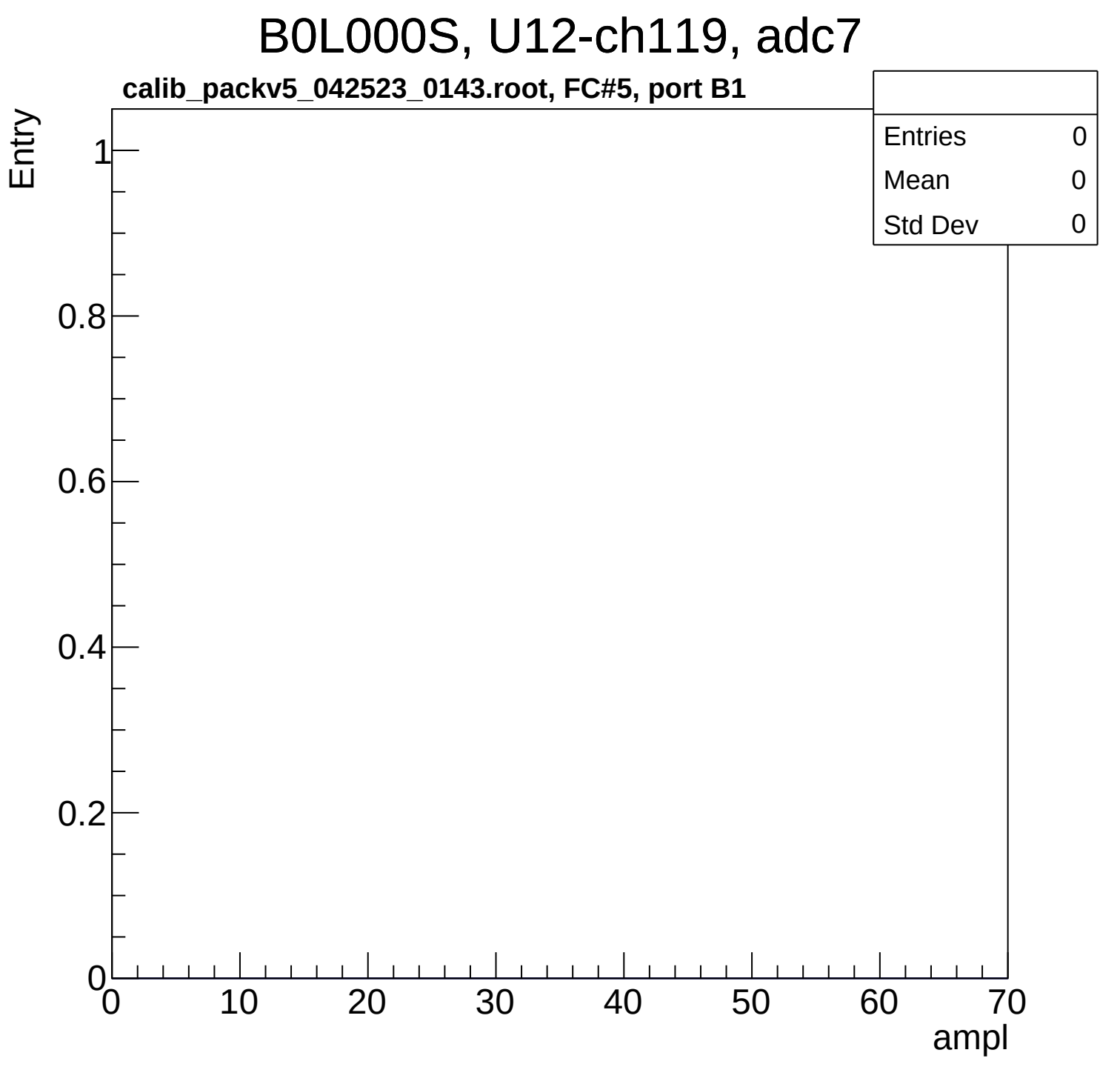
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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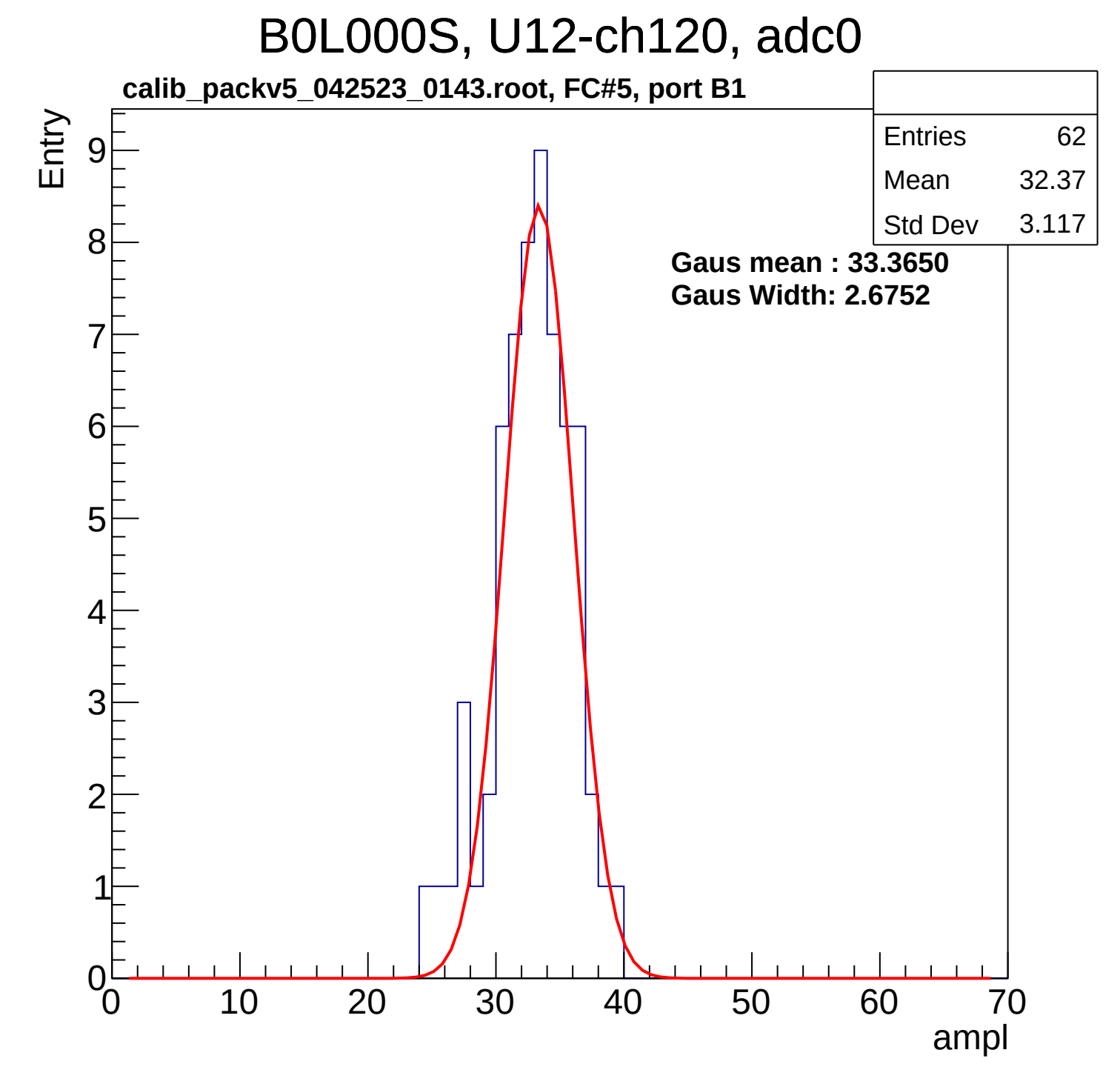
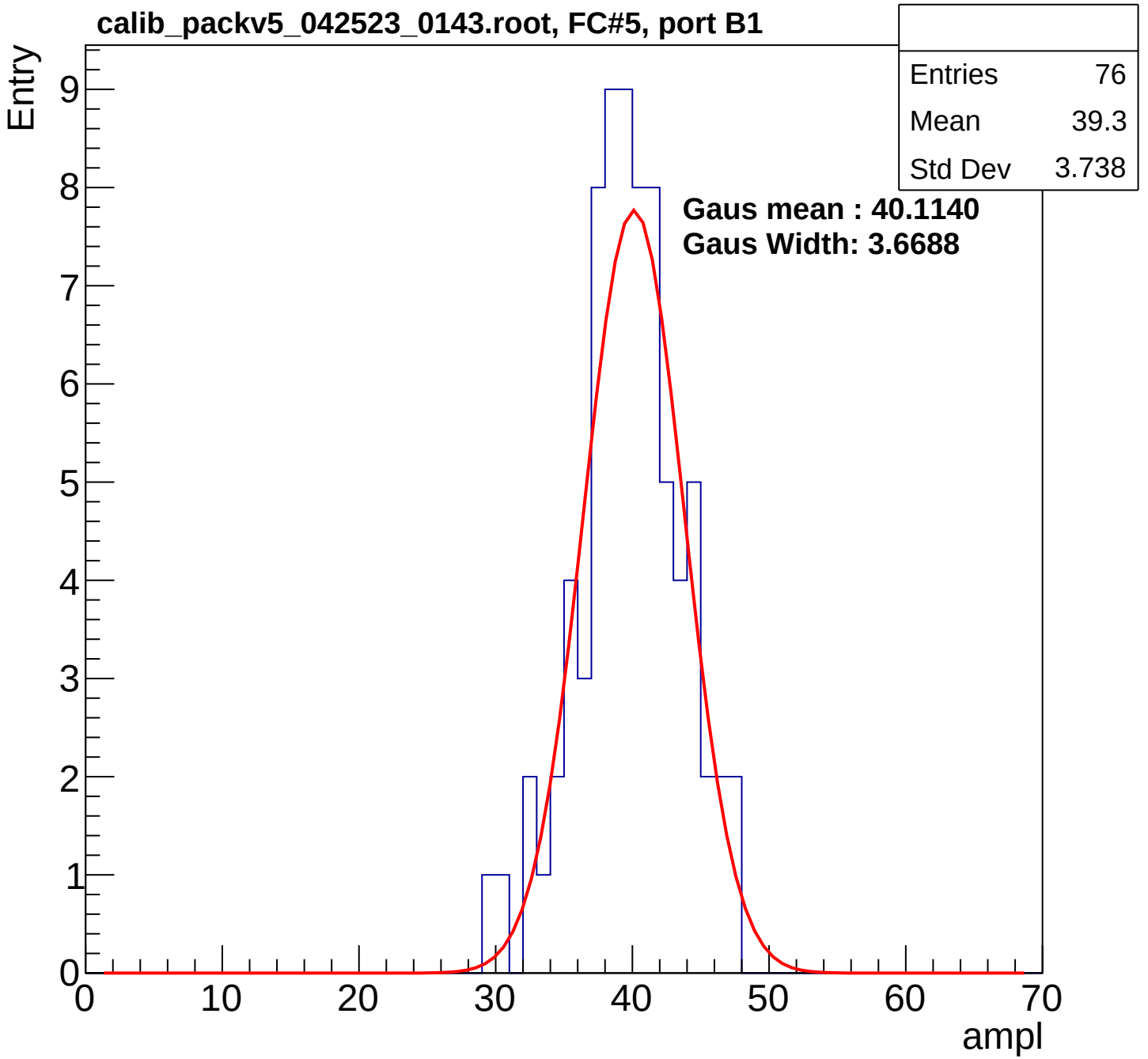
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 32.37
Std Dev 3.117
Gaus mean : 33.3650
Gaus Width: 2.6752
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 39.3
Std Dev 3.738
Gaus mean : 40.1140
Gaus Width: 3.6688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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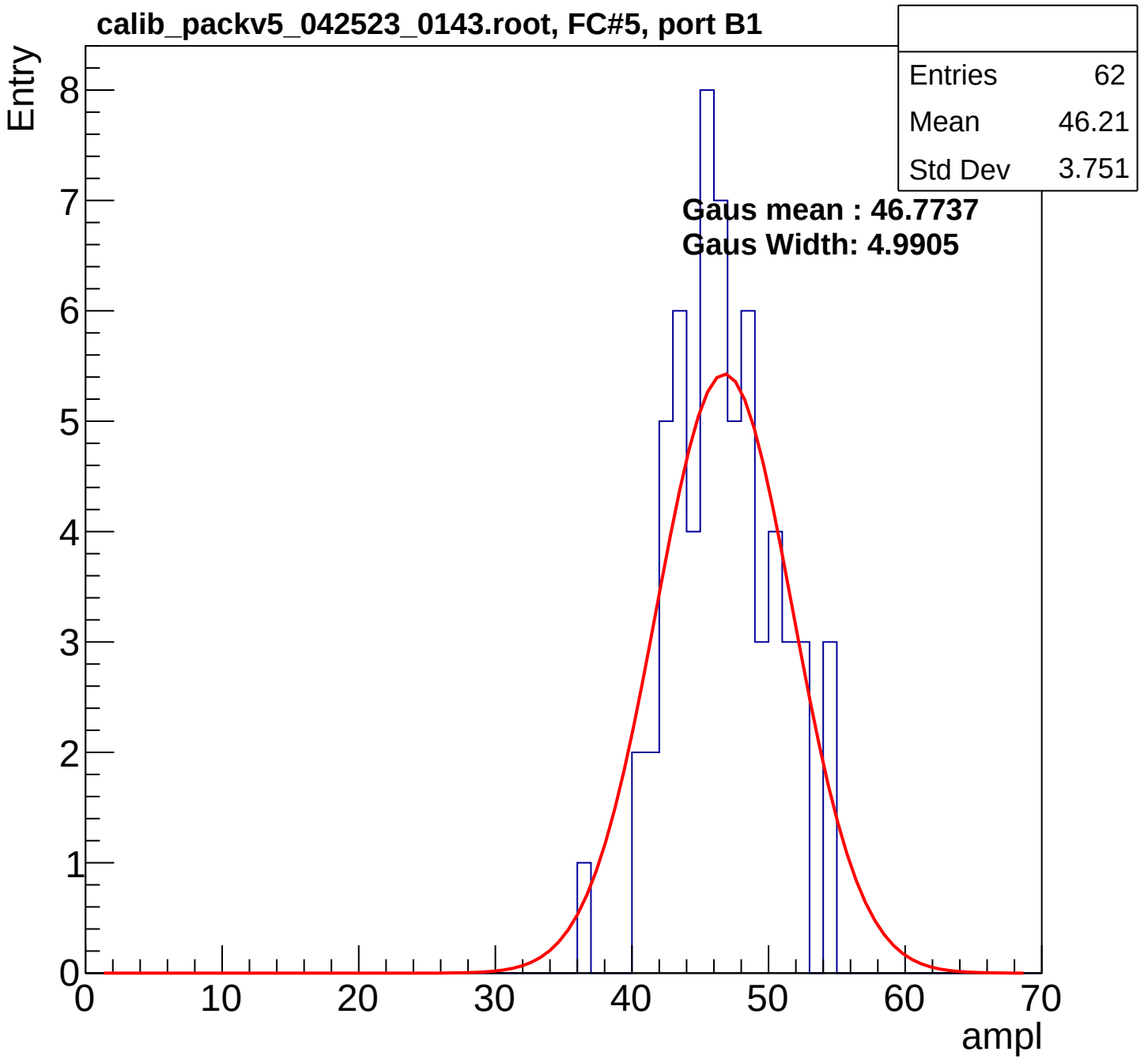
B0L000S, U12-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 46.21
Std Dev 3.751
Gaus mean : 46.7737
Gaus Width: 4.9905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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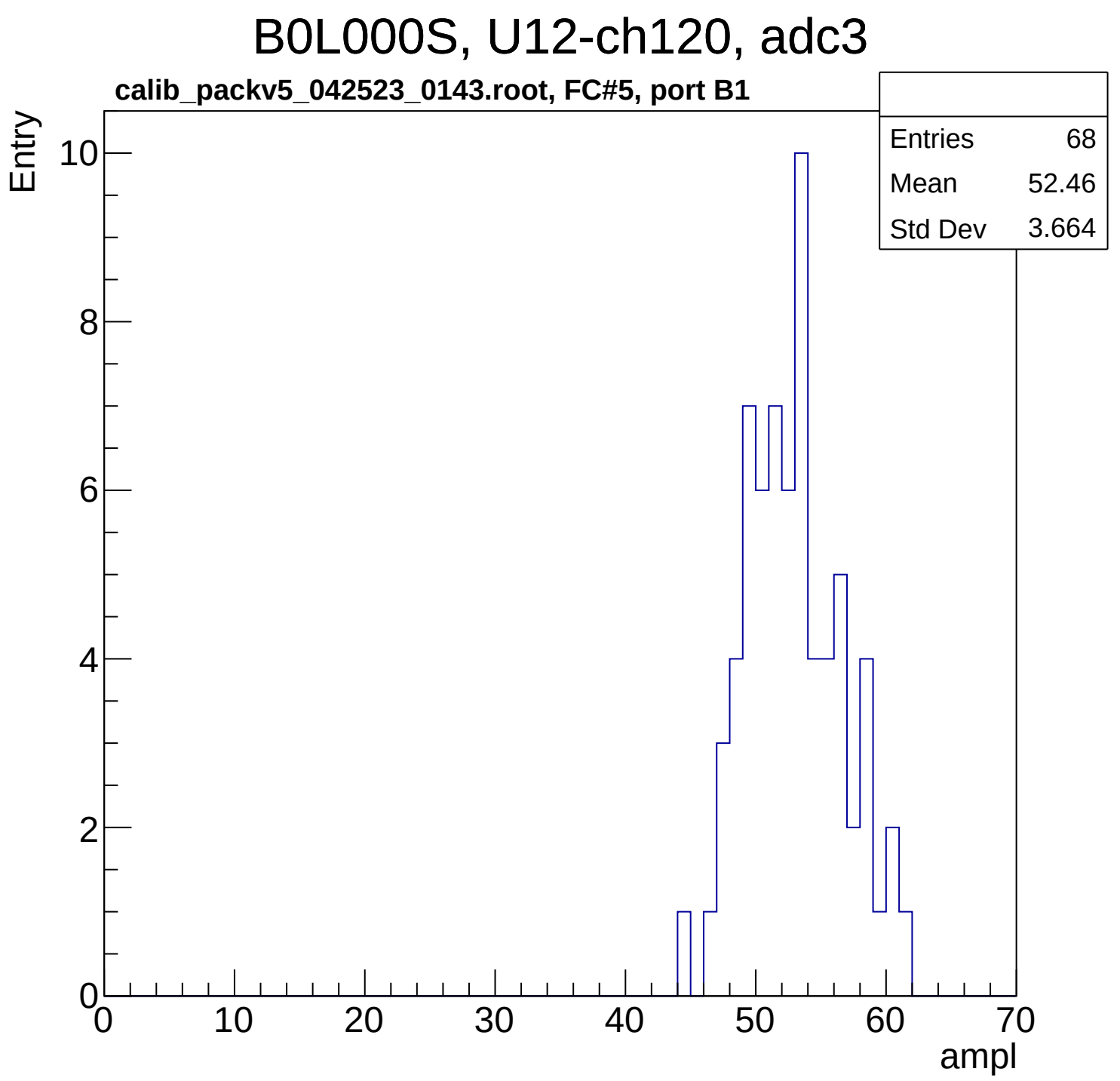
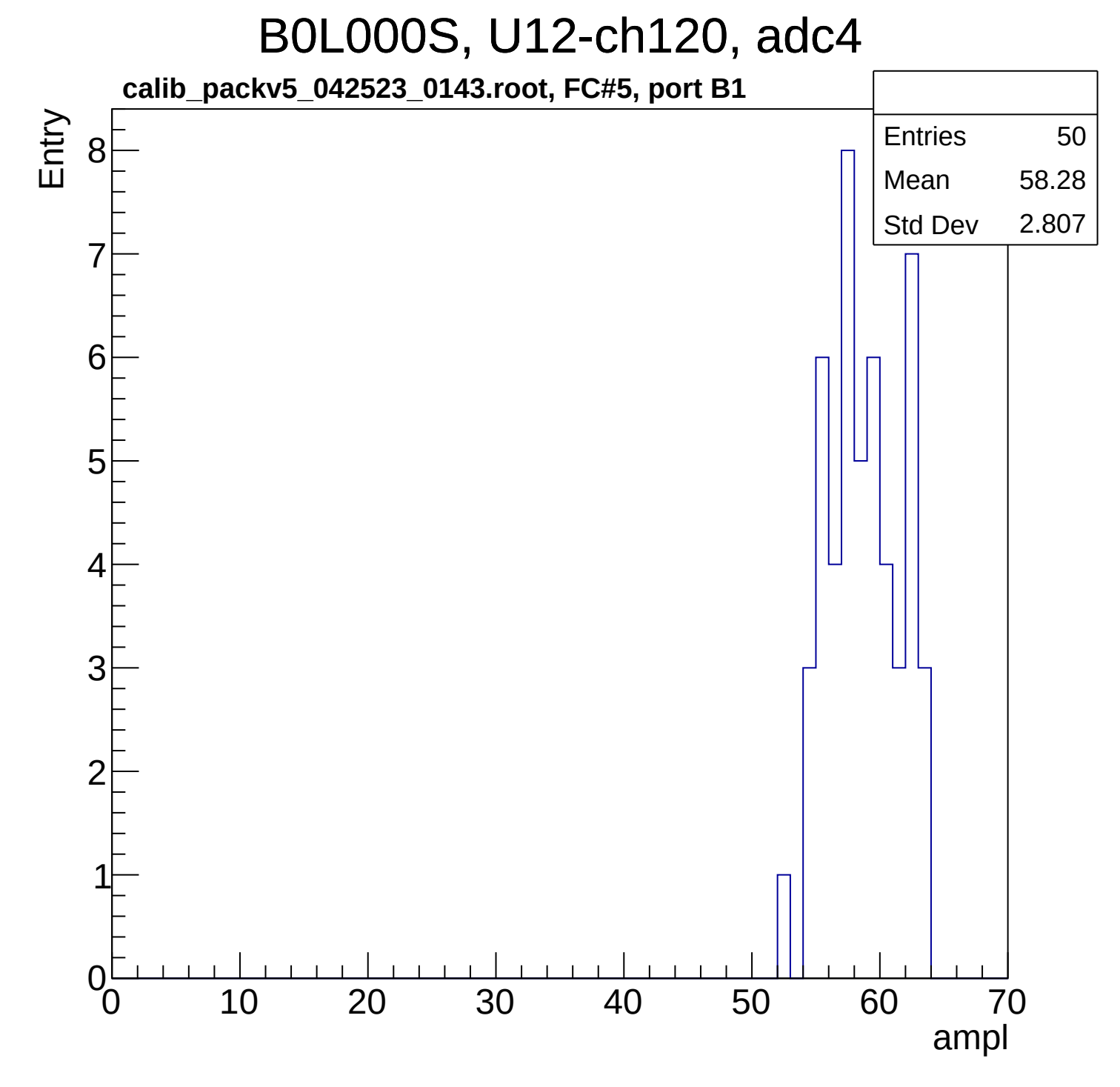
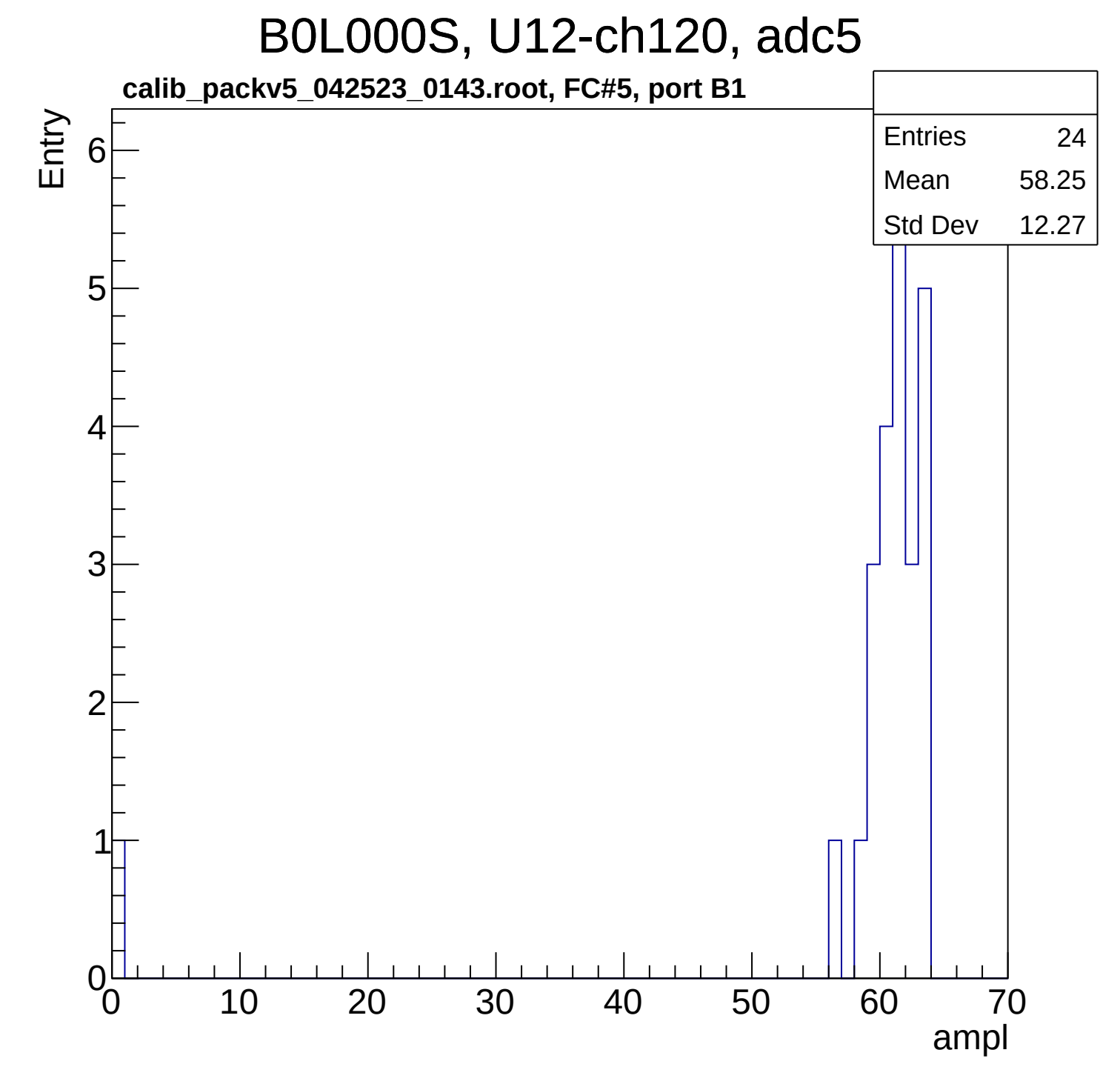
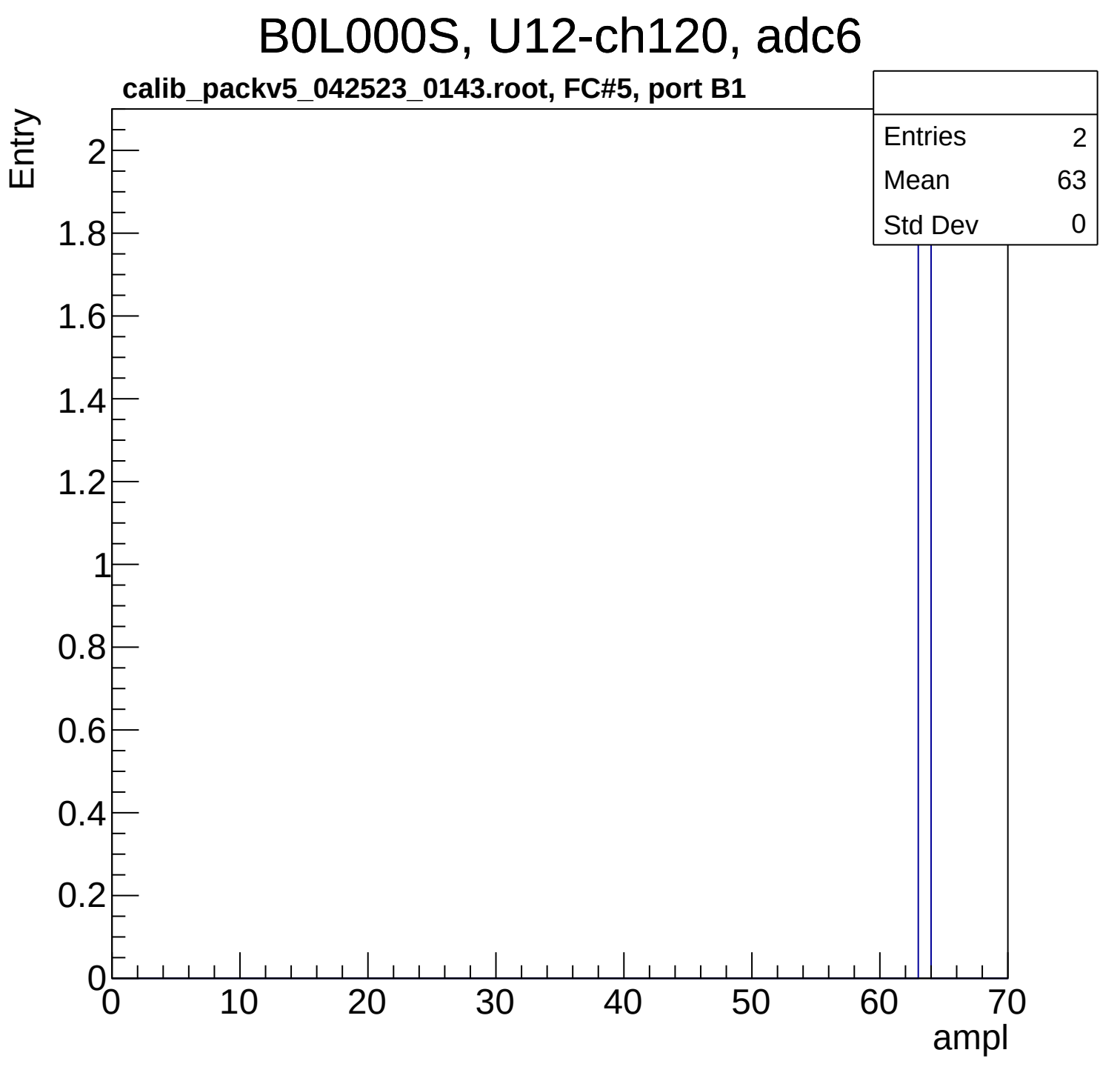

B0L000S, U12-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 52.46
Std Dev 3.664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.28
Std Dev 2.807
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.25
Std Dev 12.27
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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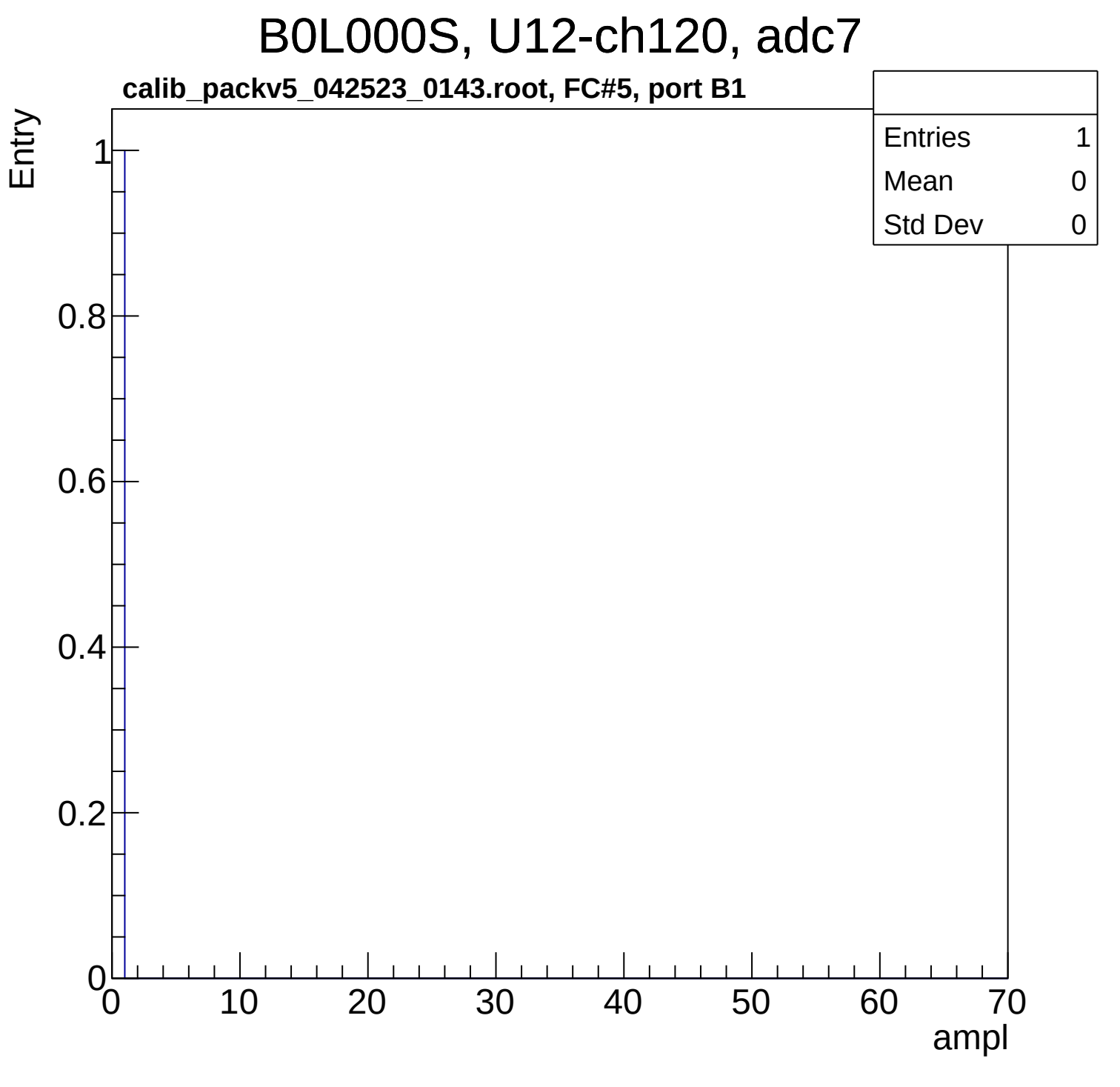
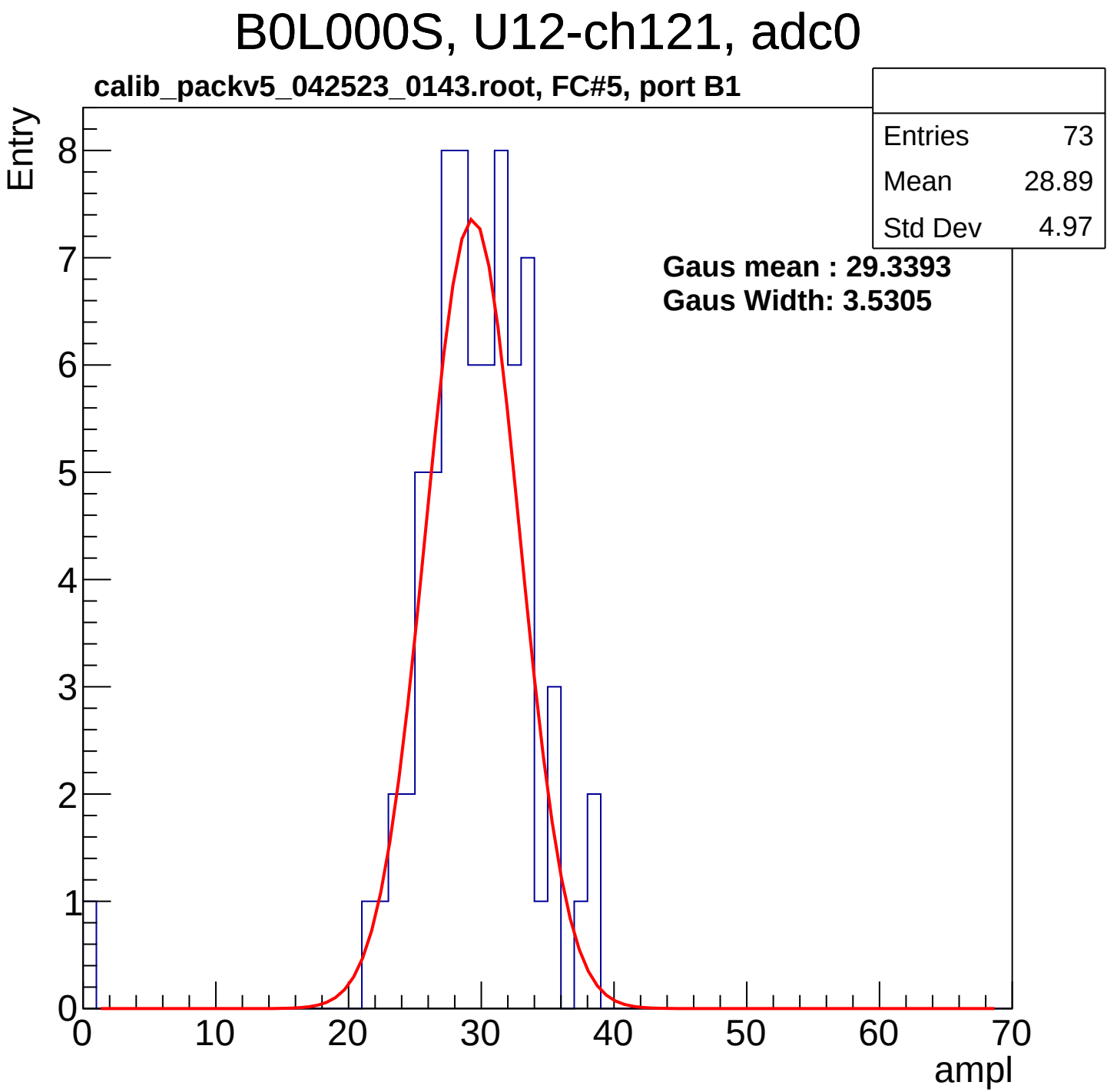
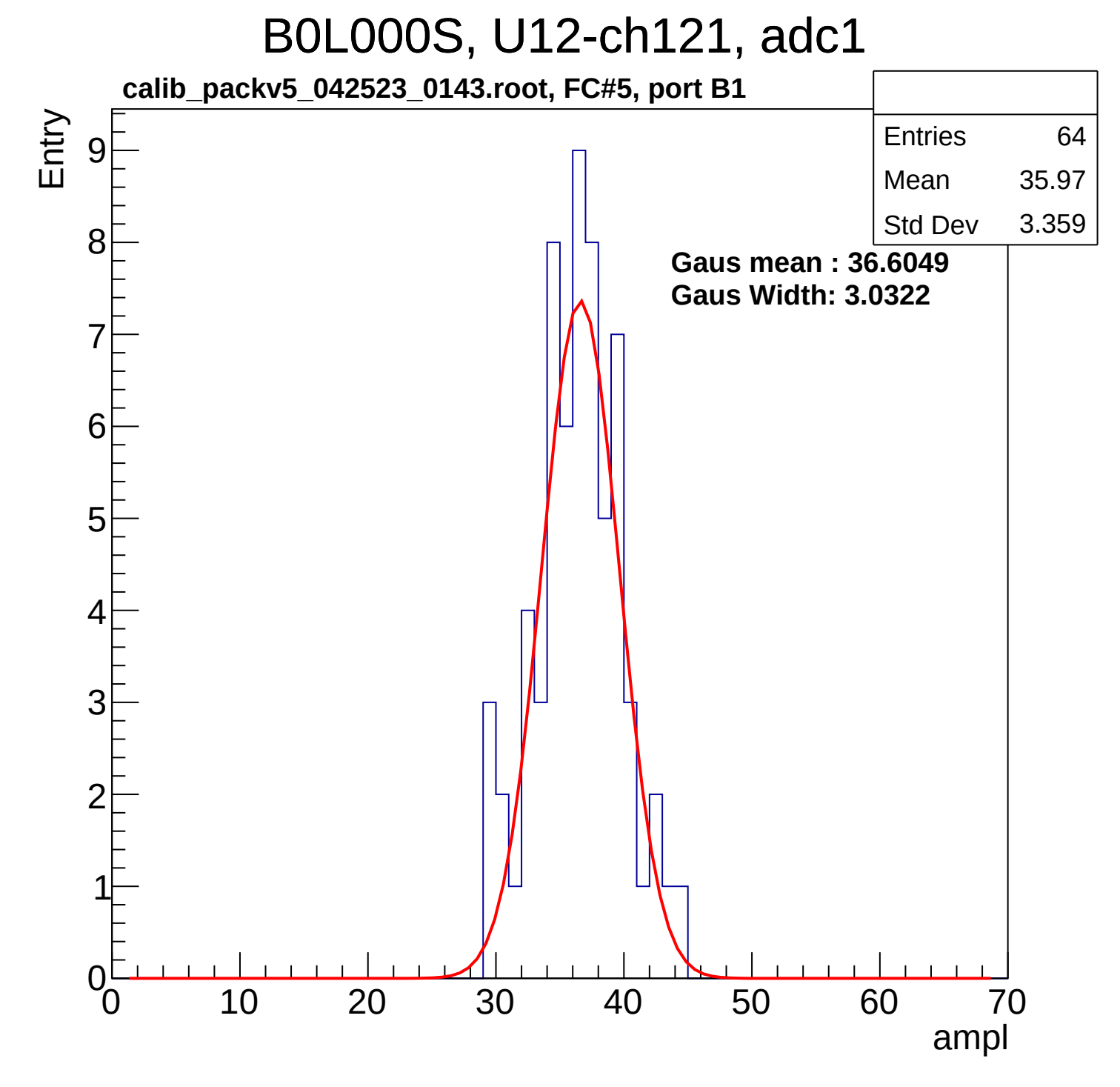
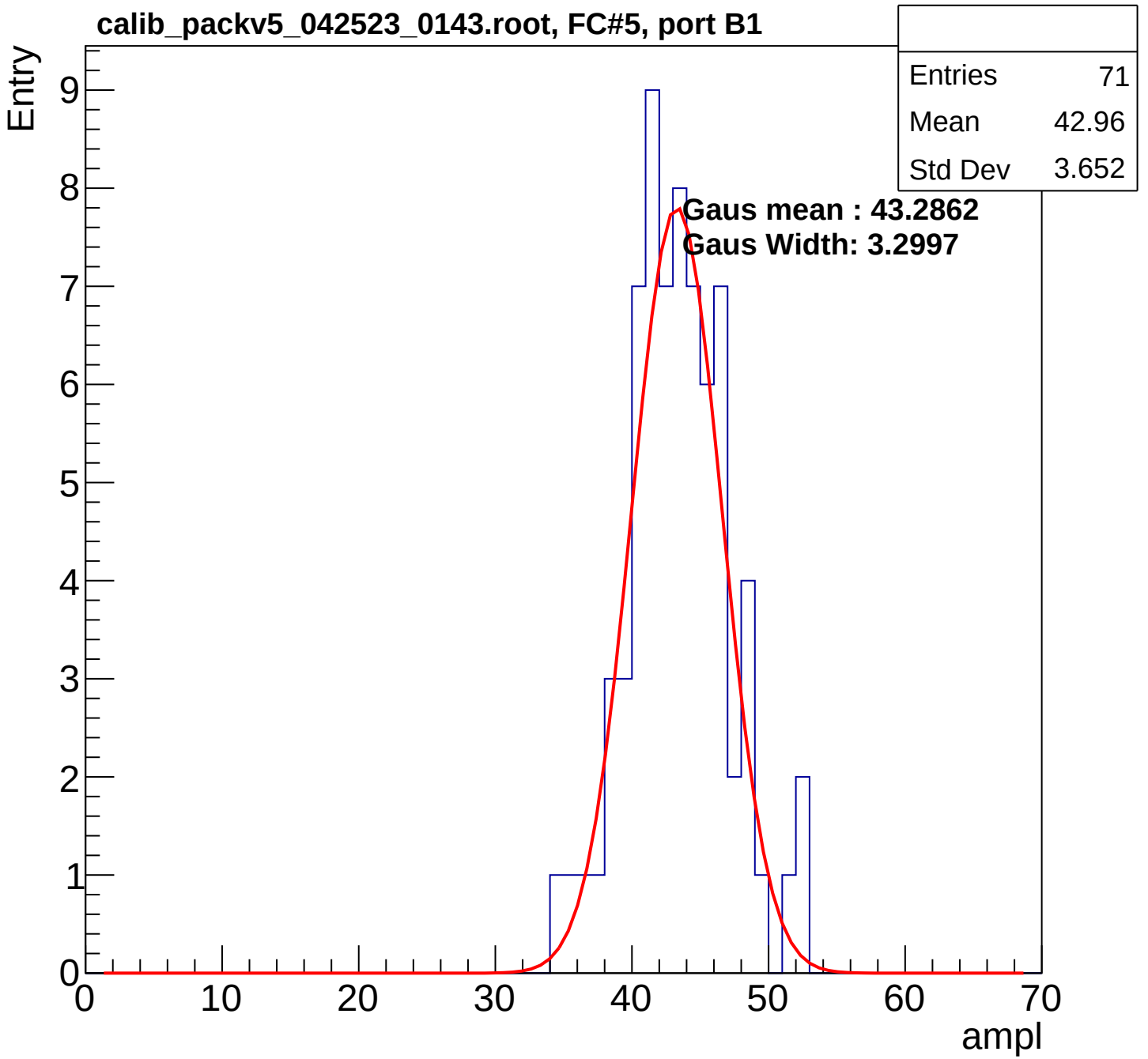
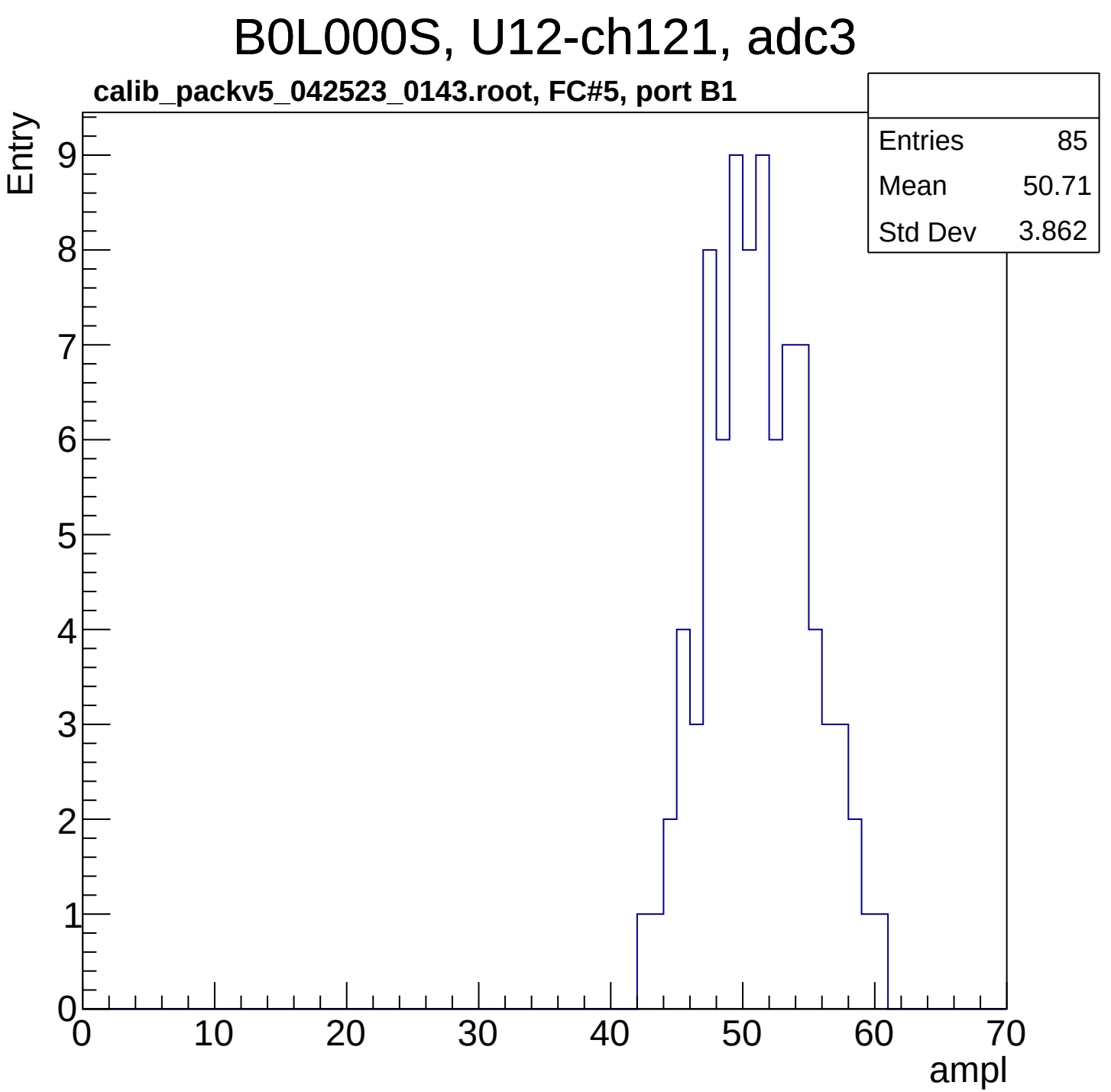

B0L000S, U12-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.89
Std Dev 4.97
Gaus mean : 29.3393
Gaus Width: 3.5305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.97
Std Dev 3.359
Gaus mean : 36.6049
Gaus Width: 3.0322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.96
Std Dev 3.652
Gaus mean : 43.2862
Gaus Width: 3.2997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 50.71
Std Dev 3.862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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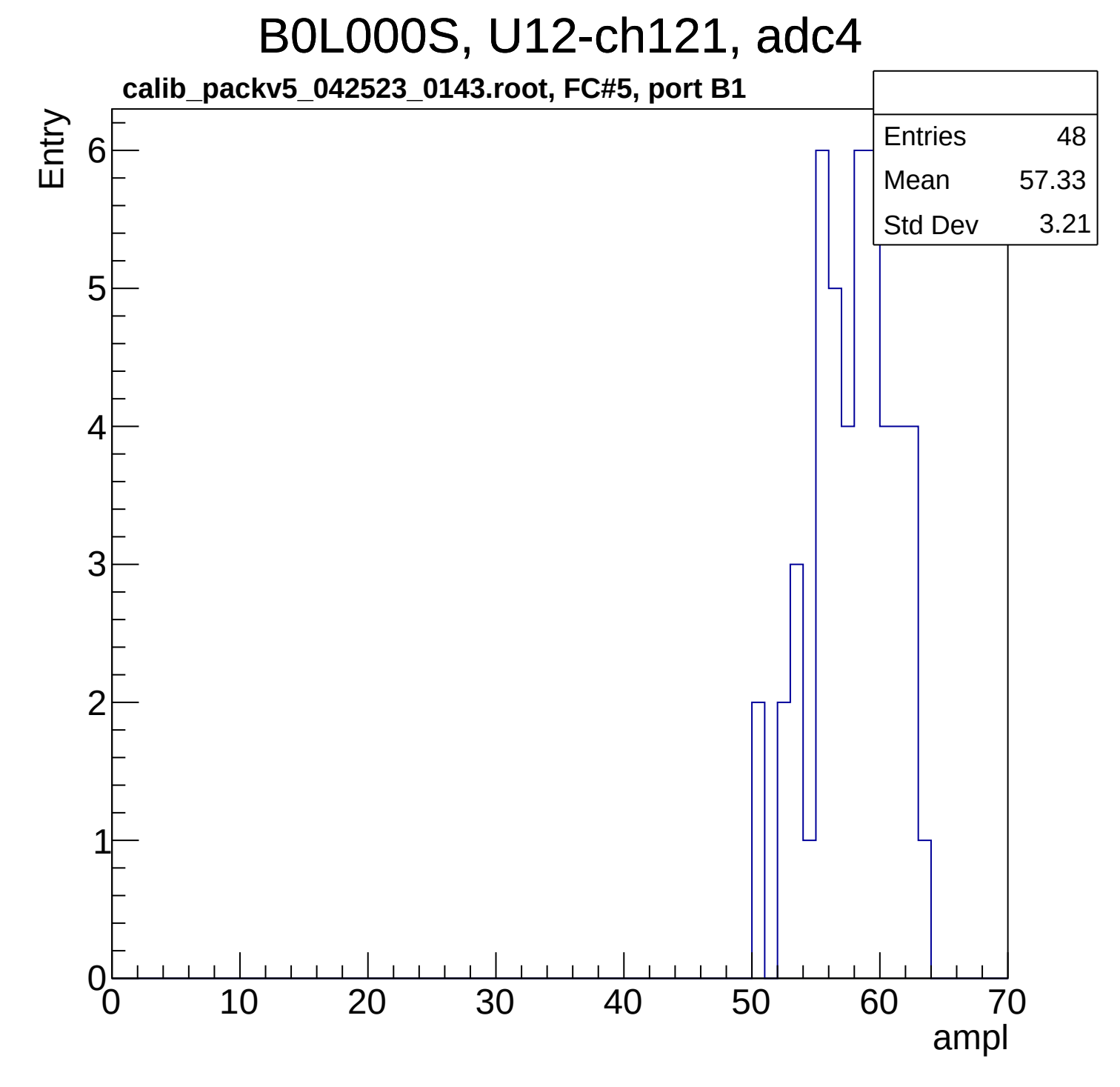
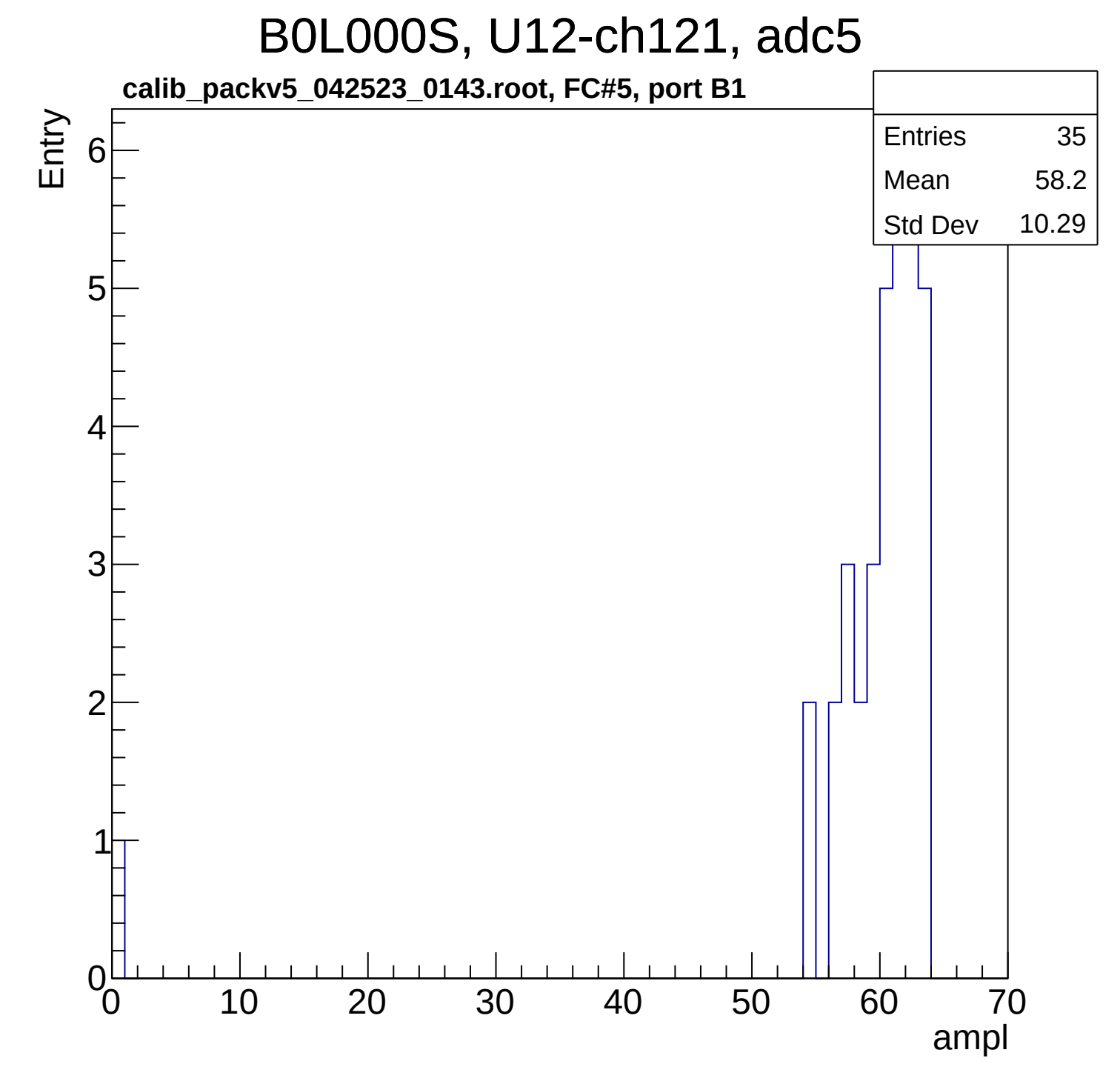
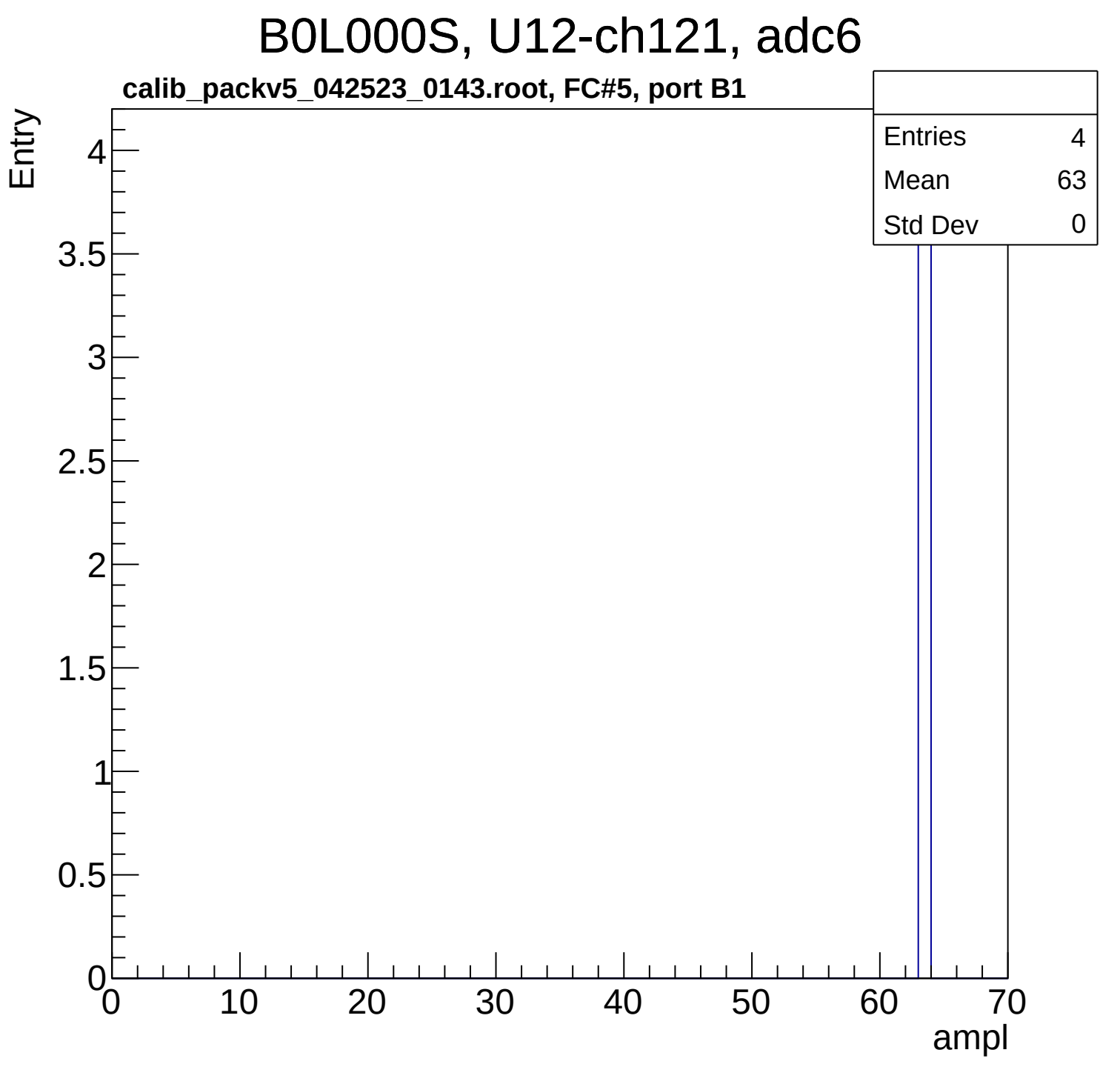
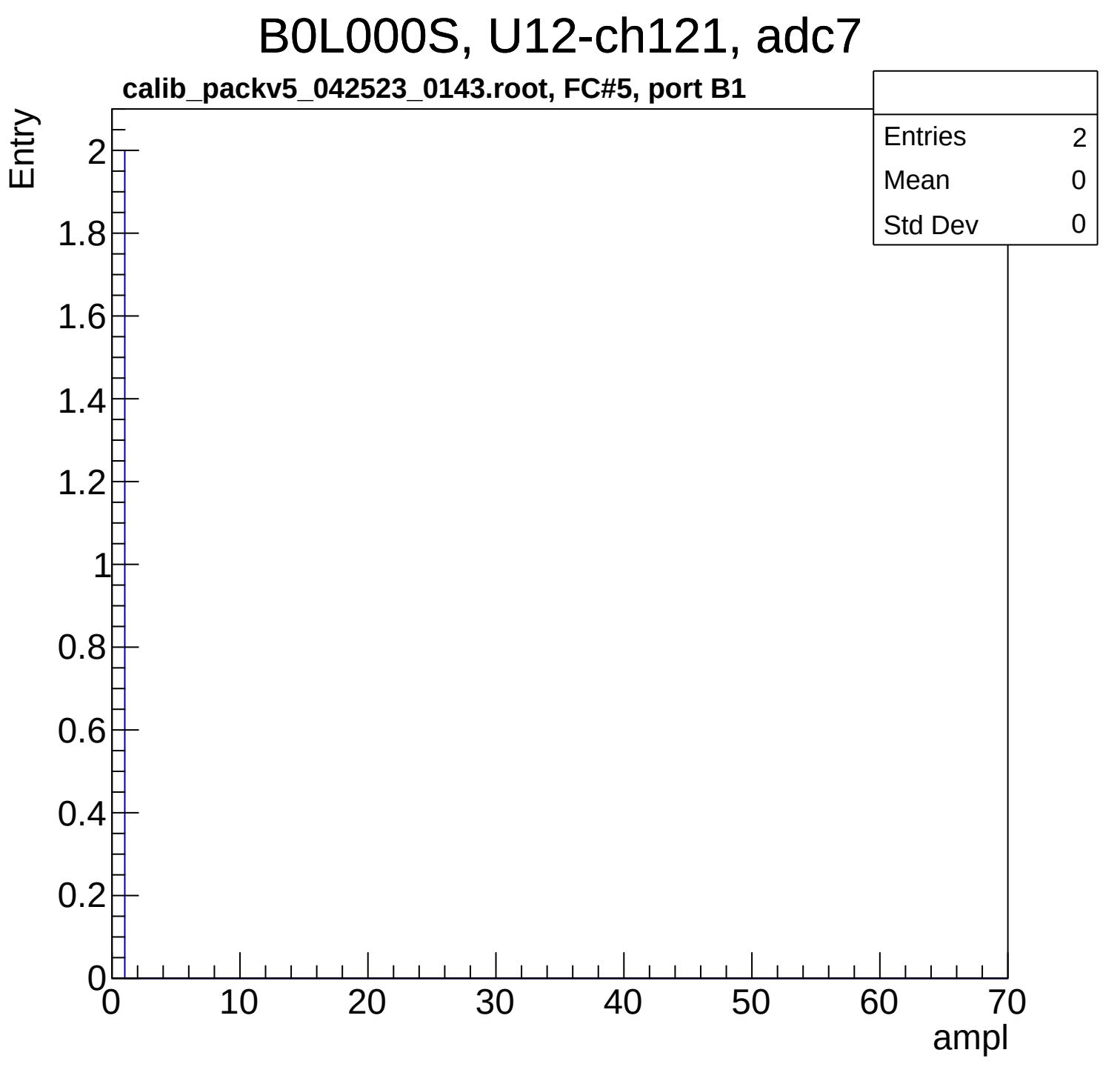
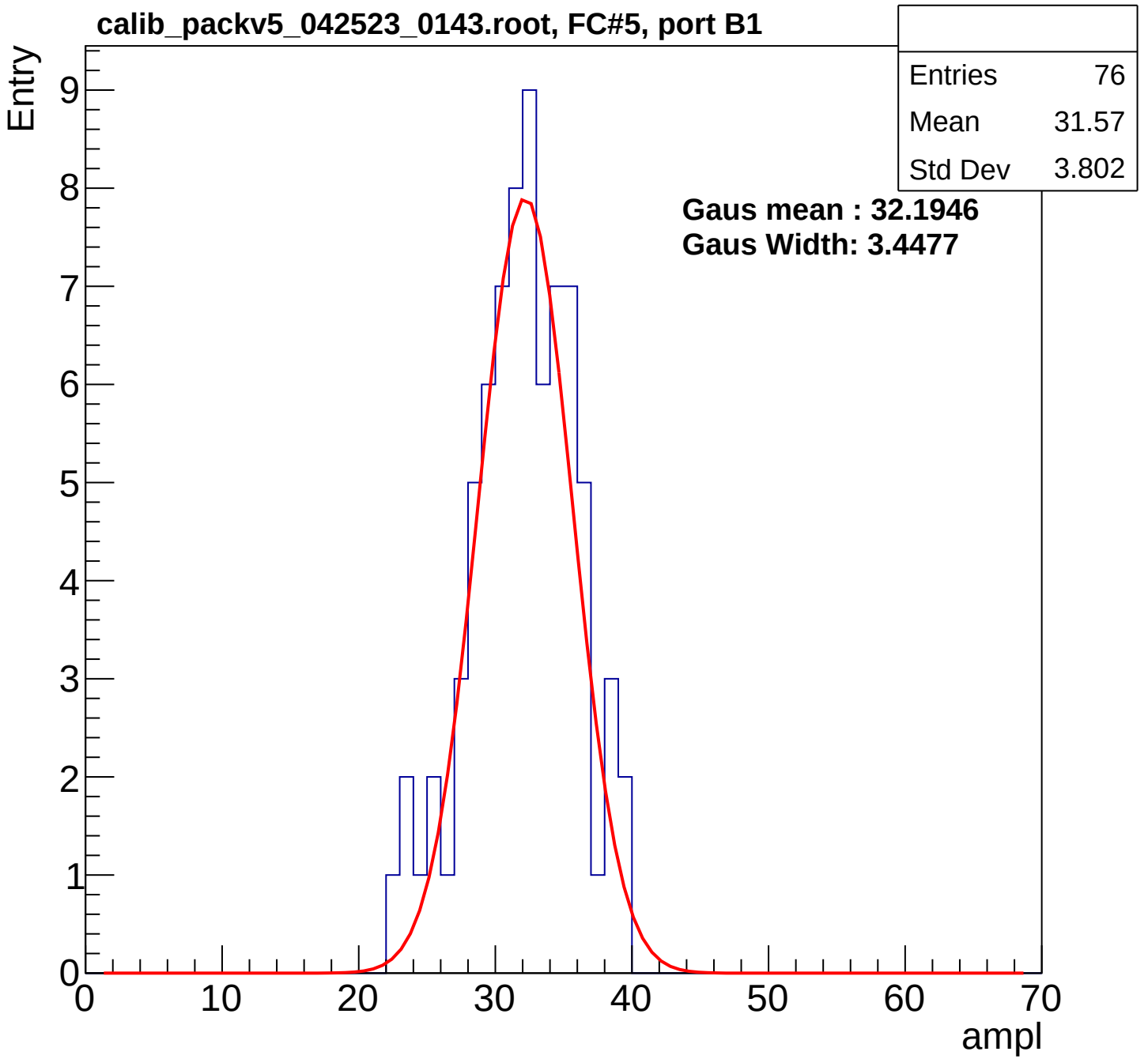

B0L000S, U12-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 57.33
Std Dev 3.21
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U12-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.2
Std Dev 10.29
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 31.57
Std Dev 3.802
Gaus mean : 32.1946
Gaus Width: 3.4477
Entry
ampl
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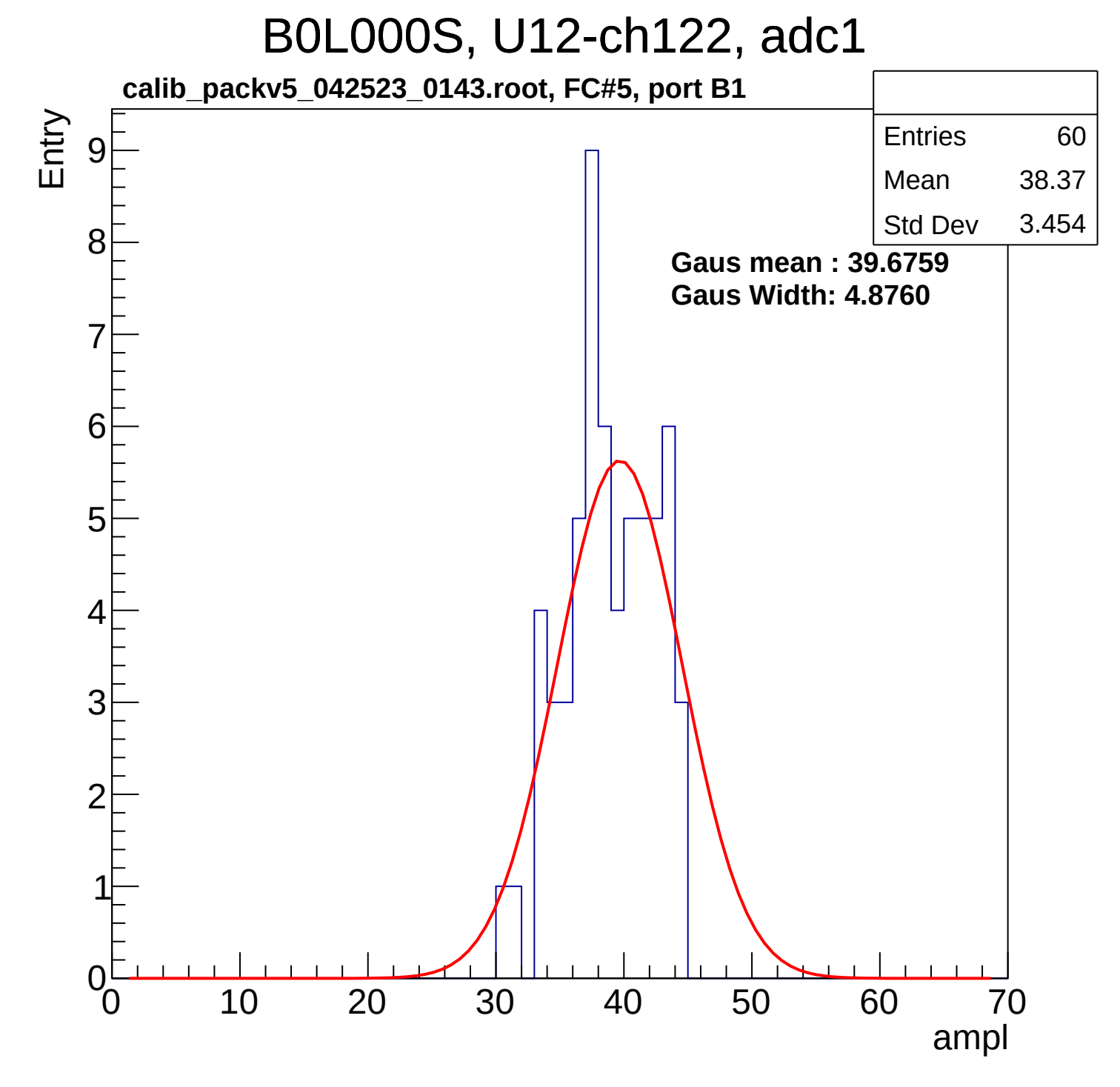
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 38.37
Std Dev 3.454
Gaus mean : 39.6759
Gaus Width: 4.8760
Entry
ampl
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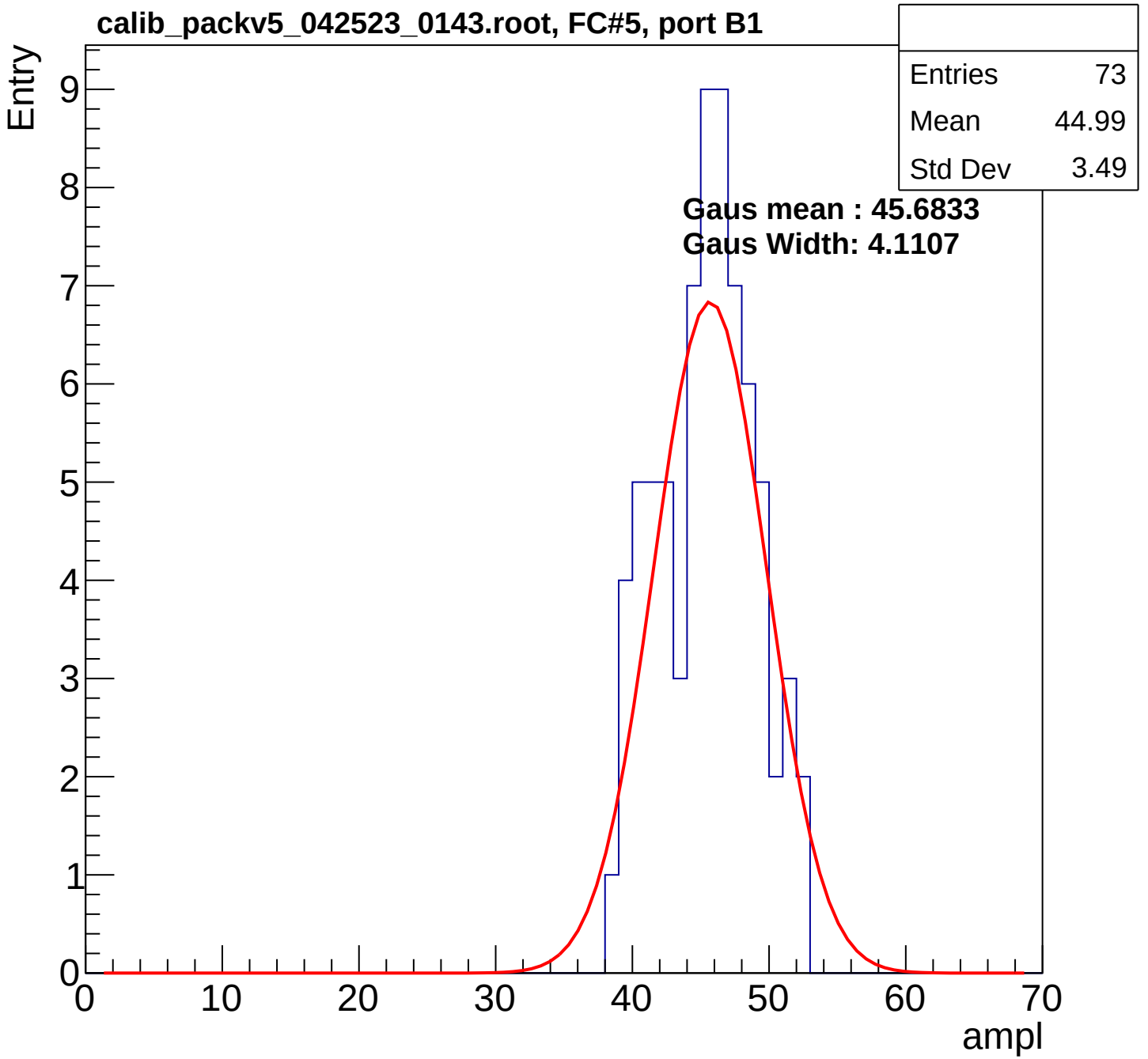
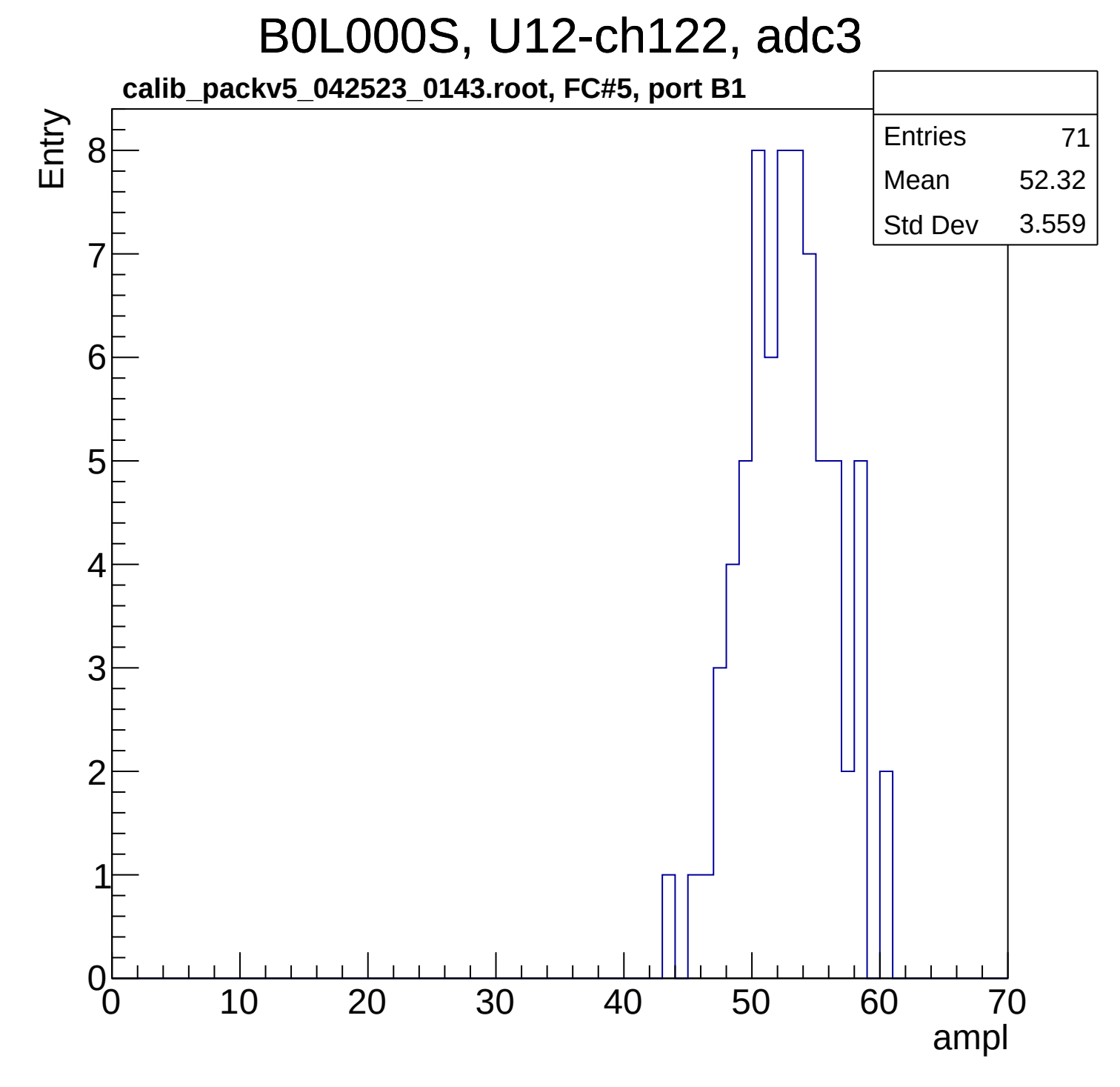
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.99
Std Dev 3.49
Gaus mean : 45.6833
Gaus Width: 4.1107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 52.32
Std Dev 3.559
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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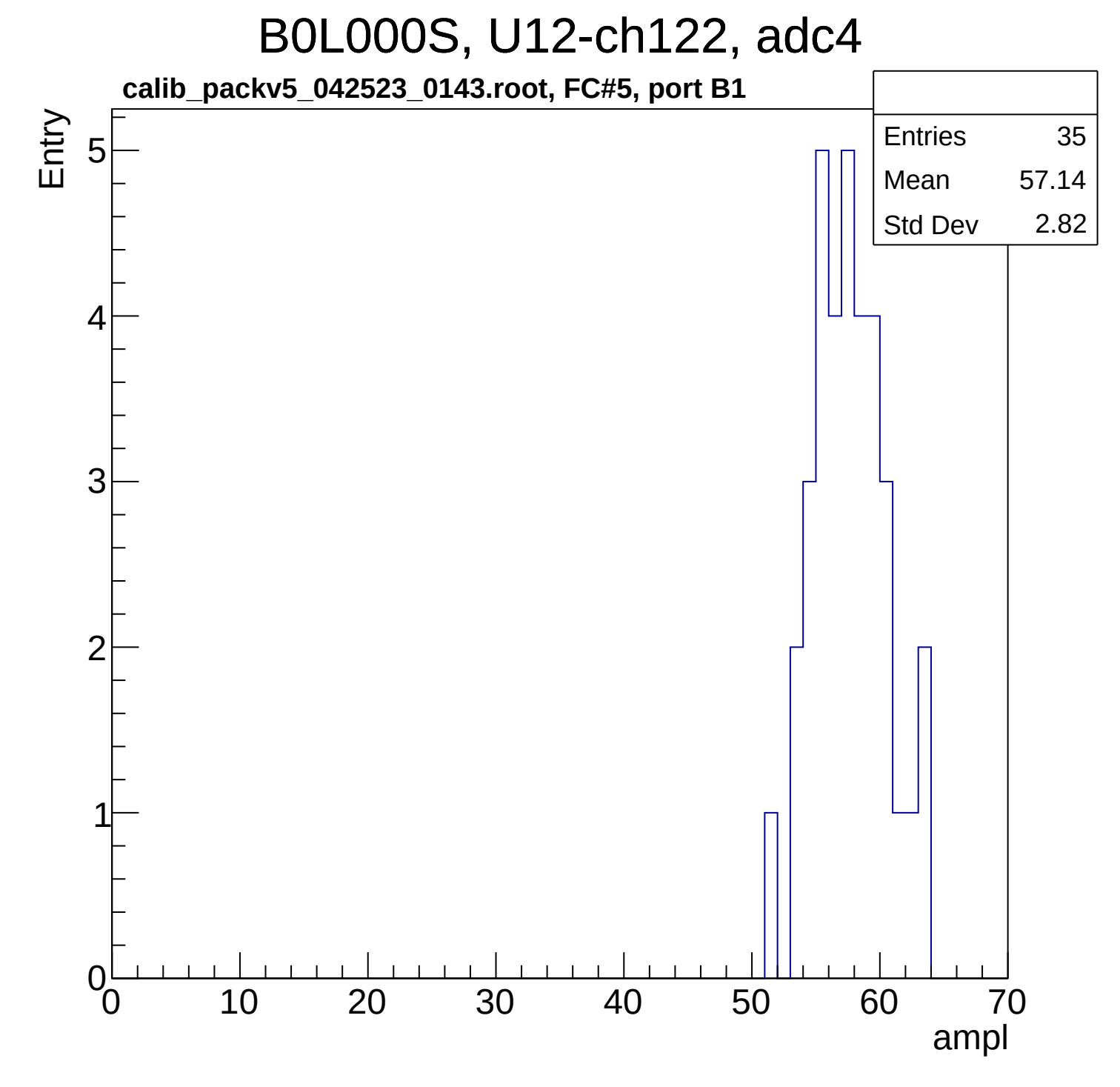
B0L000S, U12-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 57.14
Std Dev 2.82
Entry
0
1
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3
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5
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50
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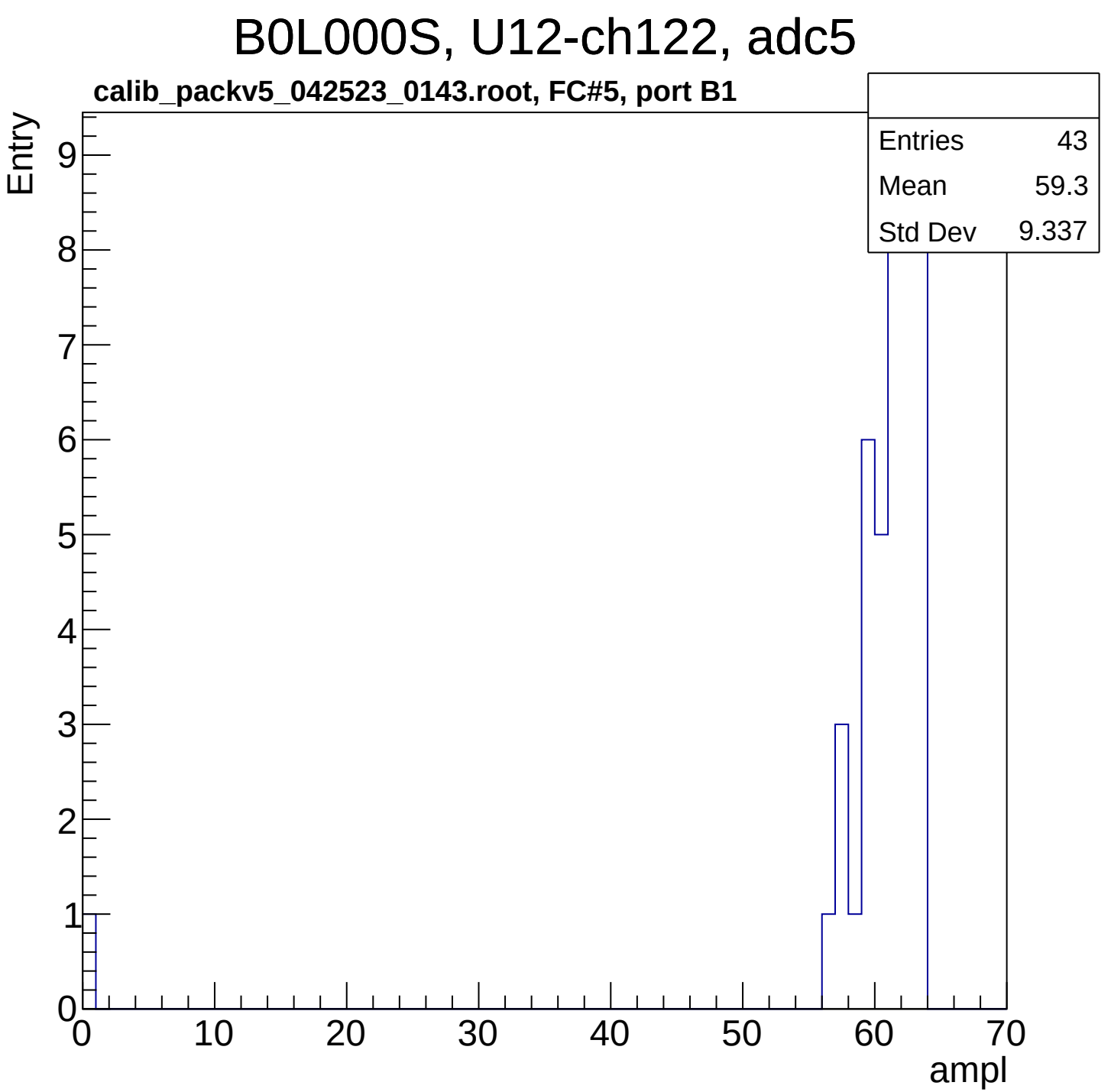
70
ampl


B0L000S, U12-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.3
Std Dev 9.337
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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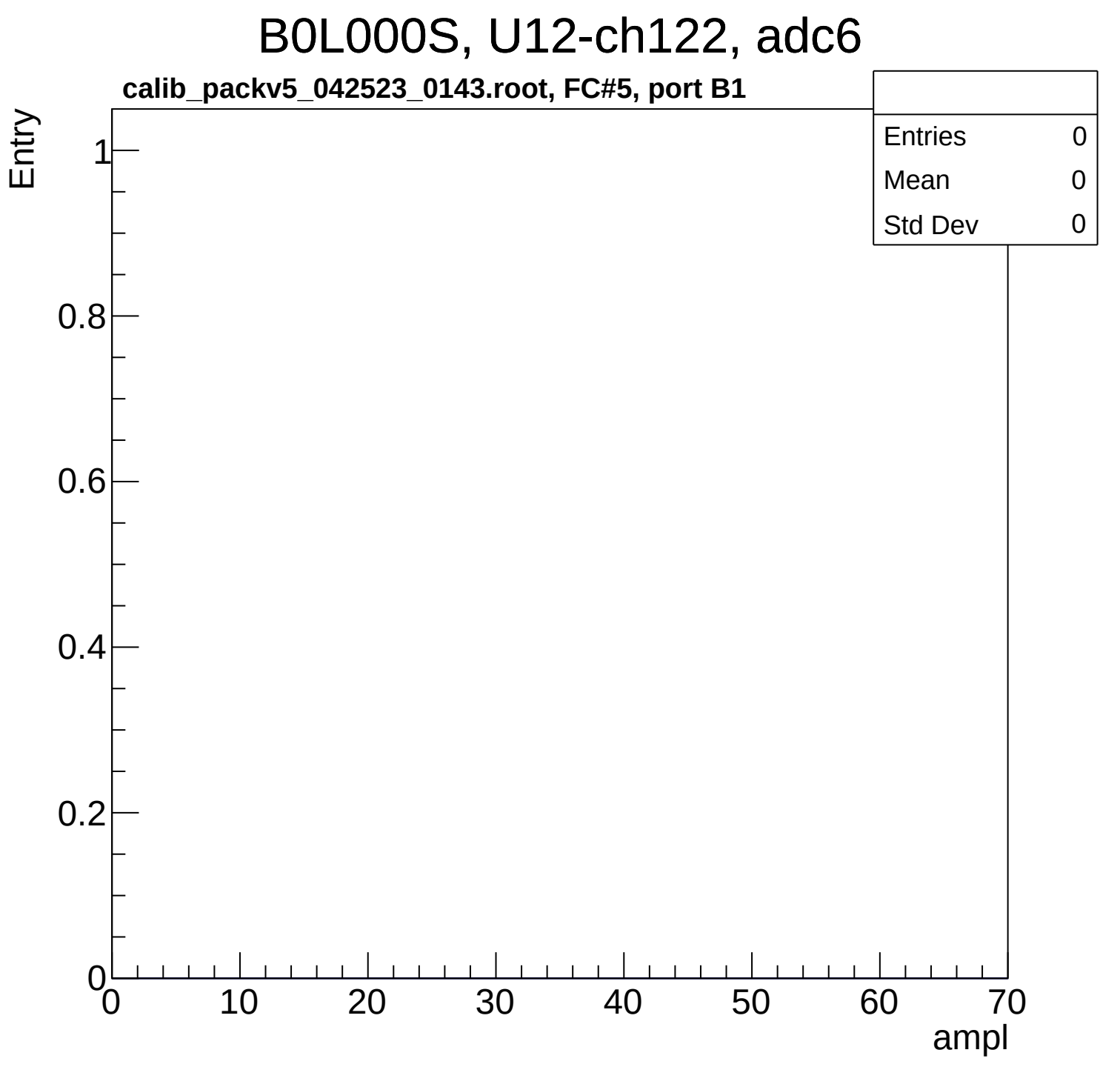
B0L000S, U12-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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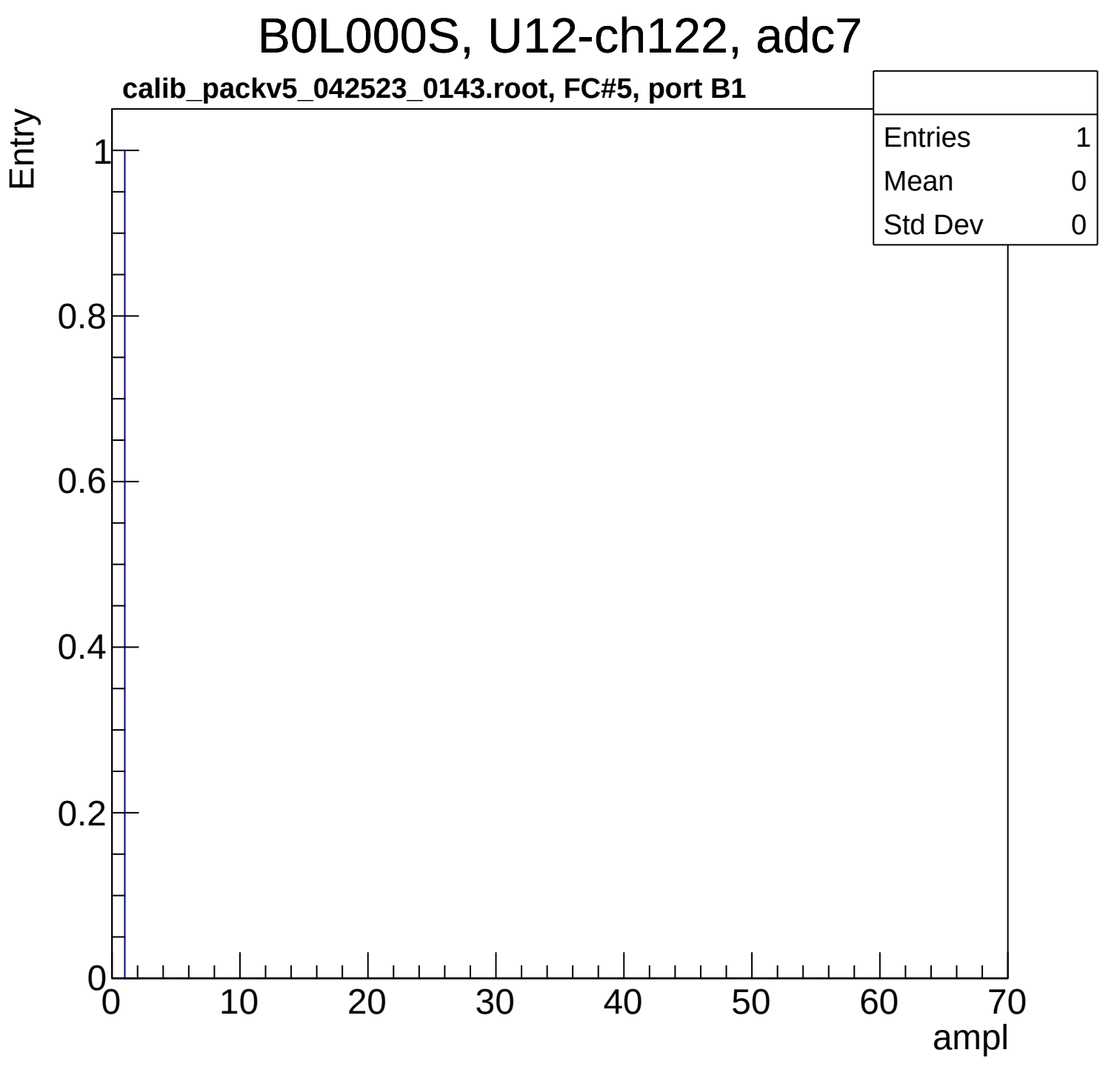
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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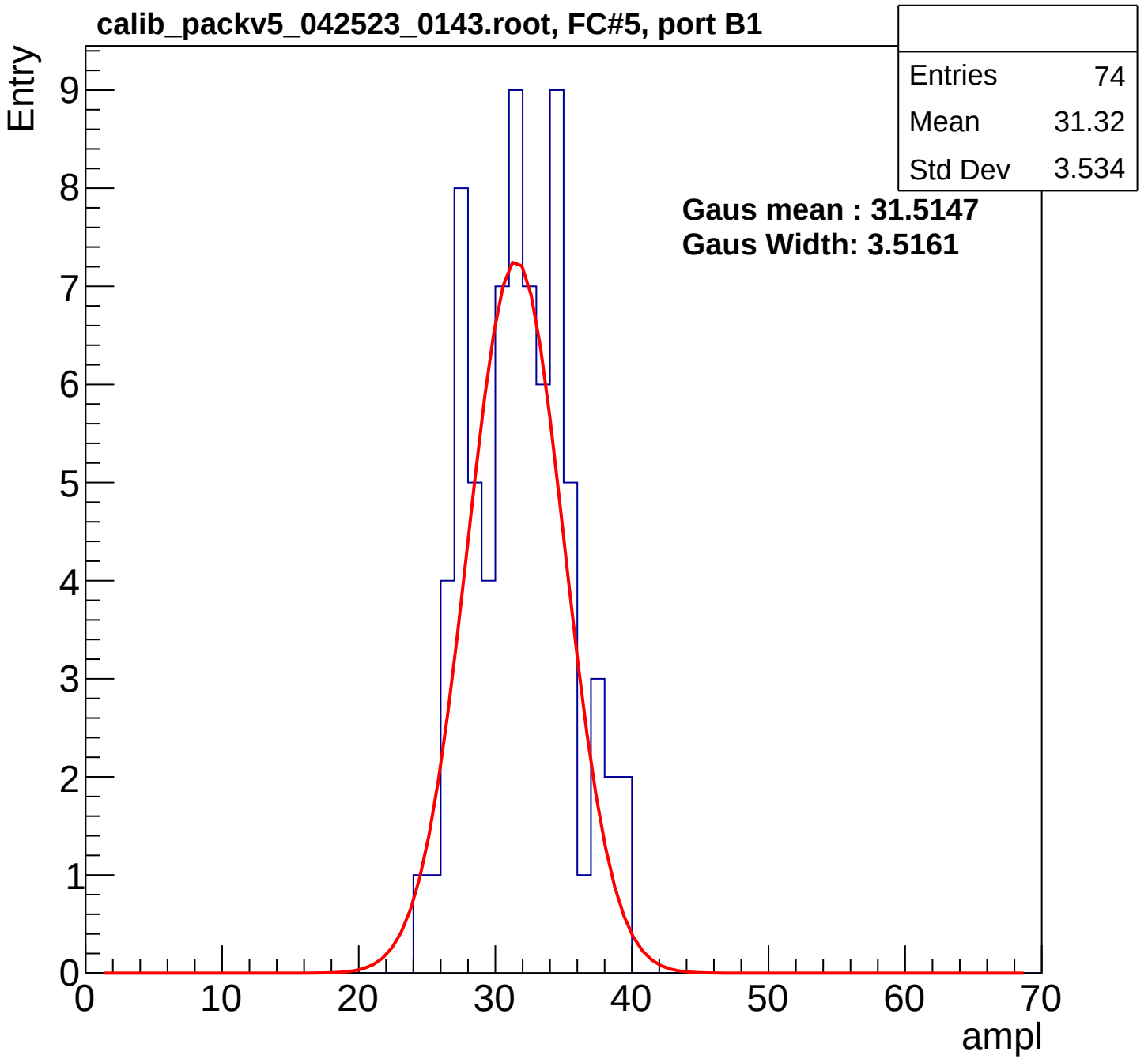
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 31.32
Std Dev 3.534
Gaus mean : 31.5147
Gaus Width: 3.5161
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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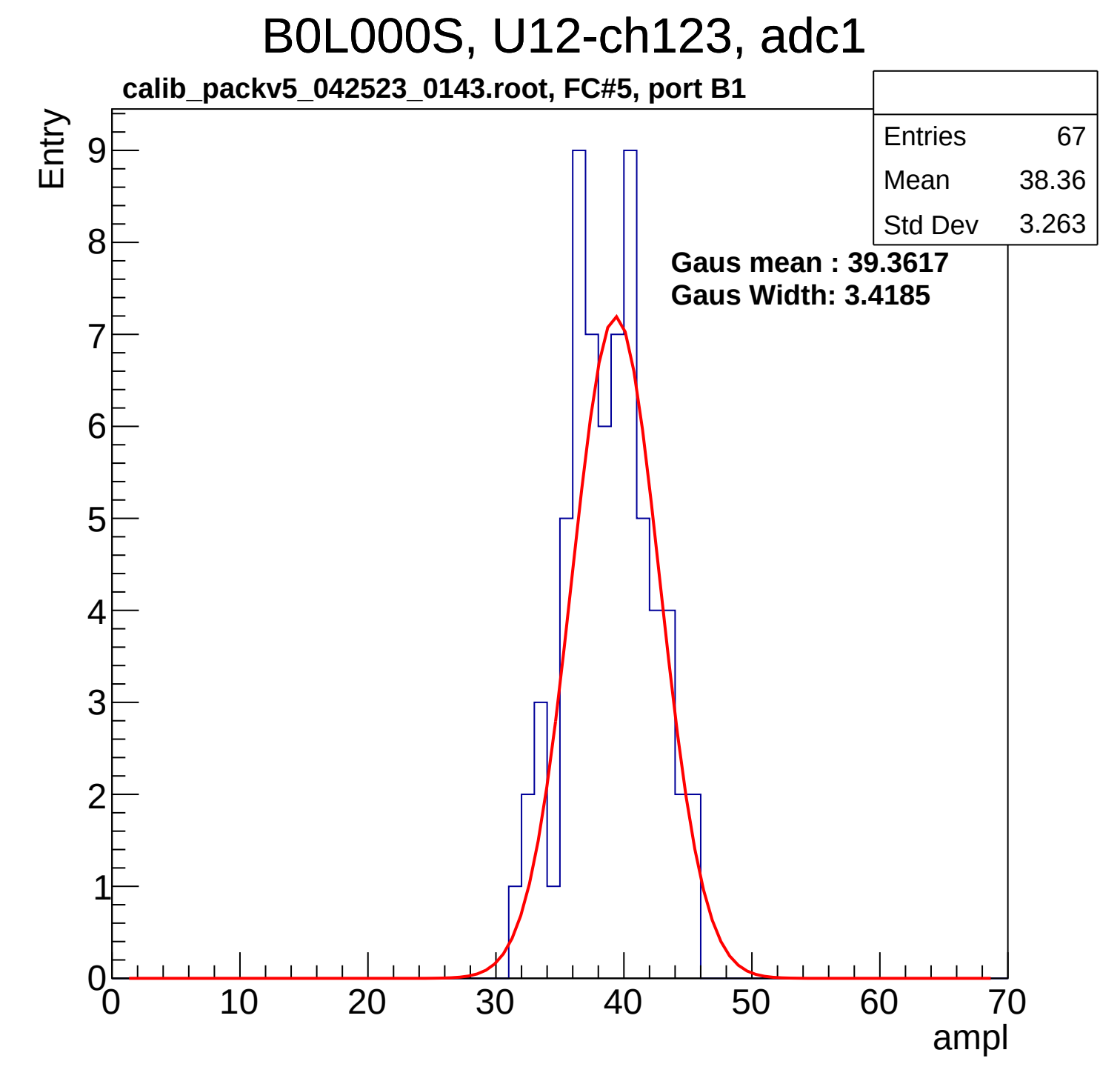
20
30
40
50
60
70


B0L000S, U12-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 38.36
Std Dev 3.263
Gaus mean : 39.3617
Gaus Width: 3.4185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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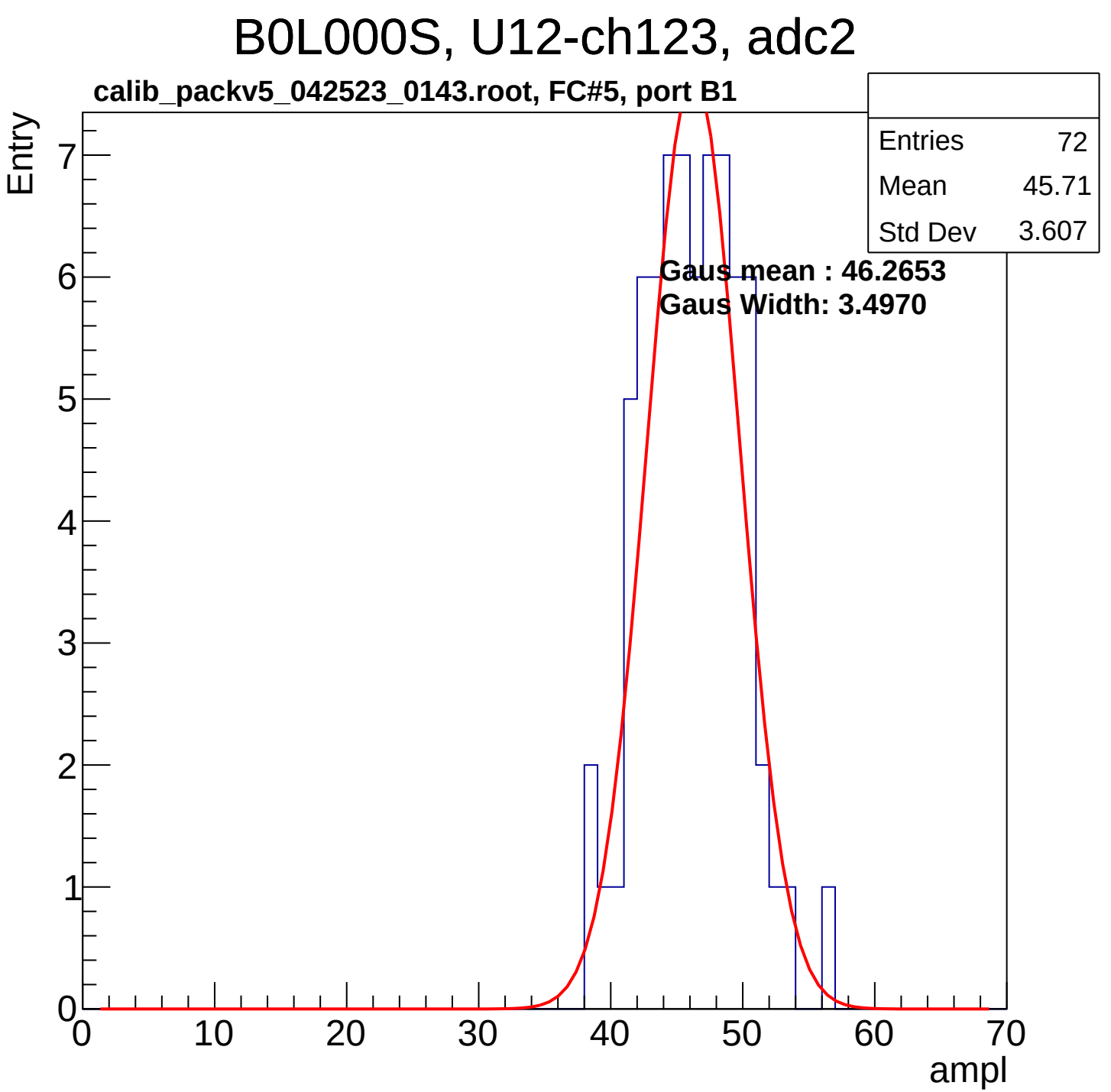
B0L000S, U12-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 45.71
Std Dev 3.607
Gaus mean : 46.2653
Gaus Width: 3.4970
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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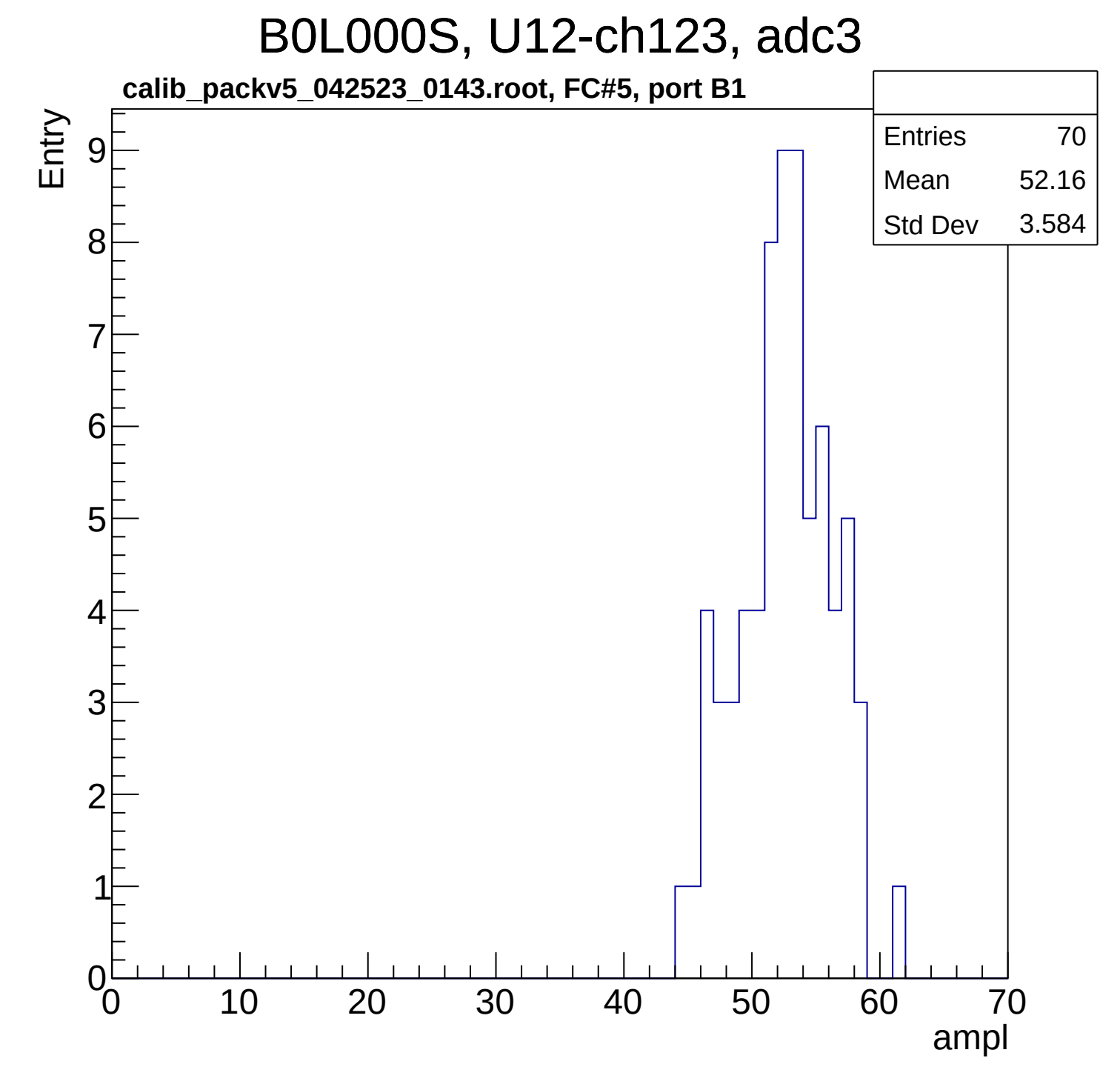
B0L000S, U12-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.16
Std Dev 3.584
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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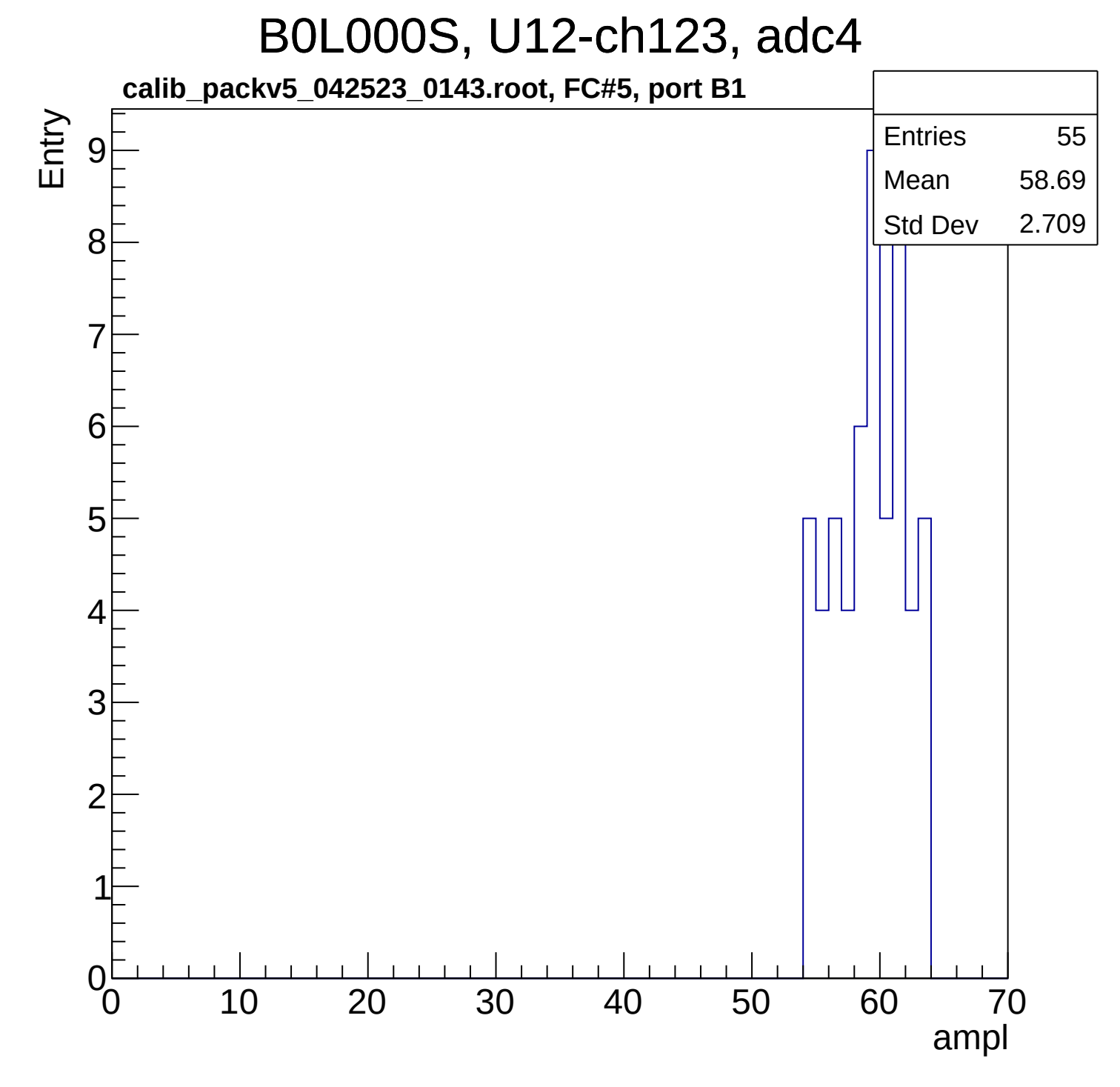
B0L000S, U12-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.69
Std Dev 2.709
Entry
ampl
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2
3
4
5
6
7
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10
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40
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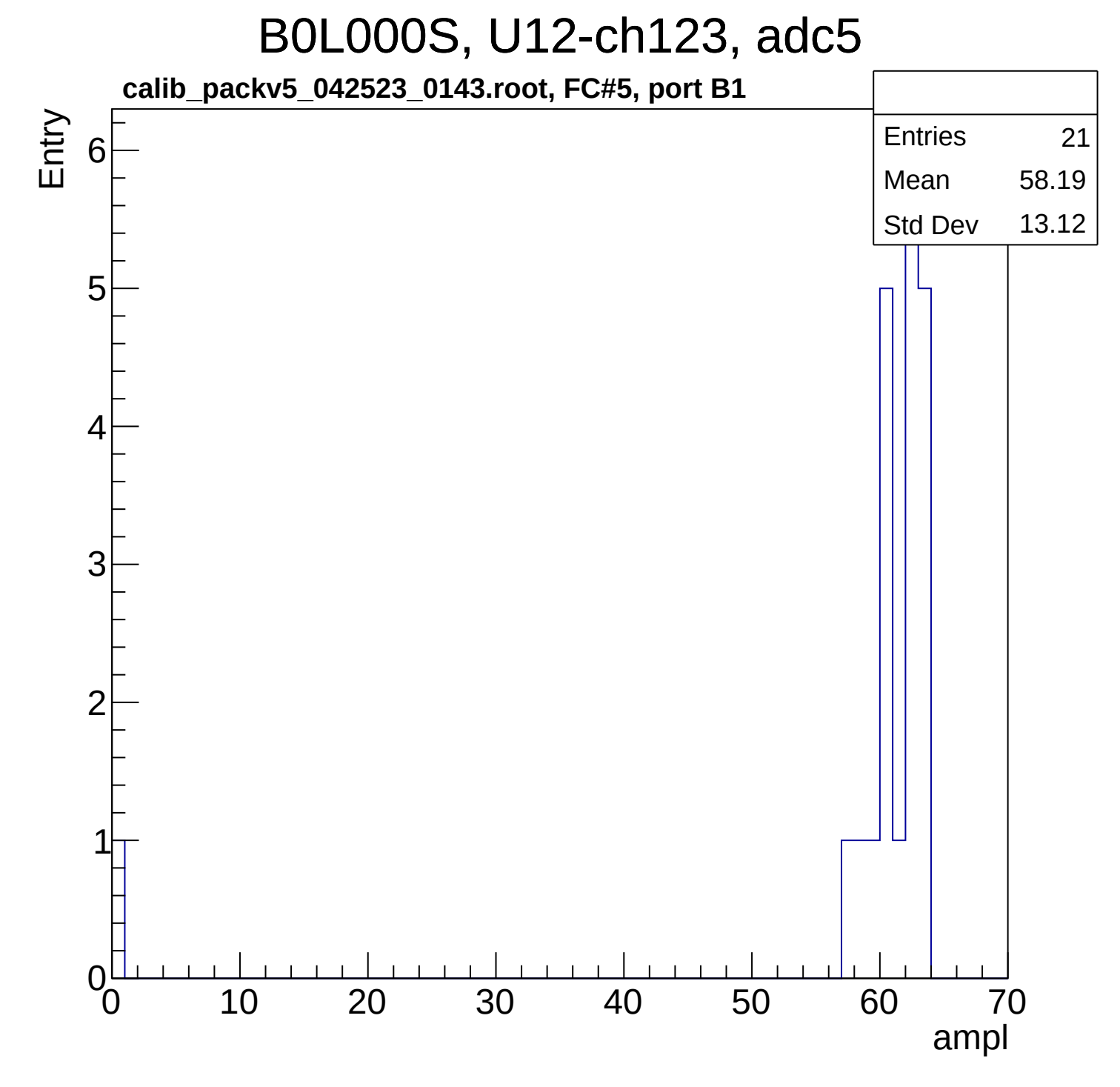
B0L000S, U12-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.19
Std Dev 13.12
Entry
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2
3
4
5
6
0
10
20
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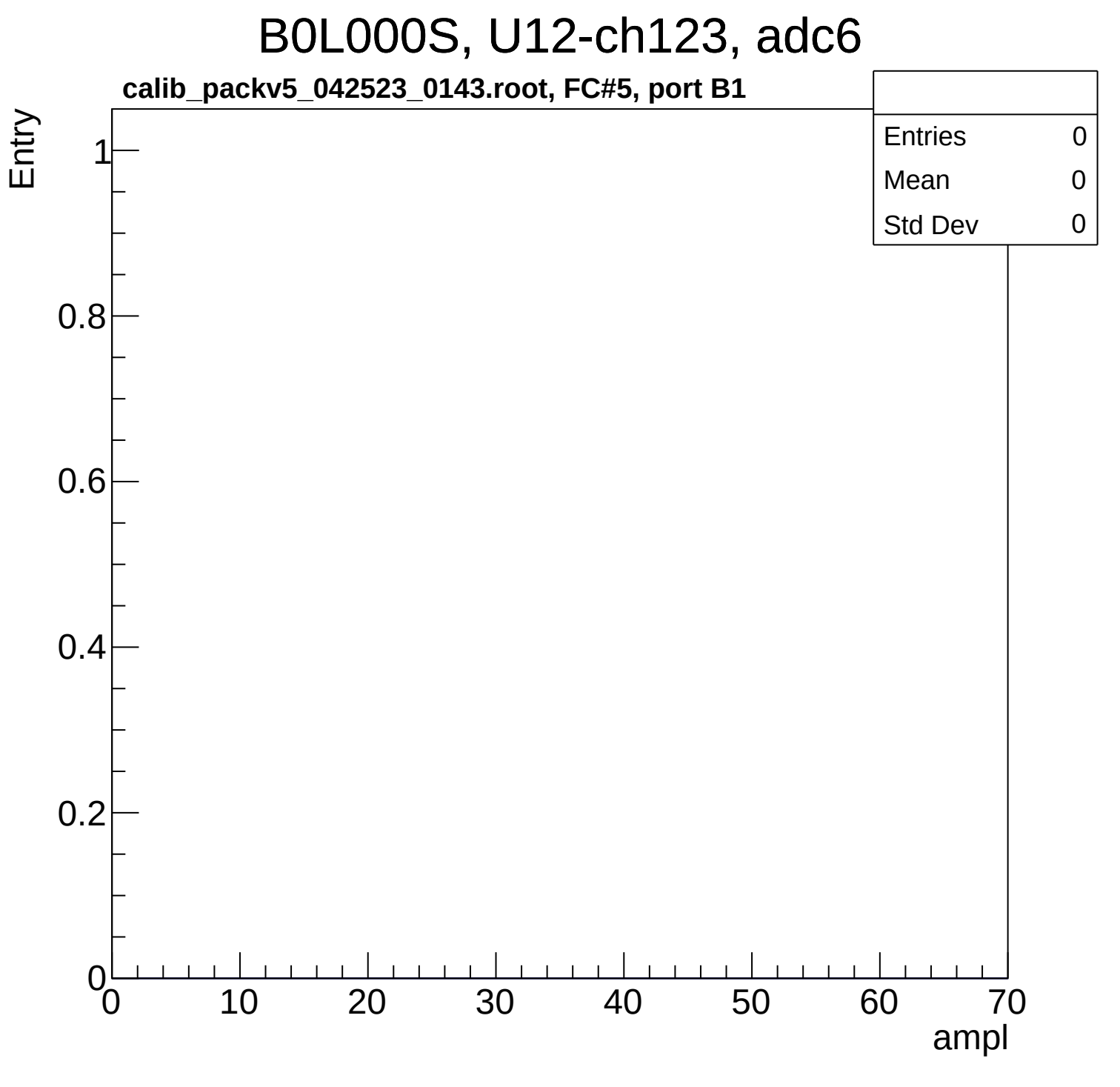
40
50
60
70
ampl


B0L000S, U12-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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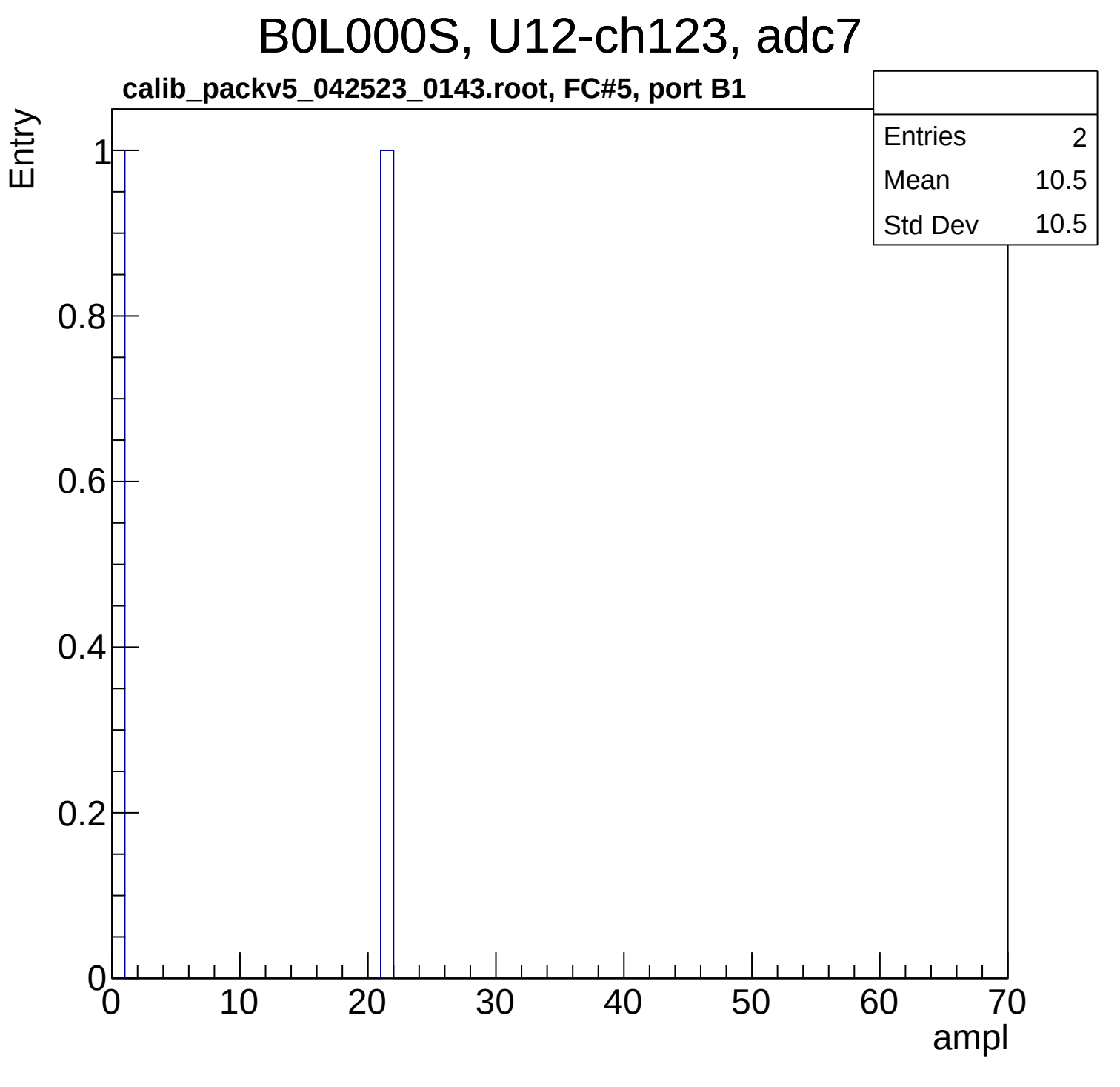
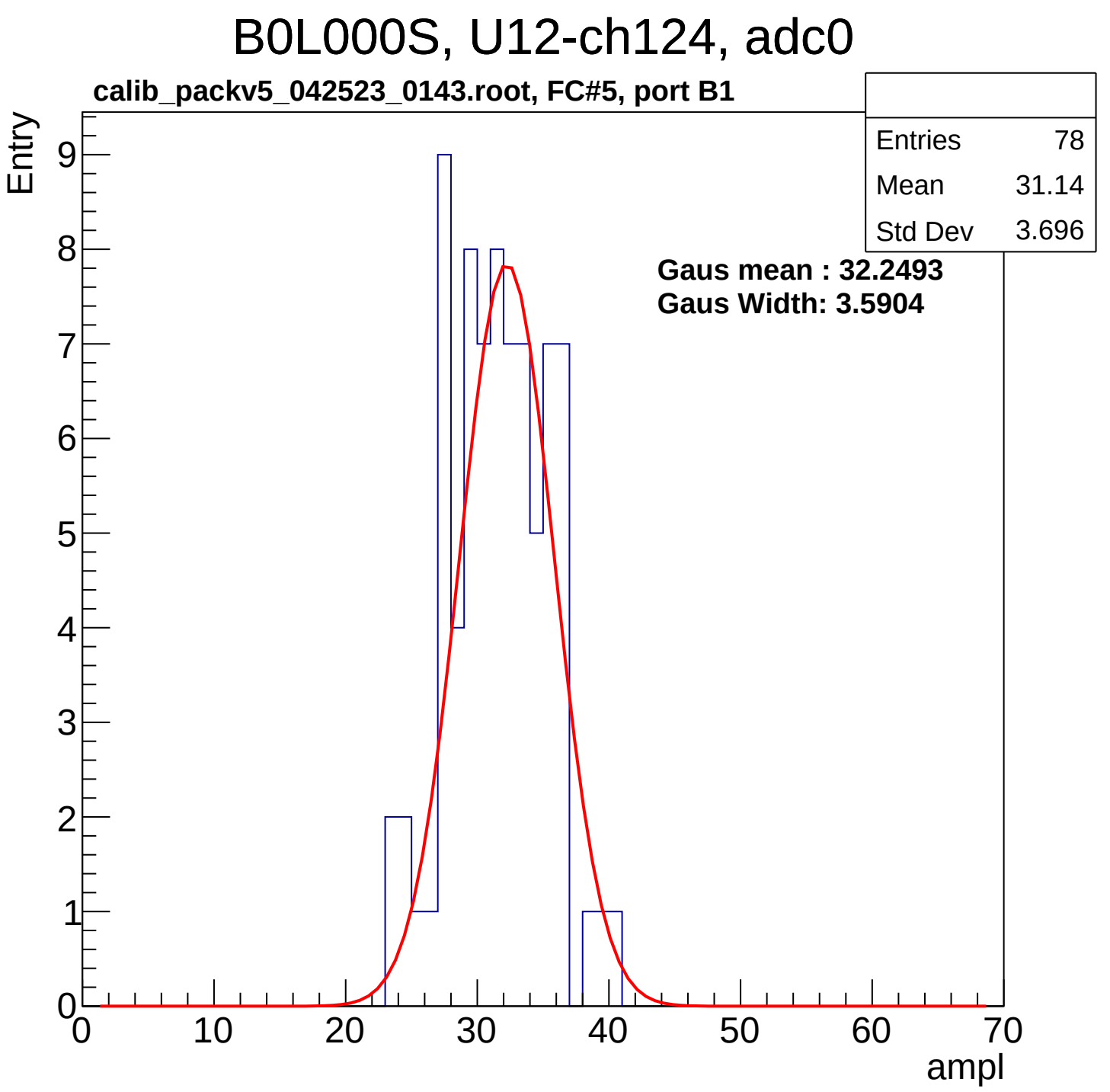
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 31.14
Std Dev 3.696
Gaus mean : 32.2493
Gaus Width: 3.5904
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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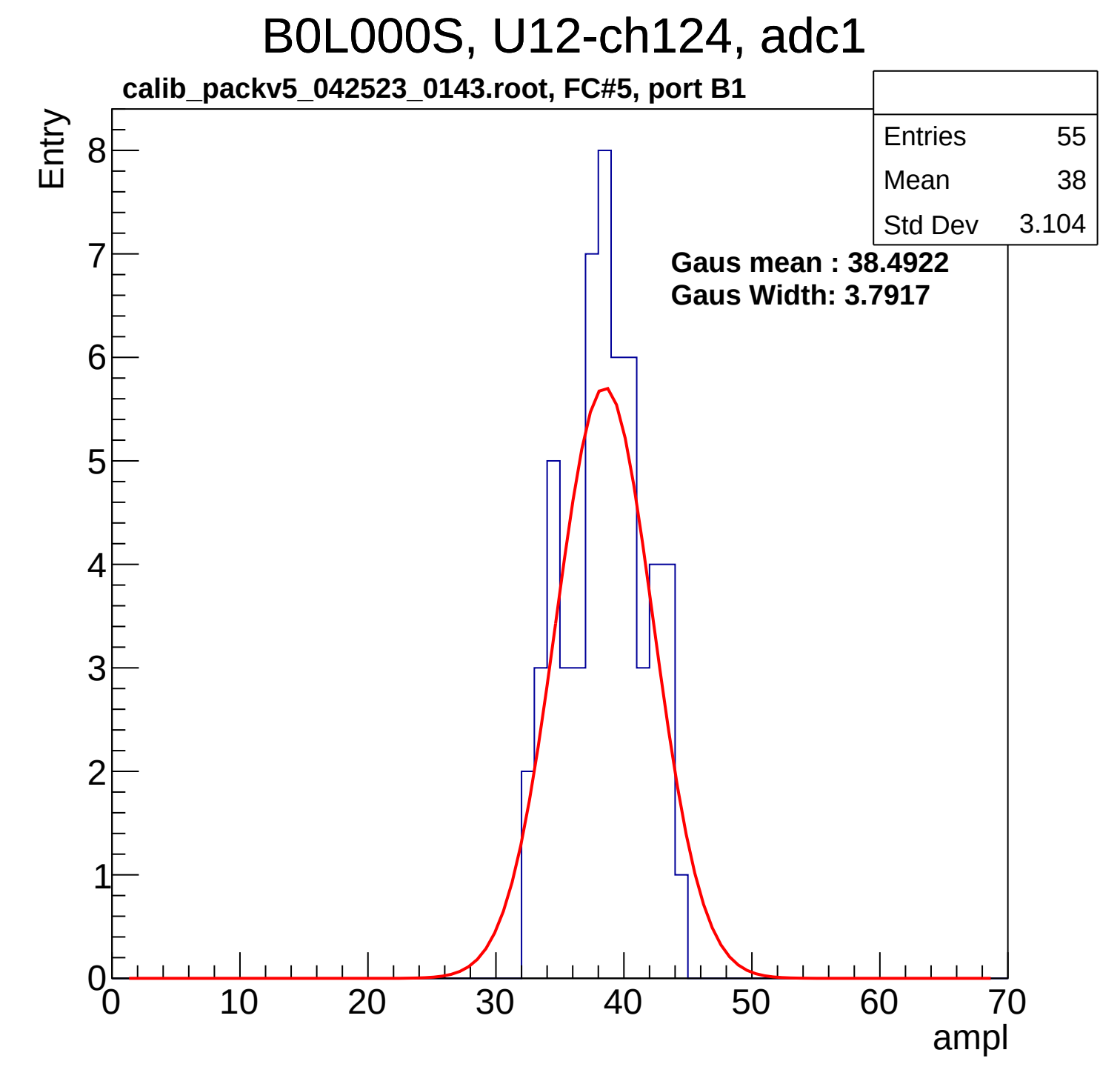
B0L000S, U12-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 38
Std Dev 3.104
Gaus mean : 38.4922
Gaus Width: 3.7917
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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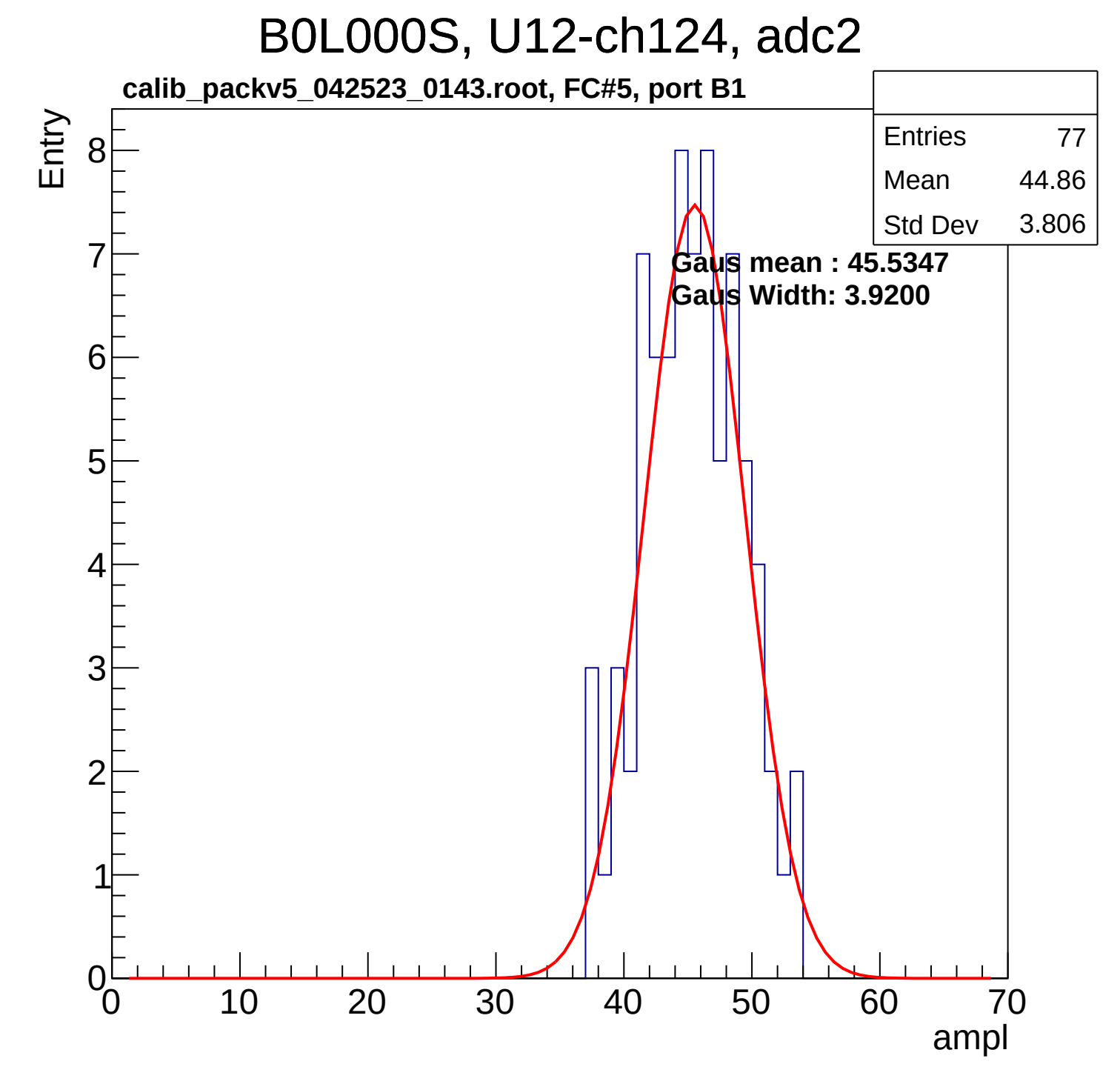
B0L000S, U12-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 44.86
Std Dev 3.806
Gaus mean : 45.5347
Gaus Width: 3.9200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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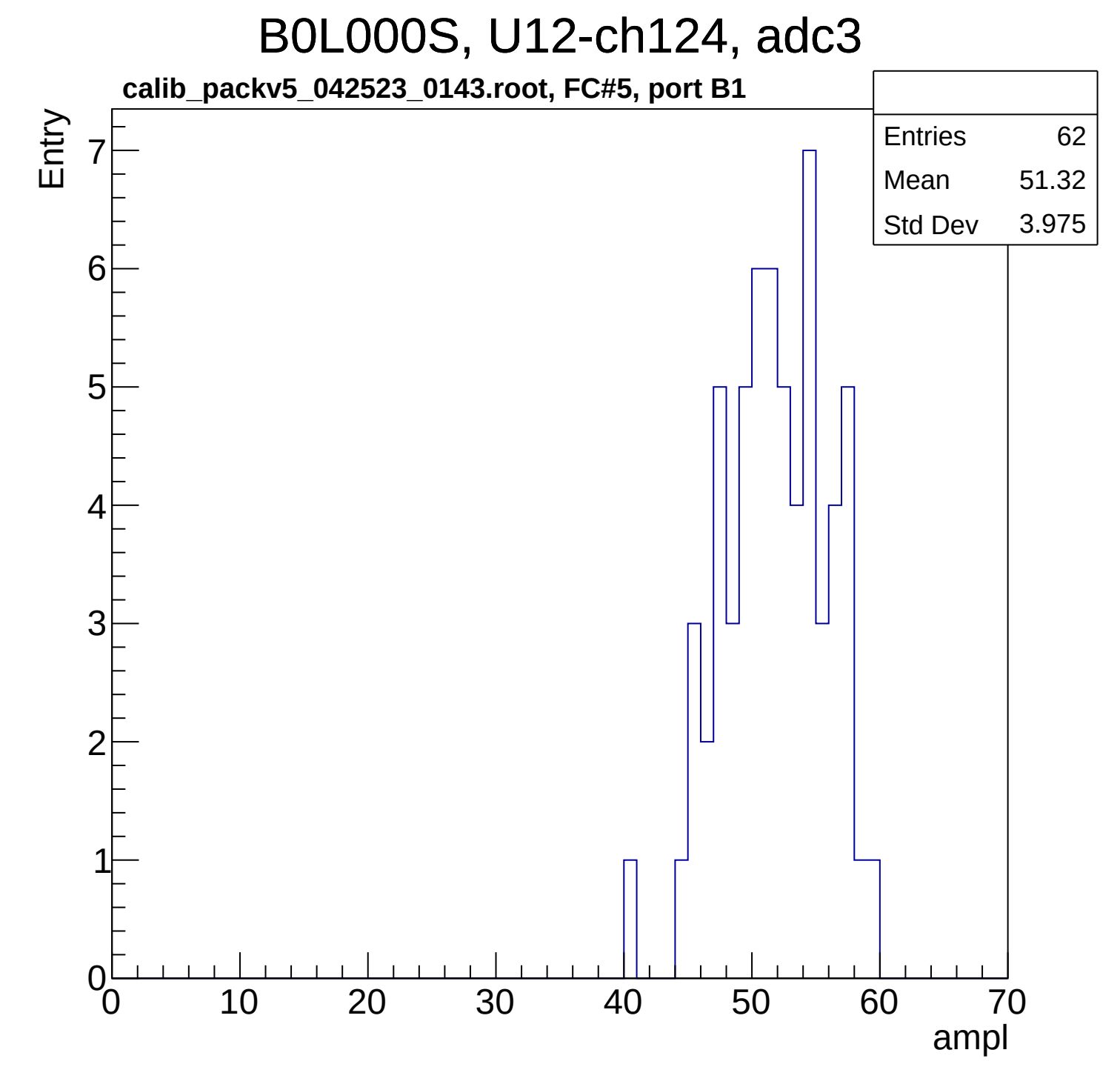
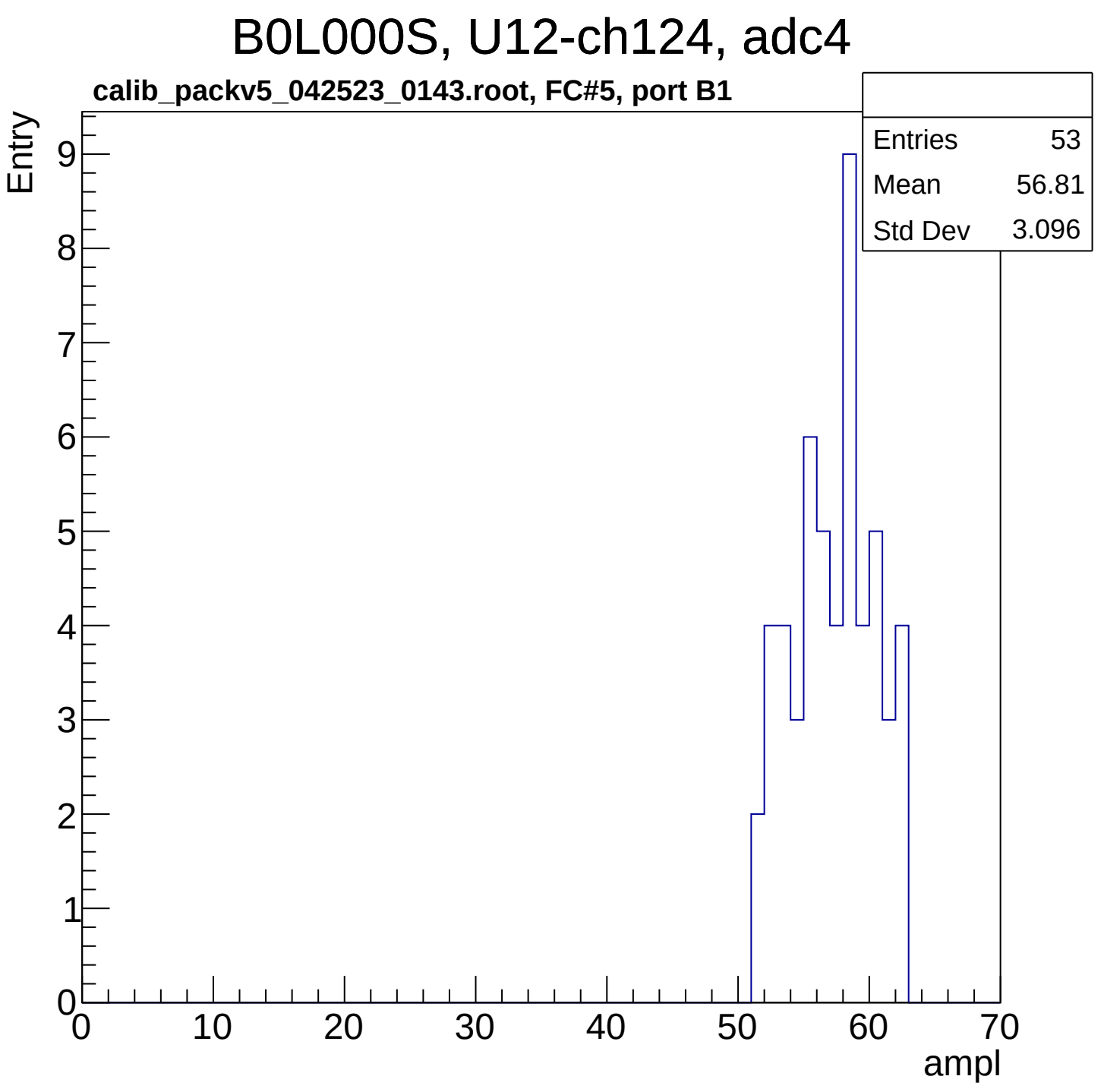
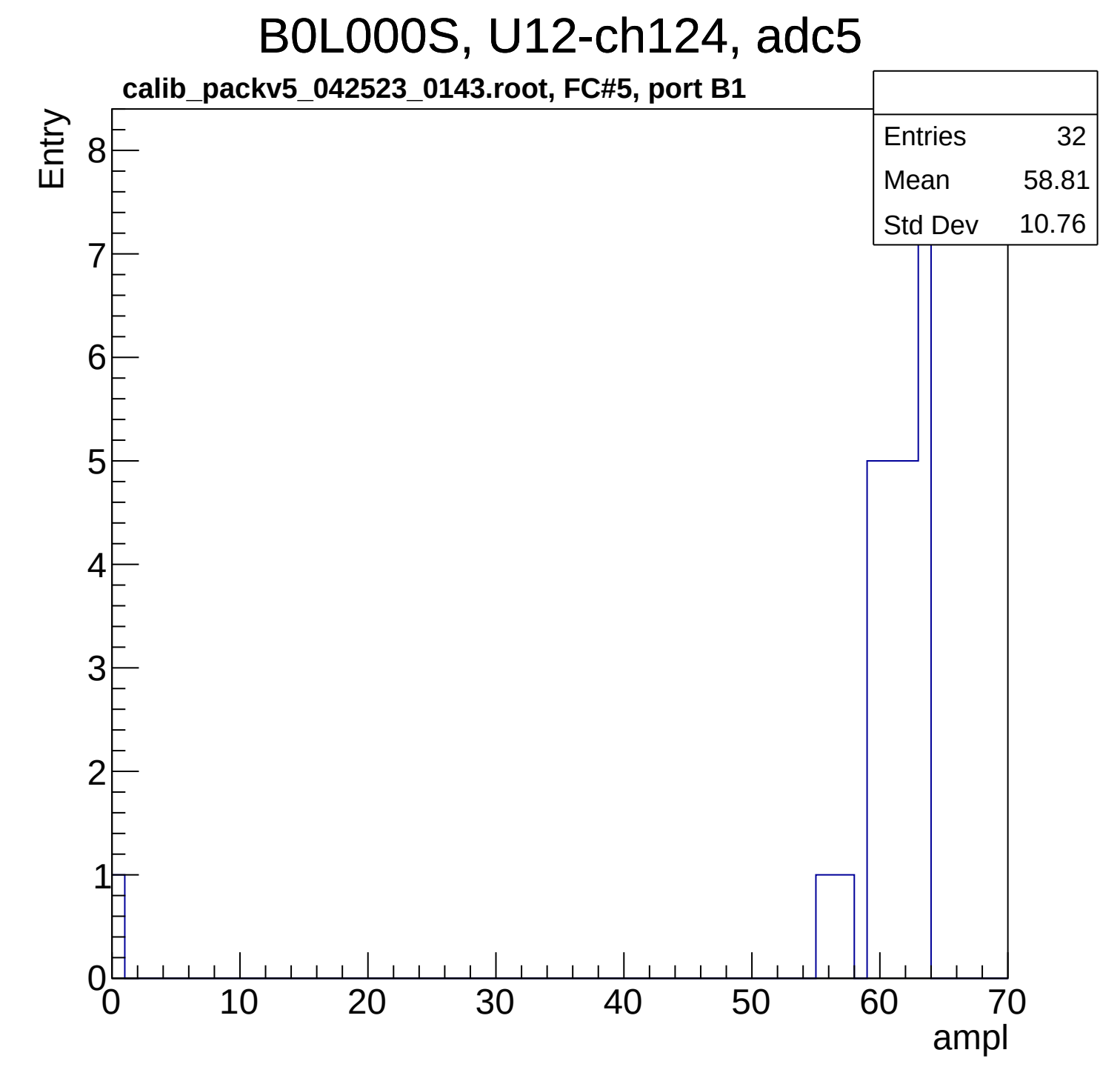

B0L000S, U12-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.32
Std Dev 3.975
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U12-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.81
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.81
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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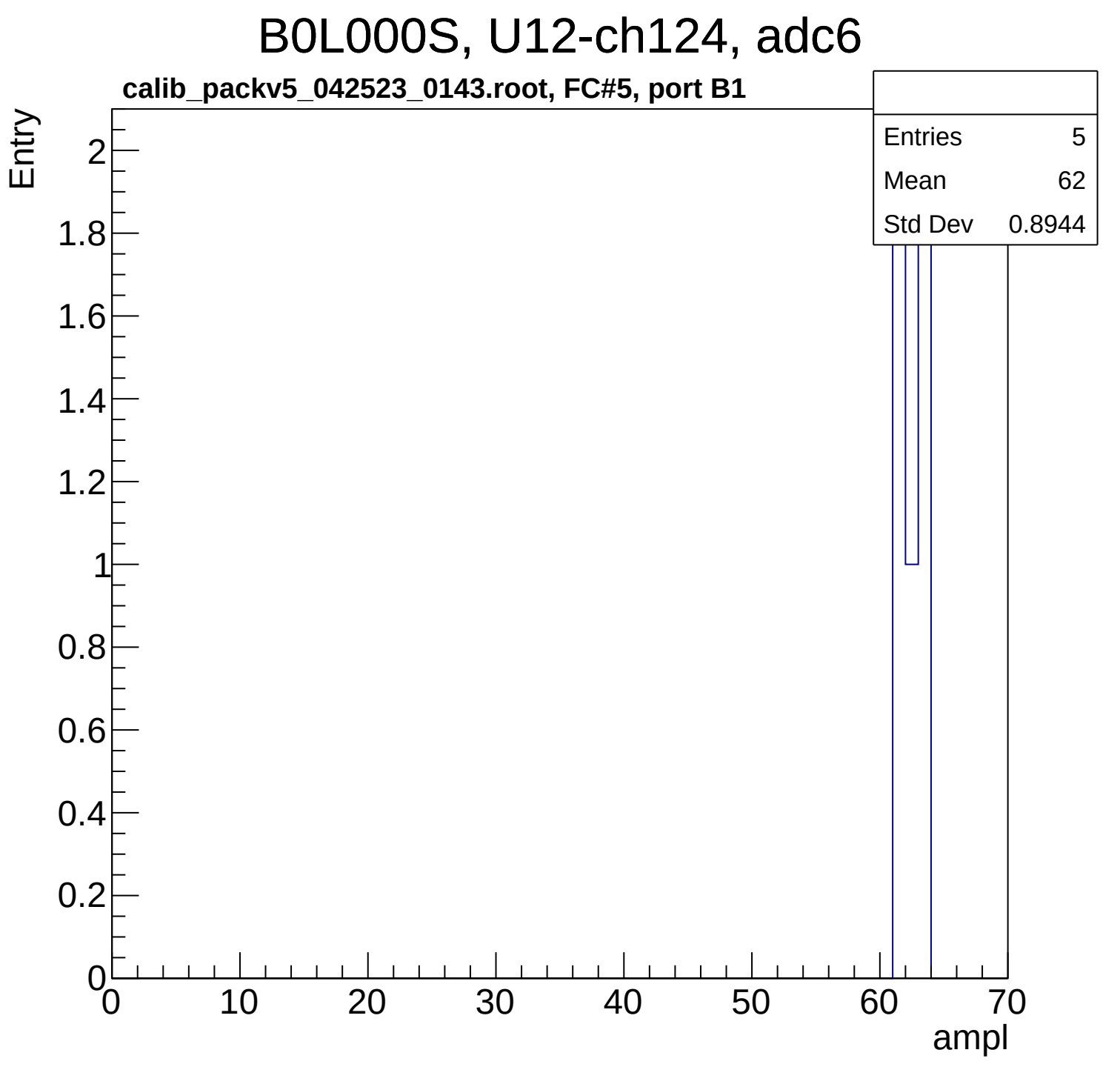
B0L000S, U12-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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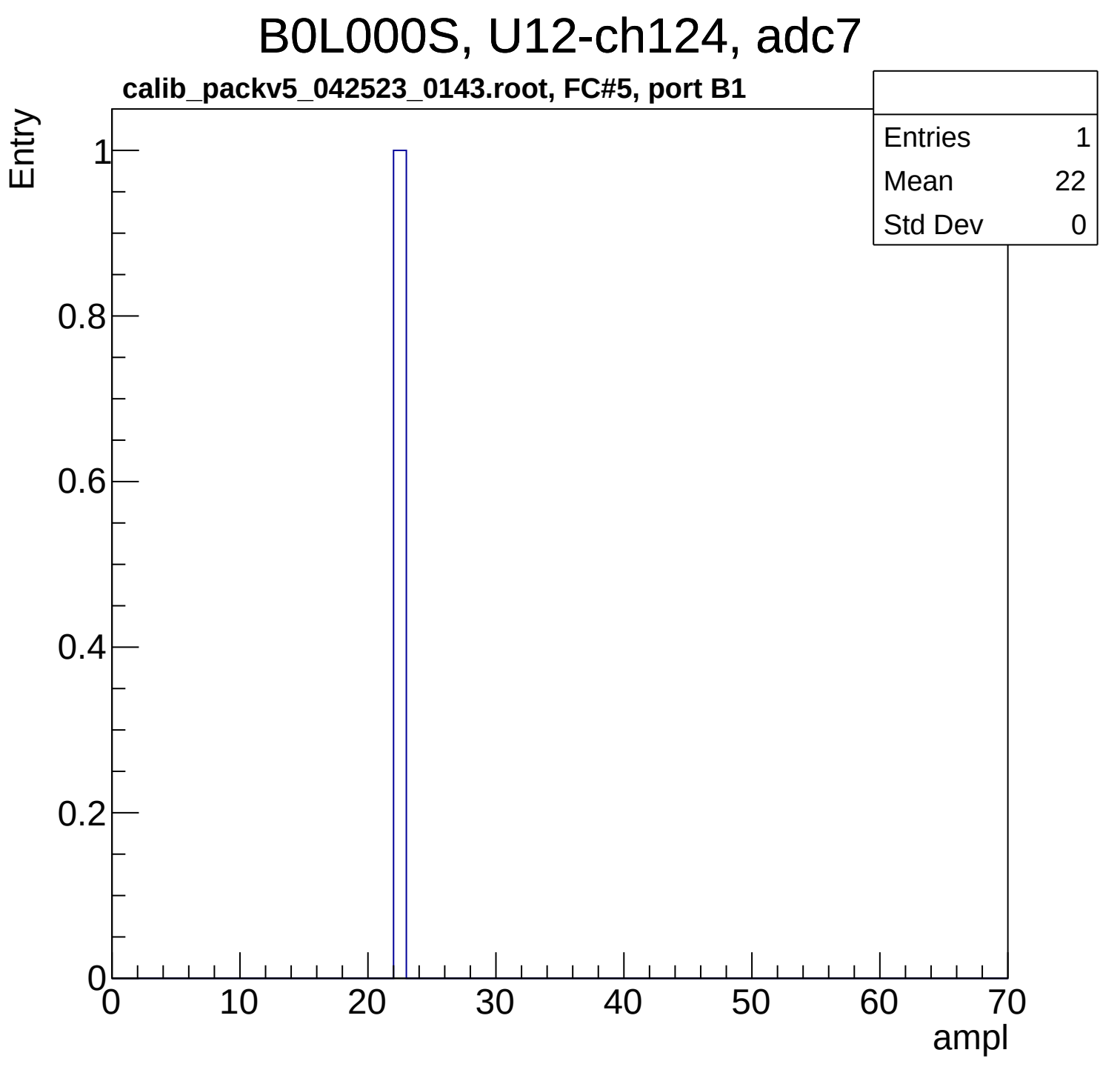
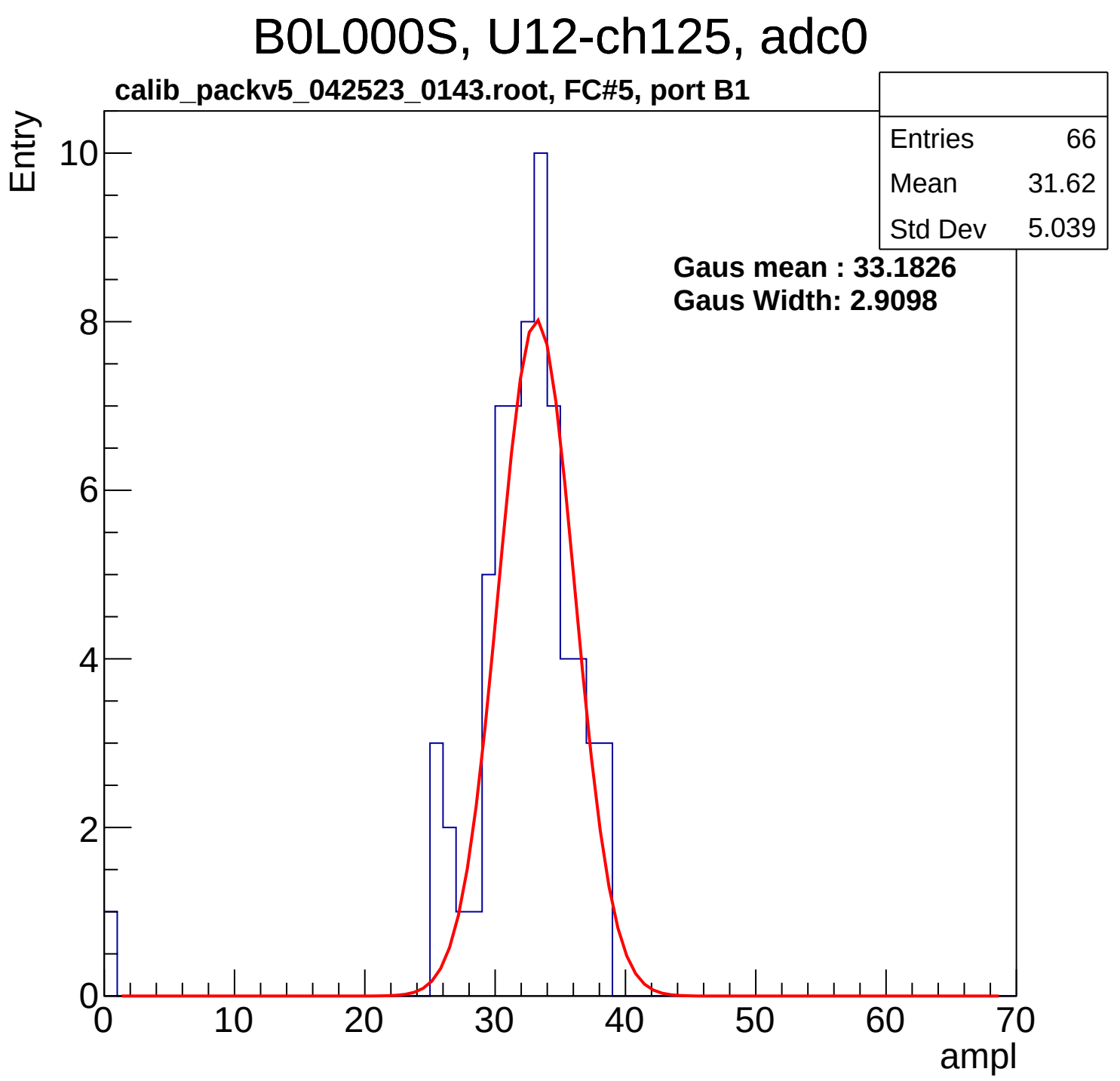
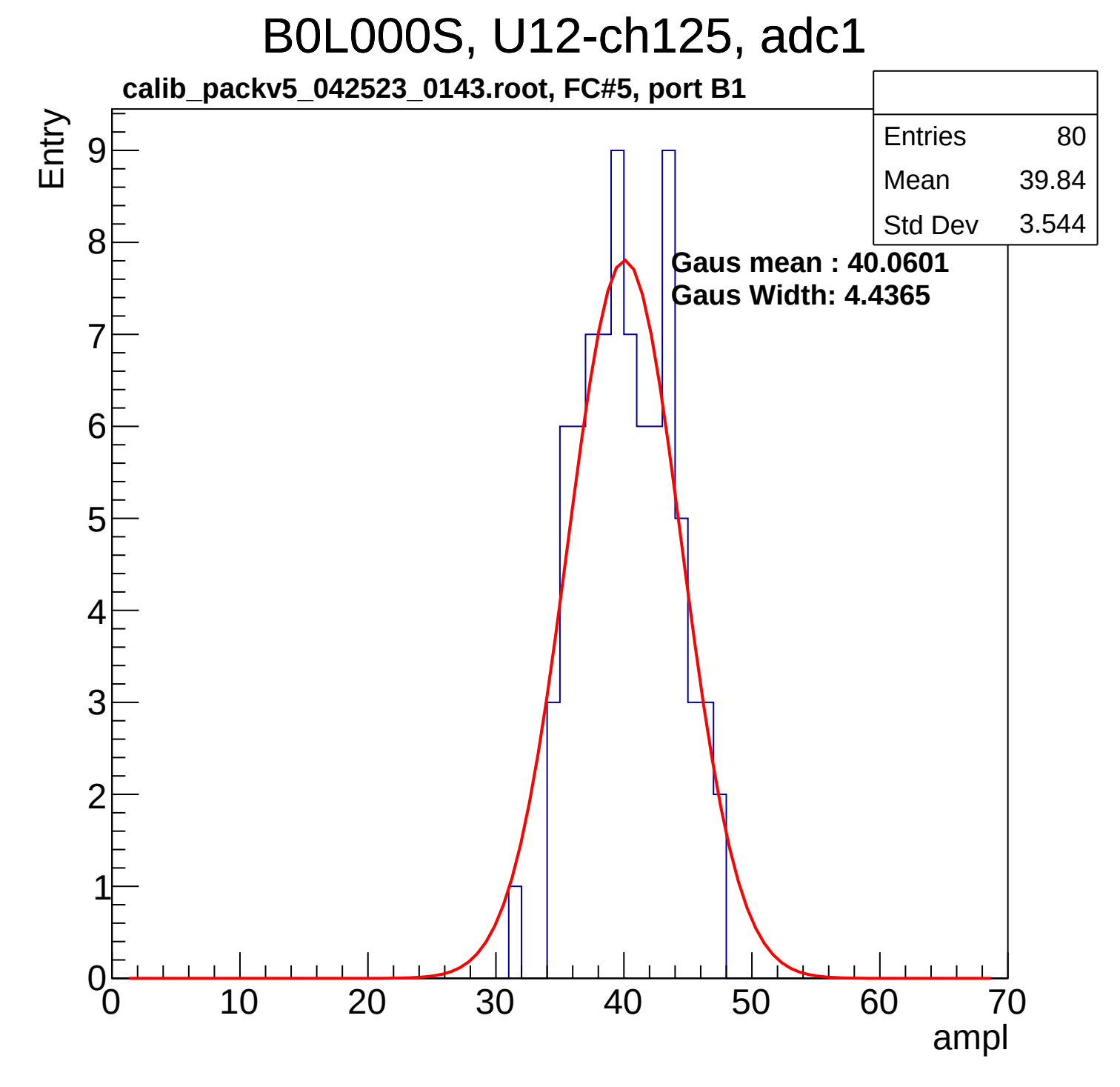
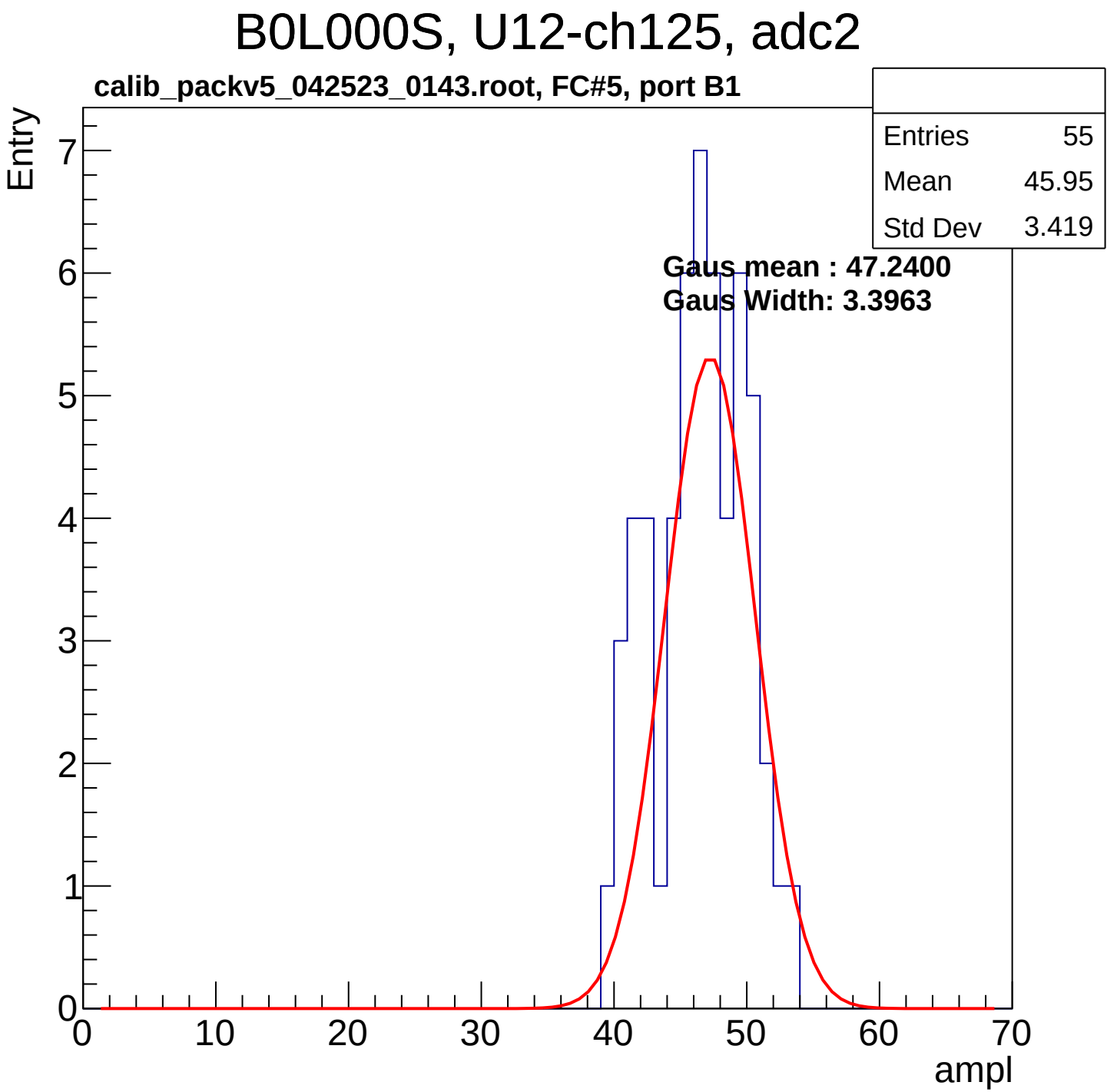

B0L000S, U12-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 31.62
Std Dev 5.039
Gaus mean : 33.1826
Gaus Width: 2.9098
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 39.84
Std Dev 3.544
Gaus mean : 40.0601
Gaus Width: 4.4365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 45.95
Std Dev 3.419
Gaus mean : 47.2400
Gaus Width: 3.3963
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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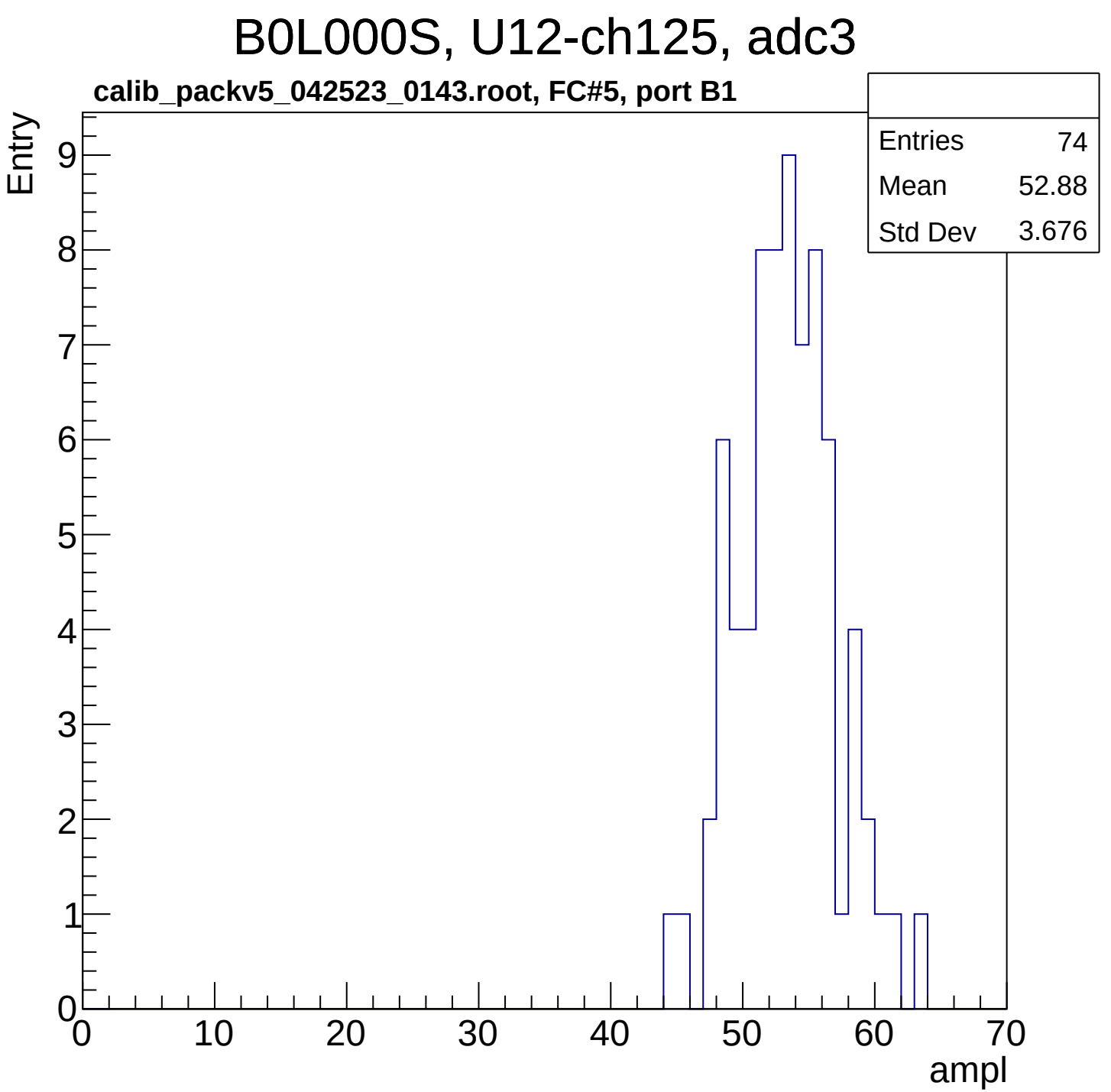
B0L000S, U12-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 52.88
Std Dev 3.676
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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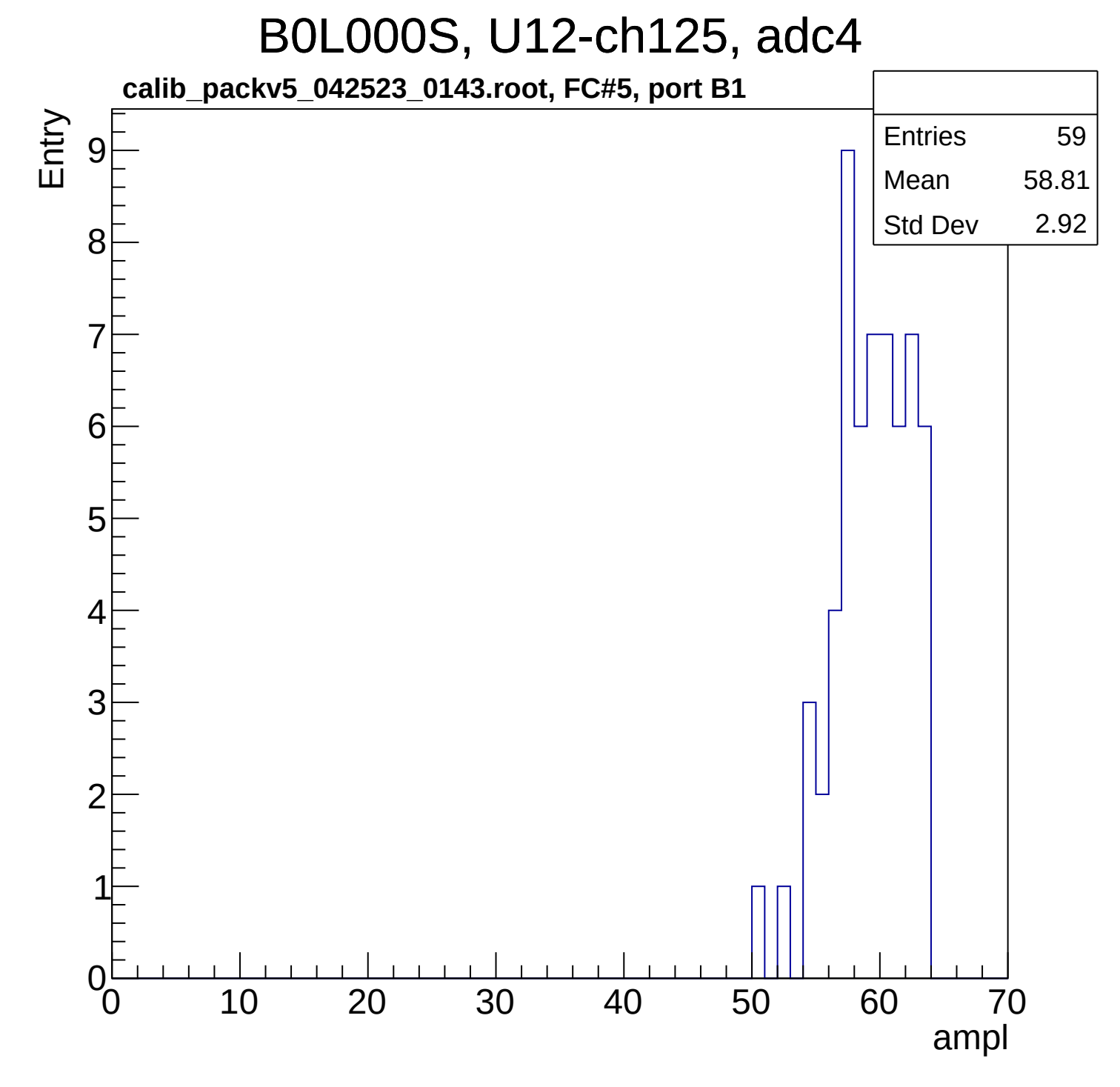
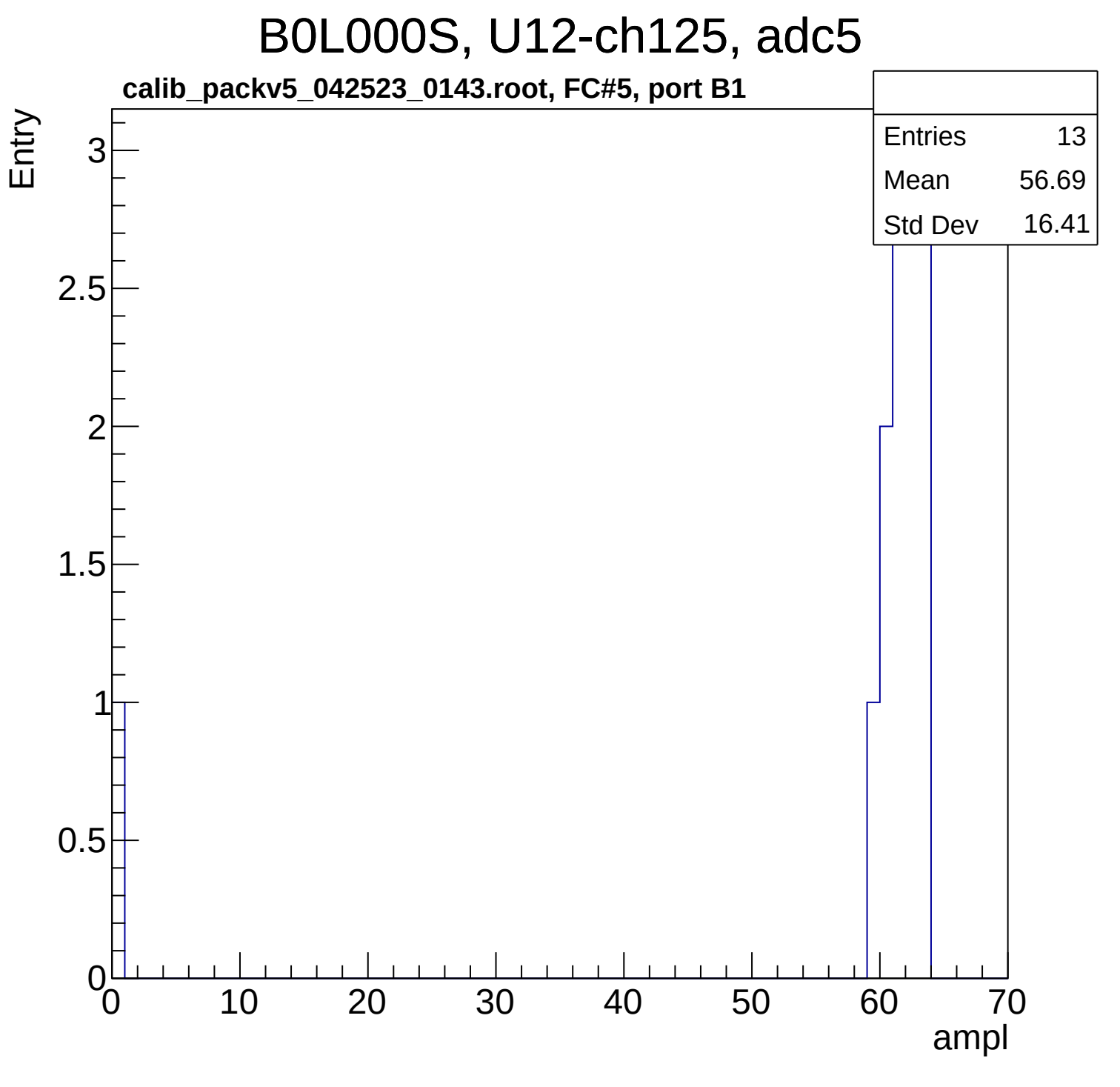
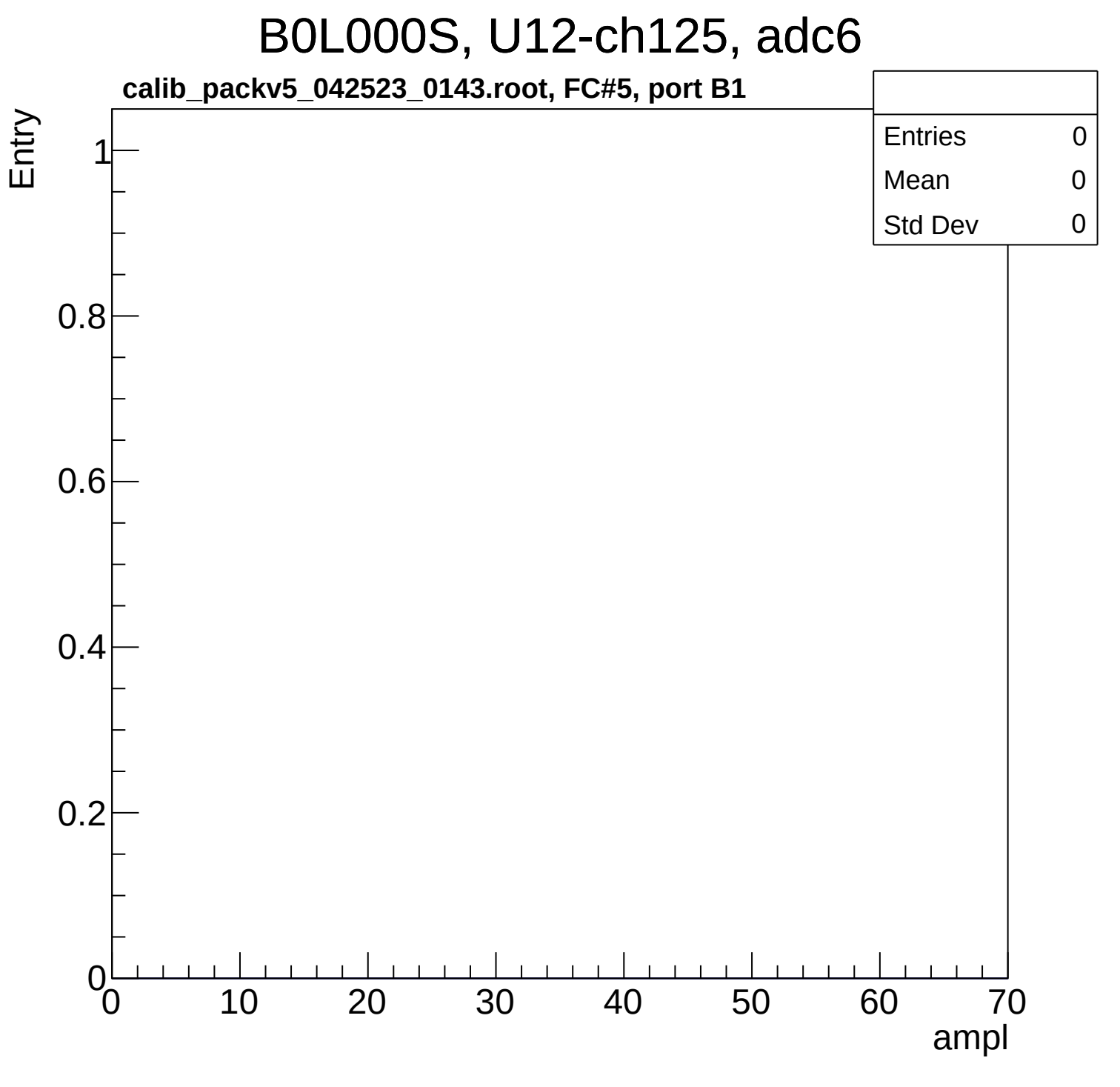
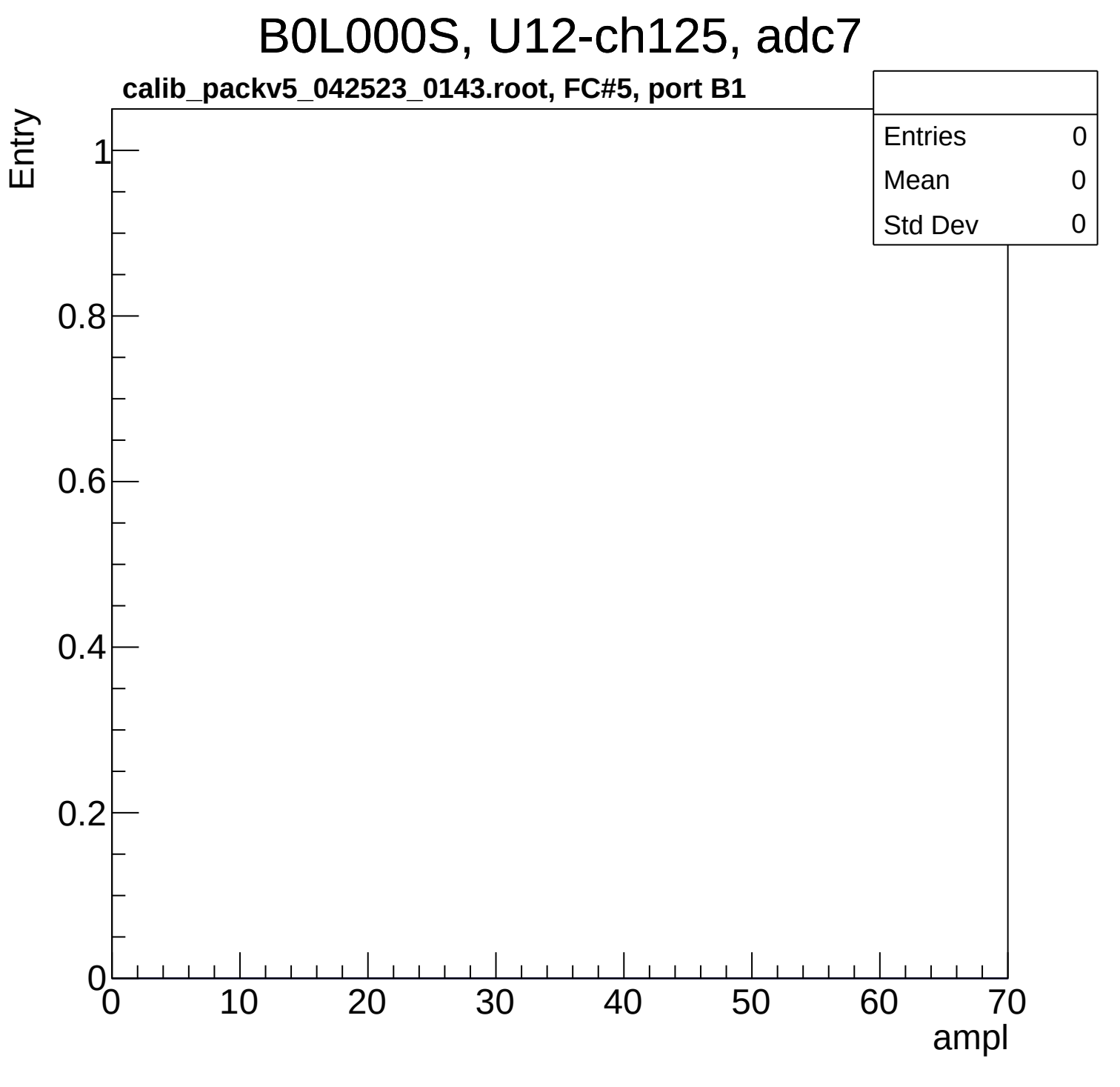

B0L000S, U12-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 58.81
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.69
Std Dev 16.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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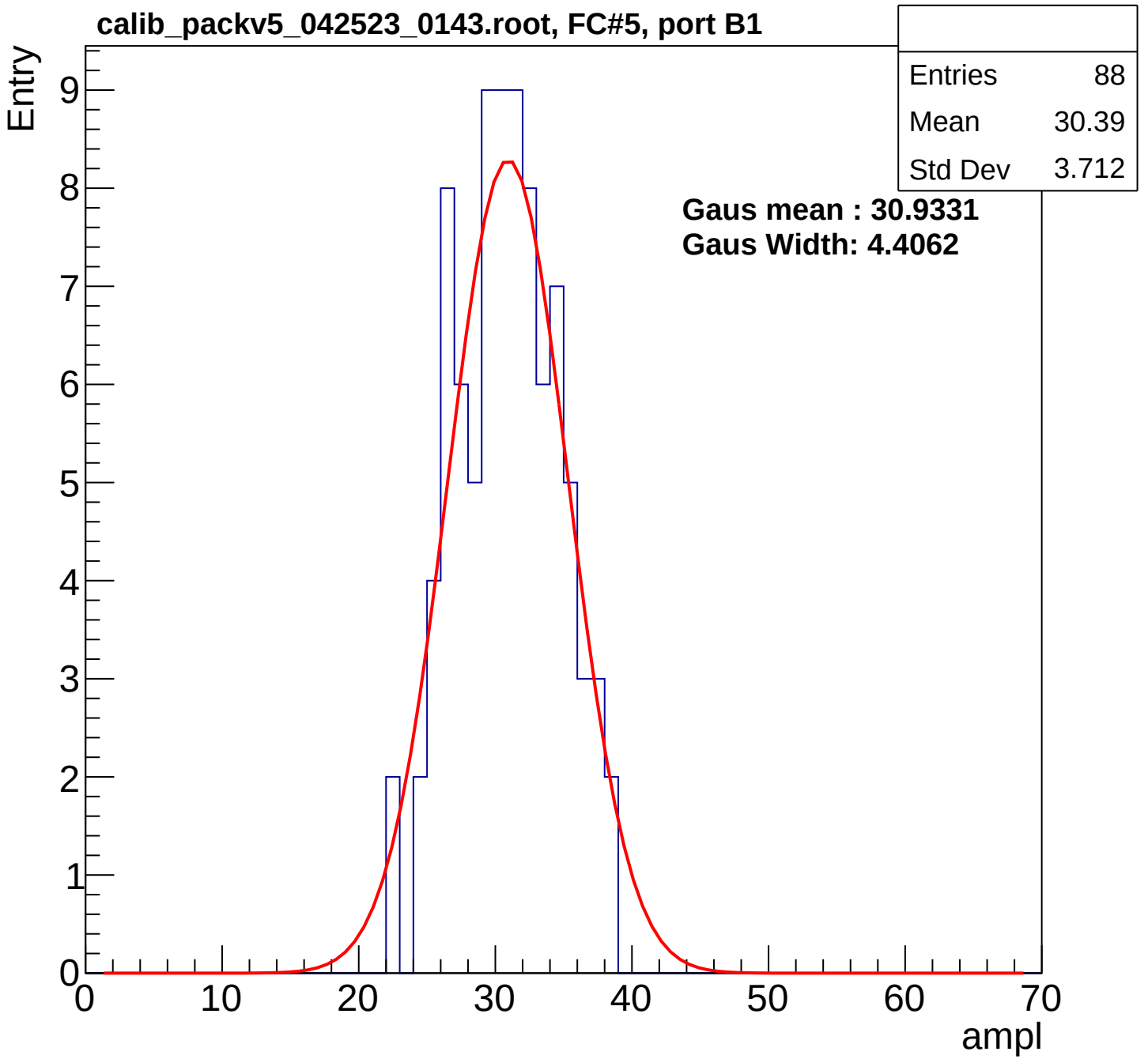
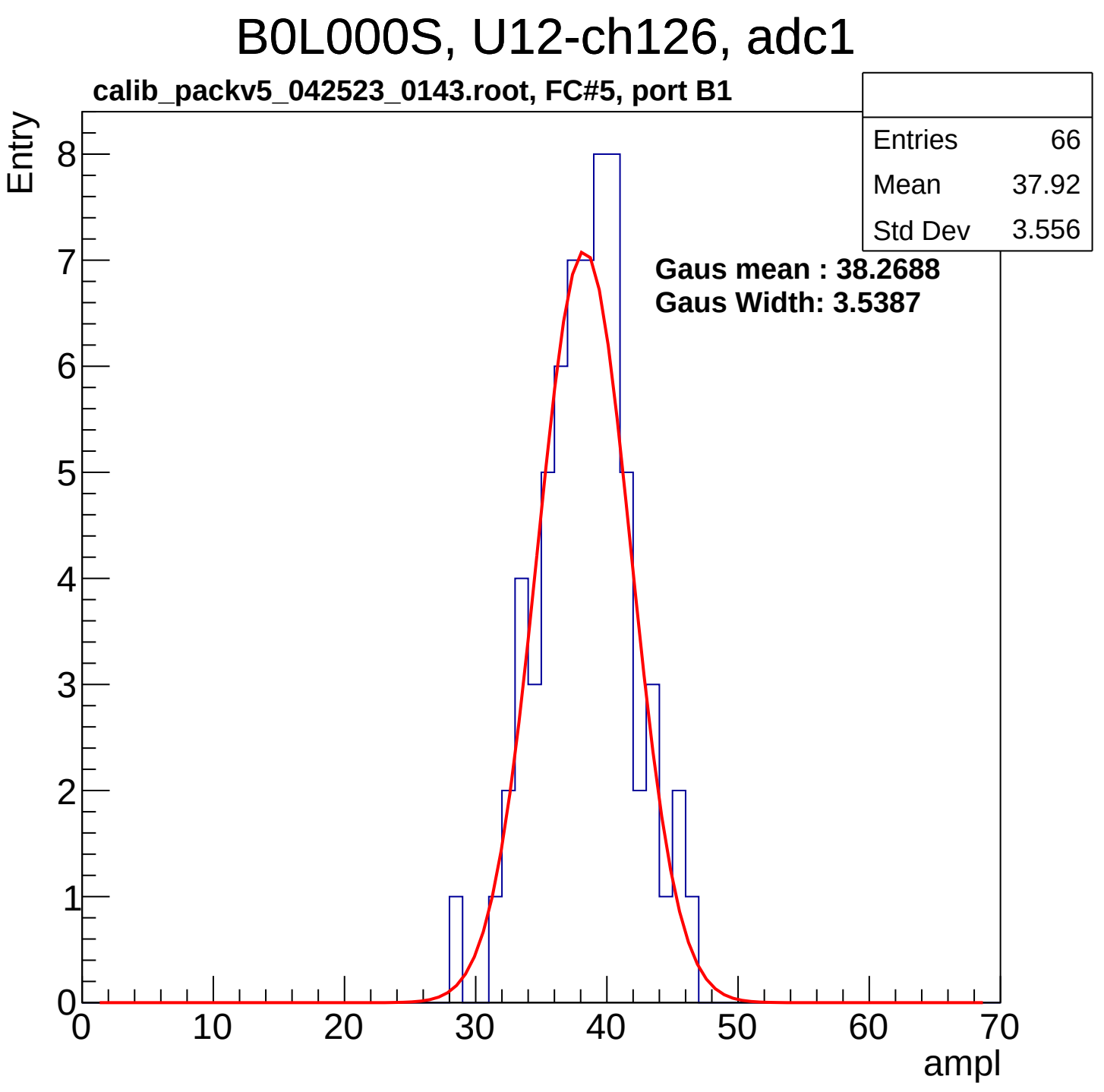
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 30.39
Std Dev 3.712
Gaus mean : 30.9331
Gaus Width: 4.4062
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U12-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.92
Std Dev 3.556
Gaus mean : 38.2688
Gaus Width: 3.5387
Entry
ampl
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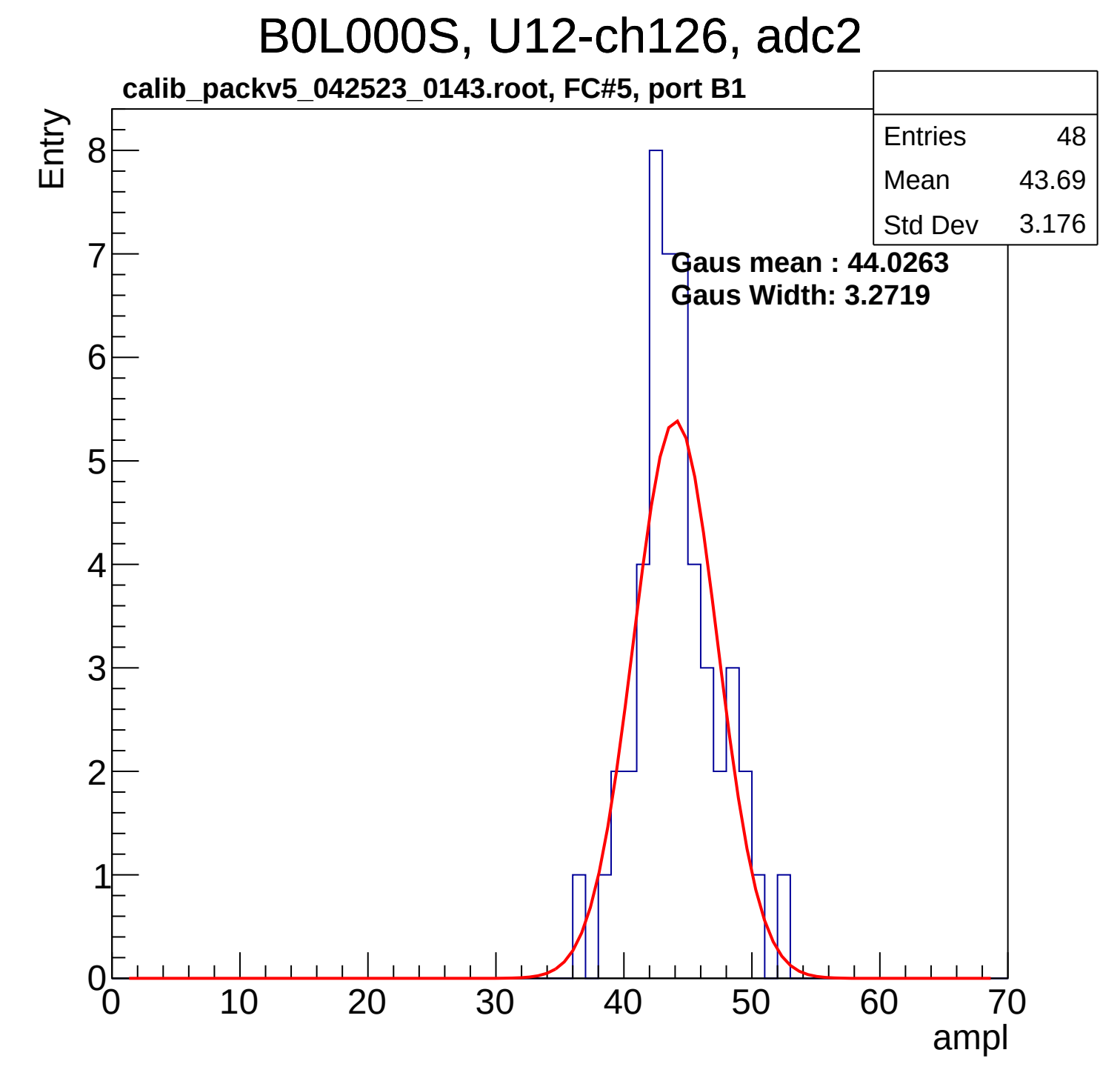
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 43.69
Std Dev 3.176
Gaus mean : 44.0263
Gaus Width: 3.2719
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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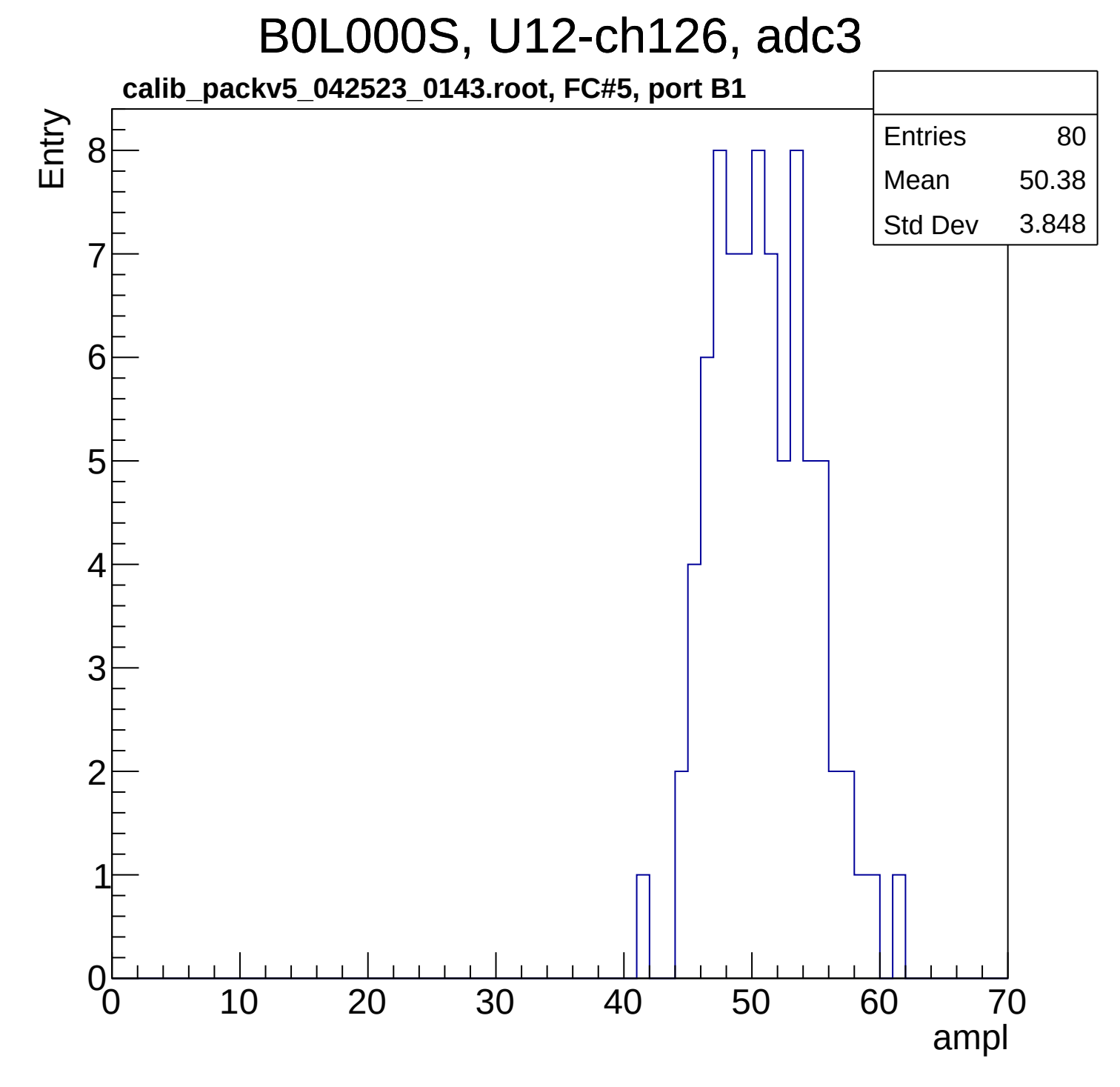
B0L000S, U12-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 50.38
Std Dev 3.848
Entry
ampl
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1
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60
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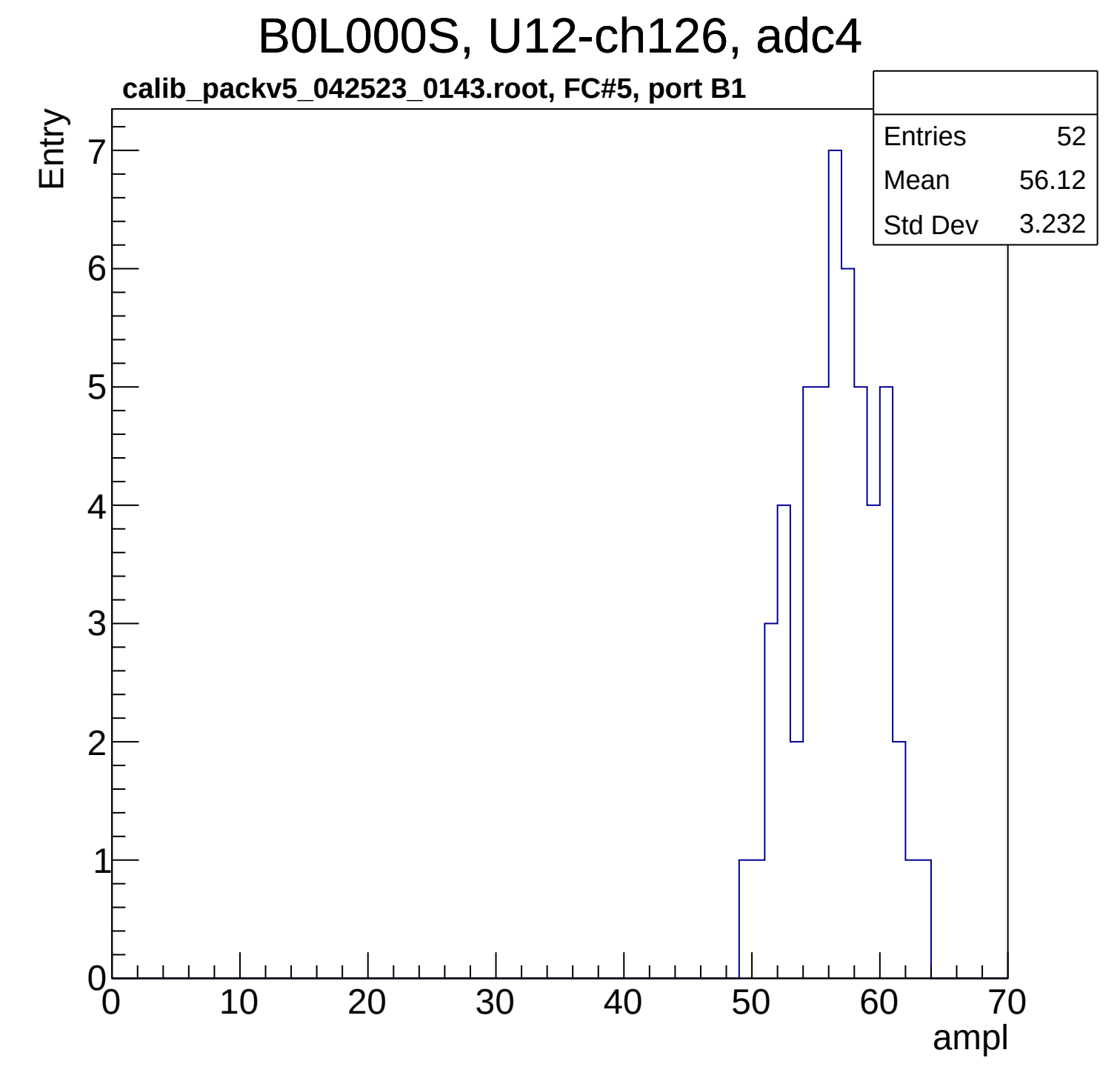
B0L000S, U12-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.12
Std Dev 3.232
Entry
ampl
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1
2
3
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0
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60
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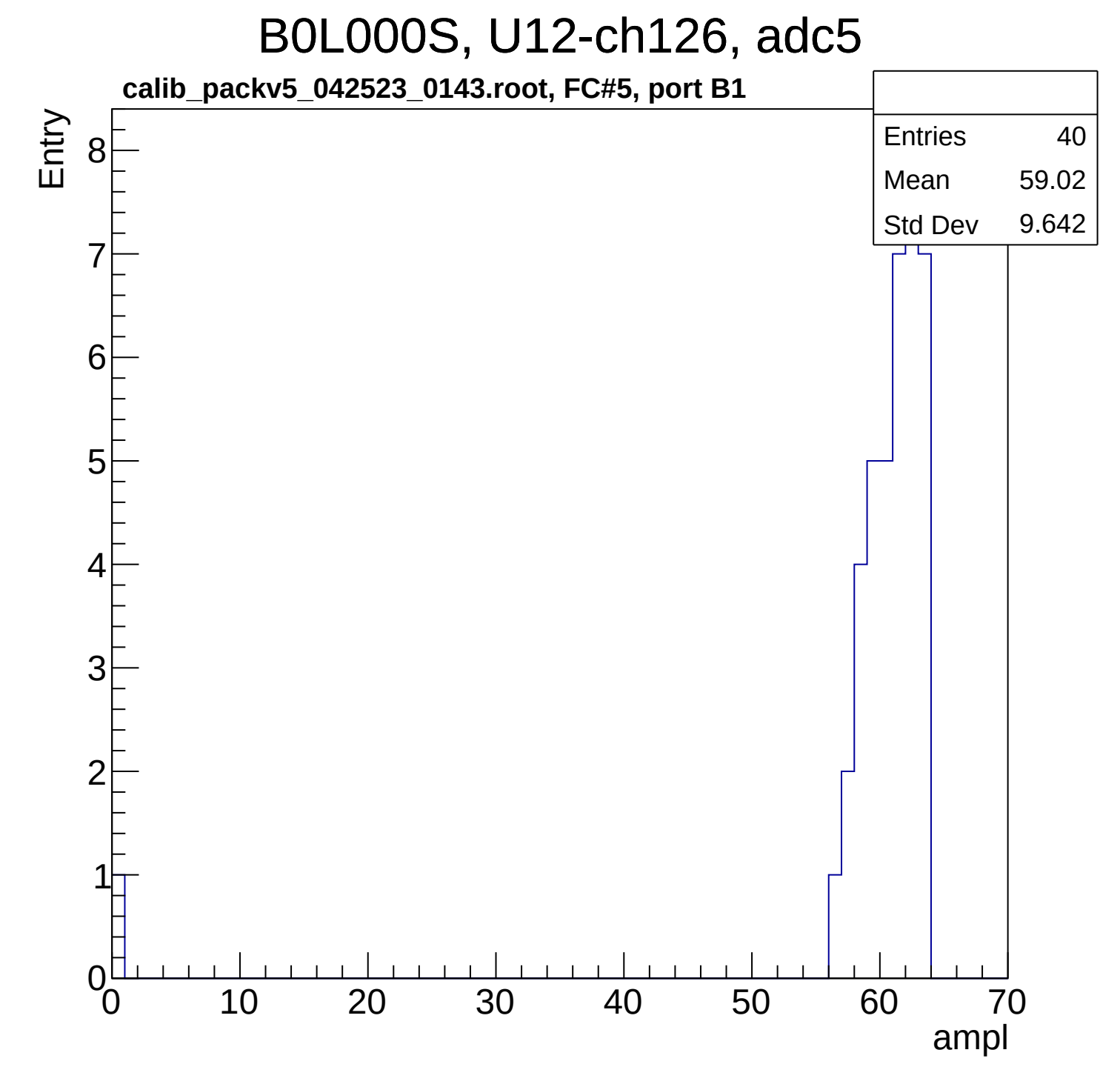
B0L000S, U12-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.02
Std Dev 9.642
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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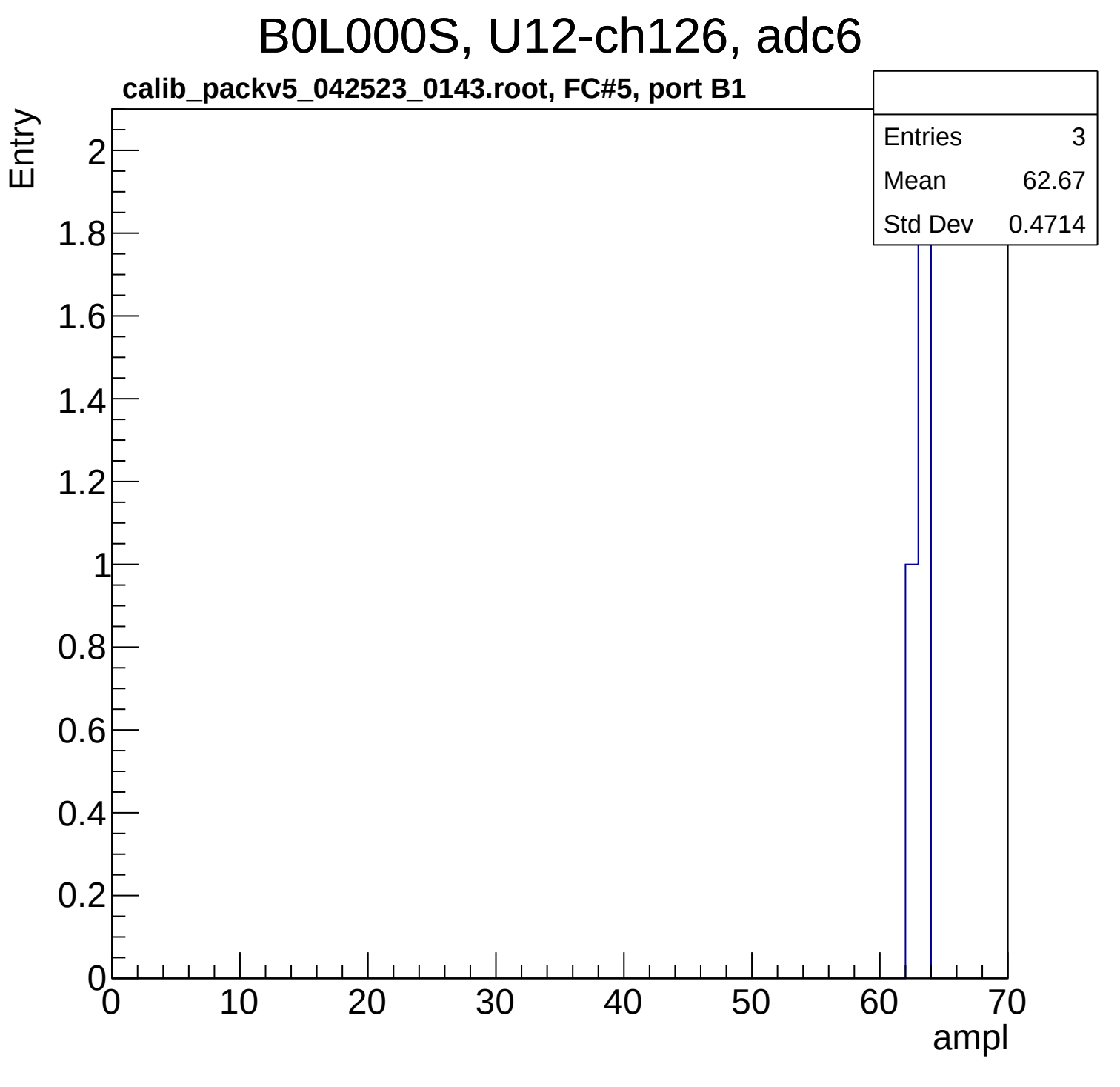
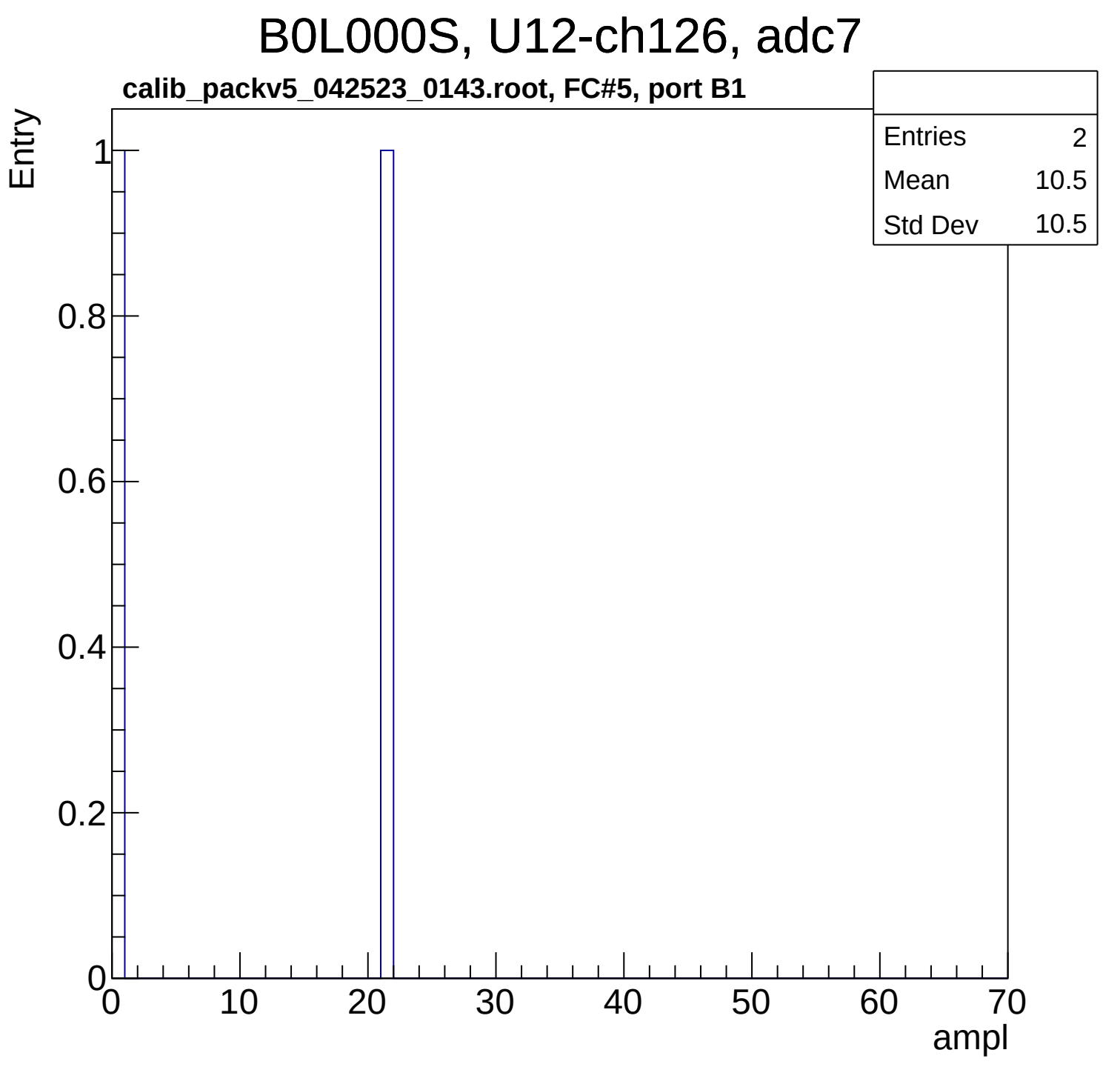

B0L000S, U12-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U12-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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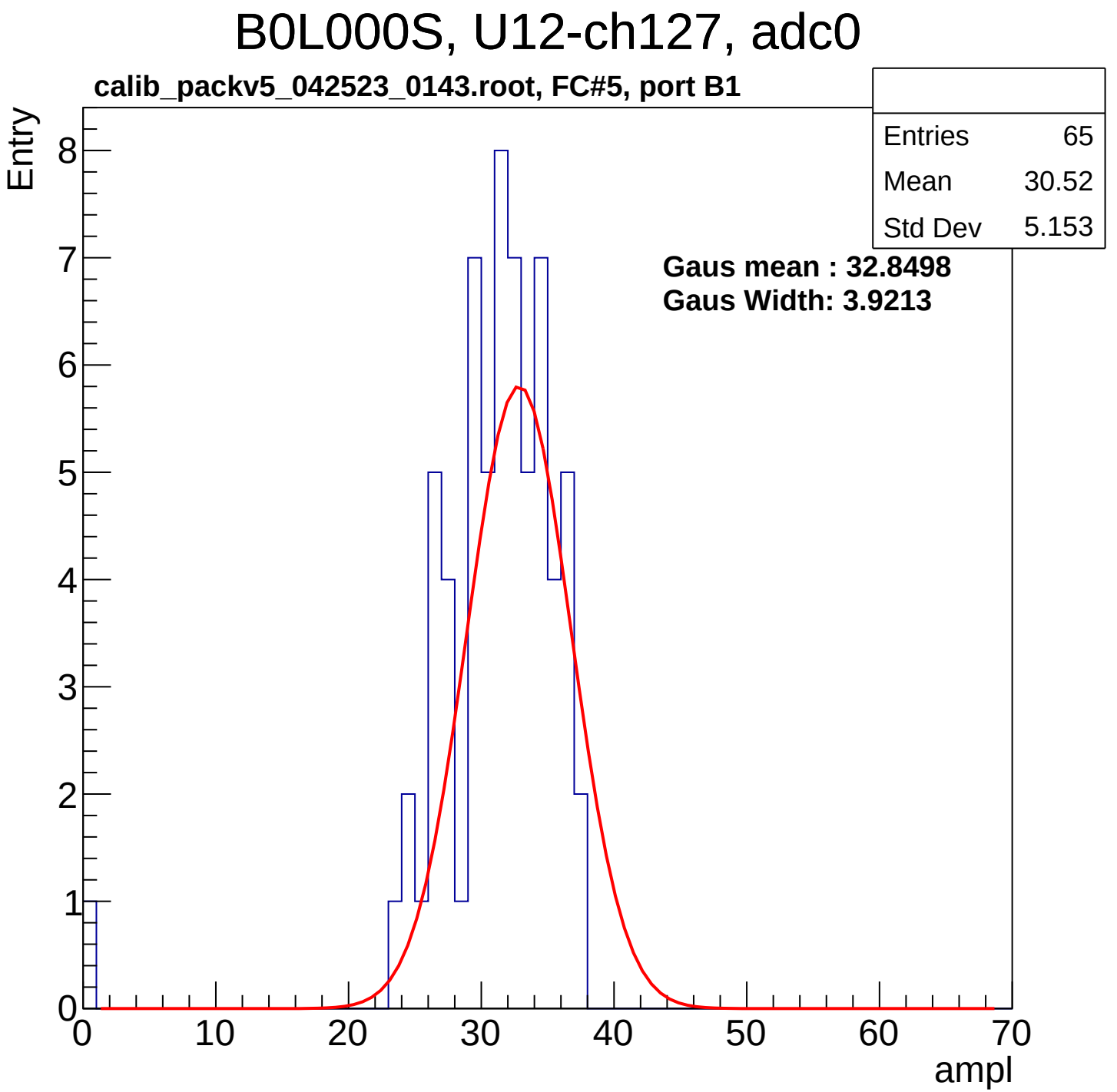
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.52
Std Dev 5.153
Gaus mean : 32.8498
Gaus Width: 3.9213
Entry
ampl
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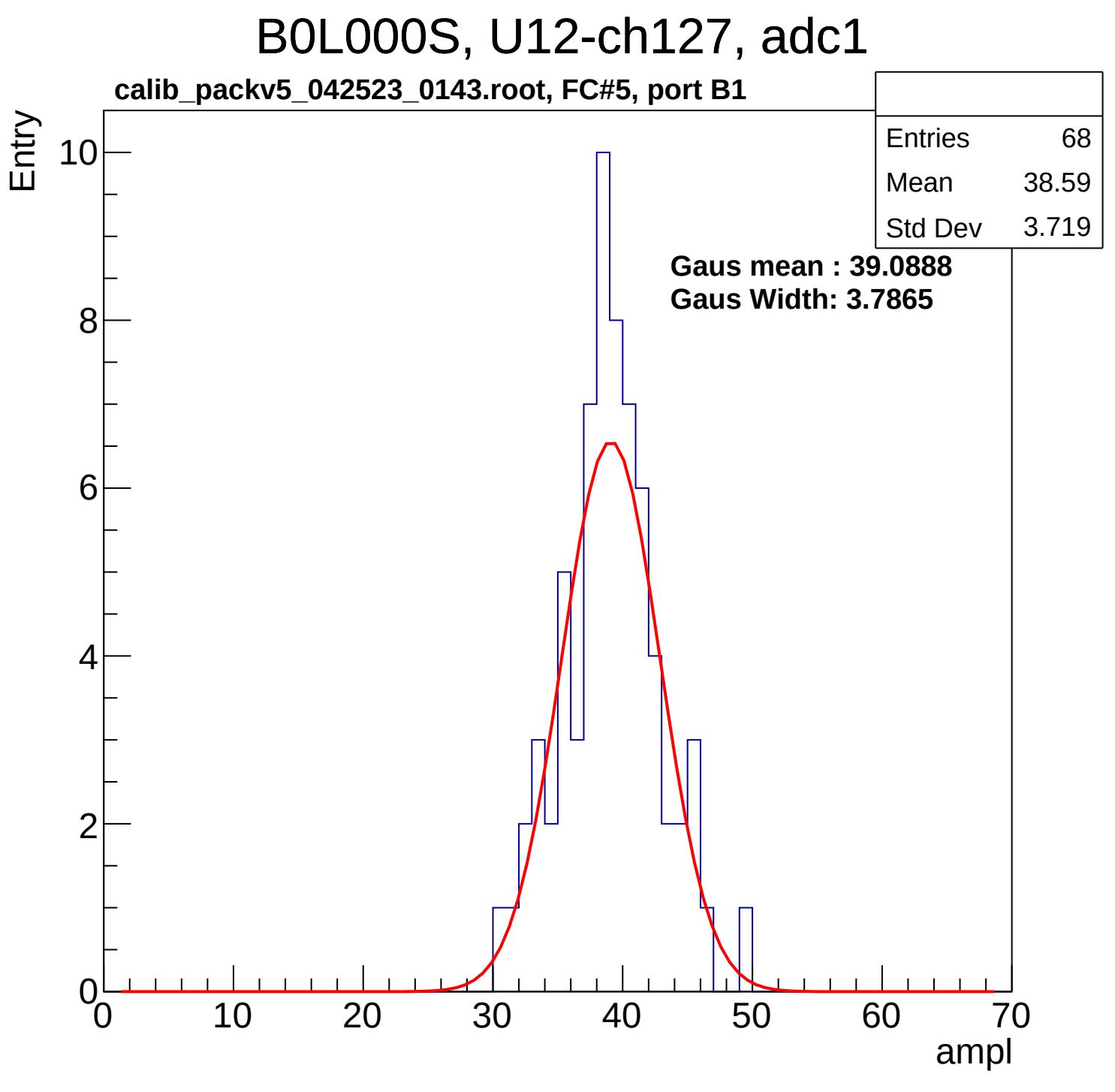
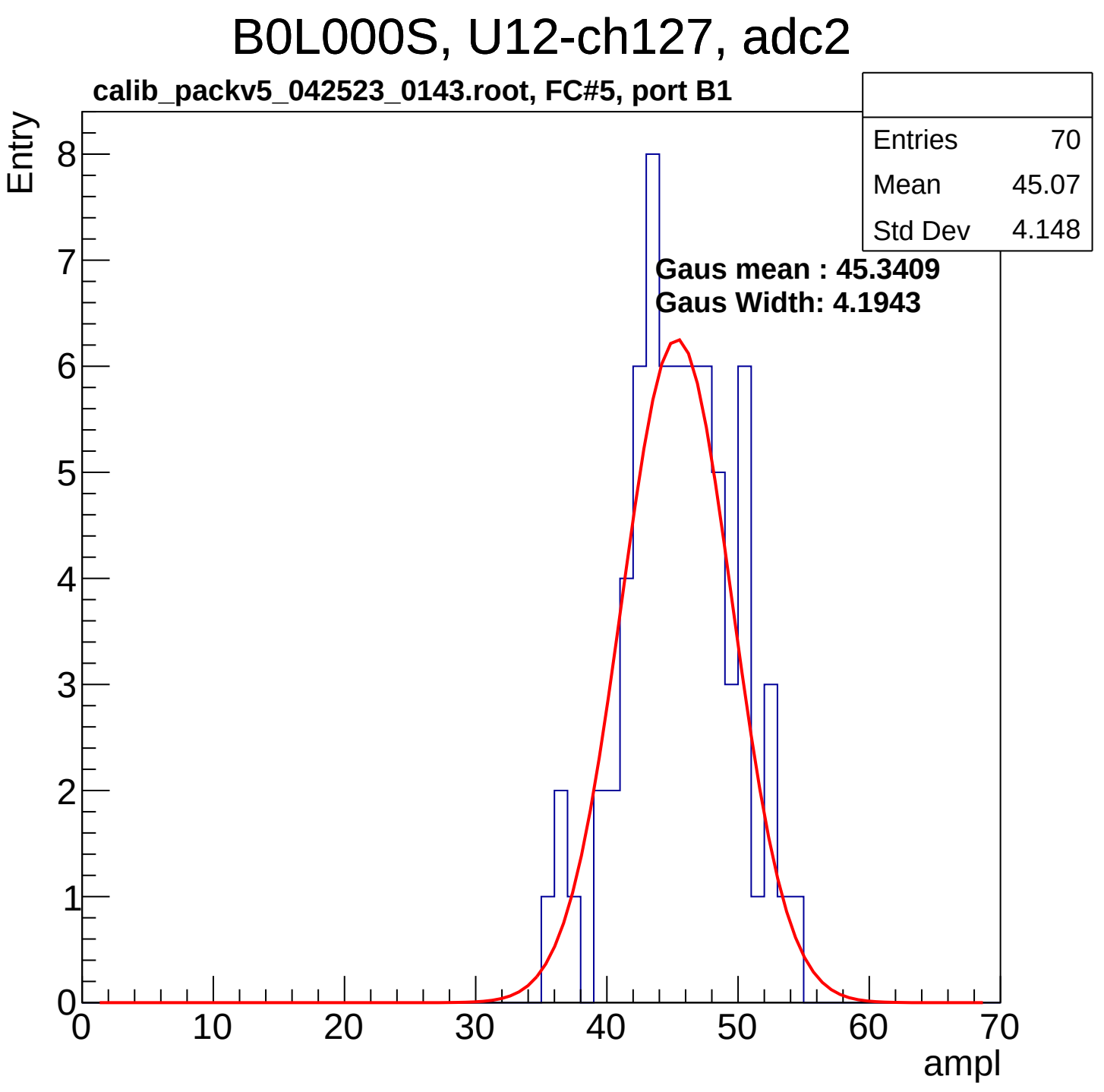
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U12-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 38.59
Std Dev 3.719
Gaus mean : 39.0888
Gaus Width: 3.7865
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U12-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.07
Std Dev 4.148
Gaus mean : 45.3409
Gaus Width: 4.1943
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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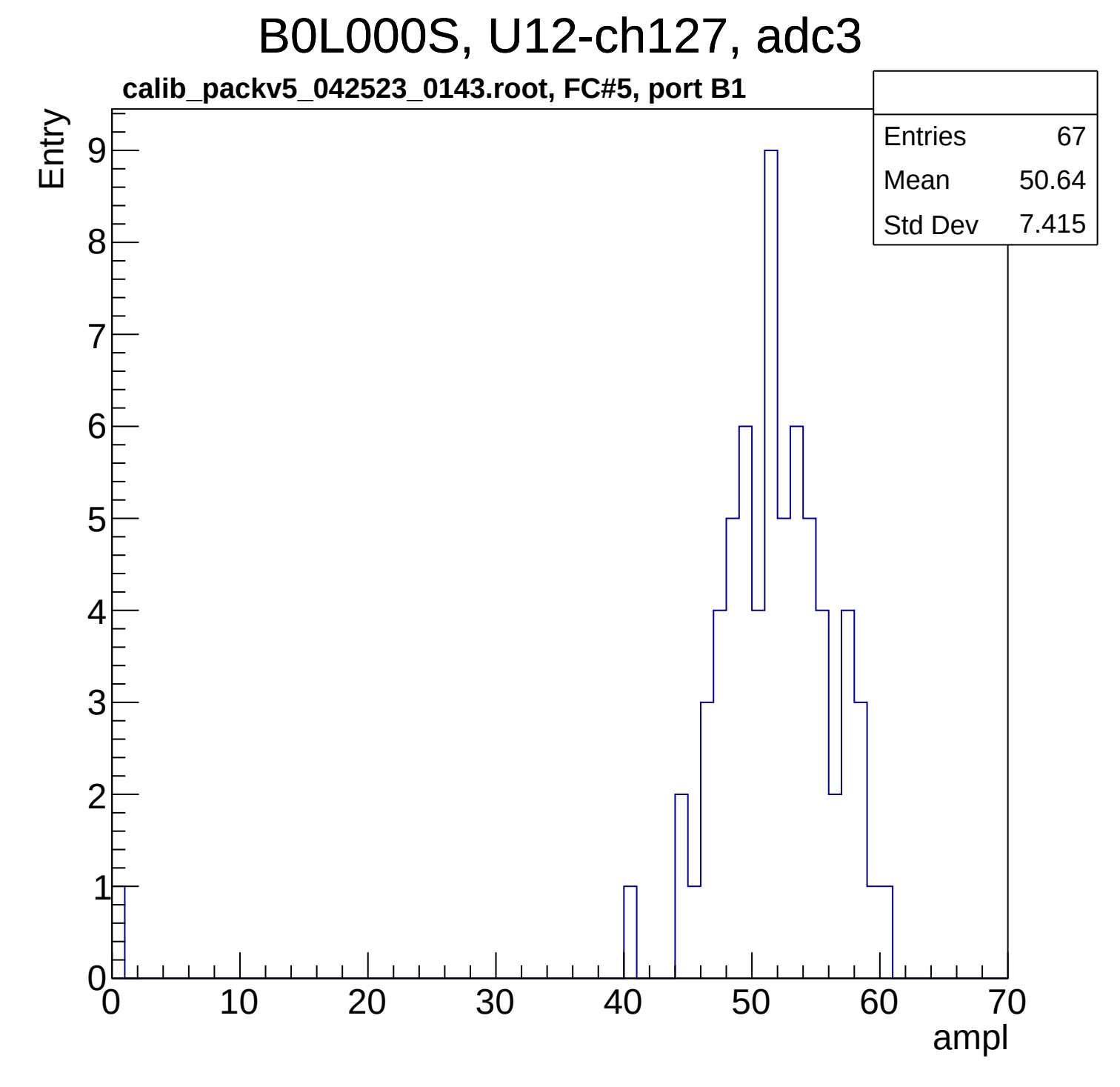
40
50
60
70


B0L000S, U12-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.64
Std Dev 7.415
Entry
ampl
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2
3
4
5
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7
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40
50
60
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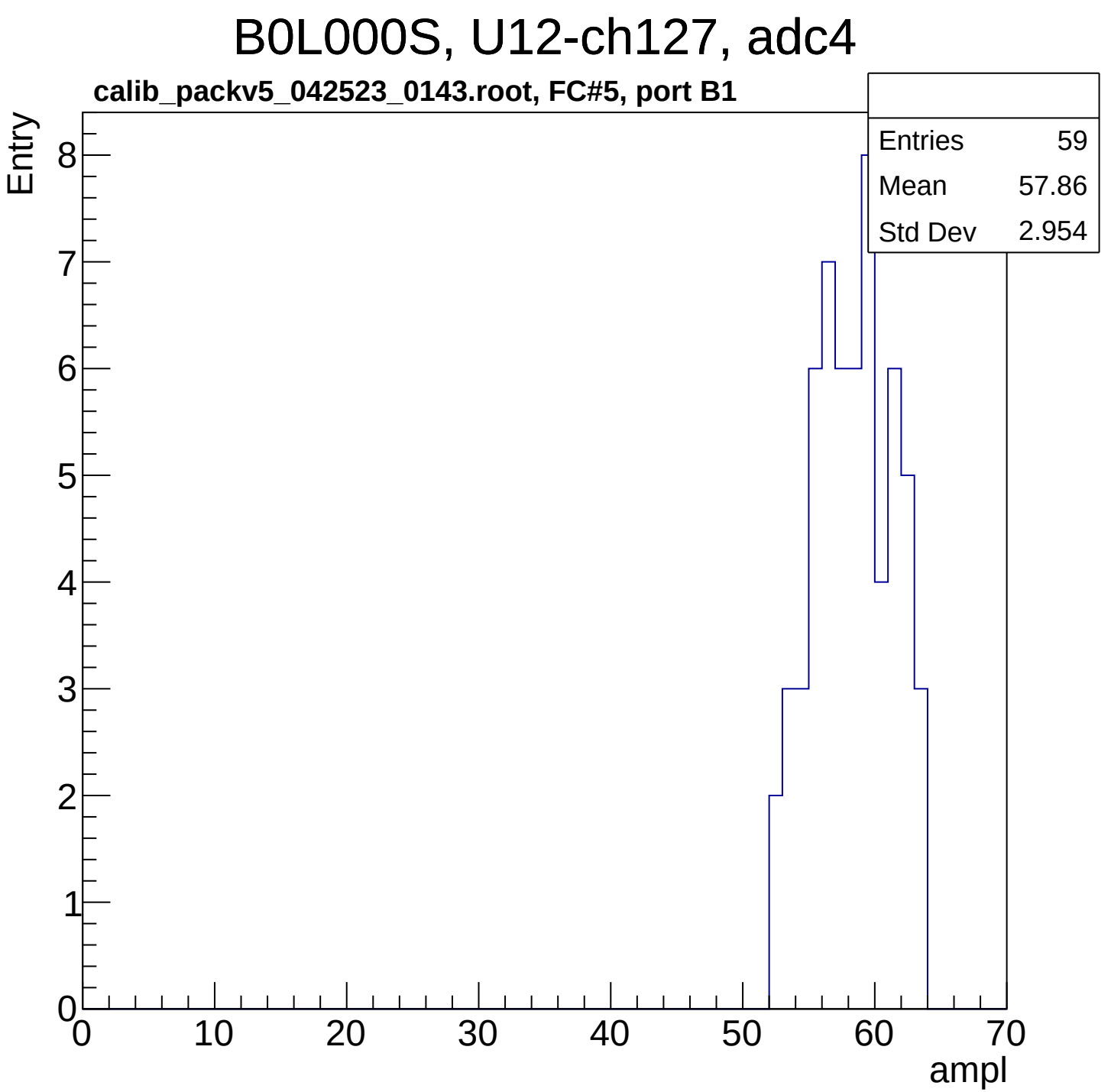
B0L000S, U12-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.86
Std Dev 2.954
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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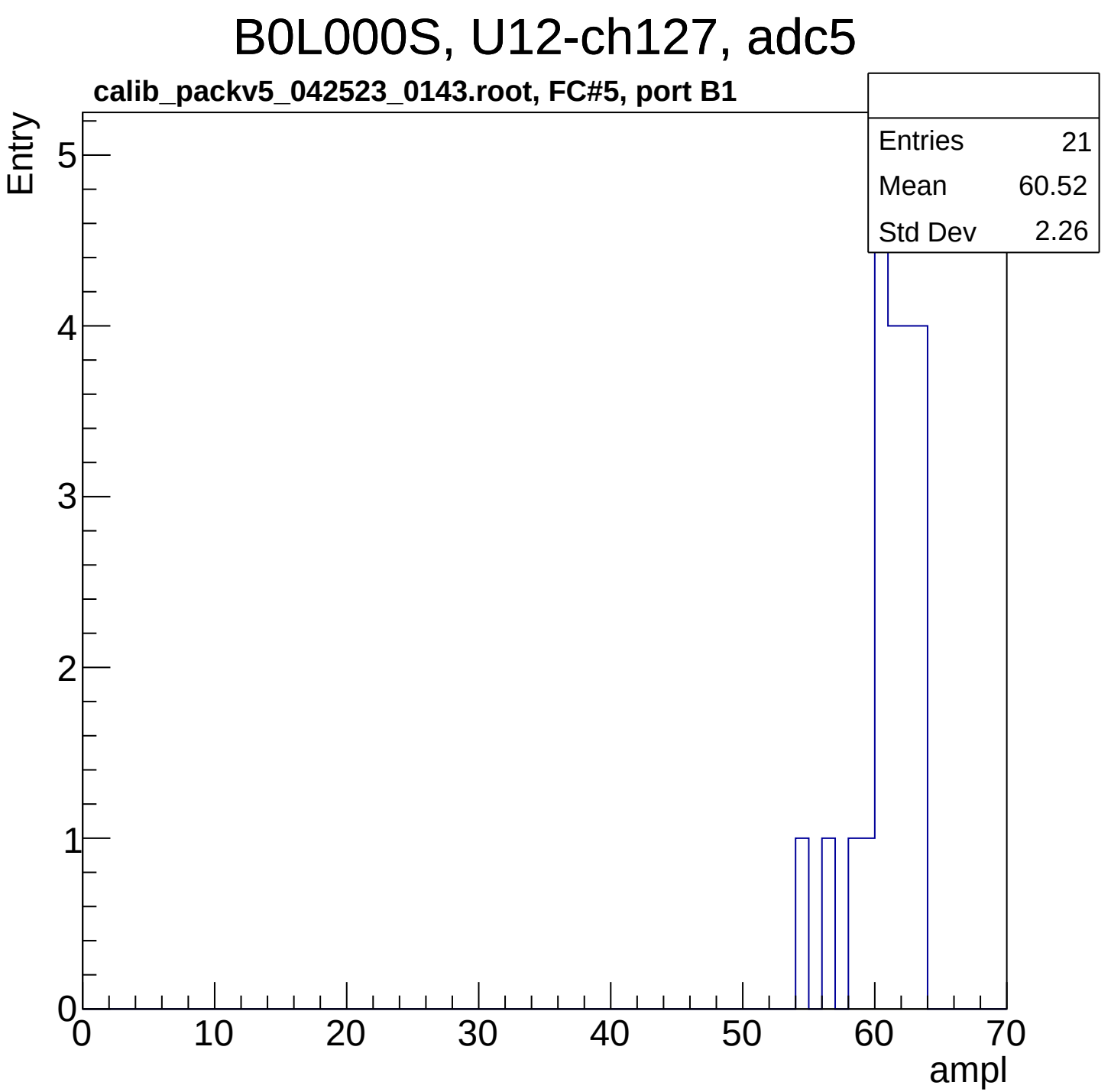
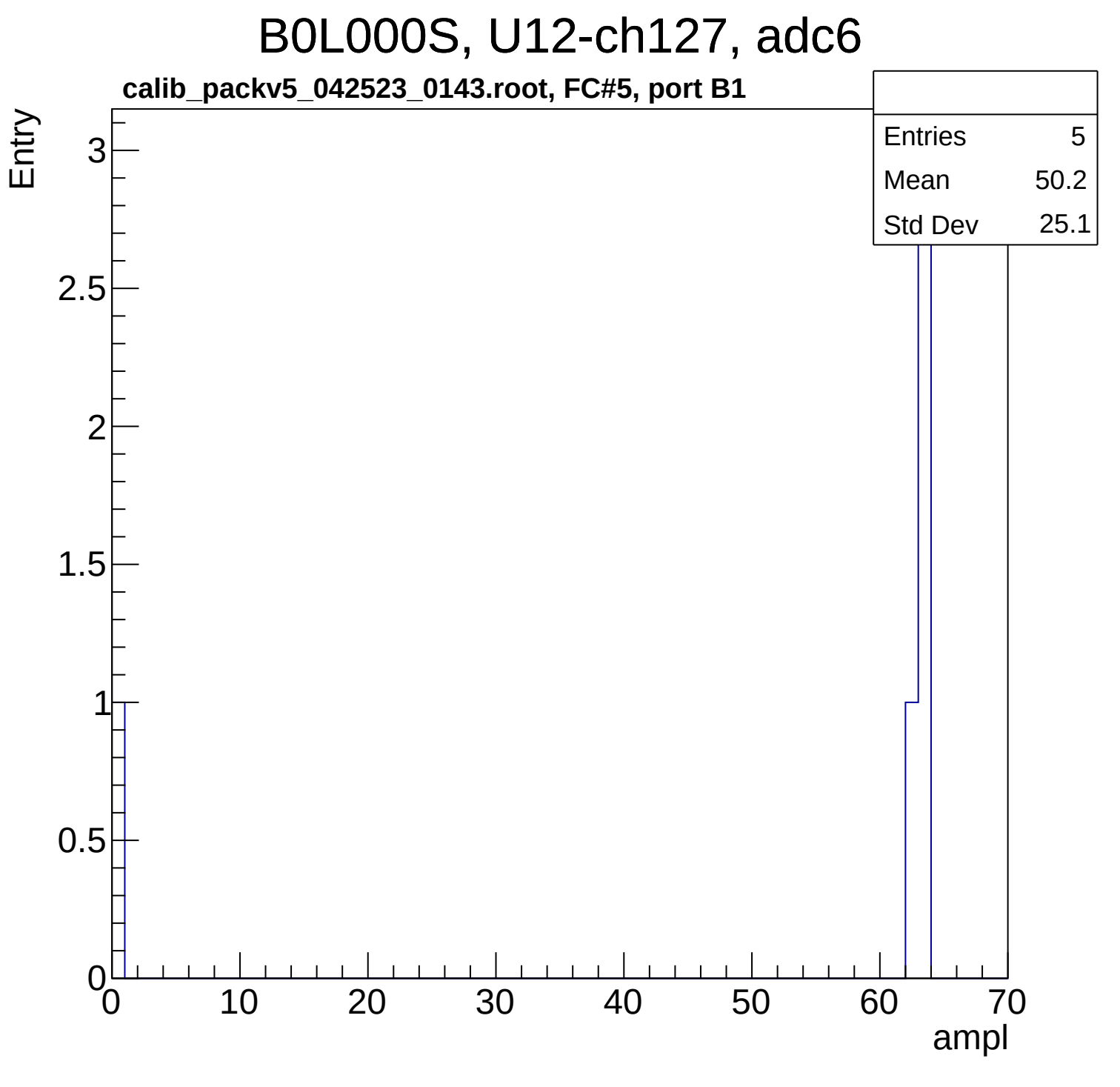
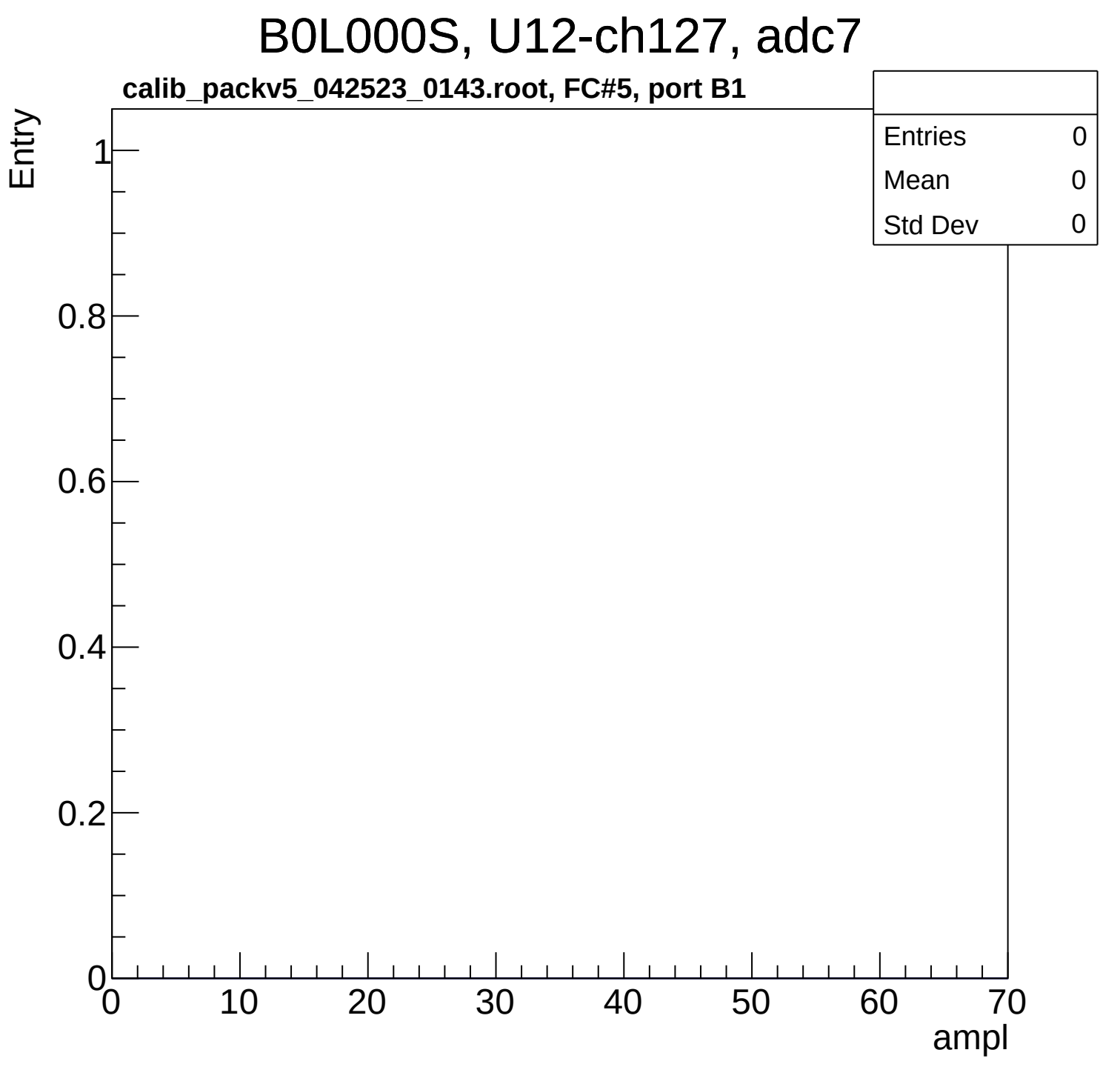
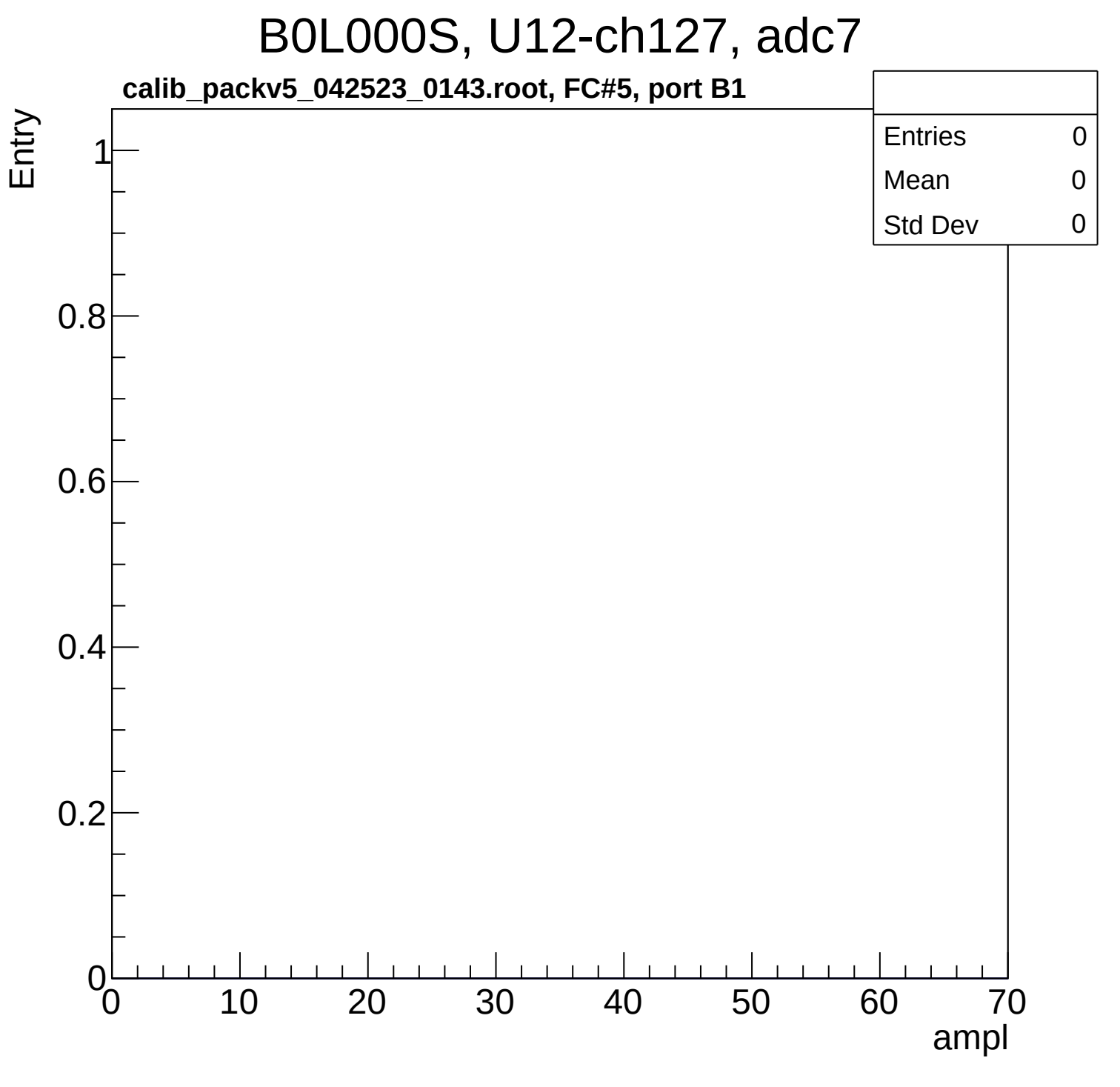
40
50
60
70


B0L000S, U12-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 60.52
Std Dev 2.26
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U12-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U12-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl