

B1L000S, U12-ch0

calib_packv5_042523_0143.root, FC#3, port B3

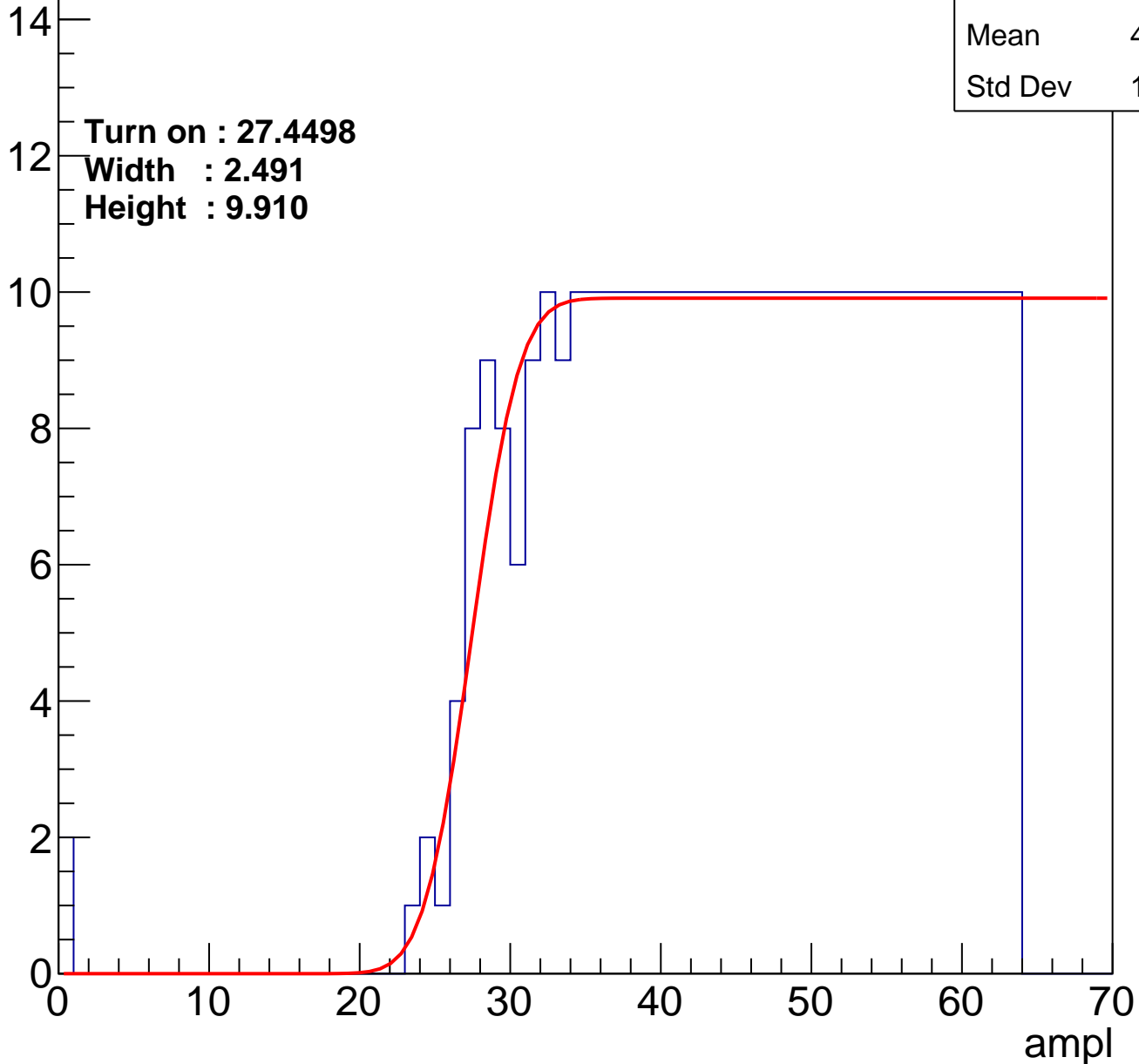
Entry

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 27.4498

Width : 2.491

Height : 9.910



B1L000S, U12-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.25
Std Dev	11.35

Turn on : 28.8884

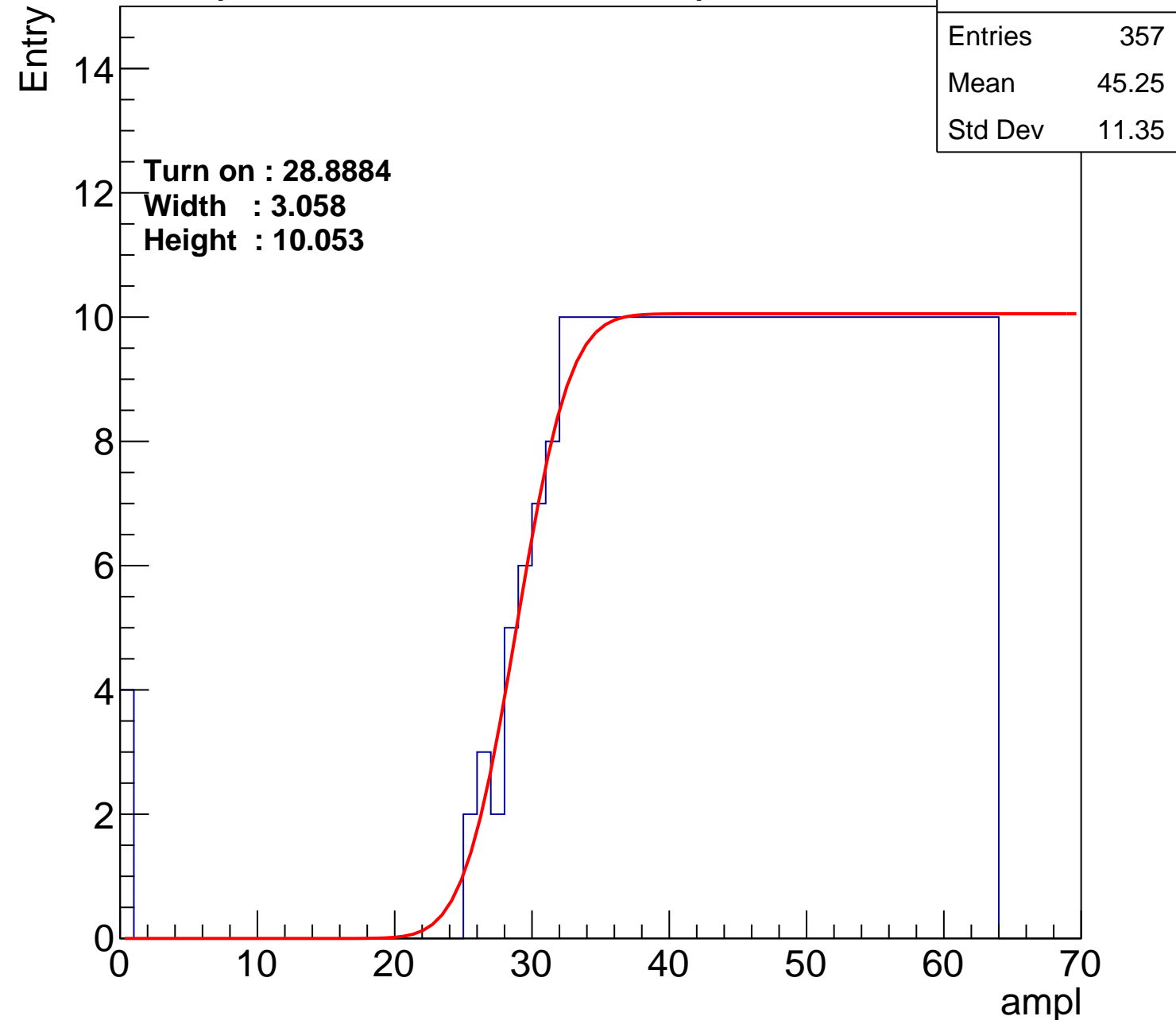
Width : 3.058

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch2

calib_packv5_042523_0143.root, FC#3, port B3

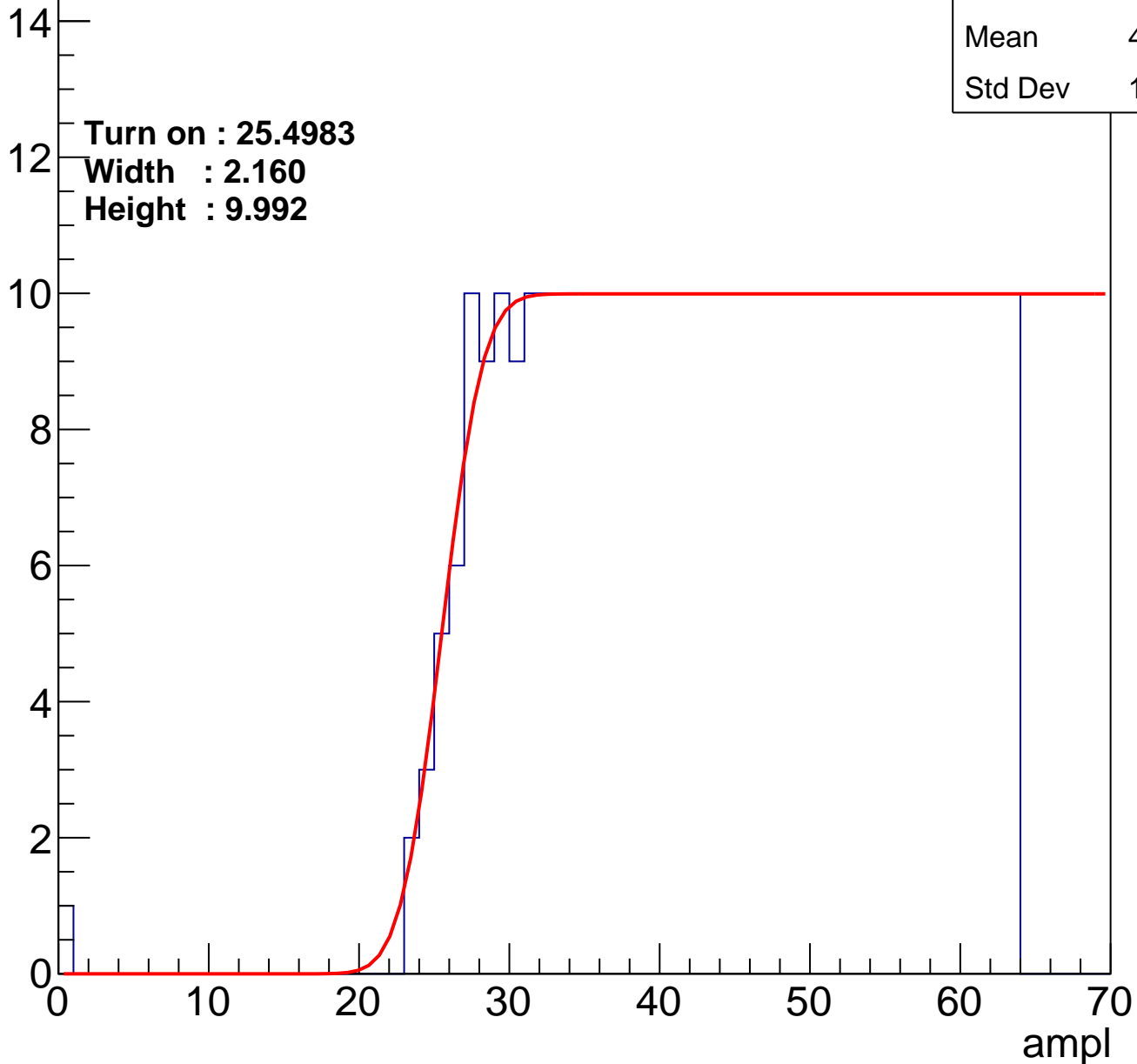
Entries	385
Mean	44.13
Std Dev	11.38

Turn on : 25.4983

Width : 2.160

Height : 9.992

Entry



B1L000S, U12-ch3

calib_packv5_042523_0143.root, FC#3, port B3

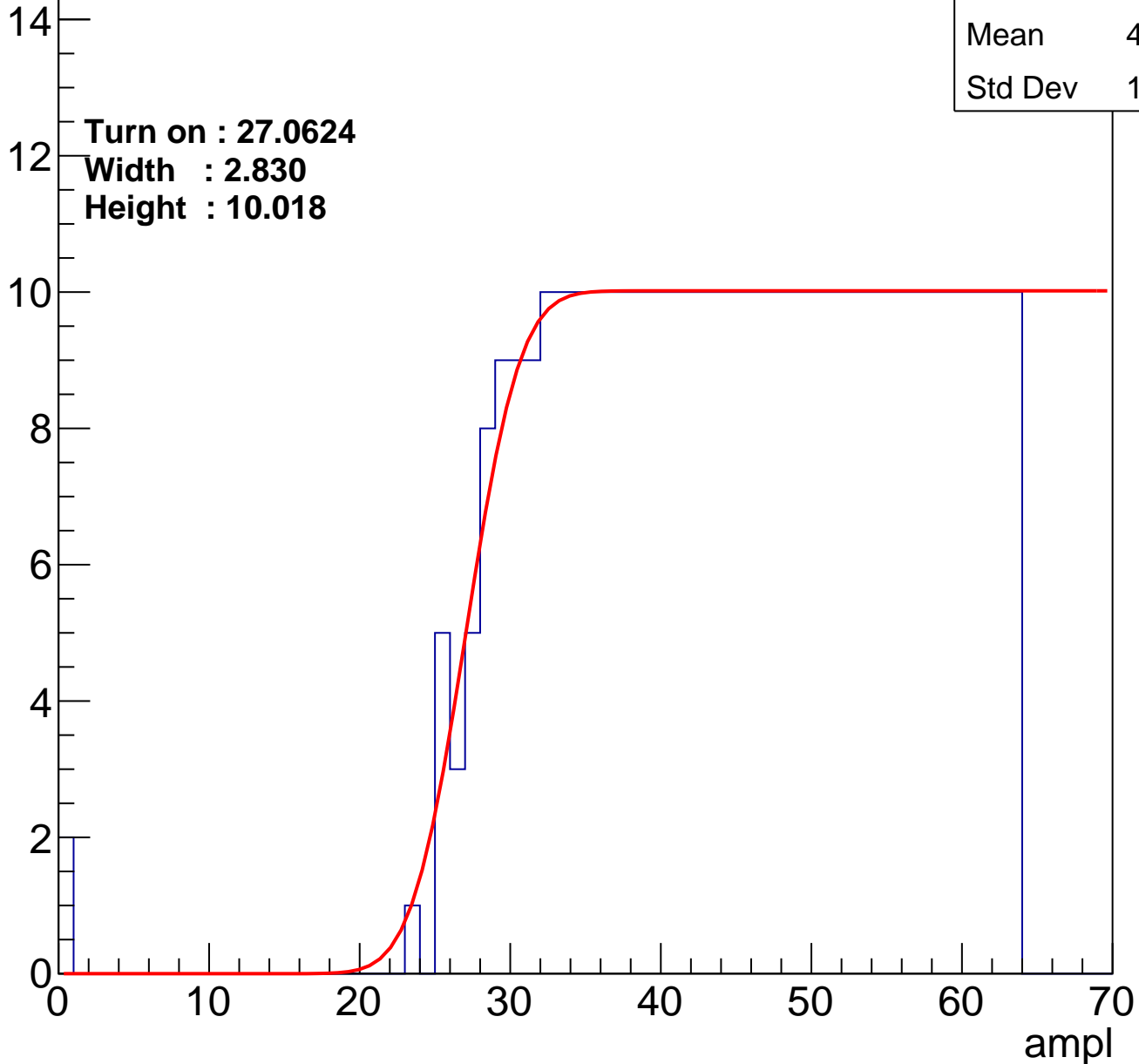
Entries	371
Mean	44.73
Std Dev	11.24

Turn on : 27.0624

Width : 2.830

Height : 10.018

Entry



B1L000S, U12-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.36
Std Dev	11.73

Turn on : 27.3391

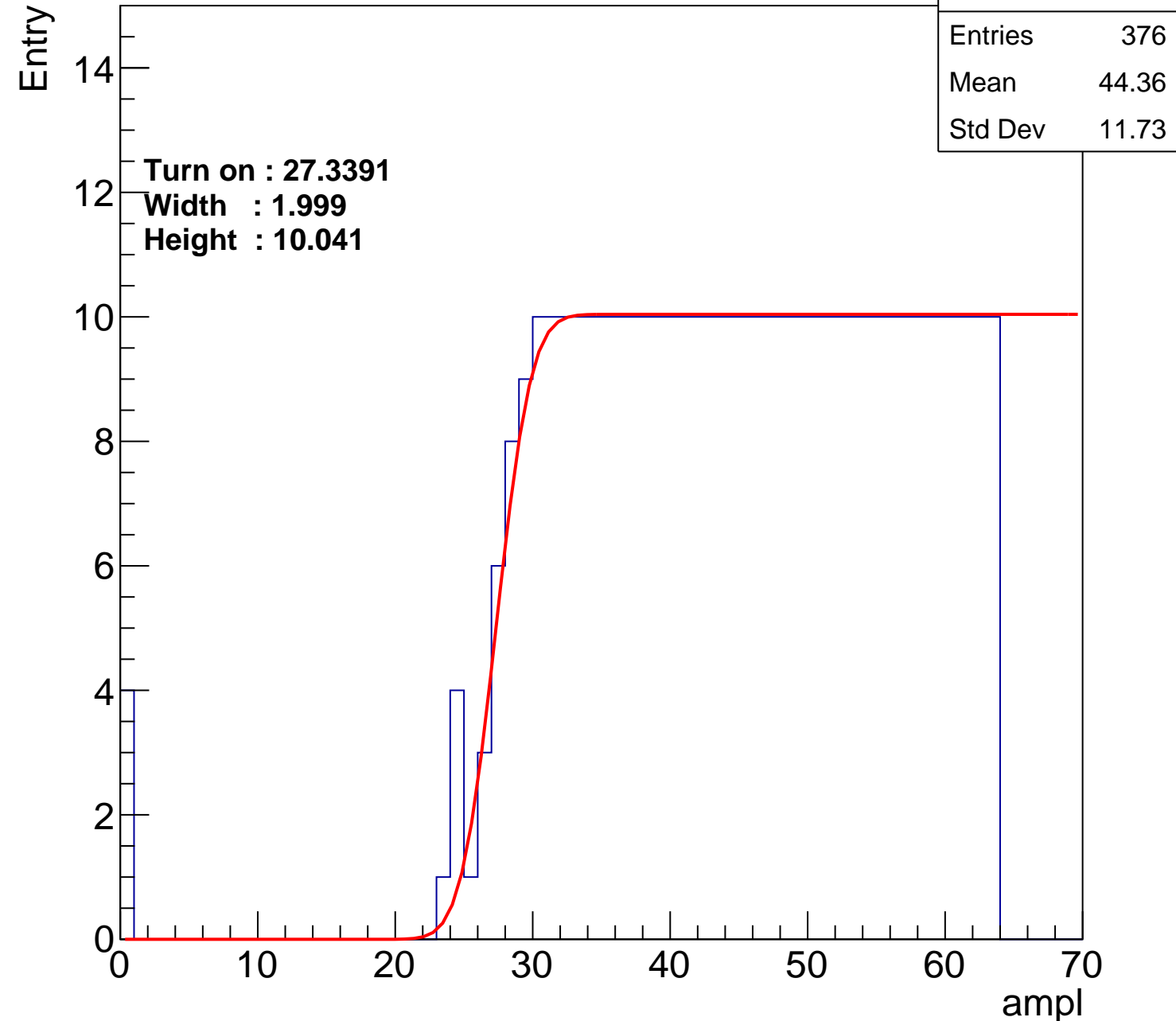
Width : 1.999

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch5

calib_packv5_042523_0143.root, FC#3, port B3

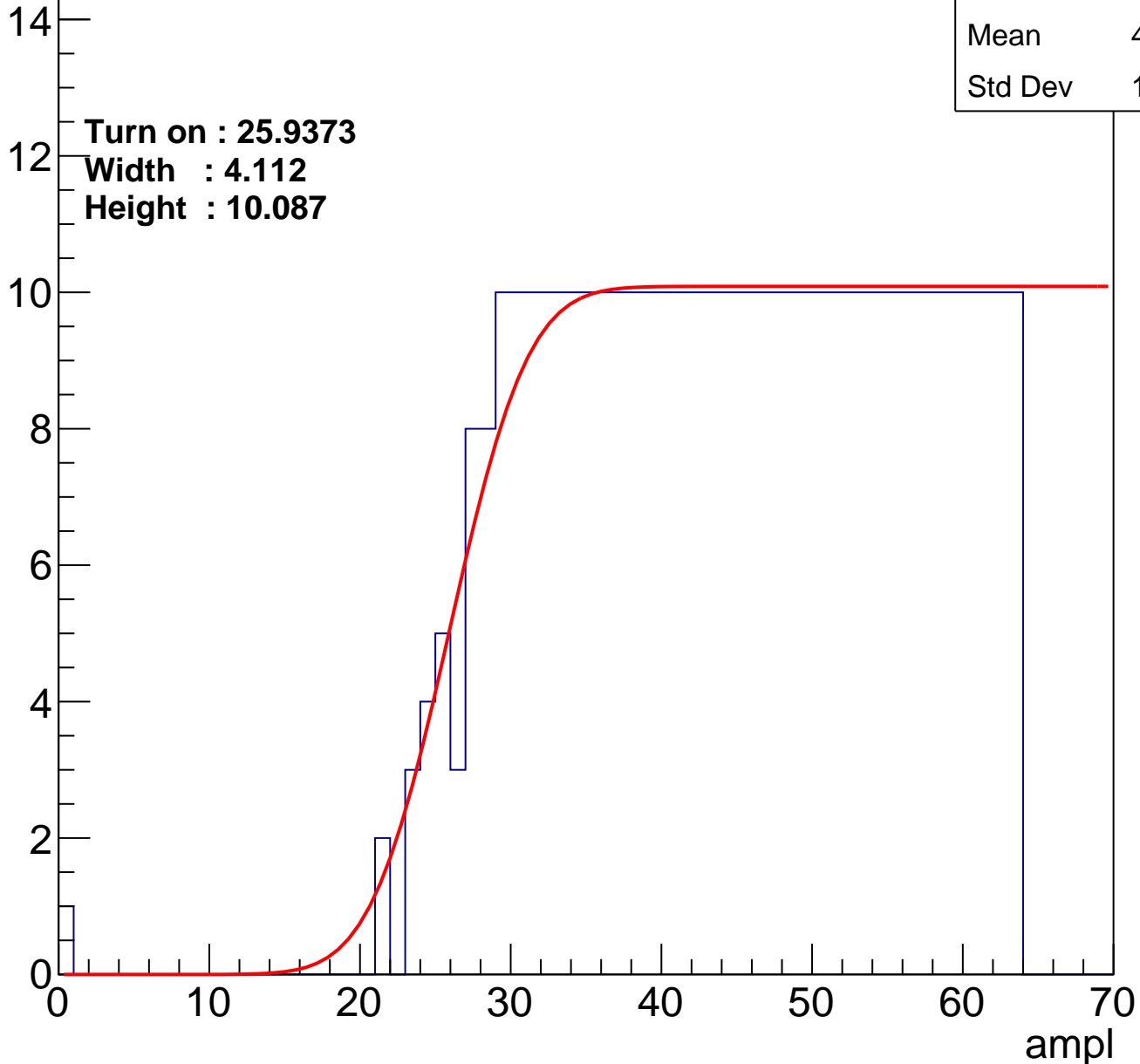
Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.9373

Width : 4.112

Height : 10.087

Entry



B1L000S, U12-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.1
Std Dev	11.43

Turn on : 26.0158

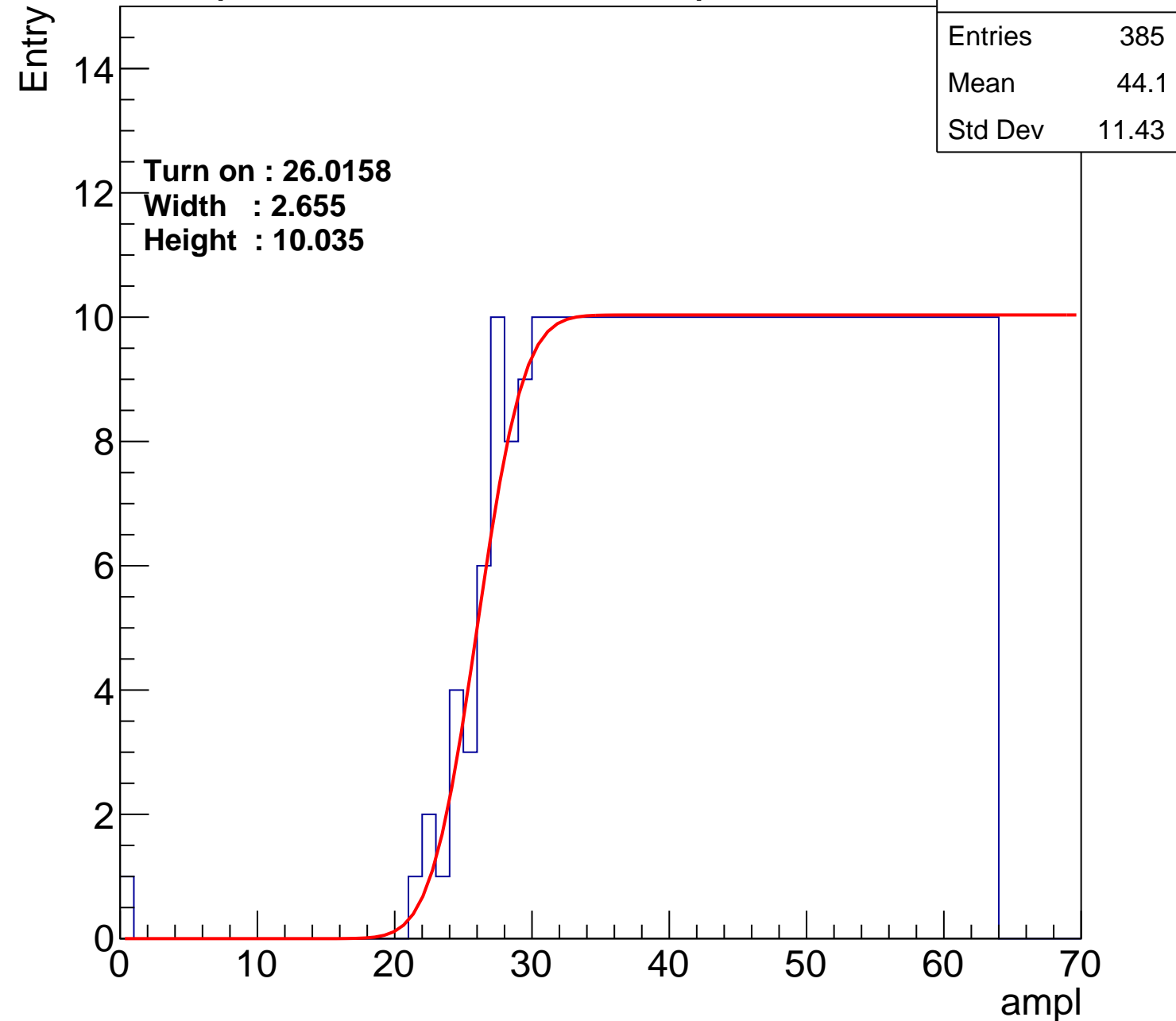
Width : 2.655

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.79
Std Dev	11.32

Turn on : 27.7149

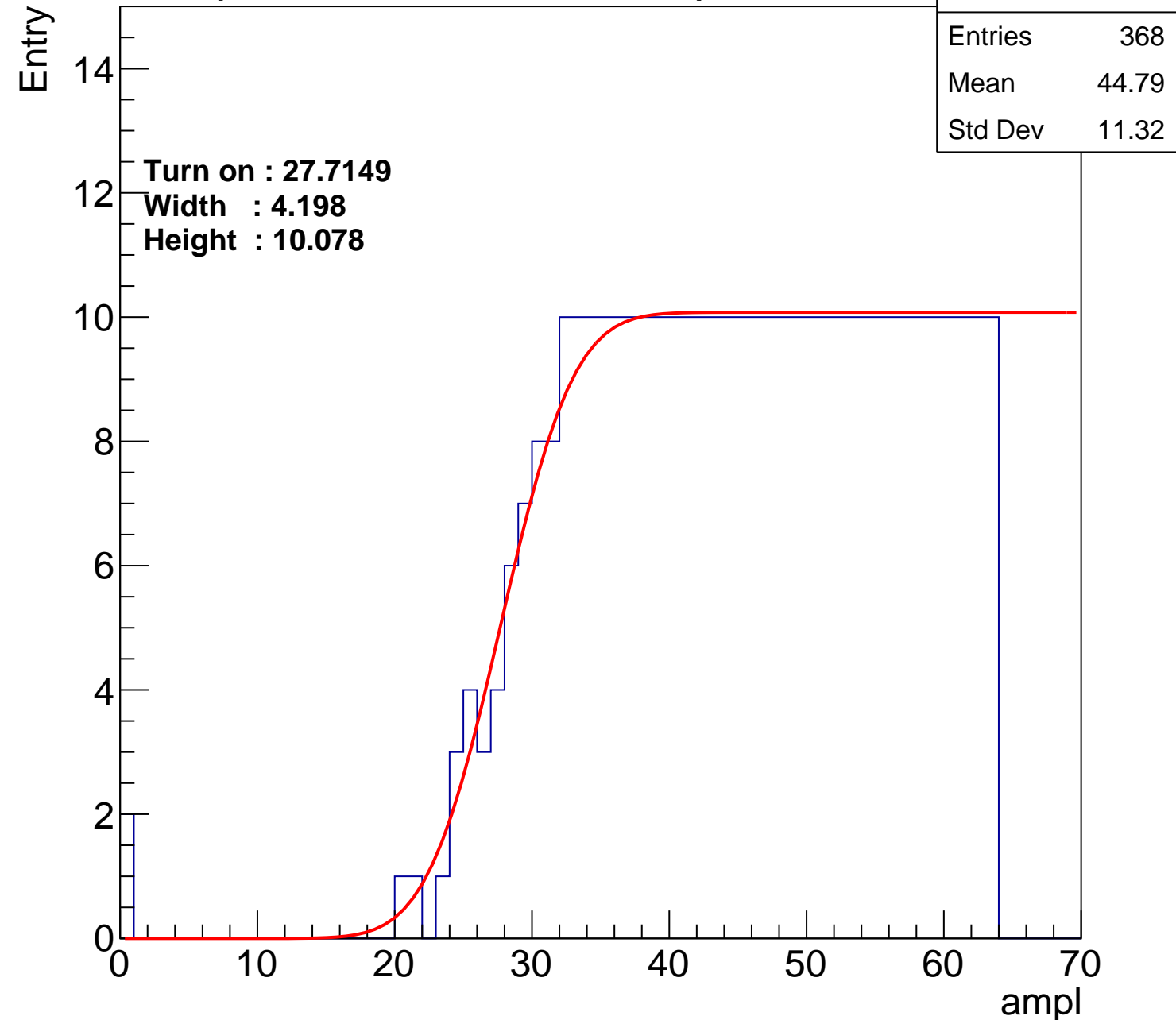
Width : 4.198

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch8

calib_packv5_042523_0143.root, FC#3, port B3

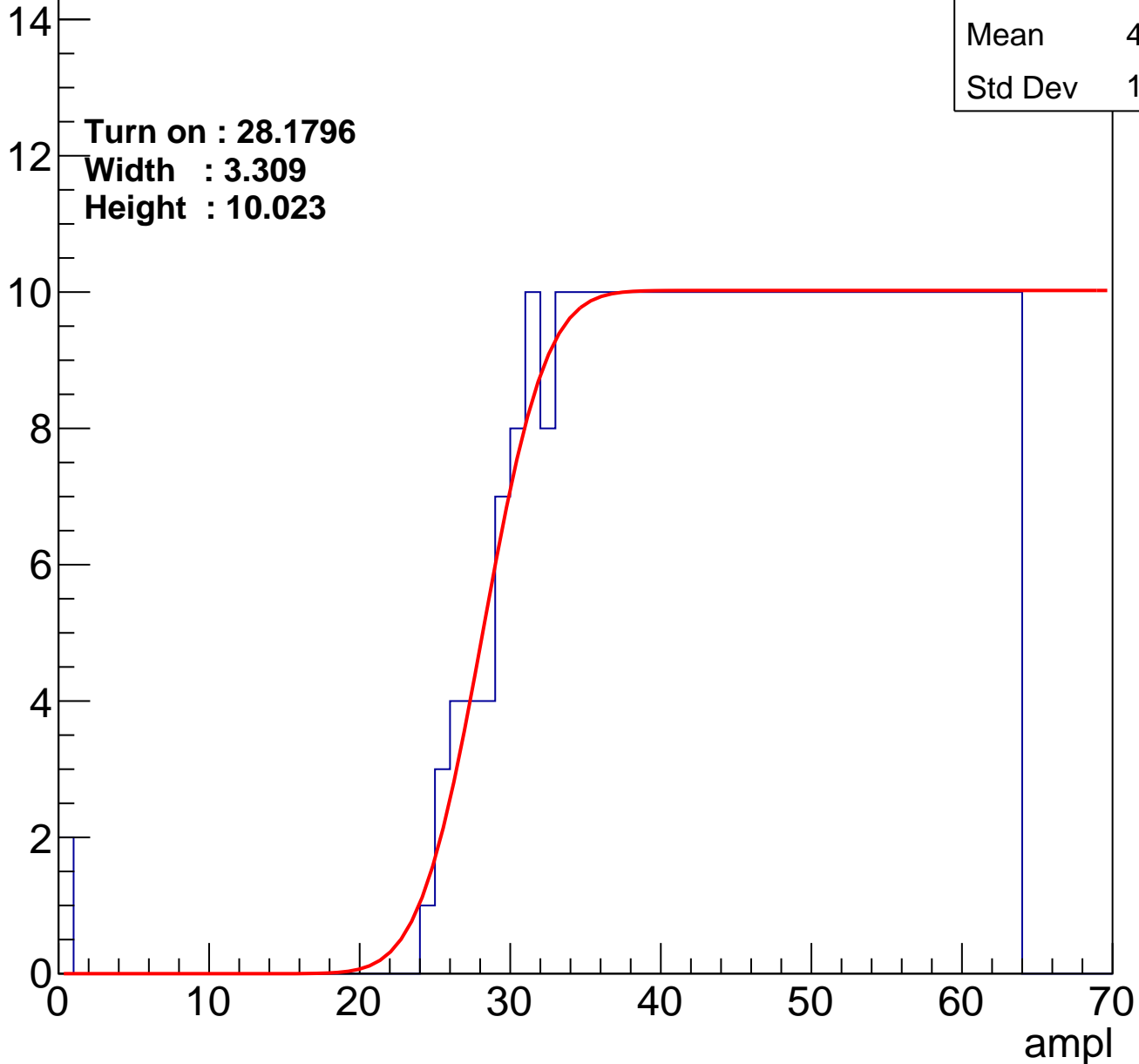
Entries	361
Mean	45.19
Std Dev	11.05

Turn on : 28.1796

Width : 3.309

Height : 10.023

Entry



B1L000S, U12-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.0530

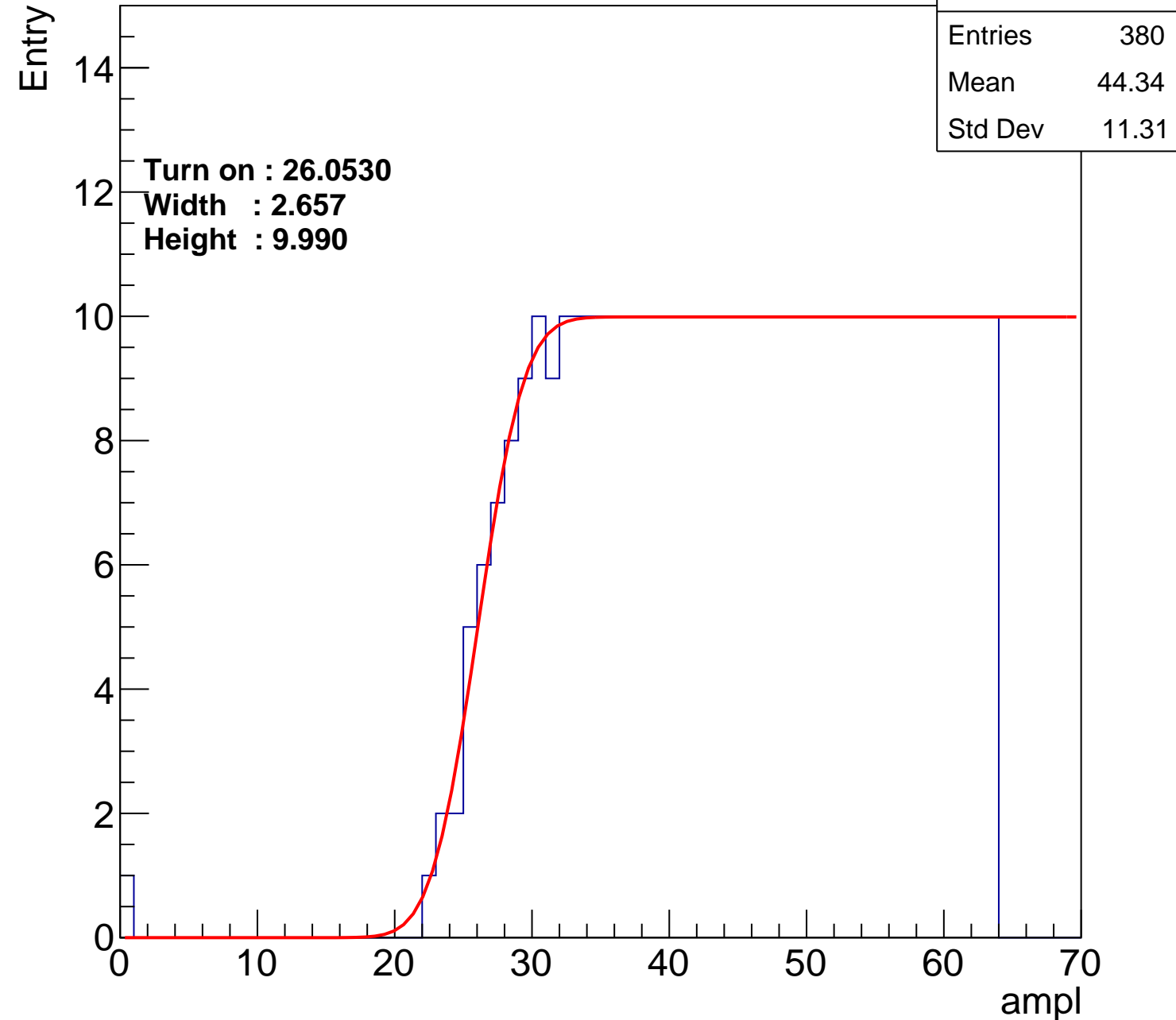
Width : 2.657

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch10

calib_packv5_042523_0143.root, FC#3, port B3

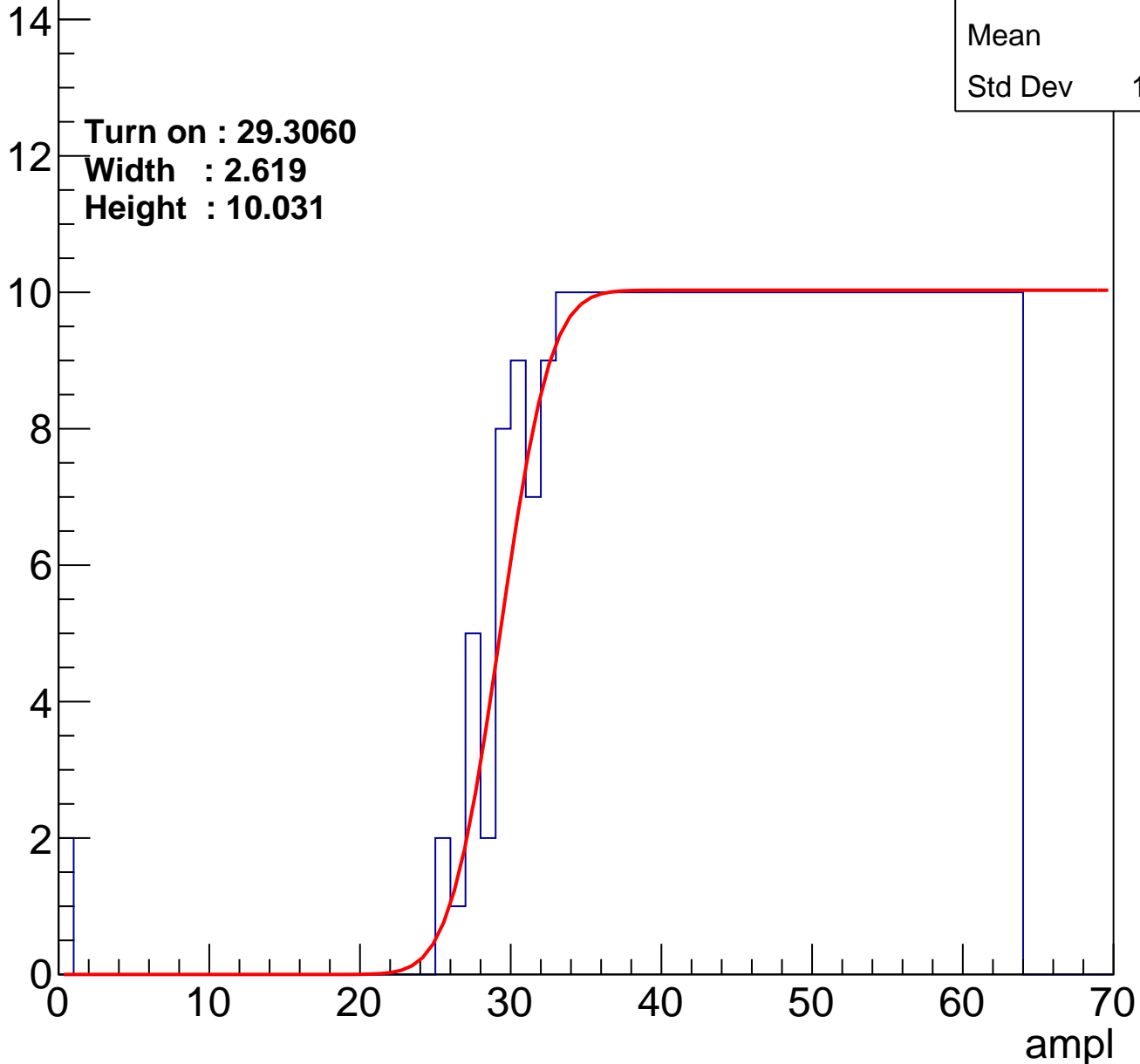
Entries	355
Mean	45.5
Std Dev	10.86

Turn on : 29.3060

Width : 2.619

Height : 10.031

Entry



B1L000S, U12-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.11
Std Dev	12.2

Turn on : 24.4597

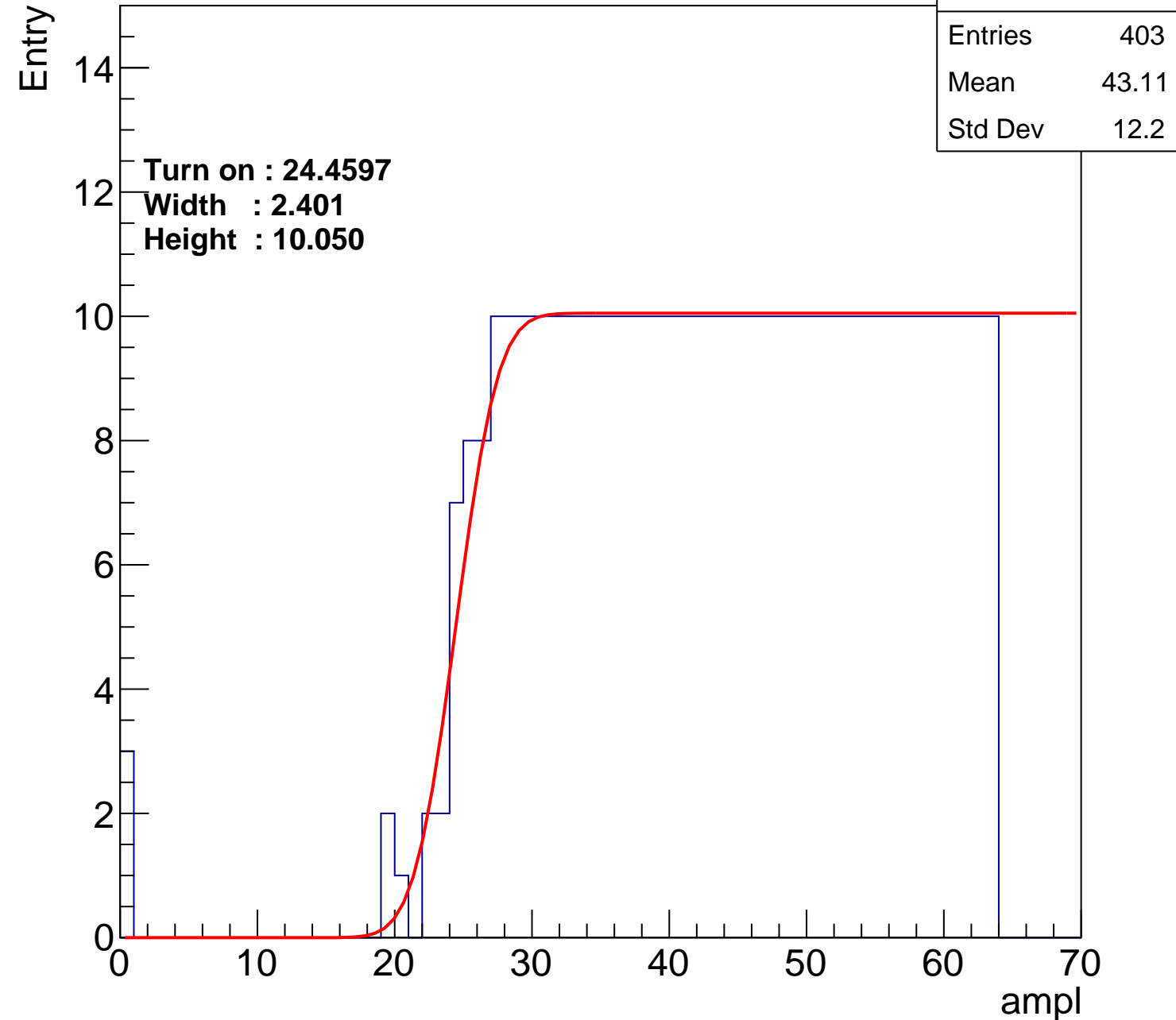
Width : 2.401

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch12

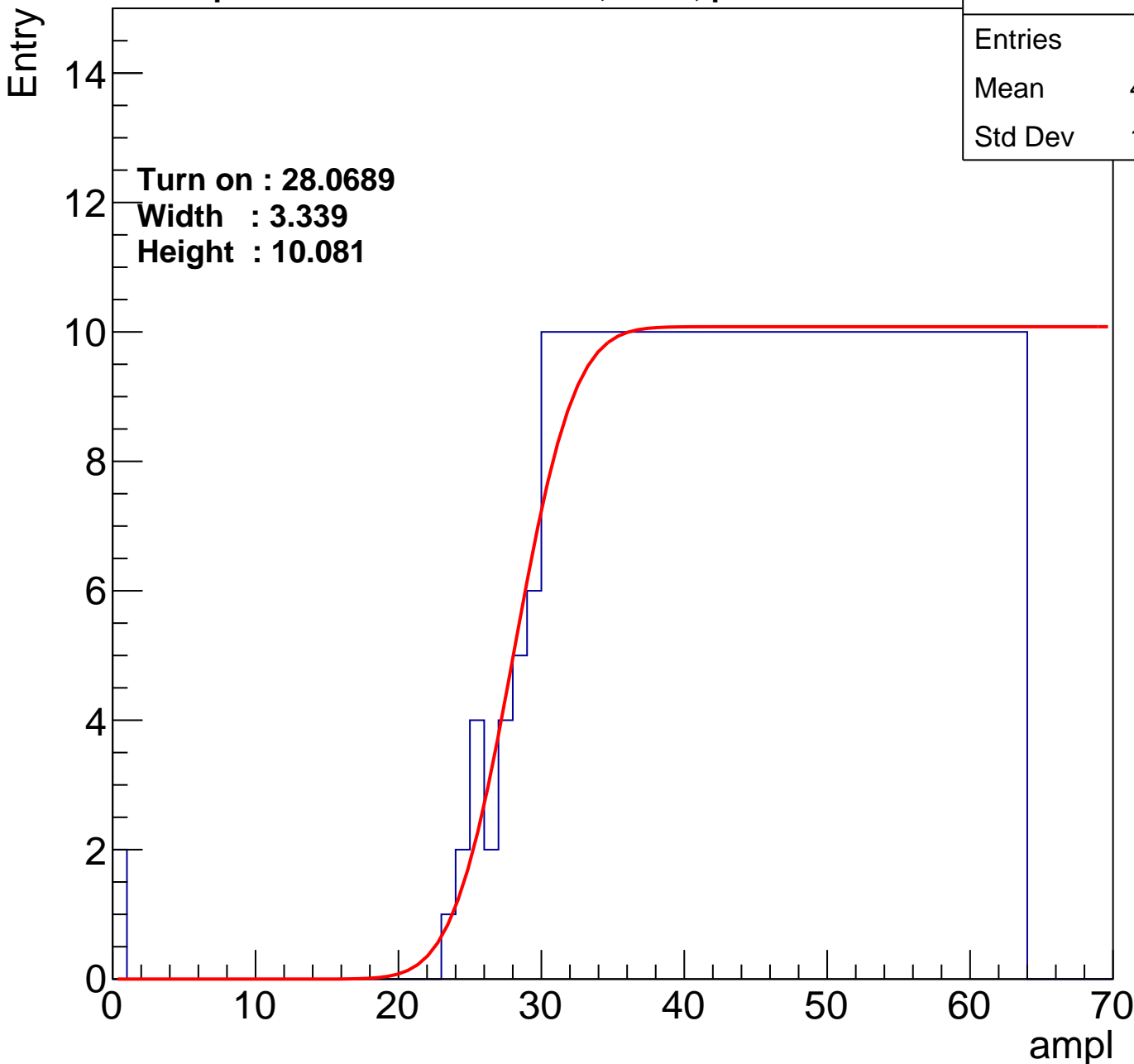
calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.96
Std Dev	11.15

Turn on : 28.0689

Width : 3.339

Height : 10.081



B1L000S, U12-ch13

calib_packv5_042523_0143.root, FC#3, port B3

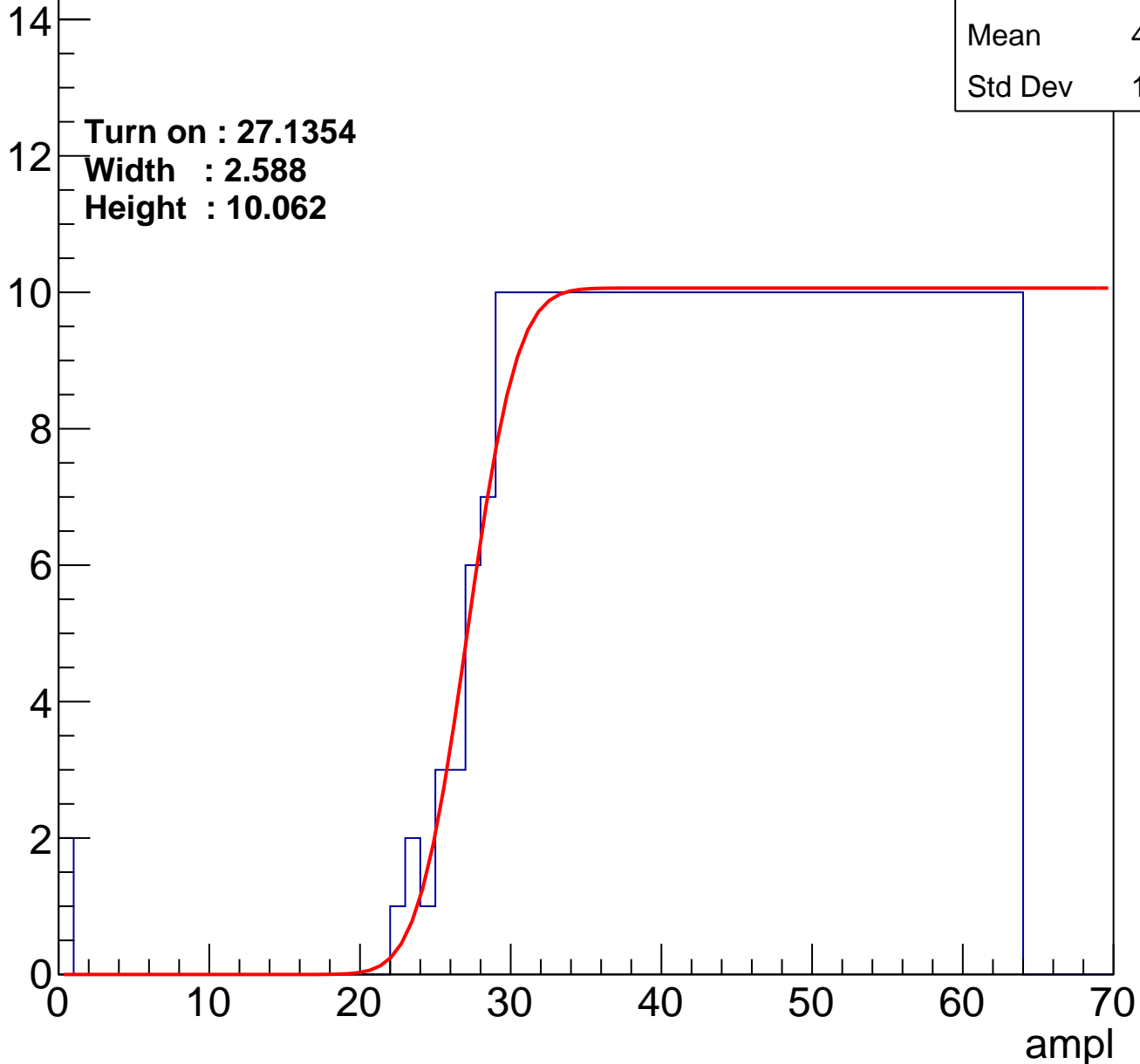
Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 27.1354

Width : 2.588

Height : 10.062

Entry



B1L000S, U12-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.97
Std Dev	11.27

Turn on : 28.1309

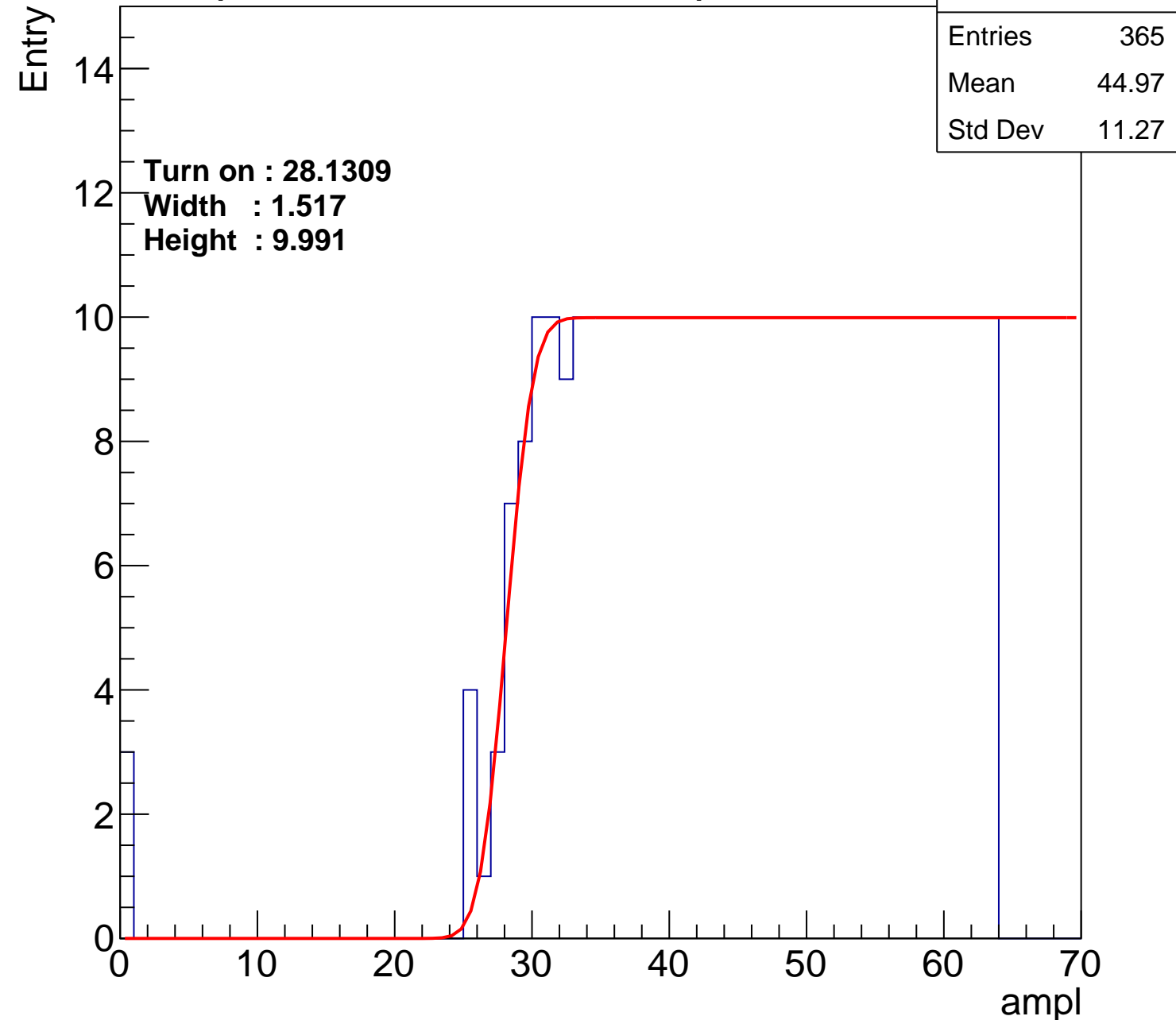
Width : 1.517

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch15

calib_packv5_042523_0143.root, FC#3, port B3

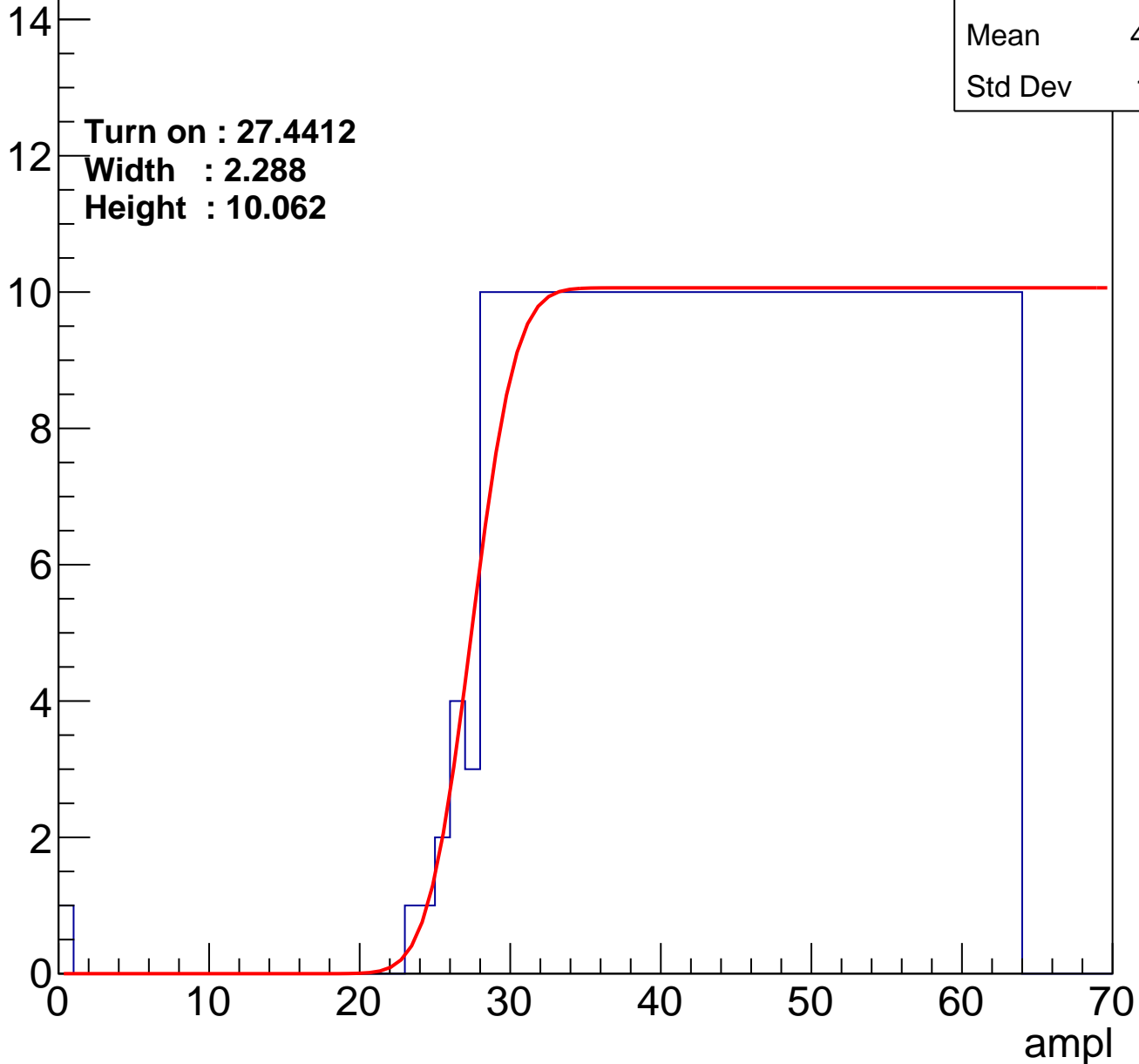
Entries	372
Mean	44.79
Std Dev	11.01

Turn on : 27.4412

Width : 2.288

Height : 10.062

Entry



B1L000S, U12-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.31
Std Dev	10.77

Turn on : 28.2195

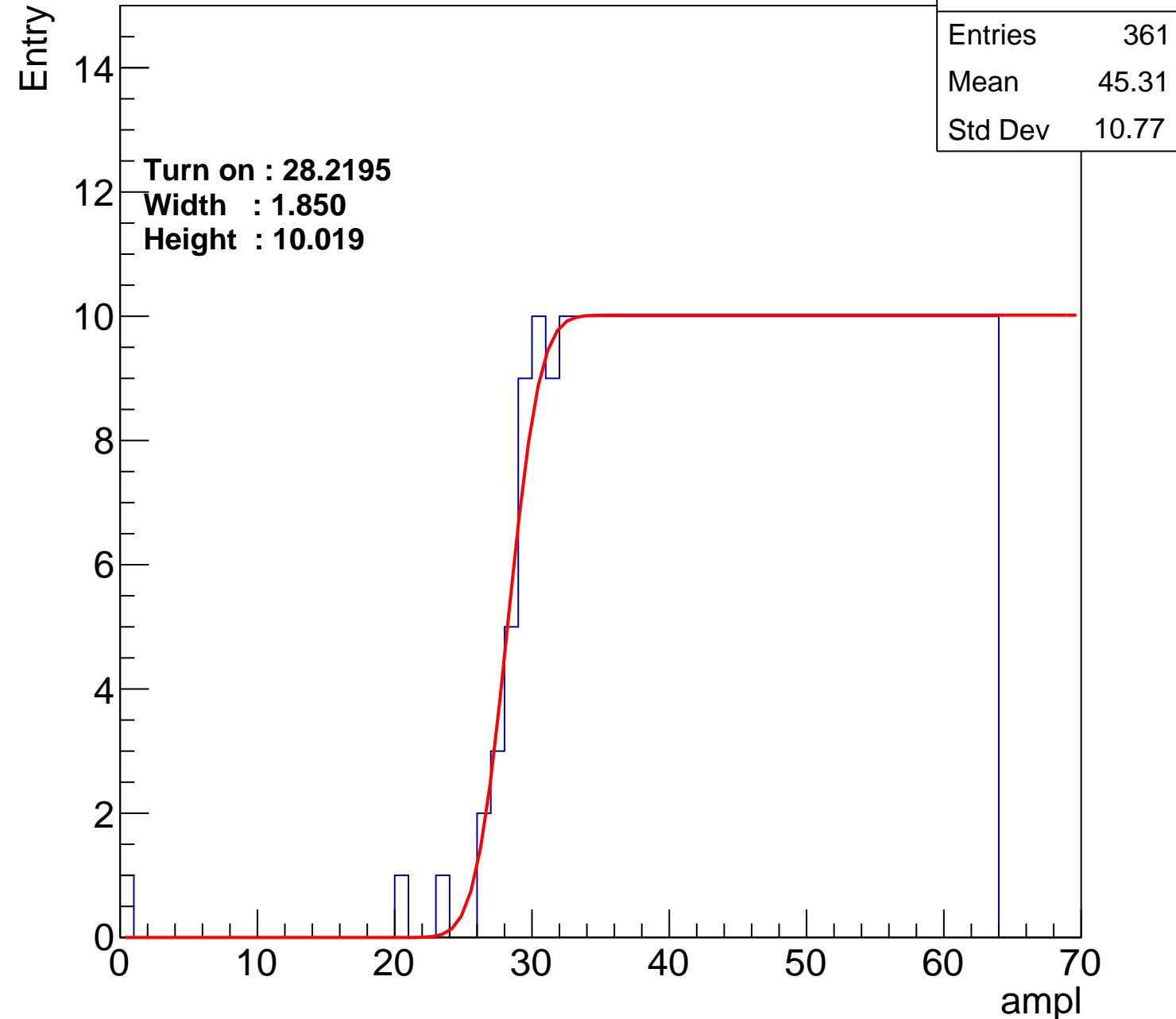
Width : 1.850

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch17

calib_packv5_042523_0143.root, FC#3, port B3

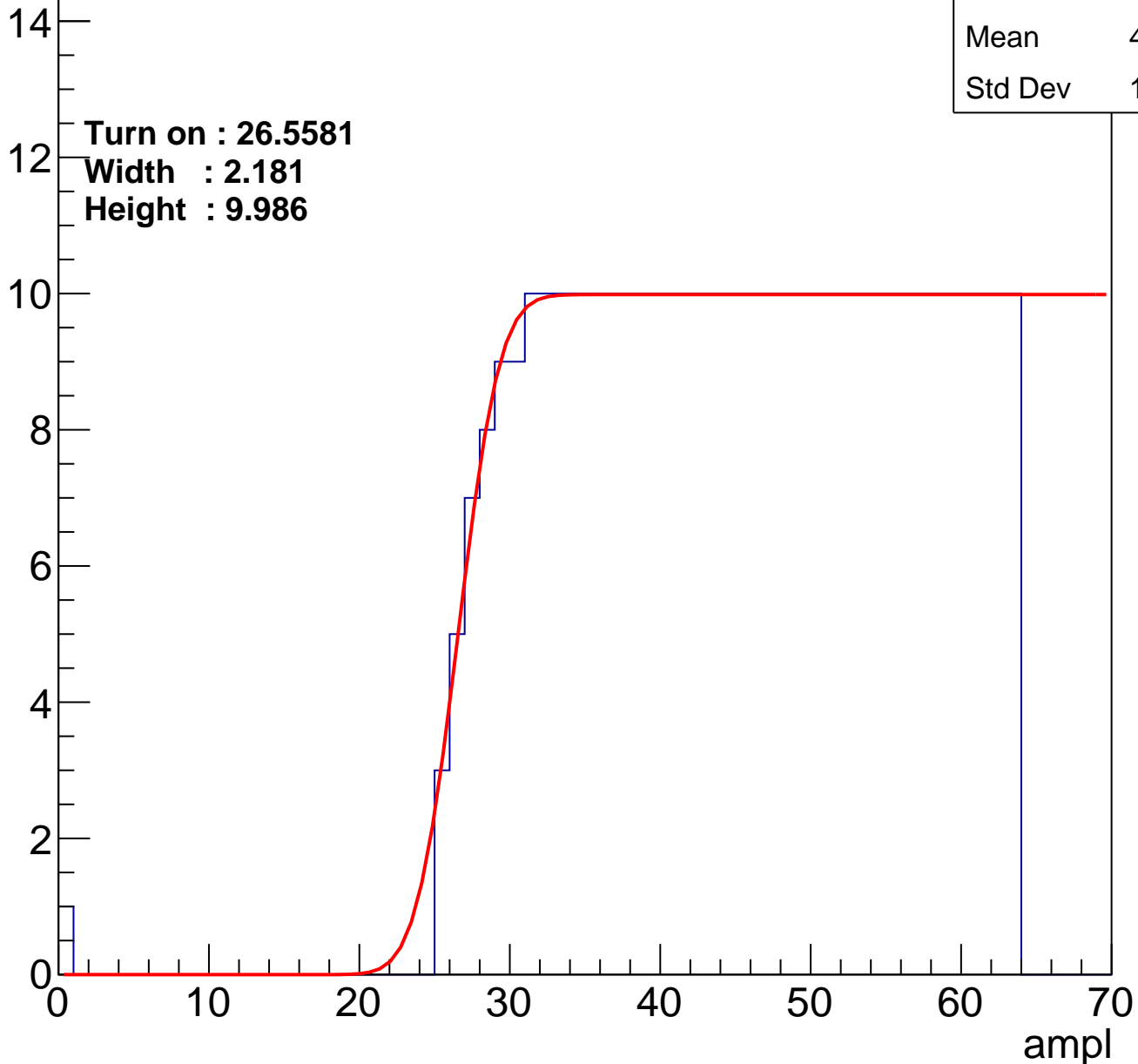
Entries	372
Mean	44.78
Std Dev	11.02

Turn on : 26.5581

Width : 2.181

Height : 9.986

Entry



B1L000S, U12-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.2495

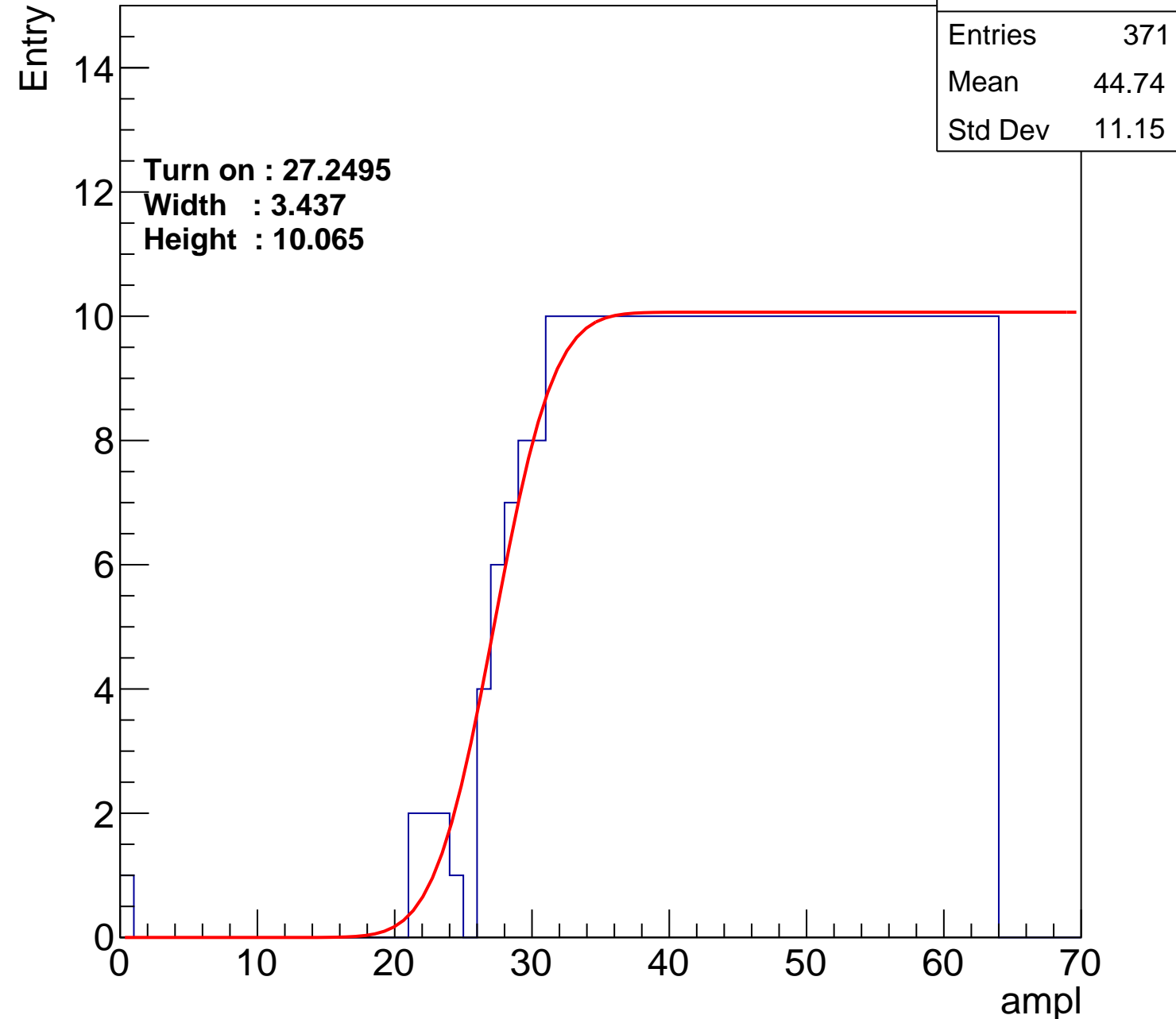
Width : 3.437

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.67
Std Dev	11.09

Turn on : 26.9546

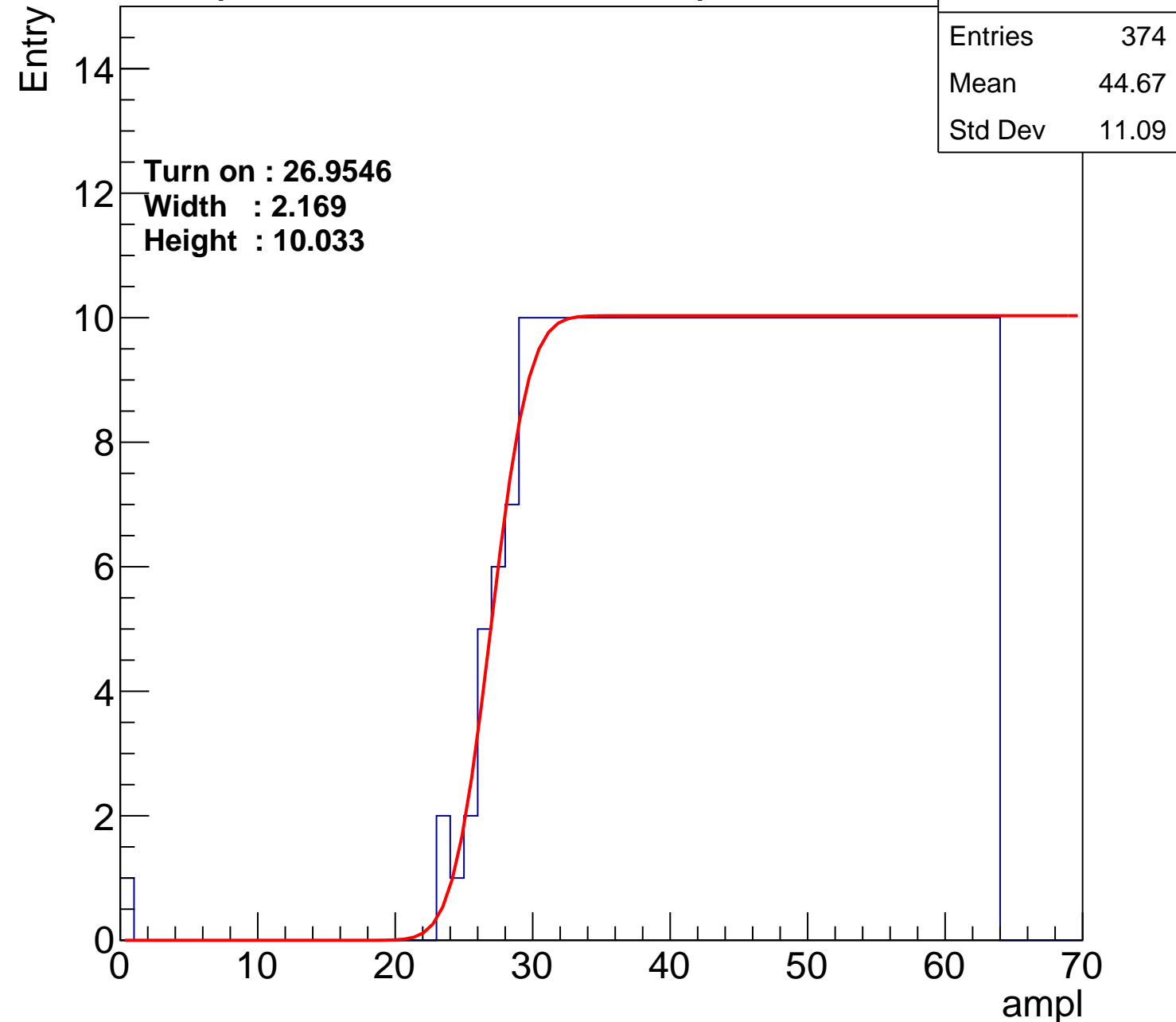
Width : 2.169

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.8946

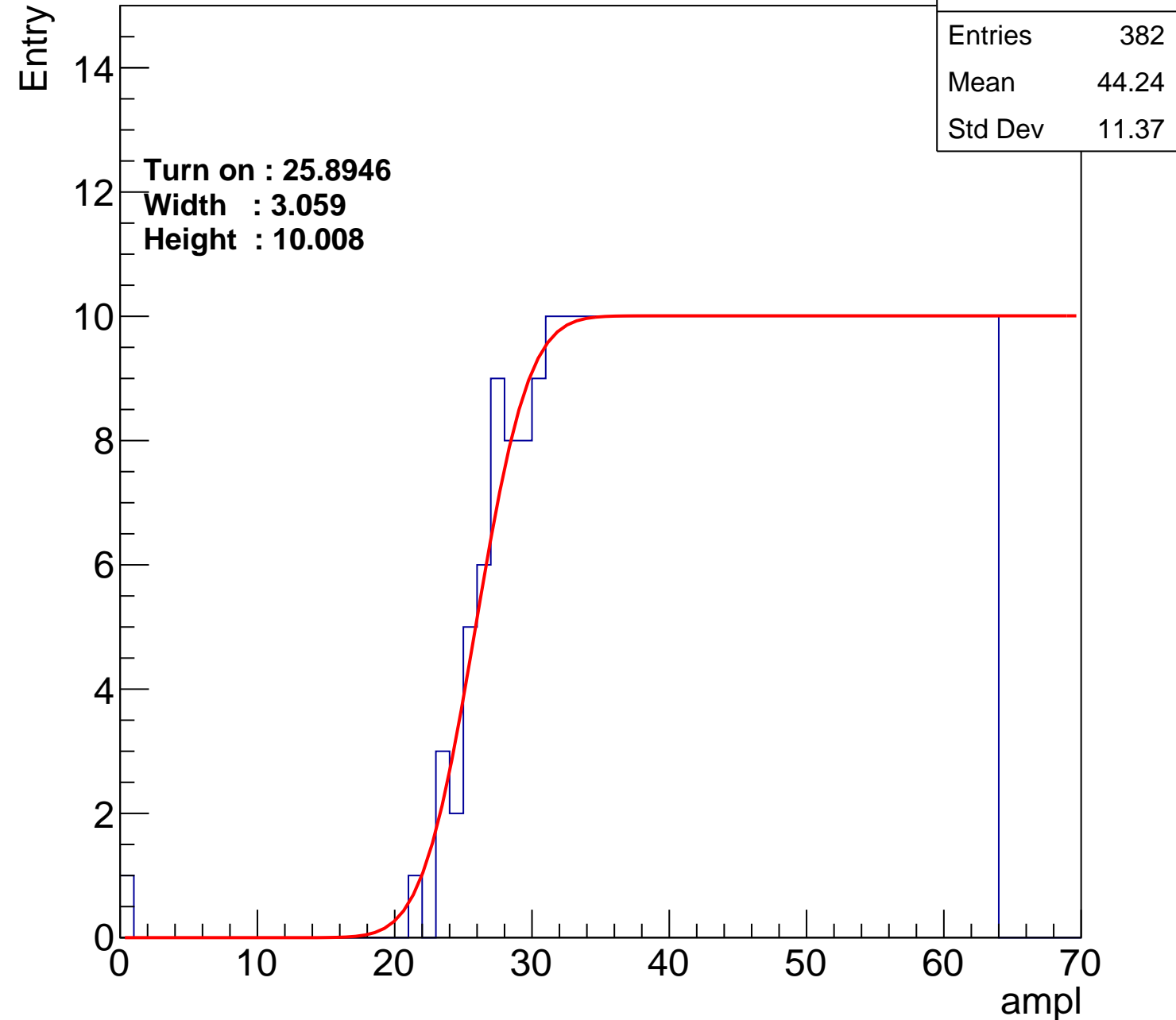
Width : 3.059

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.8464

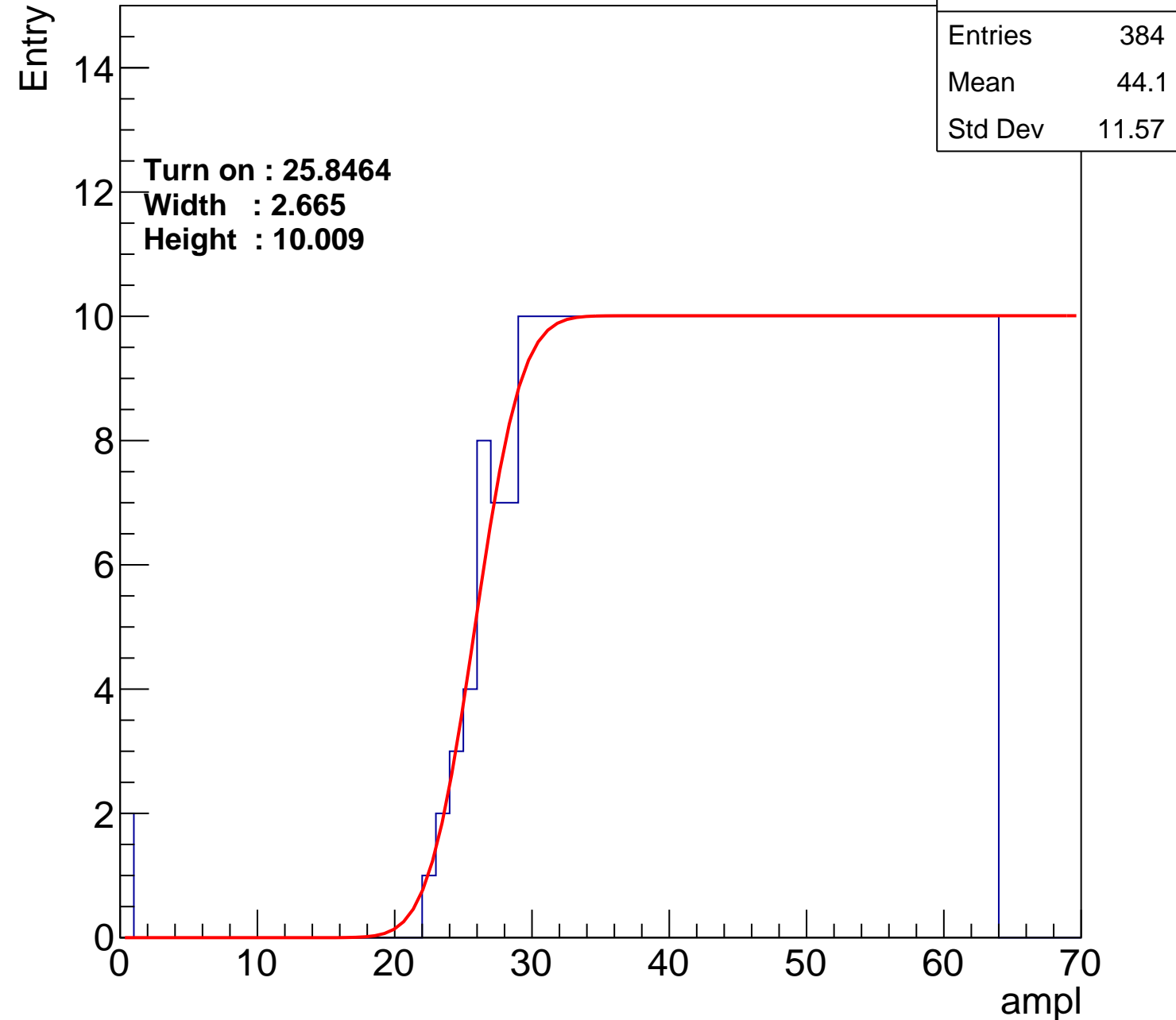
Width : 2.665

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.95
Std Dev	12.06

Turn on : 26.7536

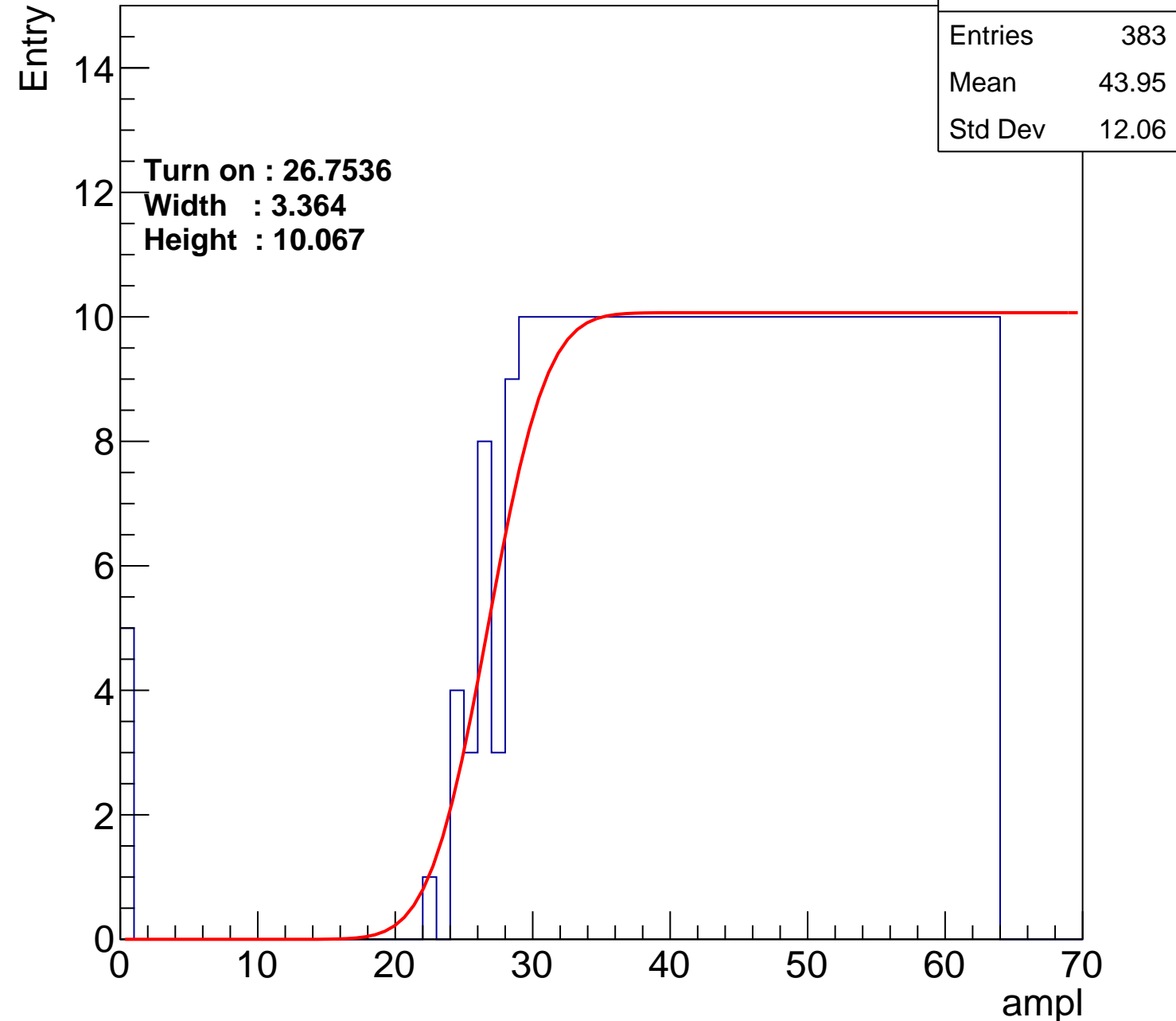
Width : 3.364

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch23

calib_packv5_042523_0143.root, FC#3, port B3

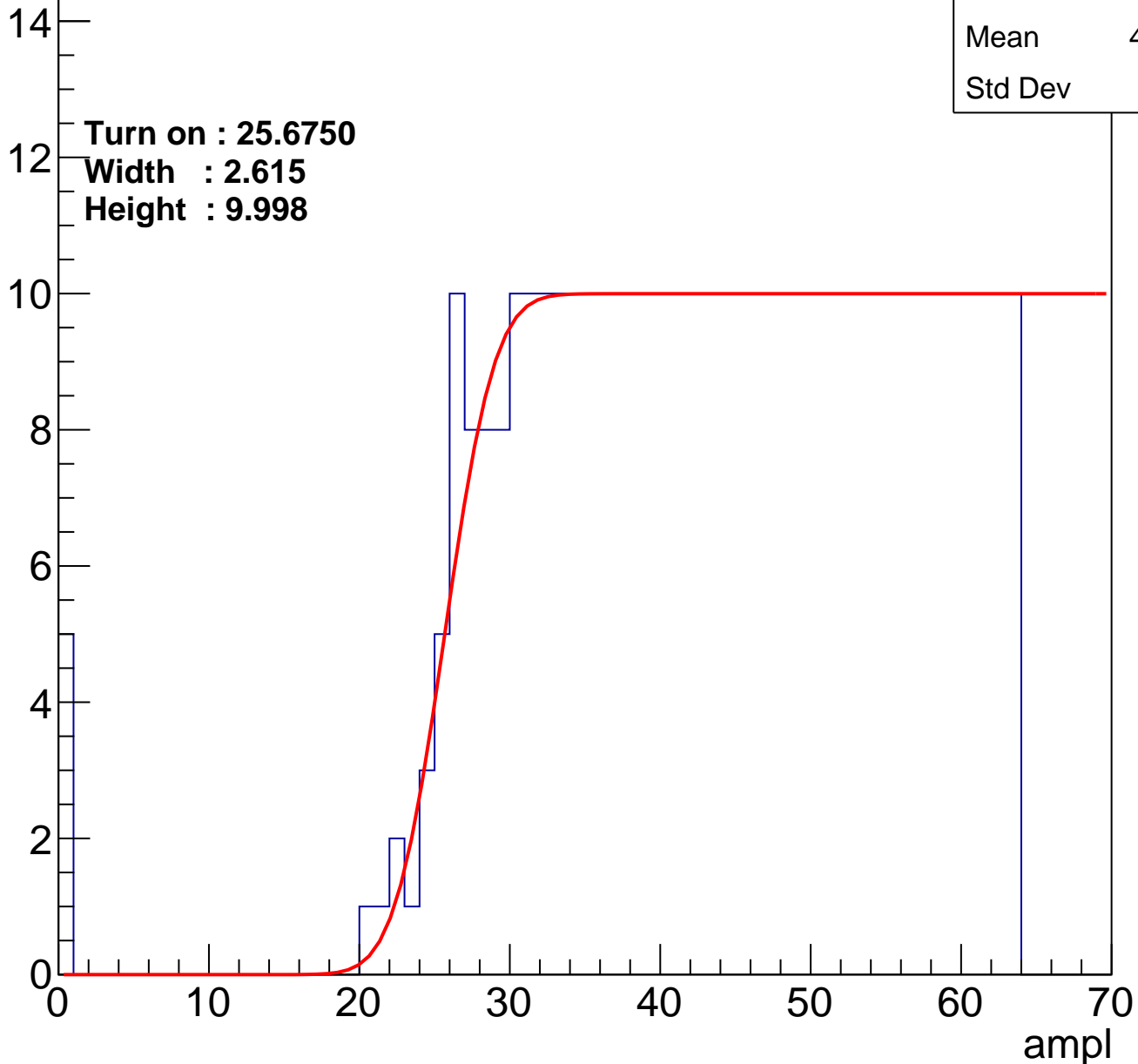
Entries	392
Mean	43.49
Std Dev	12.3

Turn on : 25.6750

Width : 2.615

Height : 9.998

Entry



B1L000S, U12-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.96
Std Dev	10.94

Turn on : 27.6167

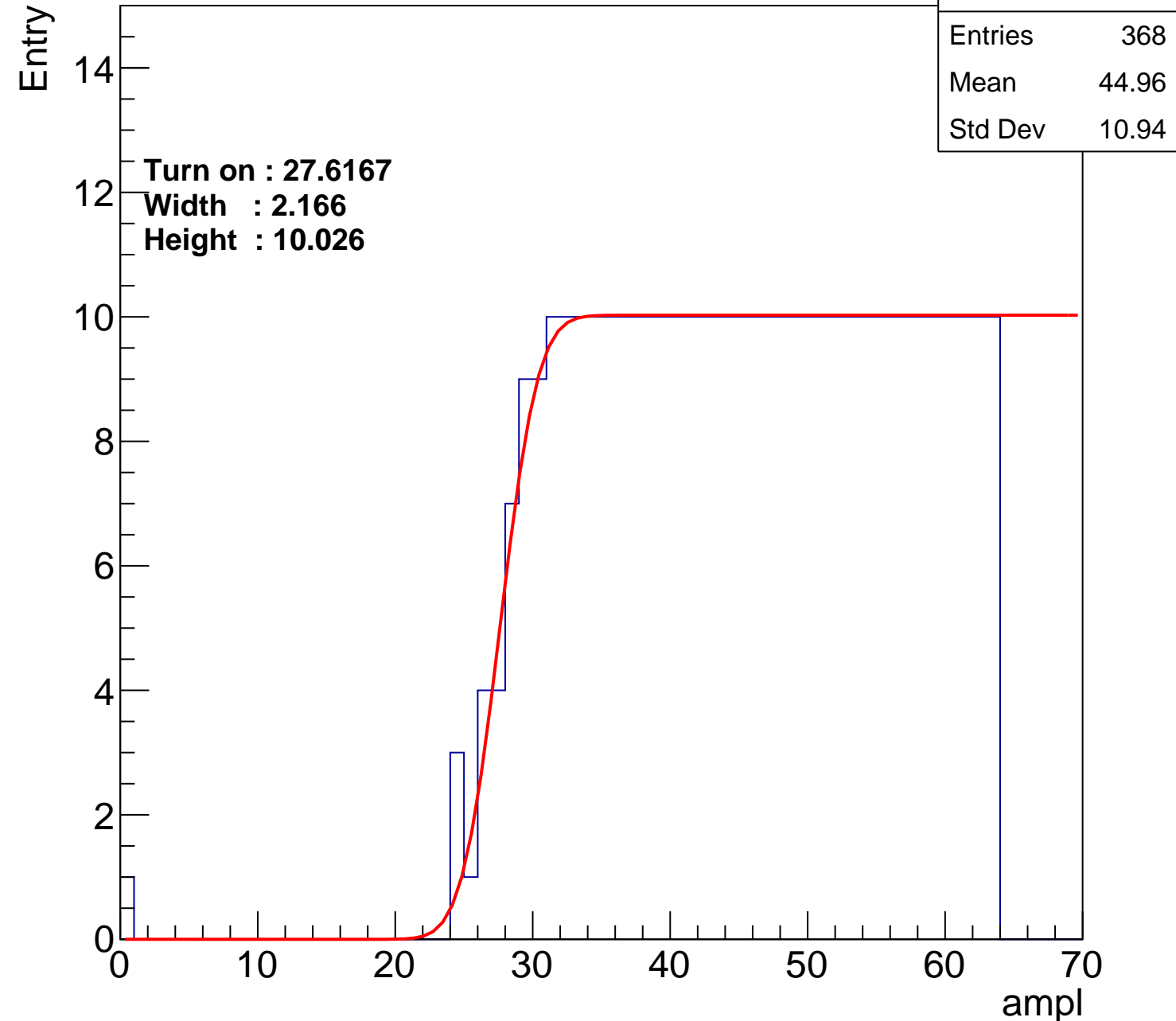
Width : 2.166

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch25

calib_packv5_042523_0143.root, FC#3, port B3

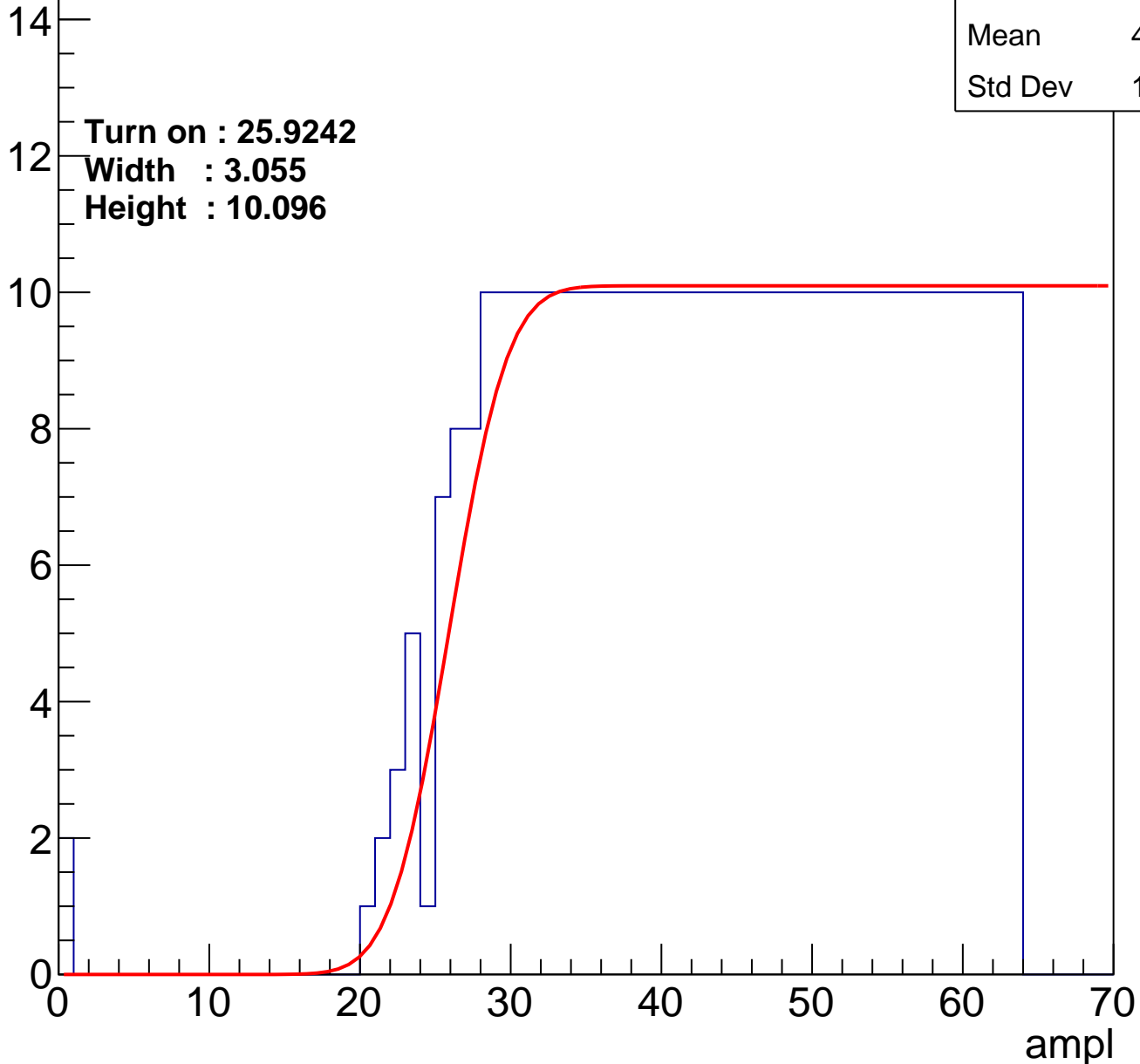
Entries	397
Mean	43.44
Std Dev	11.93

Turn on : 25.9242

Width : 3.055

Height : 10.096

Entry



B1L000S, U12-ch26

calib_packv5_042523_0143.root, FC#3, port B3

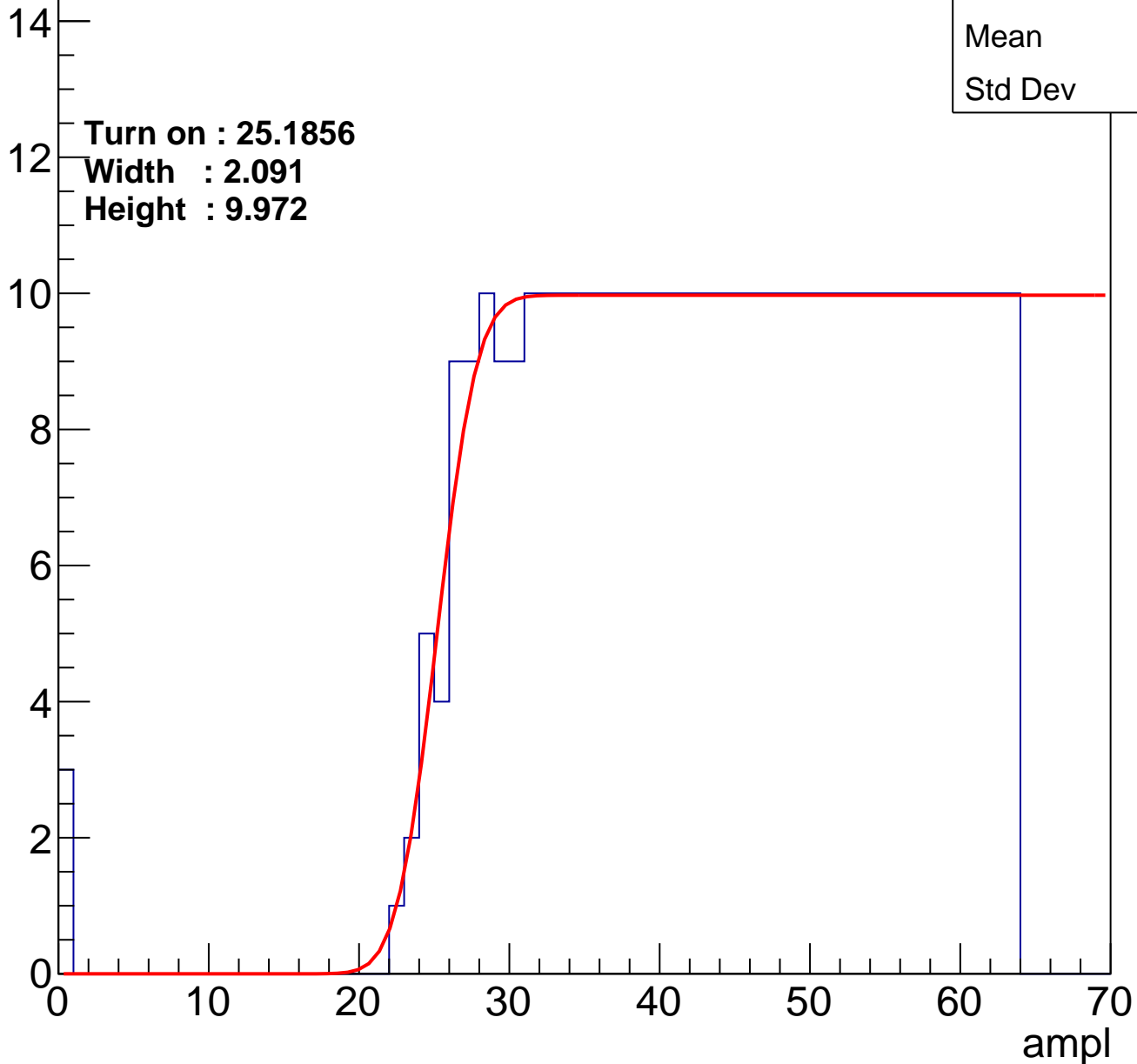
Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.1856

Width : 2.091

Height : 9.972

Entry



B1L000S, U12-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.9546

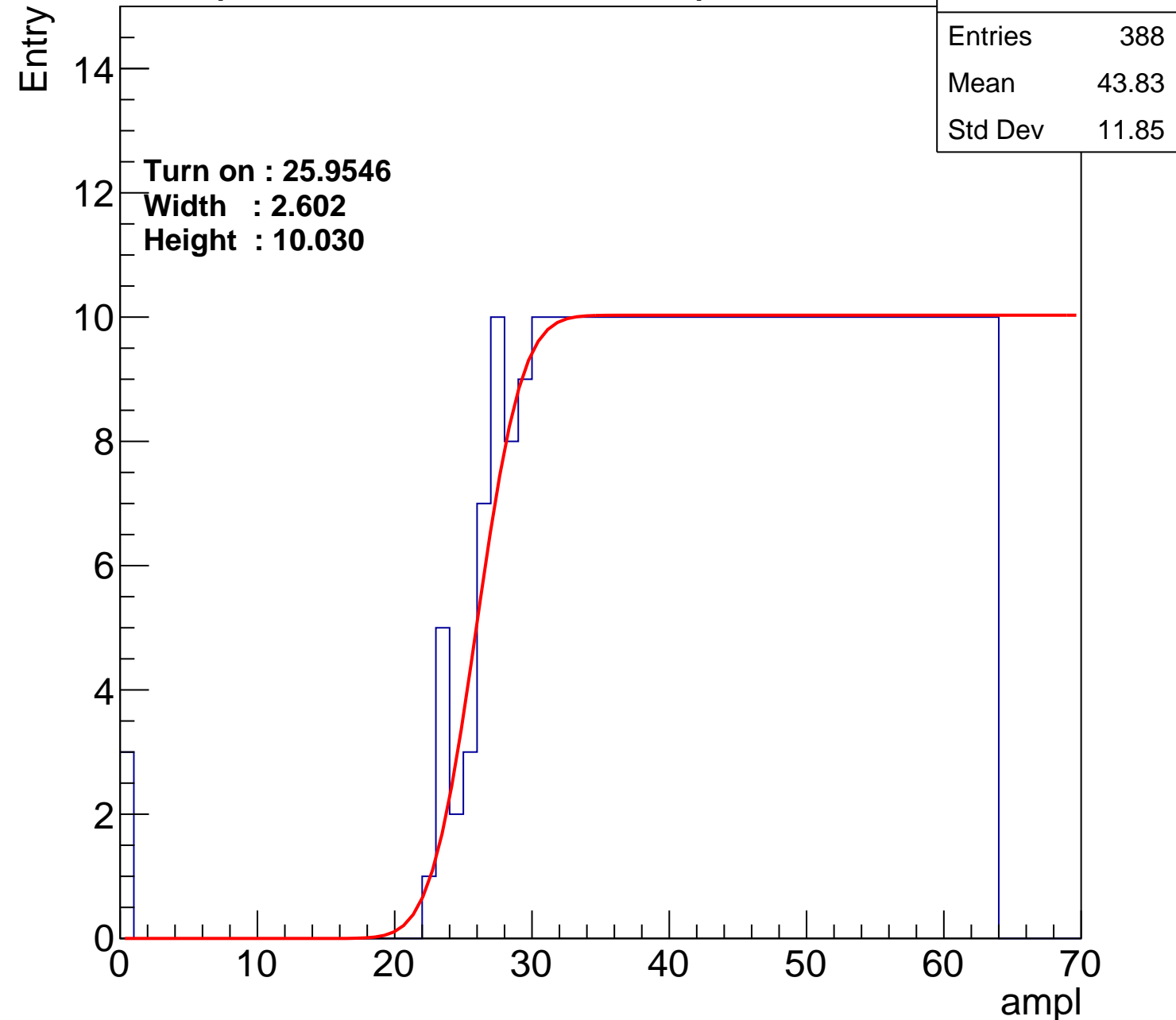
Width : 2.602

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.4486

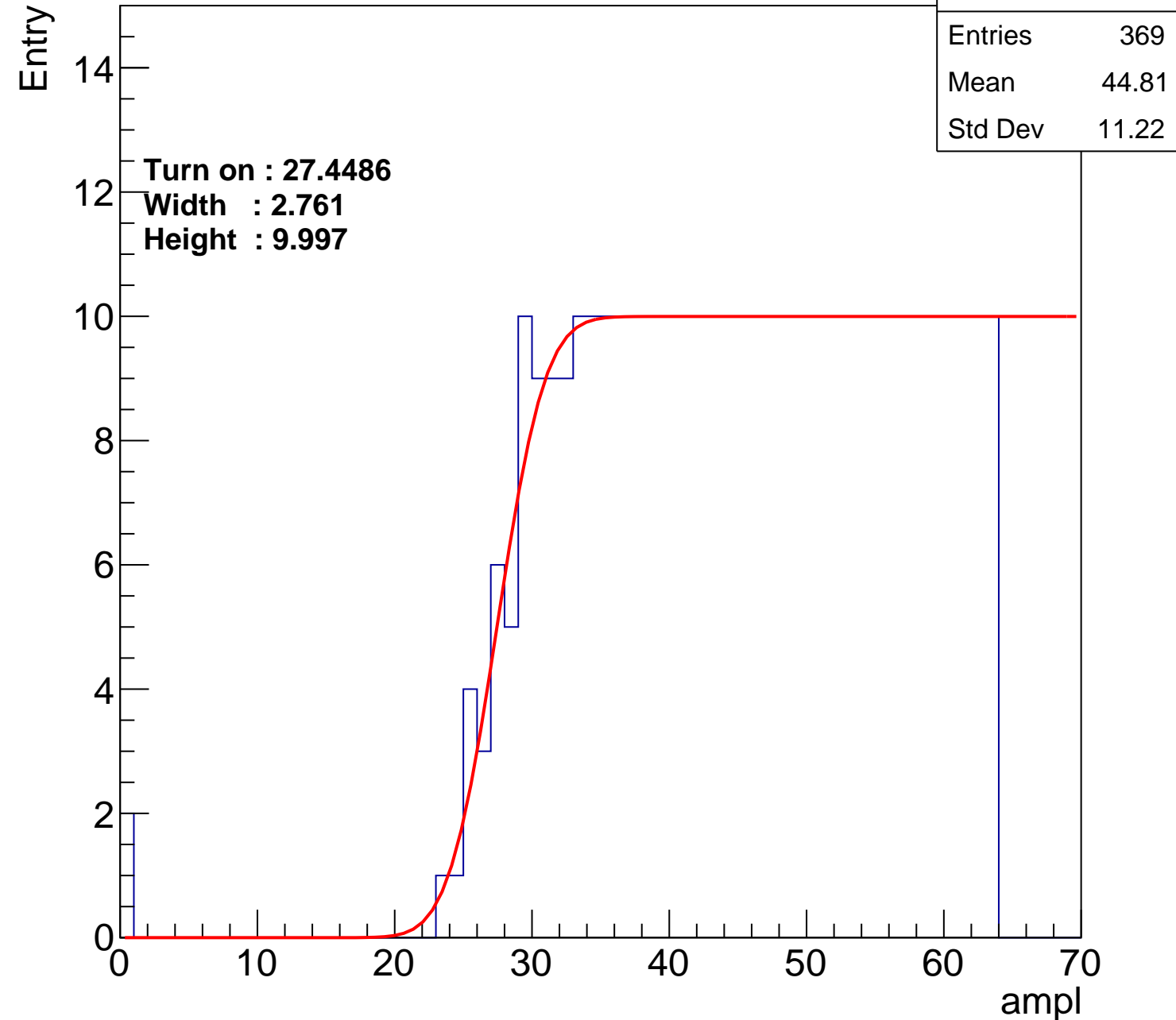
Width : 2.761

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.27
Std Dev	10.77

Turn on : 28.4033

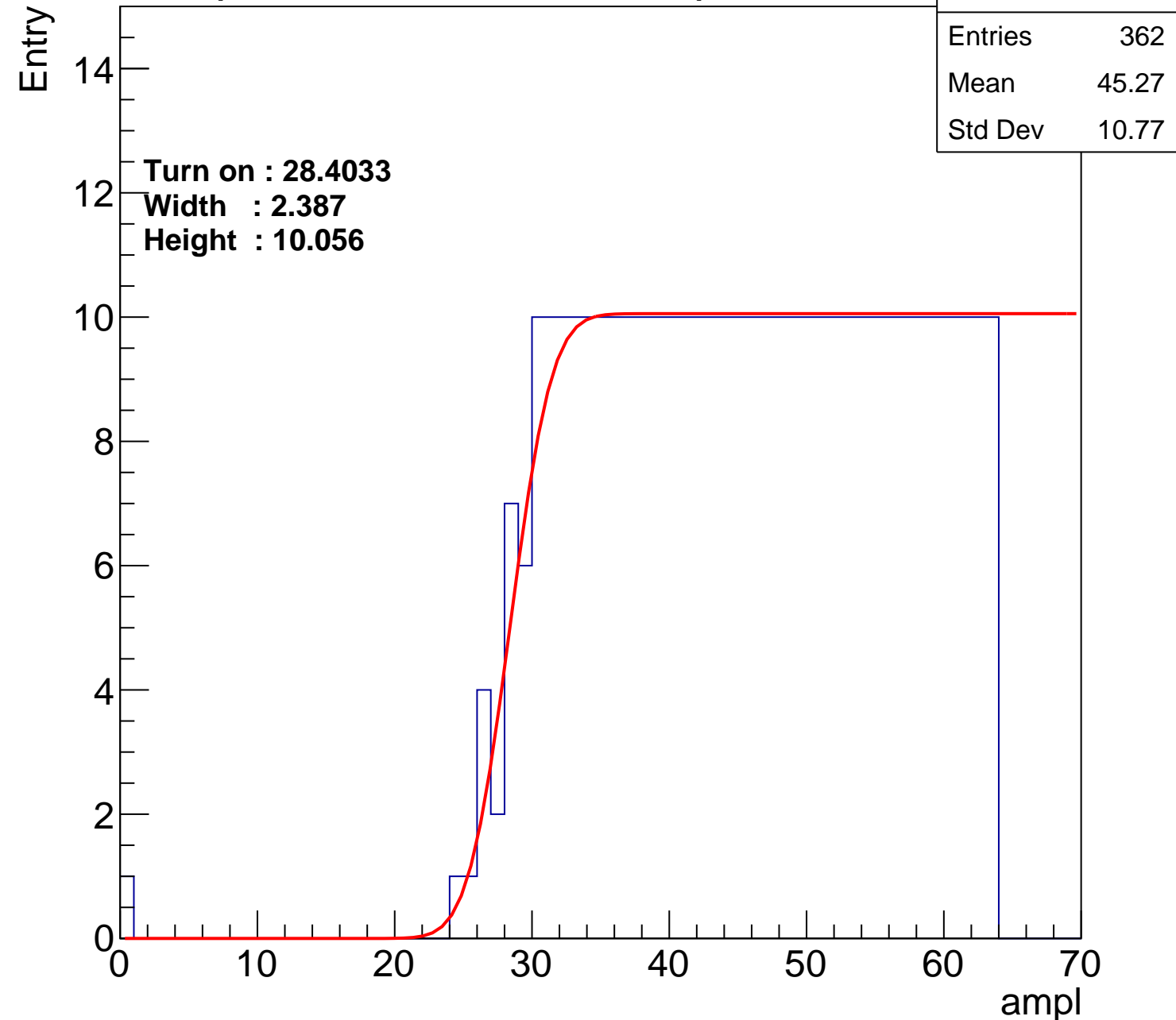
Width : 2.387

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch30

calib_packv5_042523_0143.root, FC#3, port B3

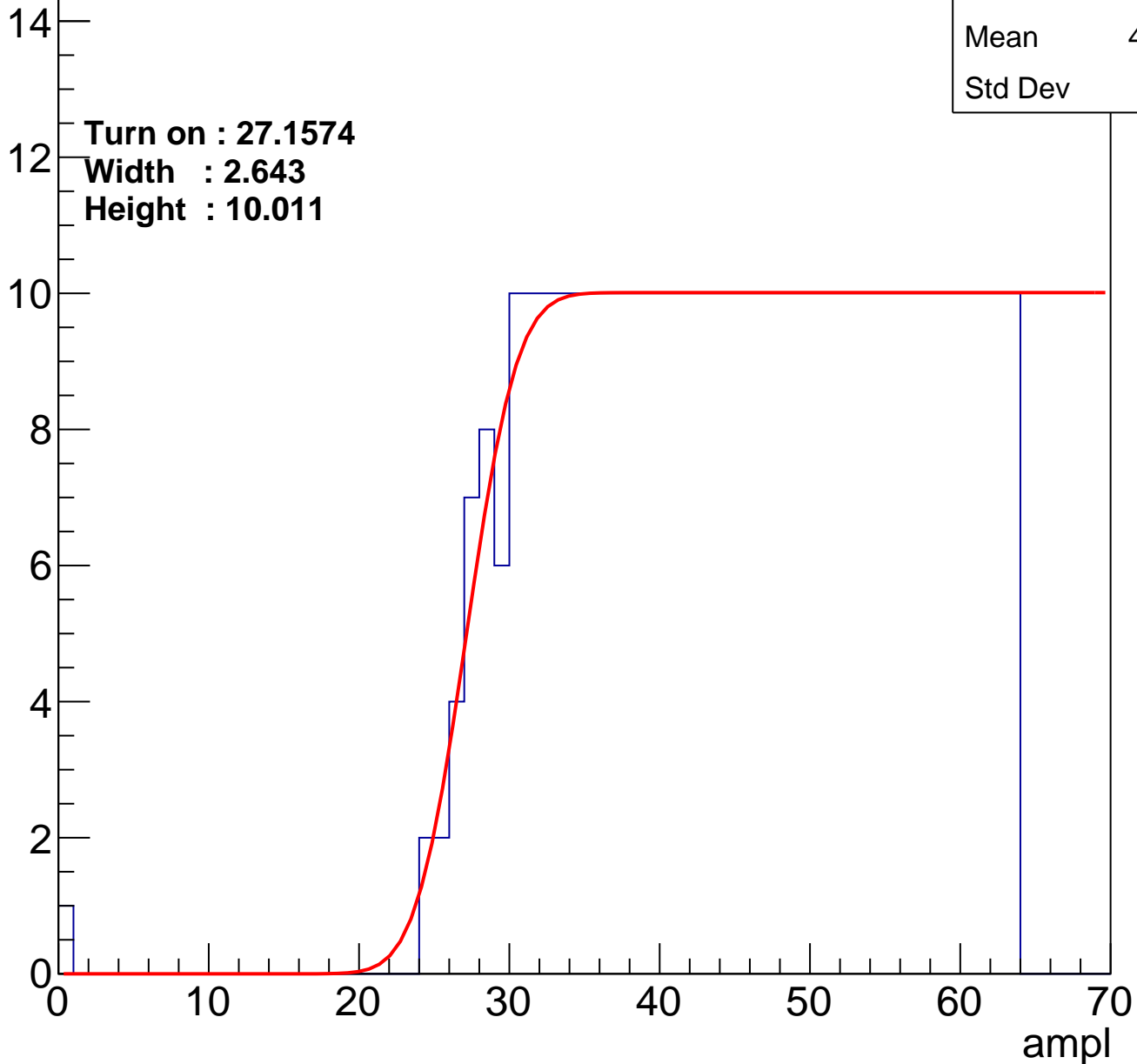
Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.1574

Width : 2.643

Height : 10.011

Entry



B1L000S, U12-ch31

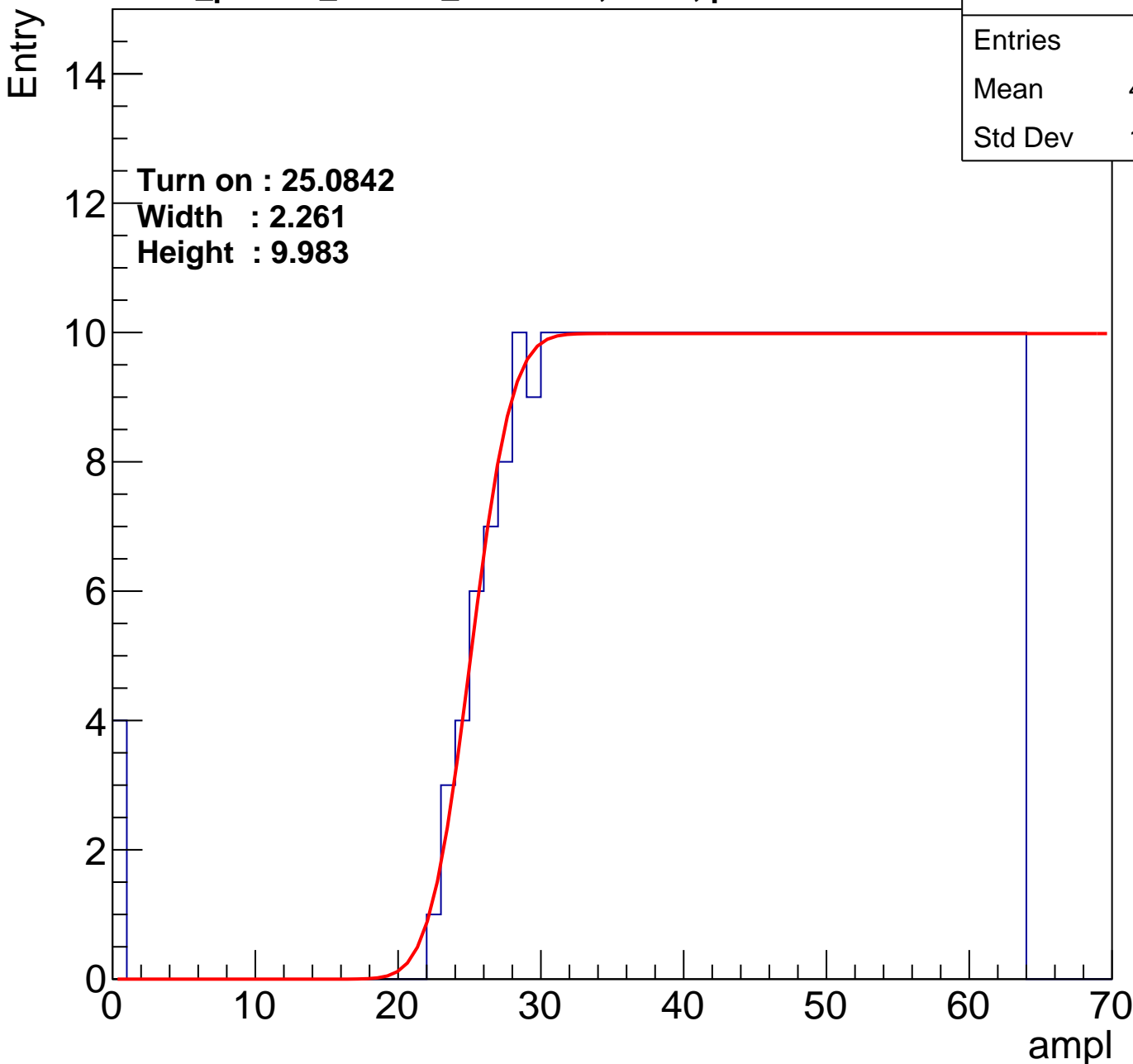
calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.59
Std Dev	12.09

Turn on : 25.0842

Width : 2.261

Height : 9.983



B1L000S, U12-ch32

calib_packv5_042523_0143.root, FC#3, port B3

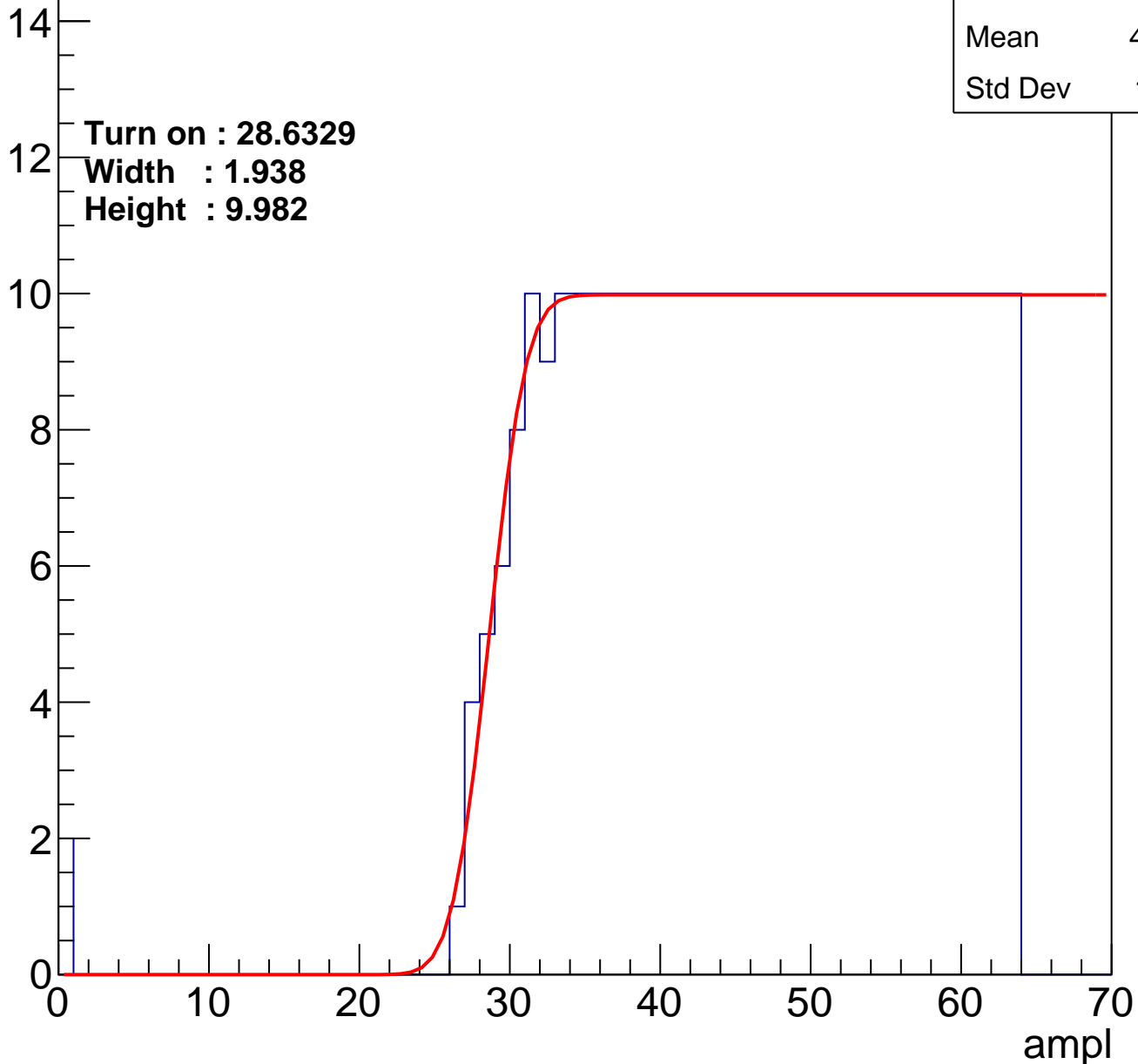
Entries	355
Mean	45.54
Std Dev	10.81

Turn on : 28.6329

Width : 1.938

Height : 9.982

Entry



B1L000S, U12-ch33

calib_packv5_042523_0143.root, FC#3, port B3

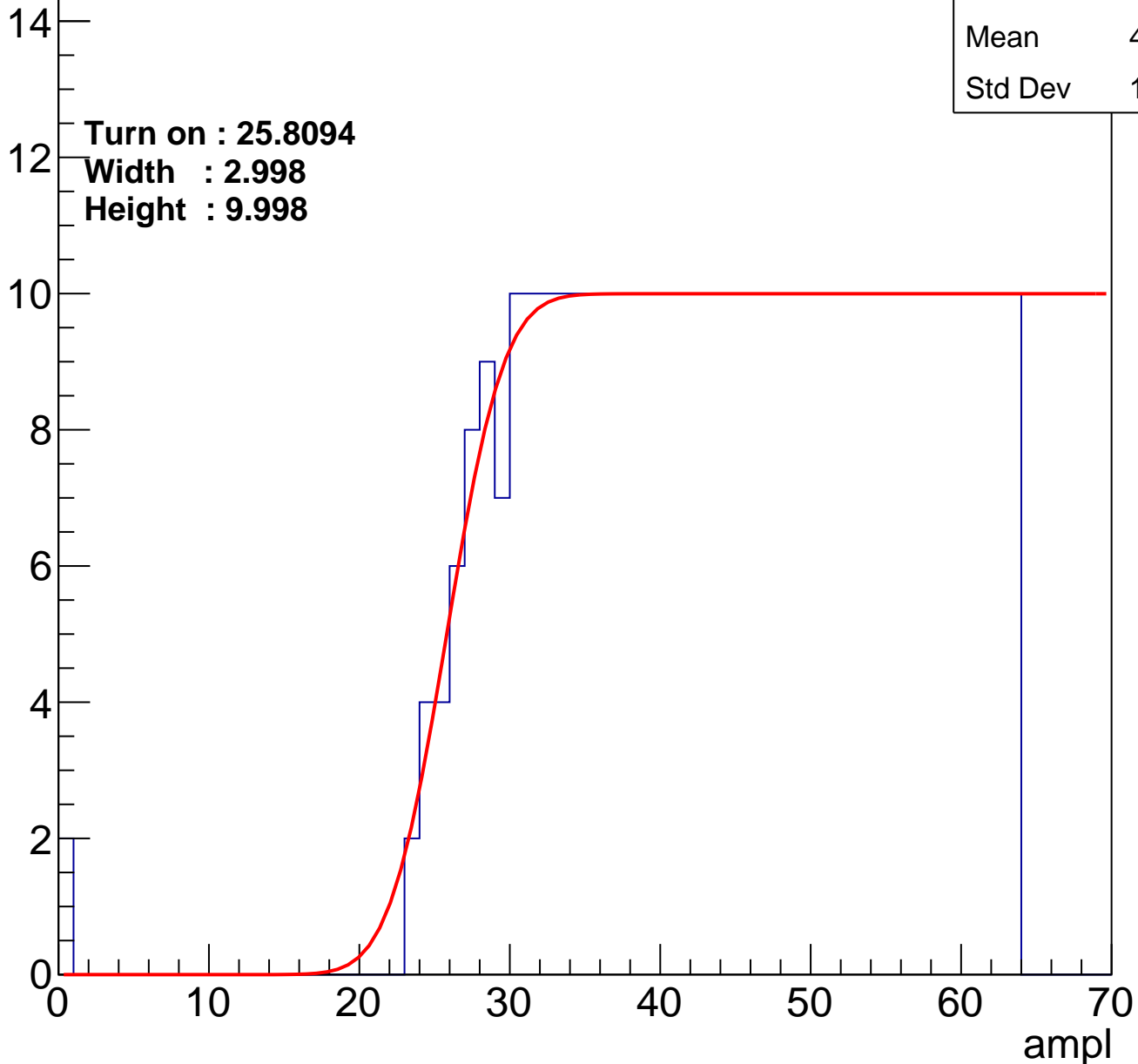
Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 25.8094

Width : 2.998

Height : 9.998

Entry



B1L000S, U12-ch34

calib_packv5_042523_0143.root, FC#3, port B3

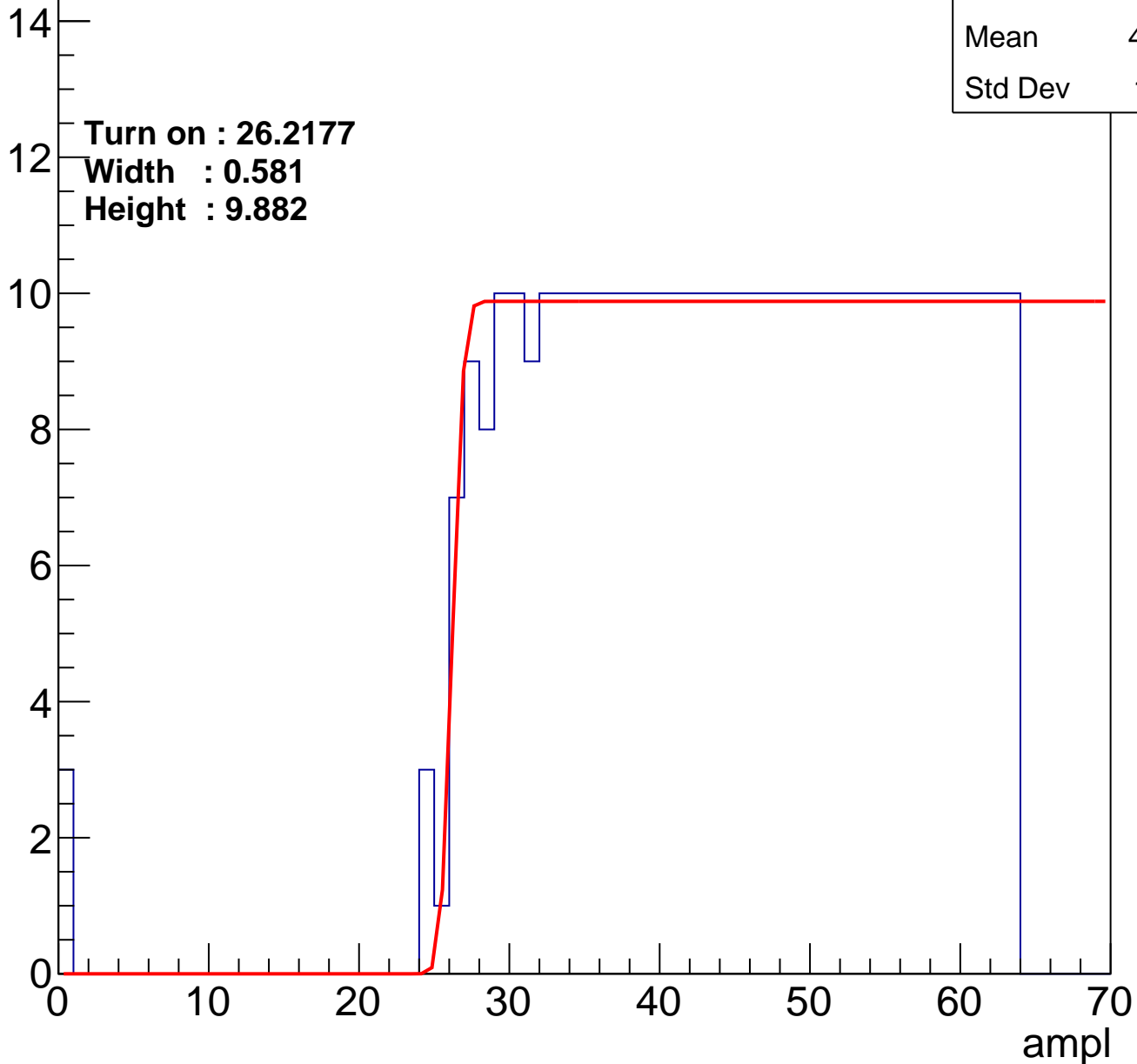
Entries	380
Mean	44.25
Std Dev	11.61

Turn on : 26.2177

Width : 0.581

Height : 9.882

Entry



B1L000S, U12-ch35

calib_packv5_042523_0143.root, FC#3, port B3

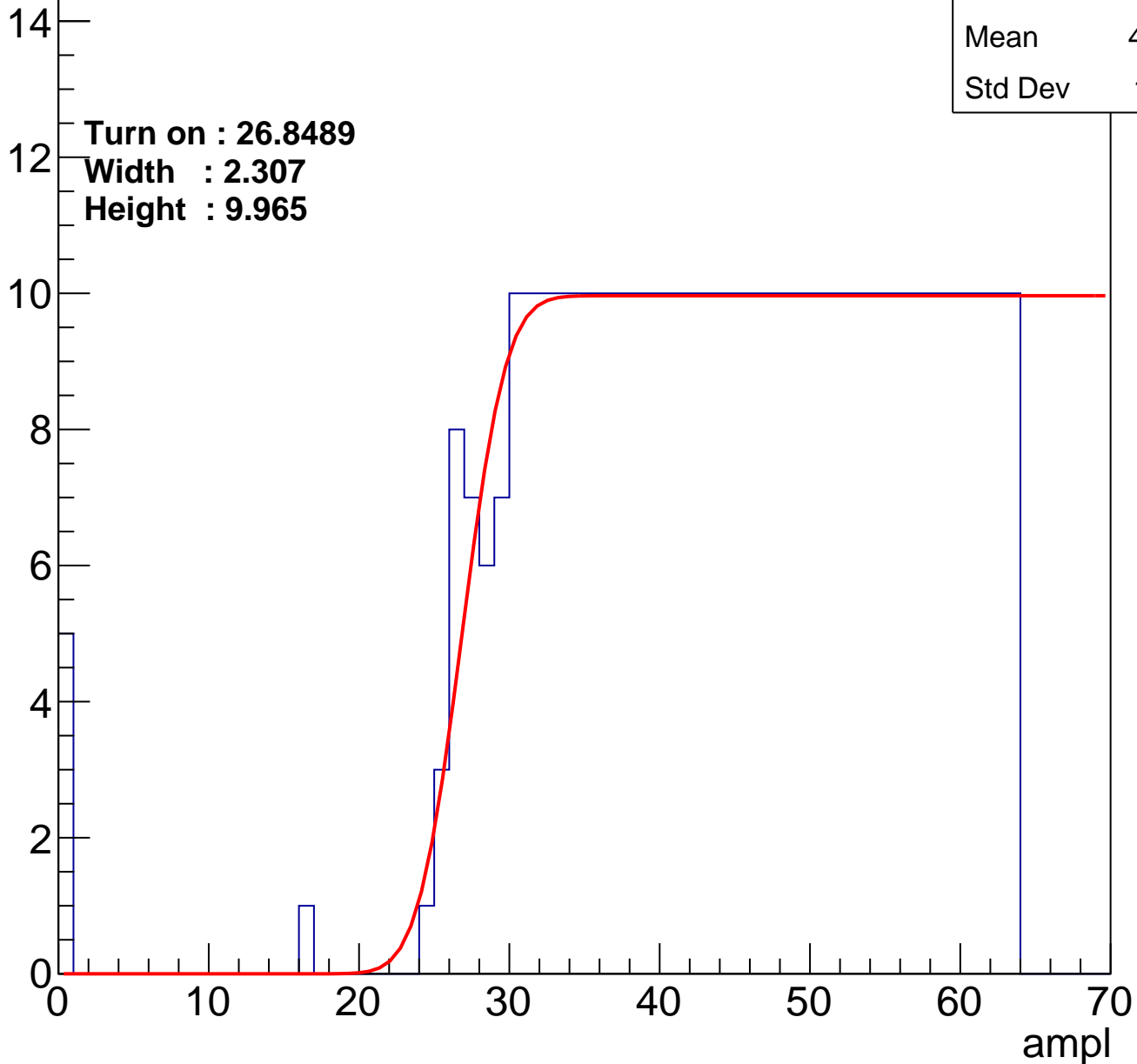
Entries	378
Mean	44.16
Std Dev	12.01

Turn on : 26.8489

Width : 2.307

Height : 9.965

Entry



B1L000S, U12-ch36

calib_packv5_042523_0143.root, FC#3, port B3

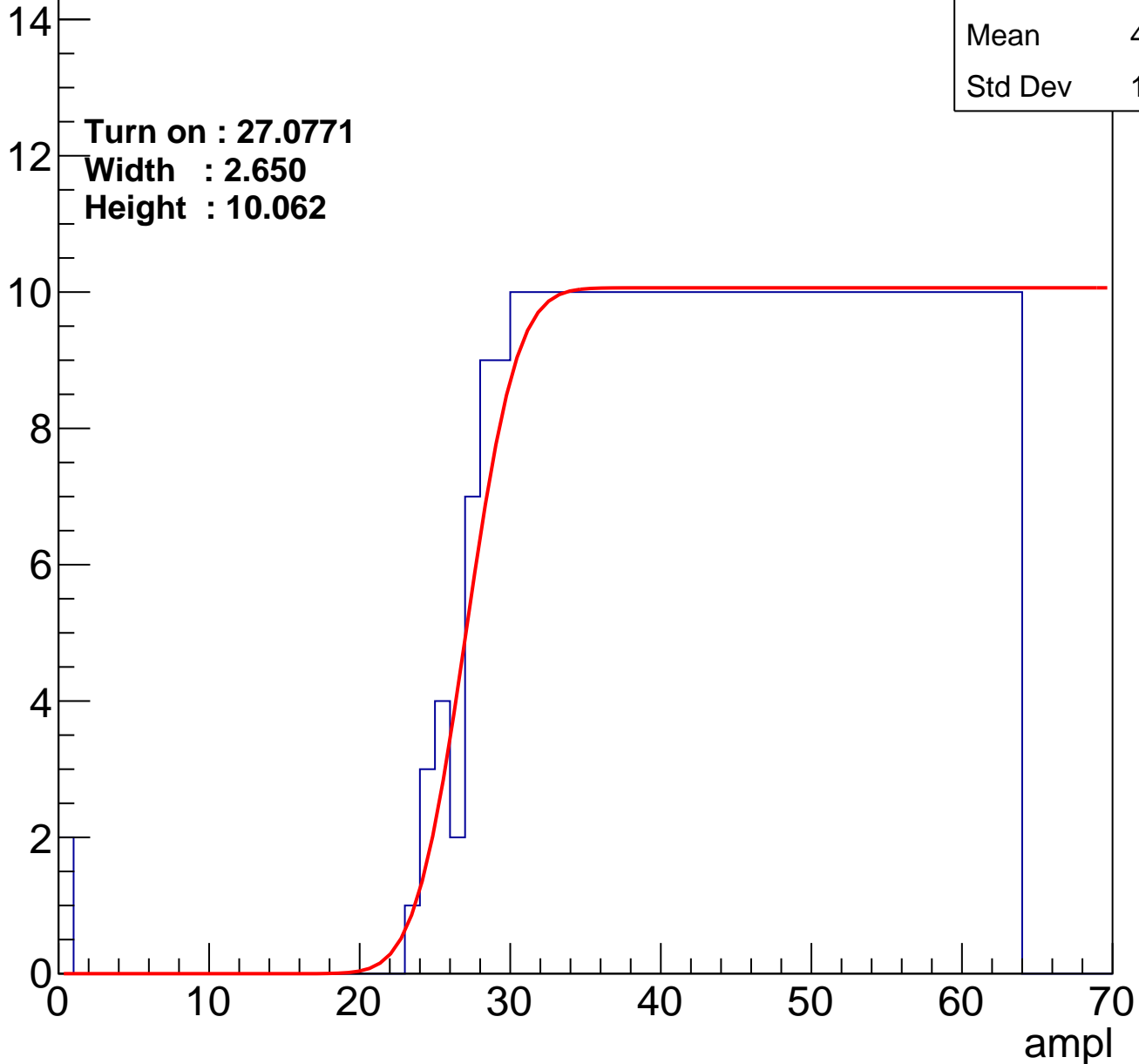
Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 27.0771

Width : 2.650

Height : 10.062

Entry



B1L000S, U12-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.06
Std Dev	10.89

Turn on : 27.8408

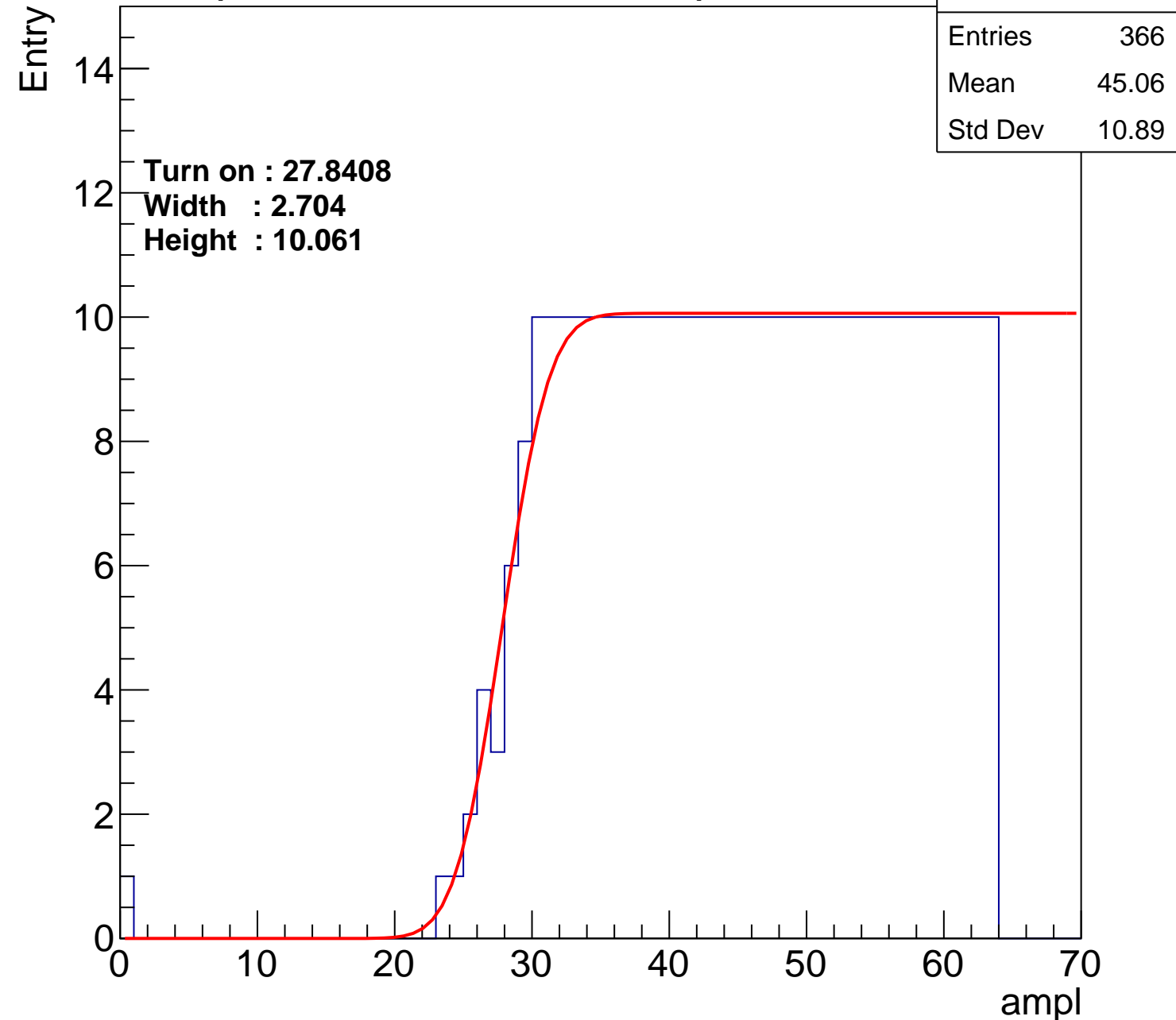
Width : 2.704

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.47
Std Dev	12.05

Turn on : 24.9604

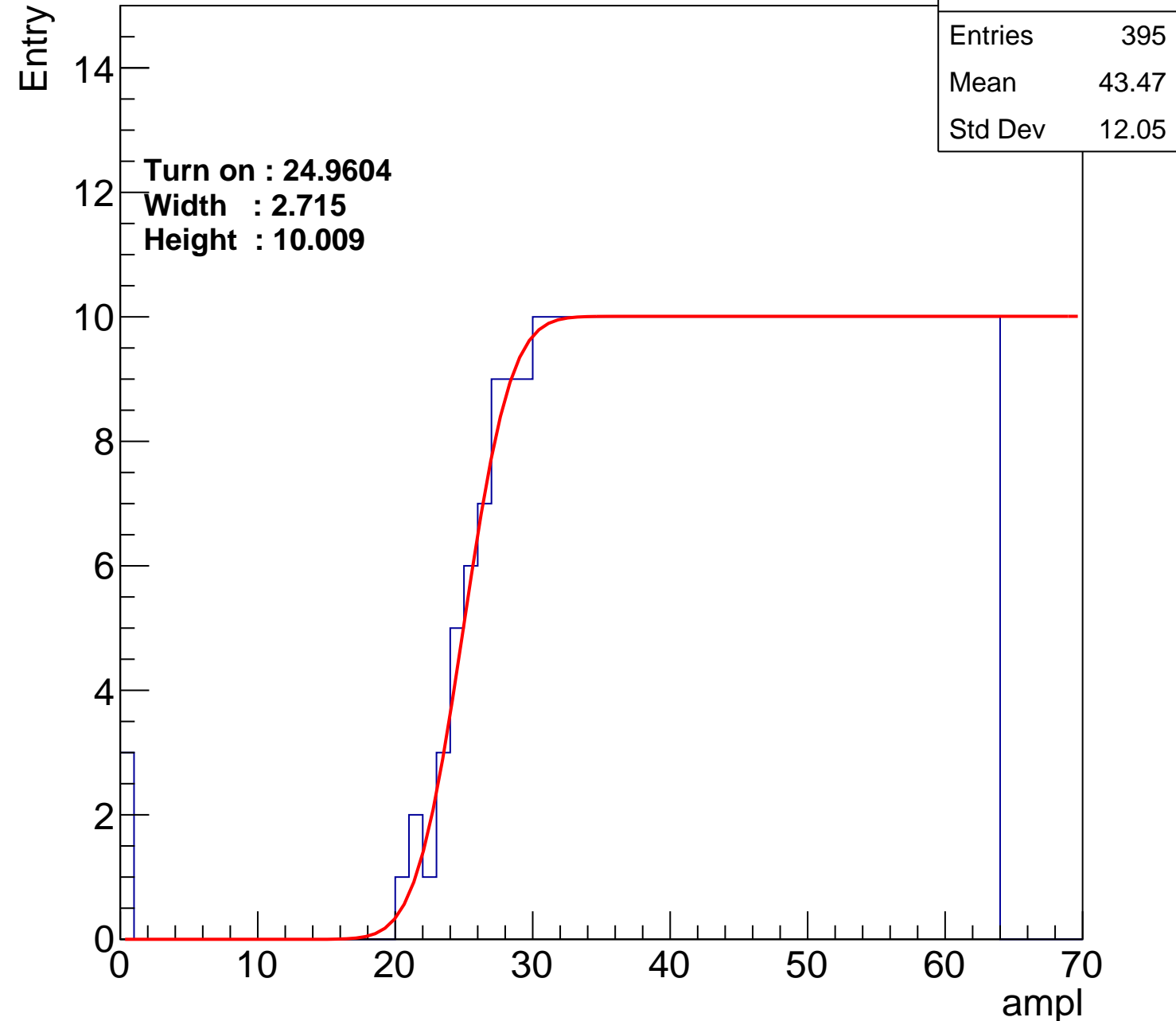
Width : 2.715

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.7100

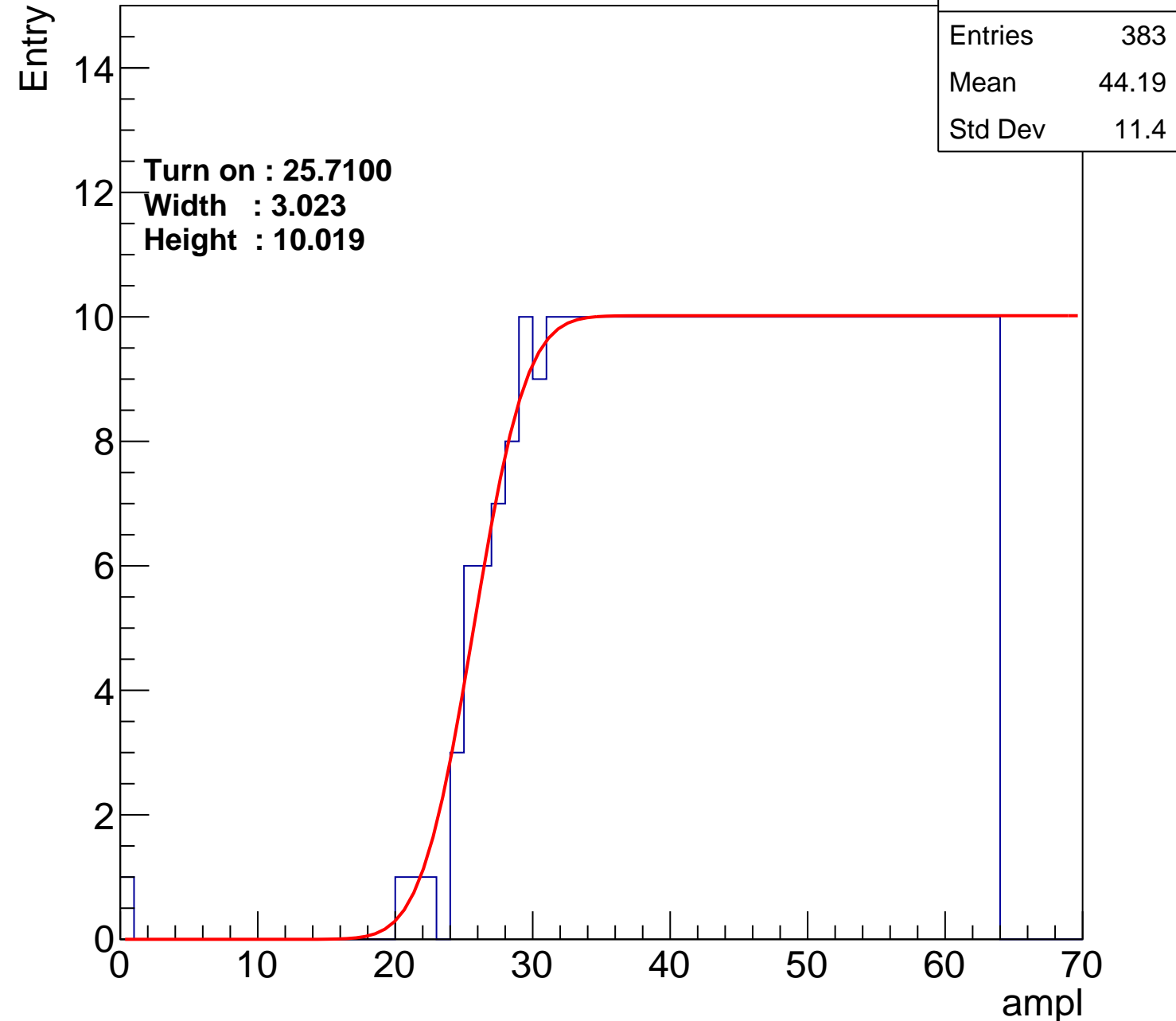
Width : 3.023

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch40

calib_packv5_042523_0143.root, FC#3, port B3

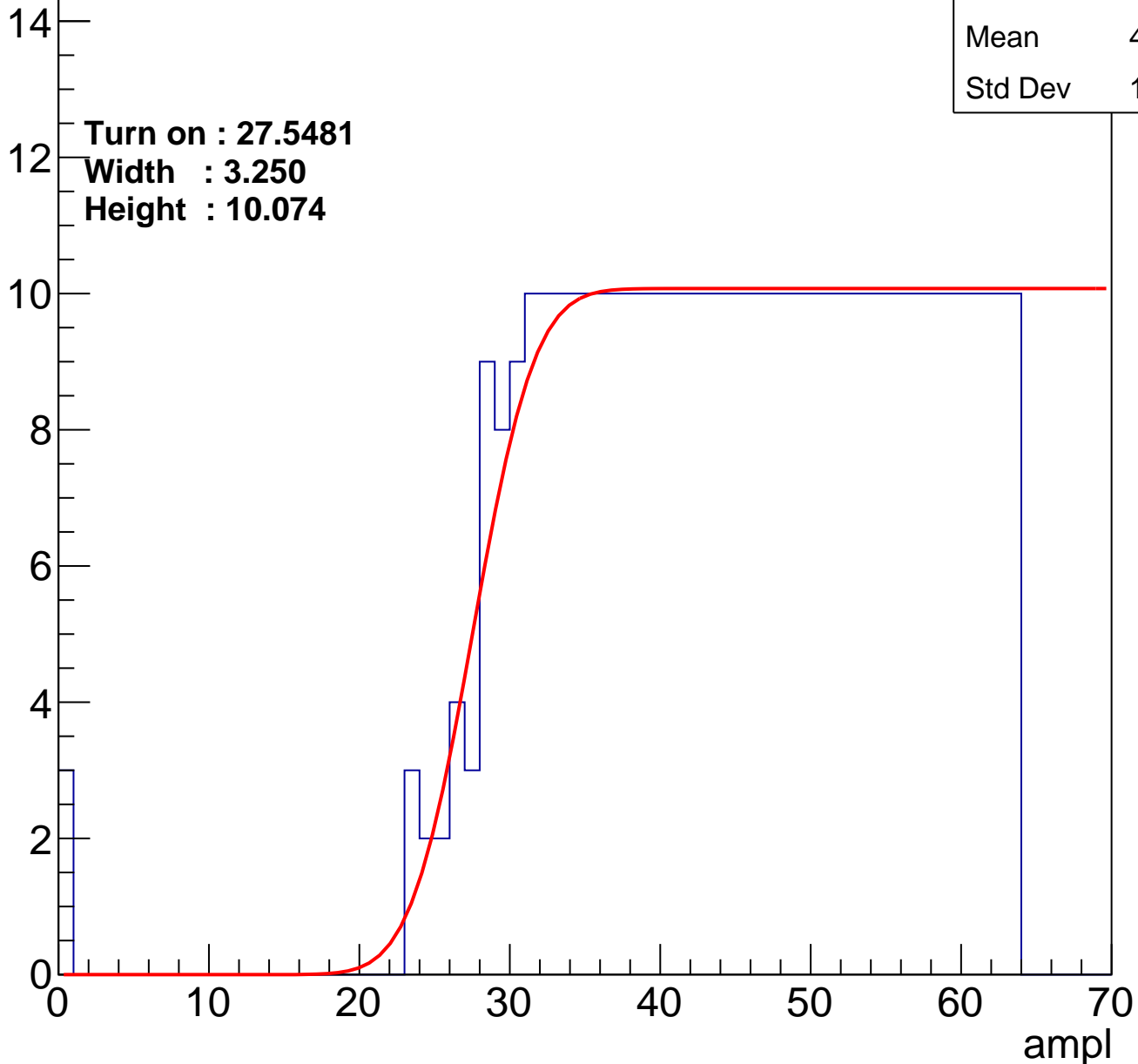
Entries	373
Mean	44.55
Std Dev	11.52

Turn on : 27.5481

Width : 3.250

Height : 10.074

Entry



B1L000S, U12-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.23
Std Dev	10.82

Turn on : 28.0523

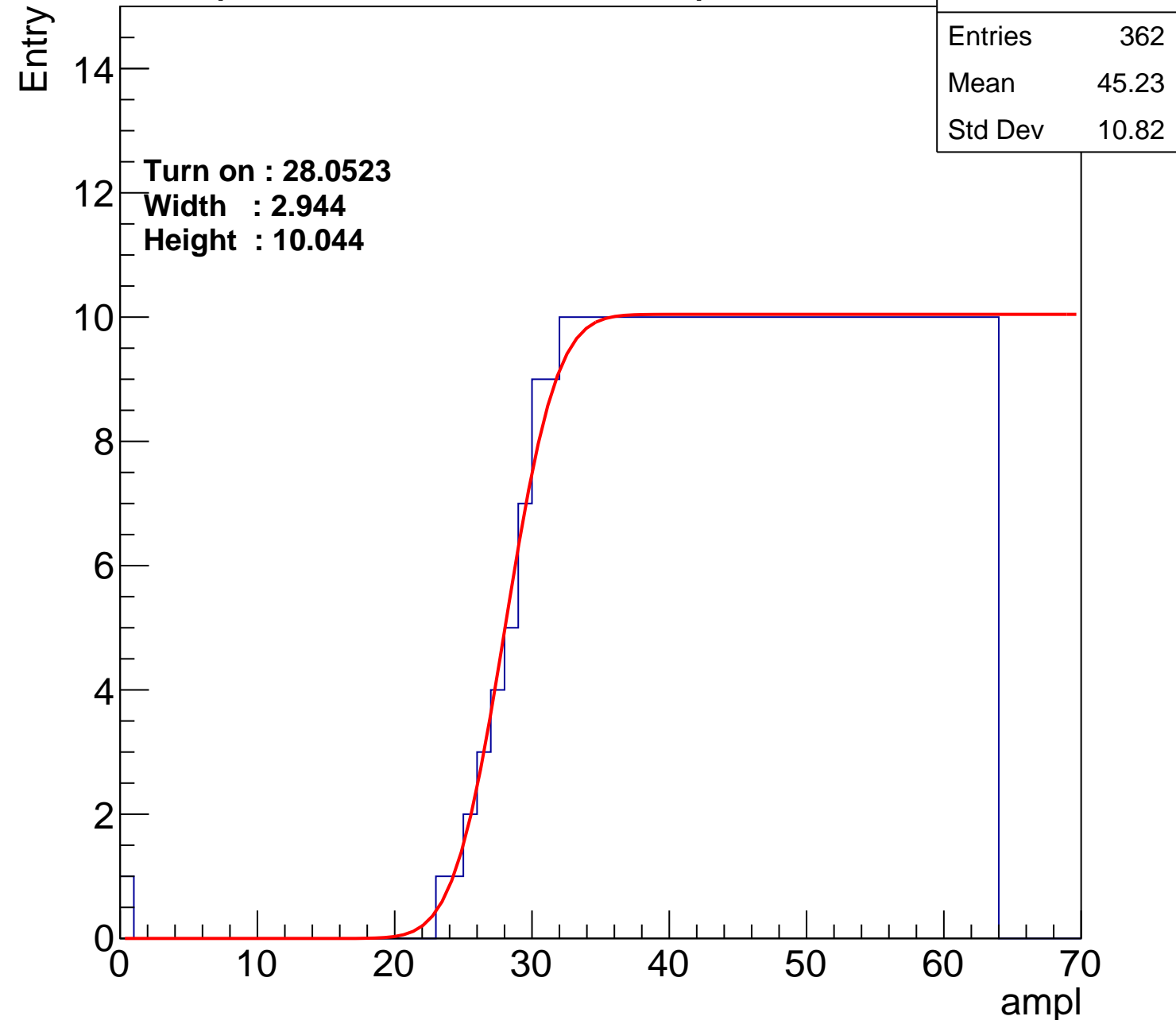
Width : 2.944

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.39
Std Dev	10.73

Turn on : 27.9829

Width : 3.680

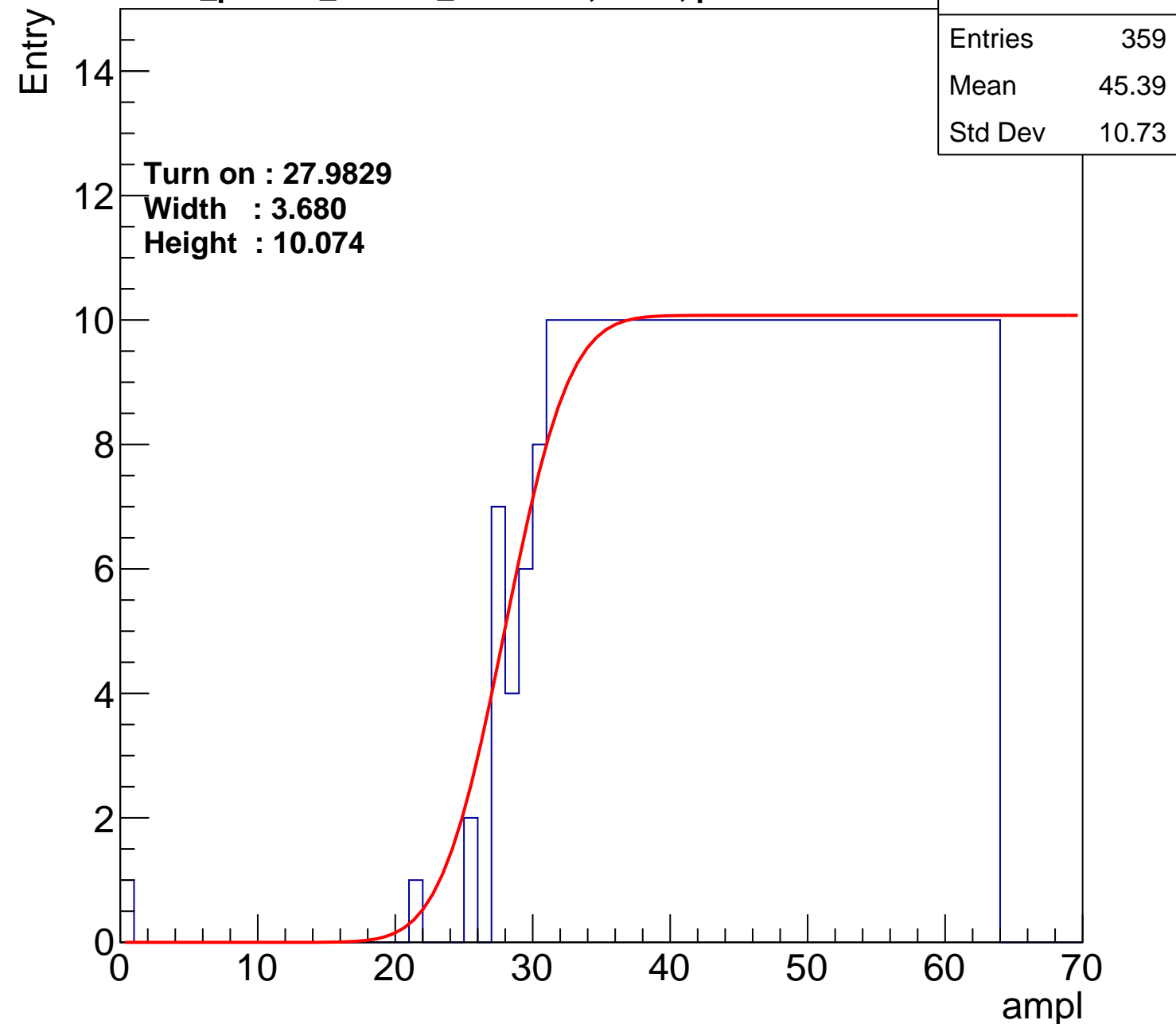
Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U12-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.13
Std Dev	11.7

Turn on : 26.8922

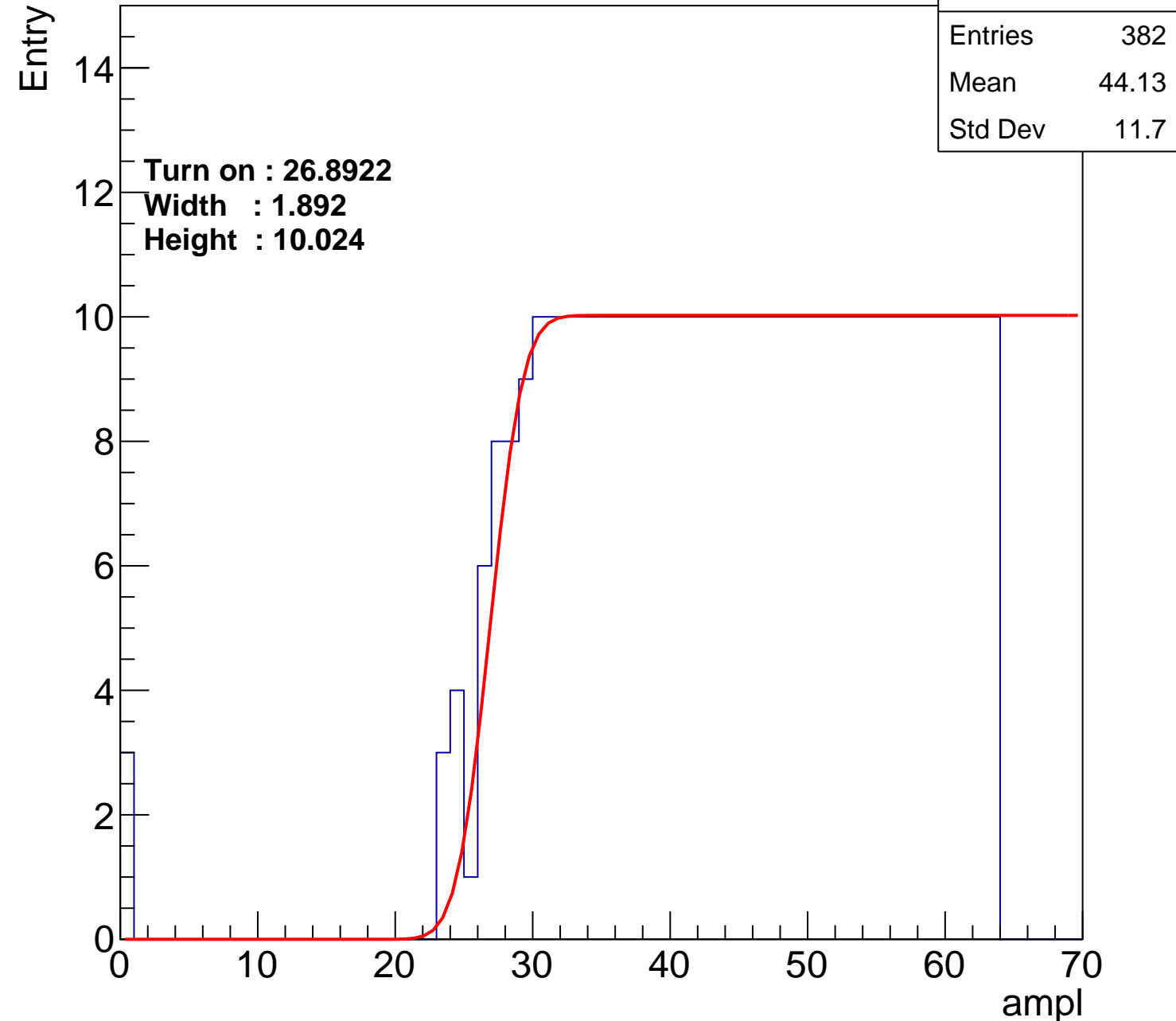
Width : 1.892

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch44

calib_packv5_042523_0143.root, FC#3, port B3

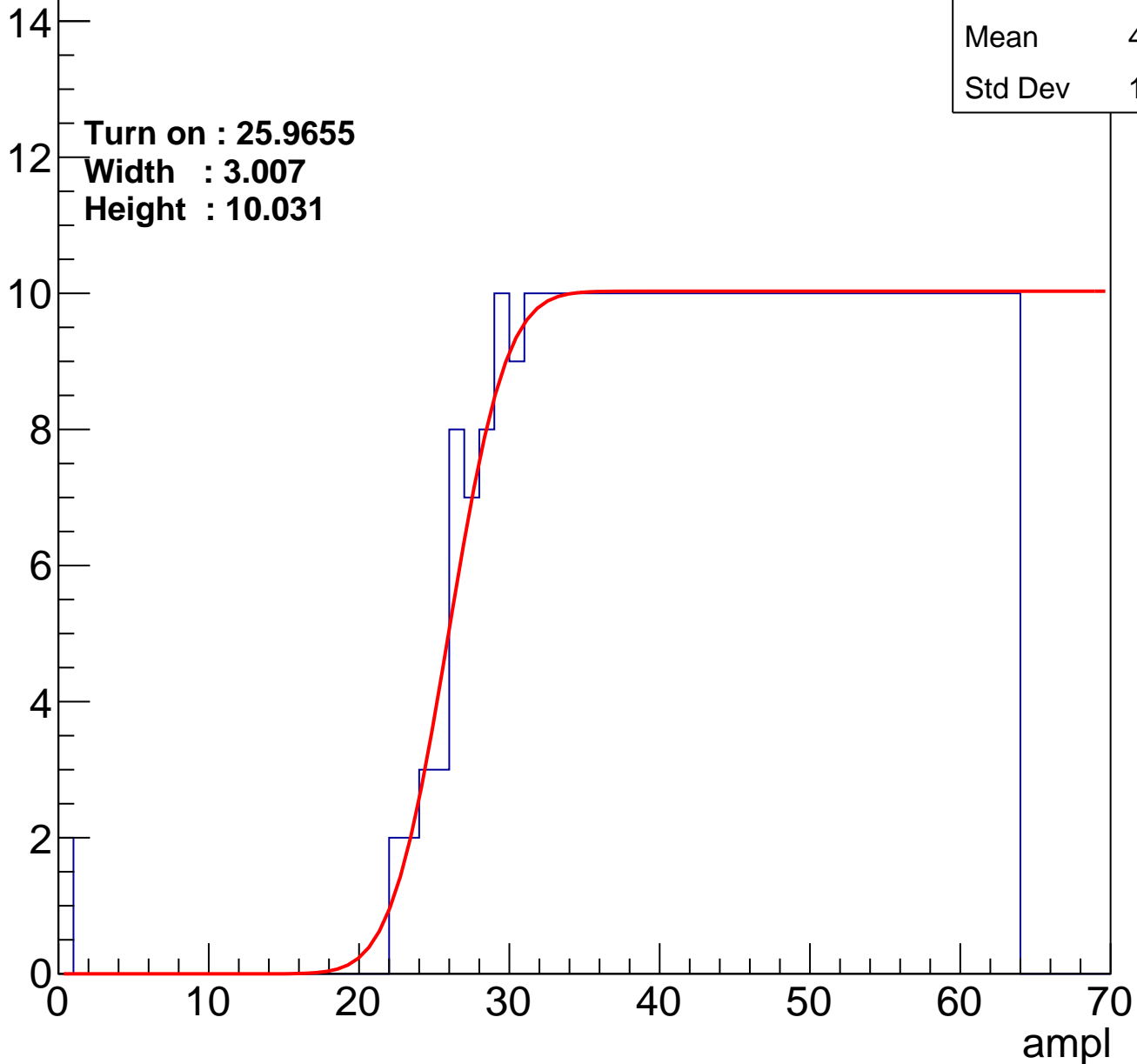
Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.9655

Width : 3.007

Height : 10.031

Entry



B1L000S, U12-ch45

calib_packv5_042523_0143.root, FC#3, port B3

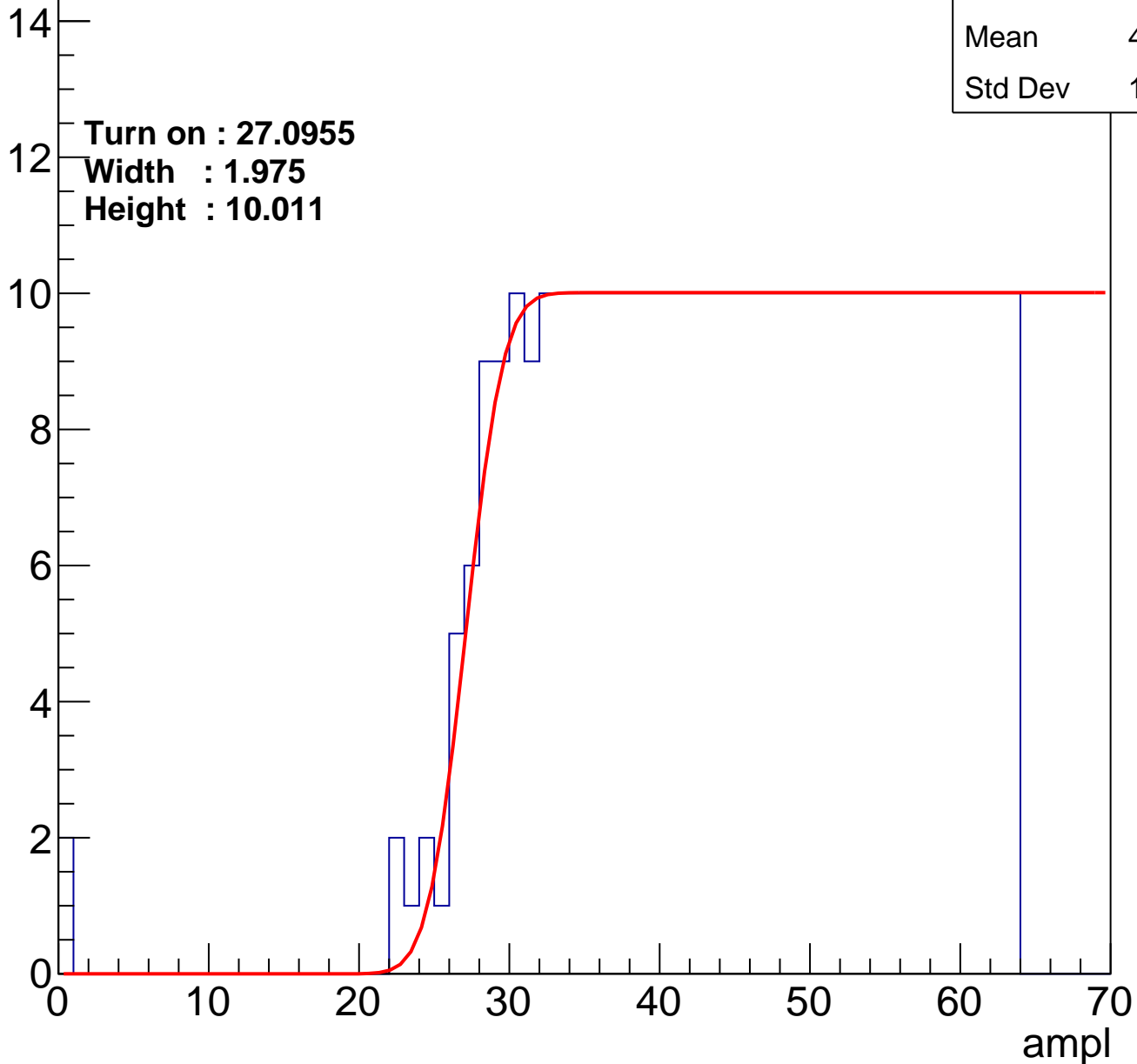
Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 27.0955

Width : 1.975

Height : 10.011

Entry



B1L000S, U12-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.89
Std Dev	11.2

Turn on : 27.7129

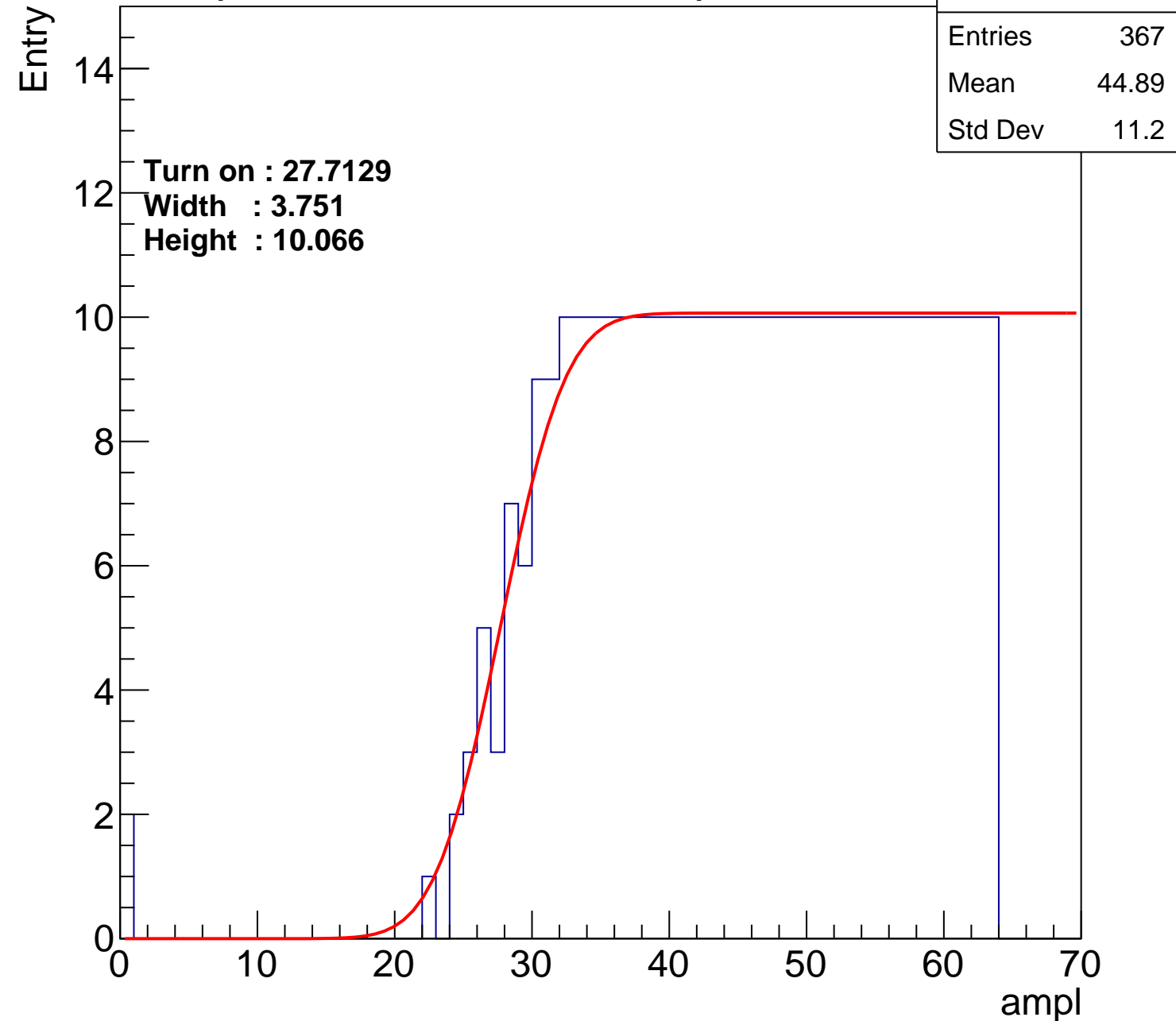
Width : 3.751

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	351
Mean	45.6
Std Dev	11.02

Turn on : 30.1302

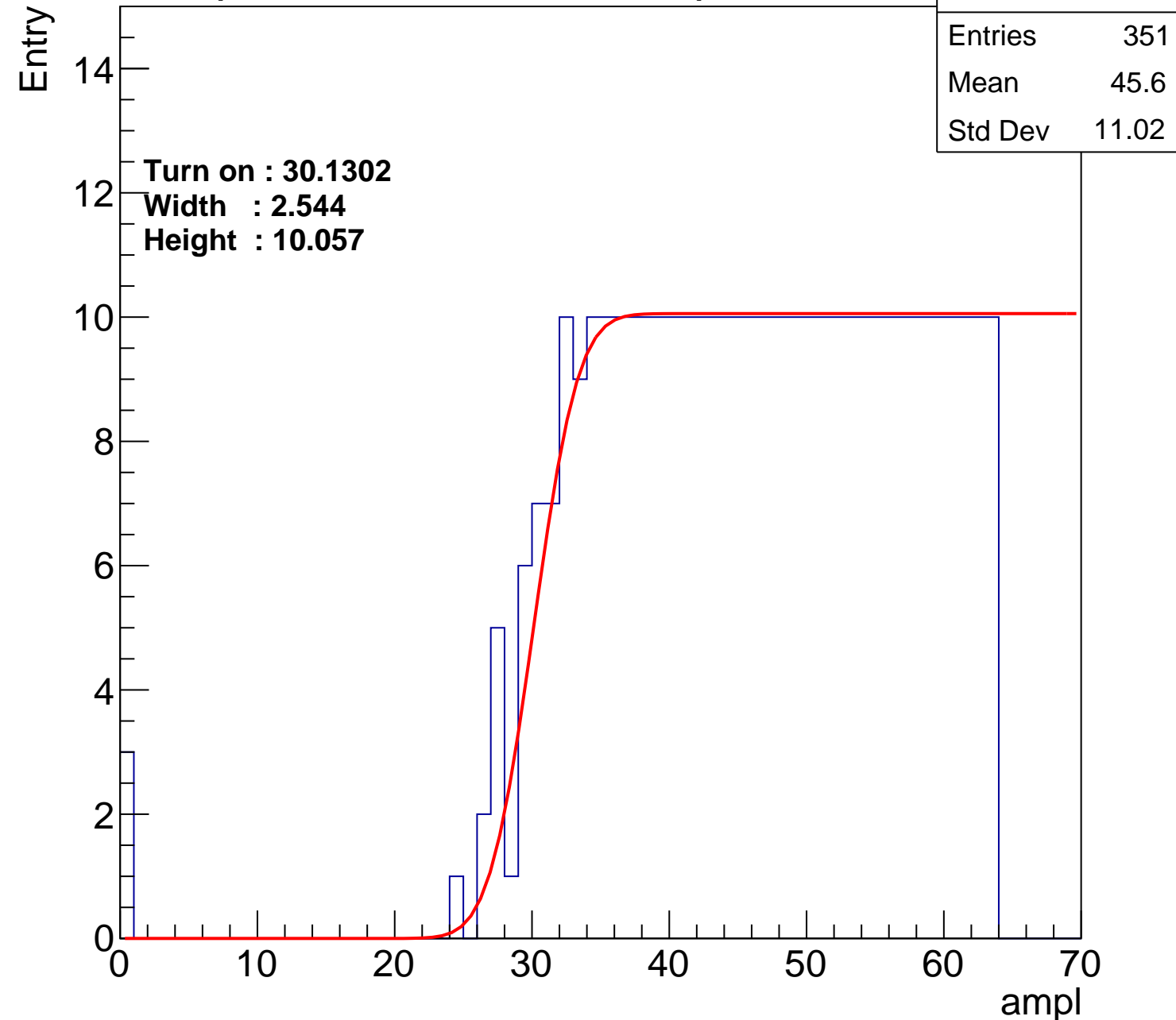
Width : 2.544

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.0572

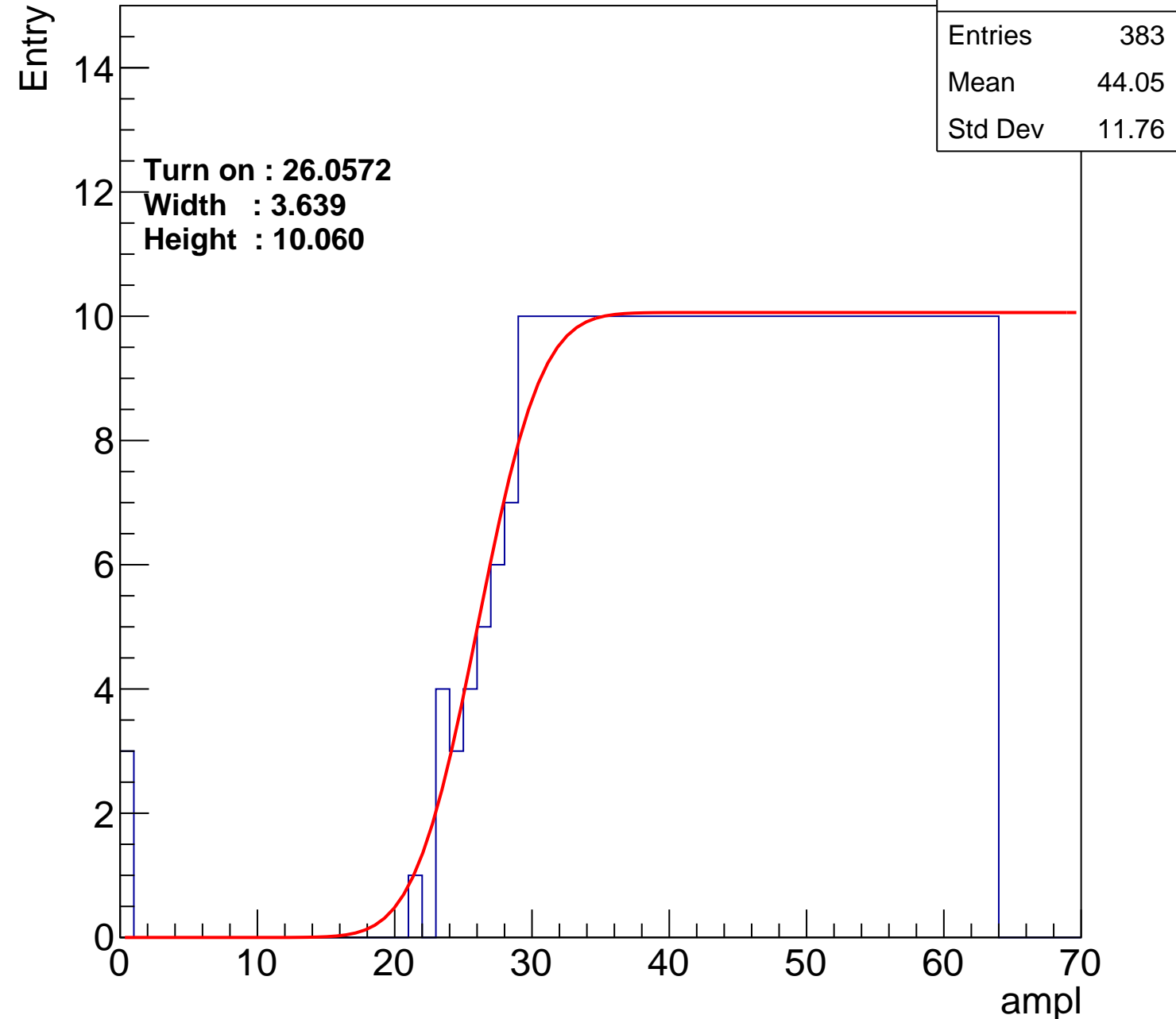
Width : 3.639

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch49

calib_packv5_042523_0143.root, FC#3, port B3

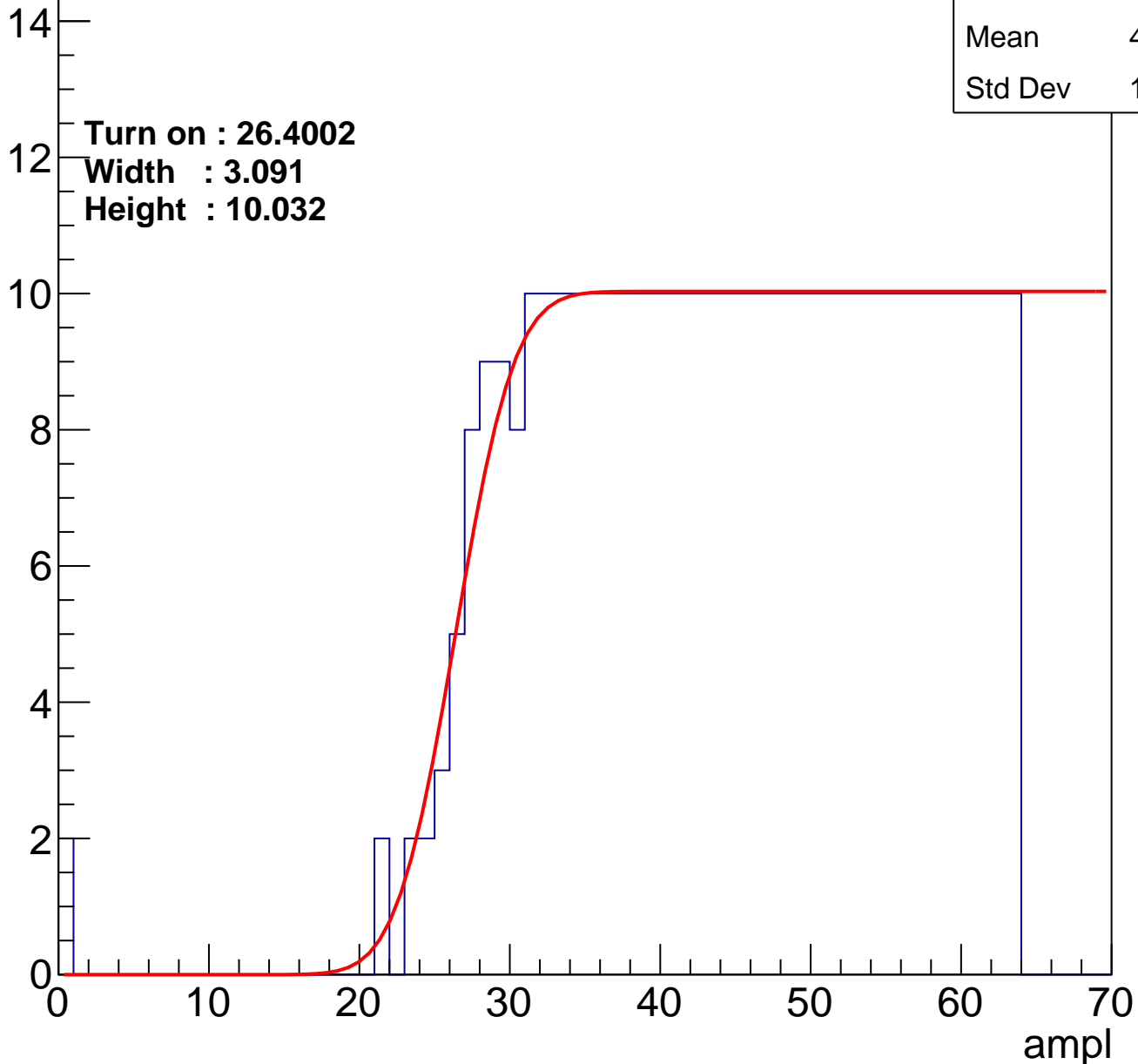
Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.4002

Width : 3.091

Height : 10.032

Entry



B1L000S, U12-ch50

calib_packv5_042523_0143.root, FC#3, port B3

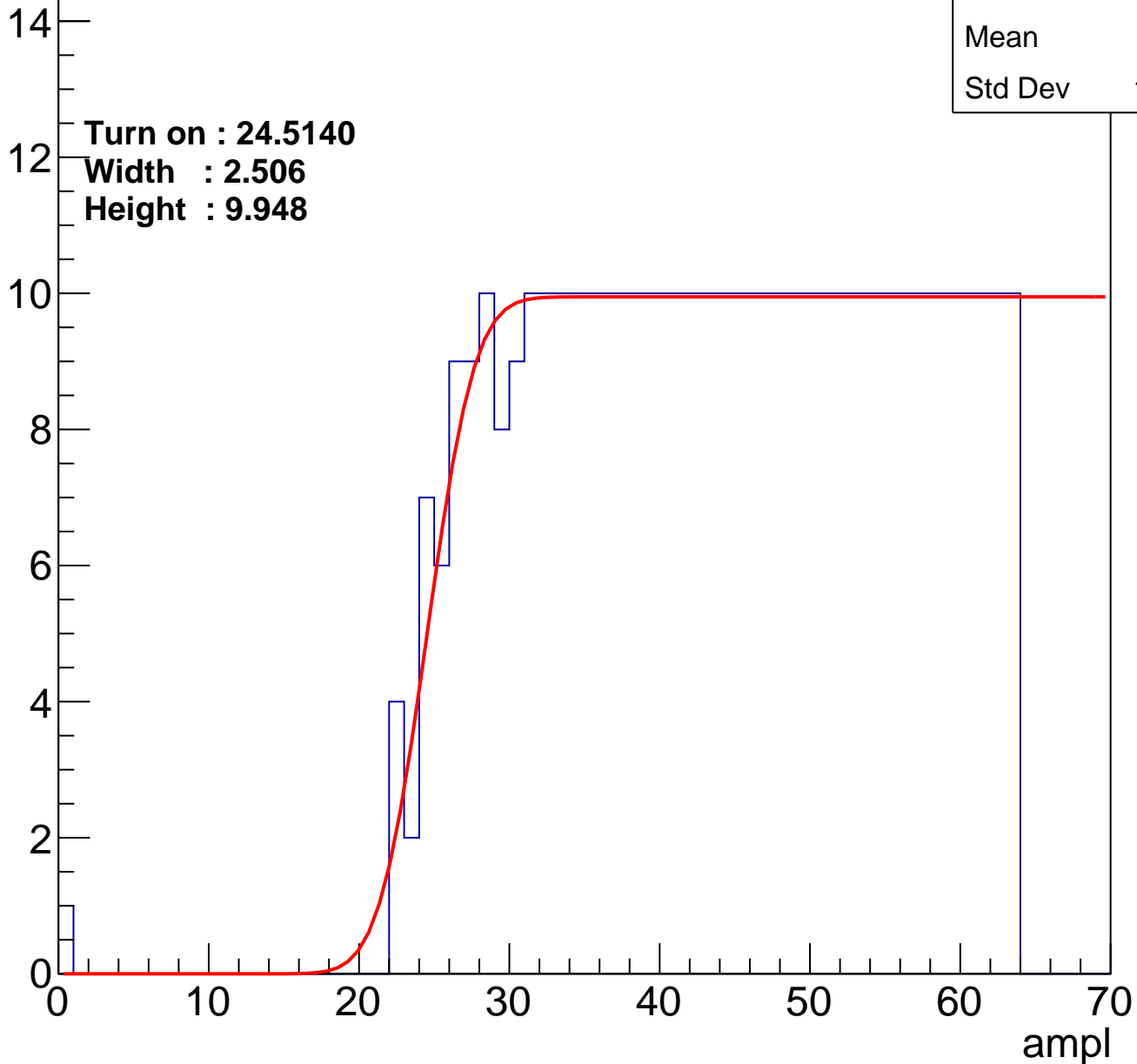
Entries	395
Mean	43.6
Std Dev	11.71

Turn on : 24.5140

Width : 2.506

Height : 9.948

Entry



B1L000S, U12-ch51

calib_packv5_042523_0143.root, FC#3, port B3

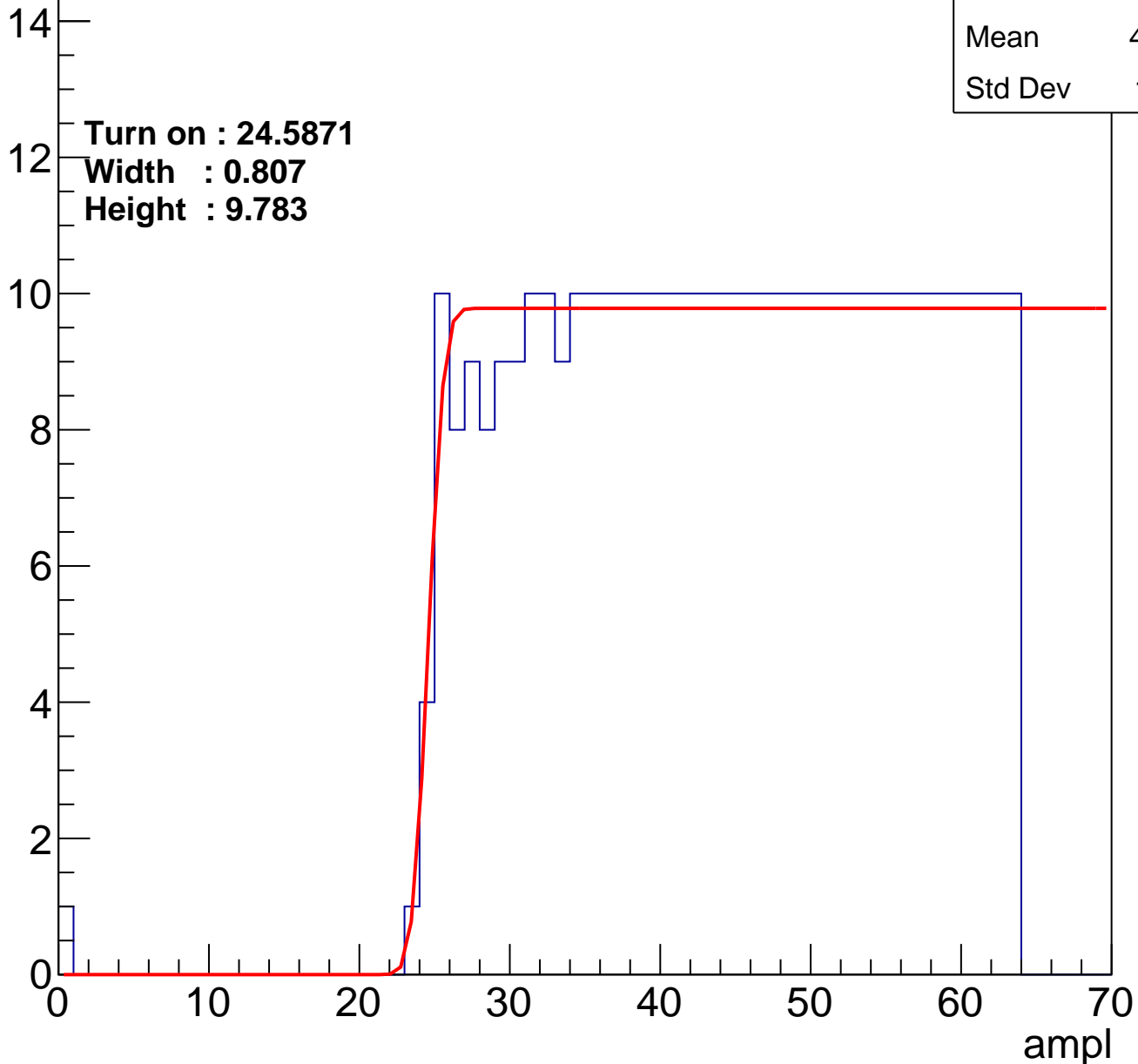
Entries	388
Mean	43.95
Std Dev	11.51

Turn on : 24.5871

Width : 0.807

Height : 9.783

Entry



B1L000S, U12-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.4
Std Dev	11.4

Turn on : 26.3345

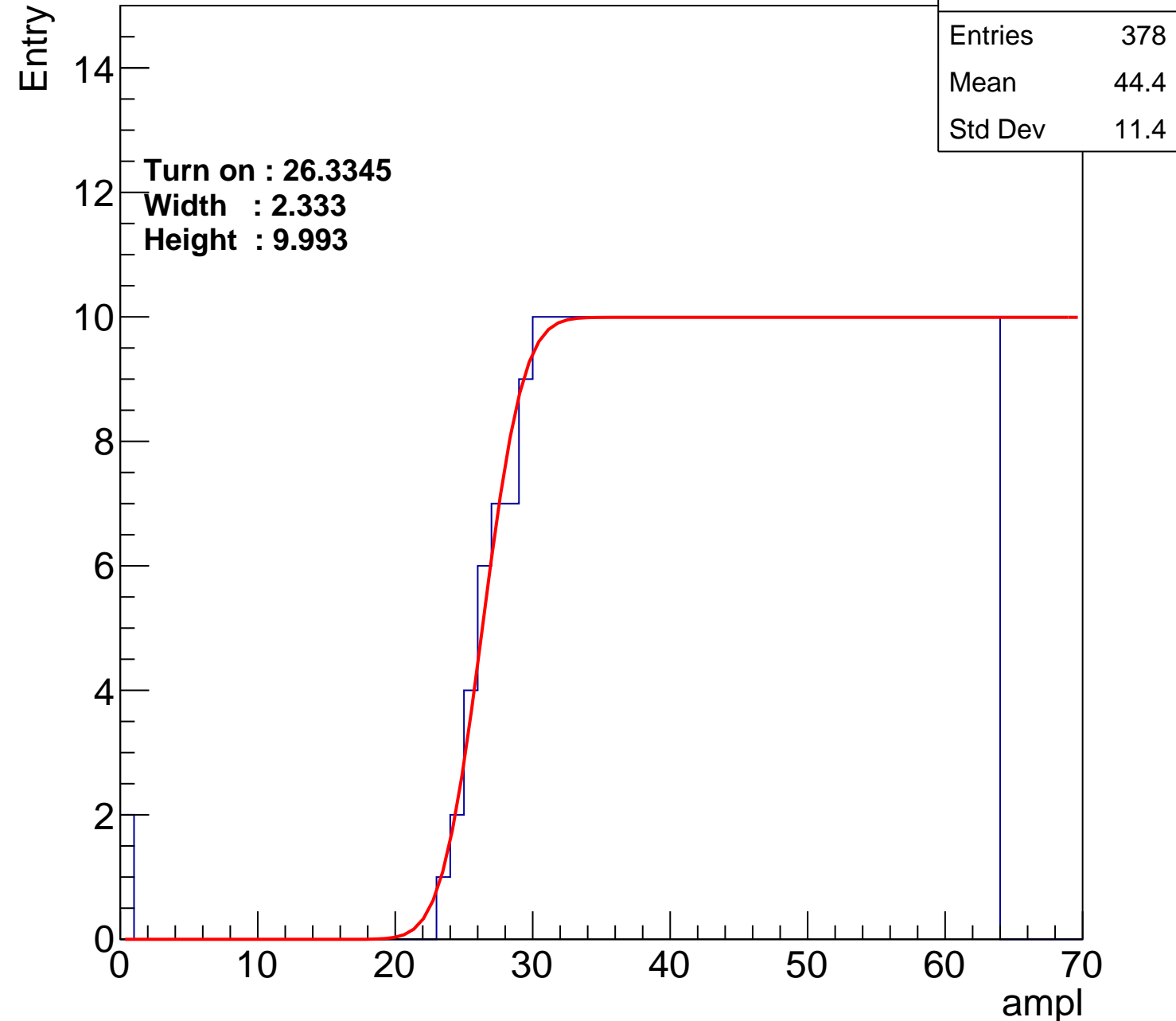
Width : 2.333

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 26.5780

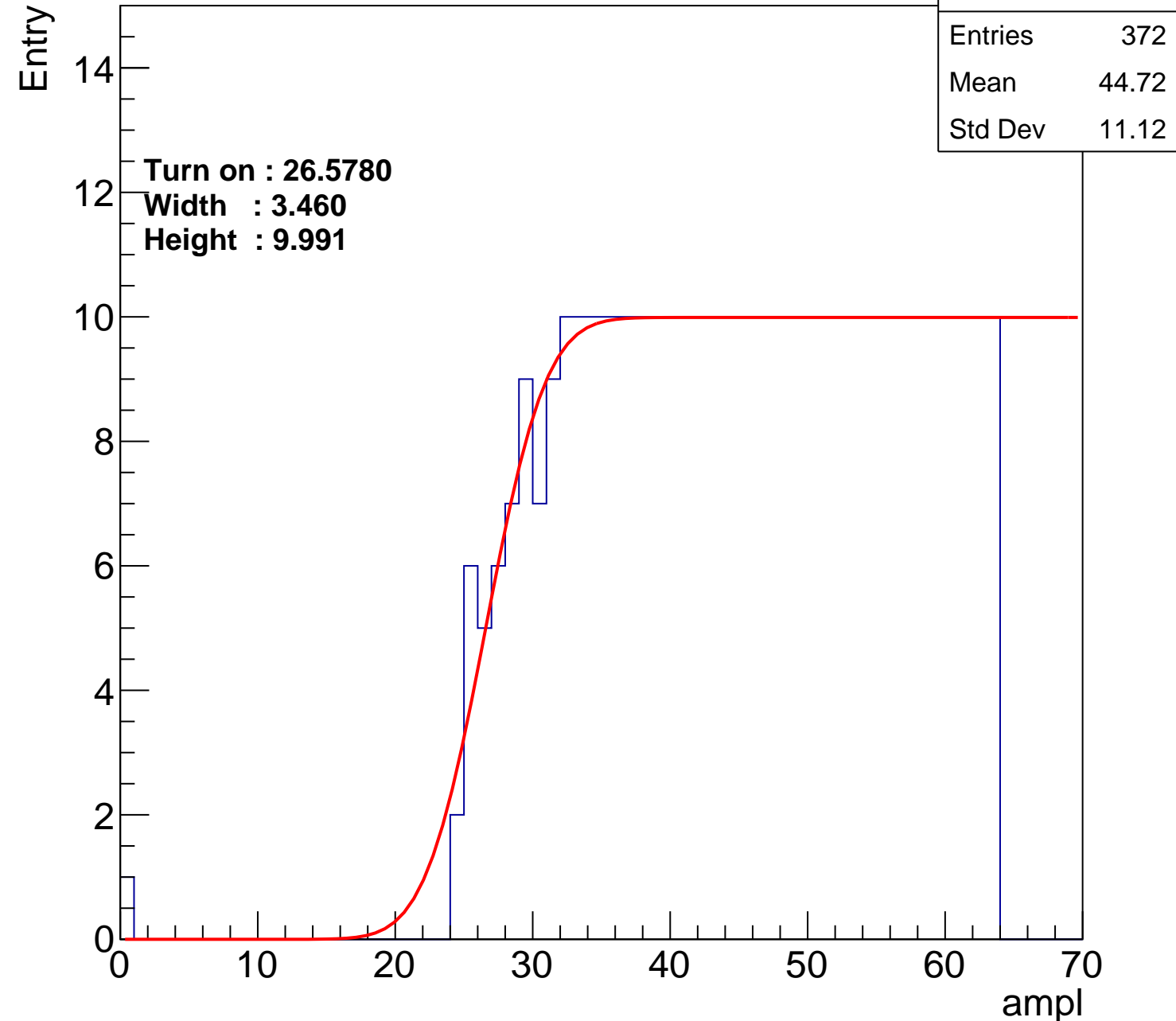
Width : 3.460

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.38
Std Dev	11.63

Turn on : 27.0130

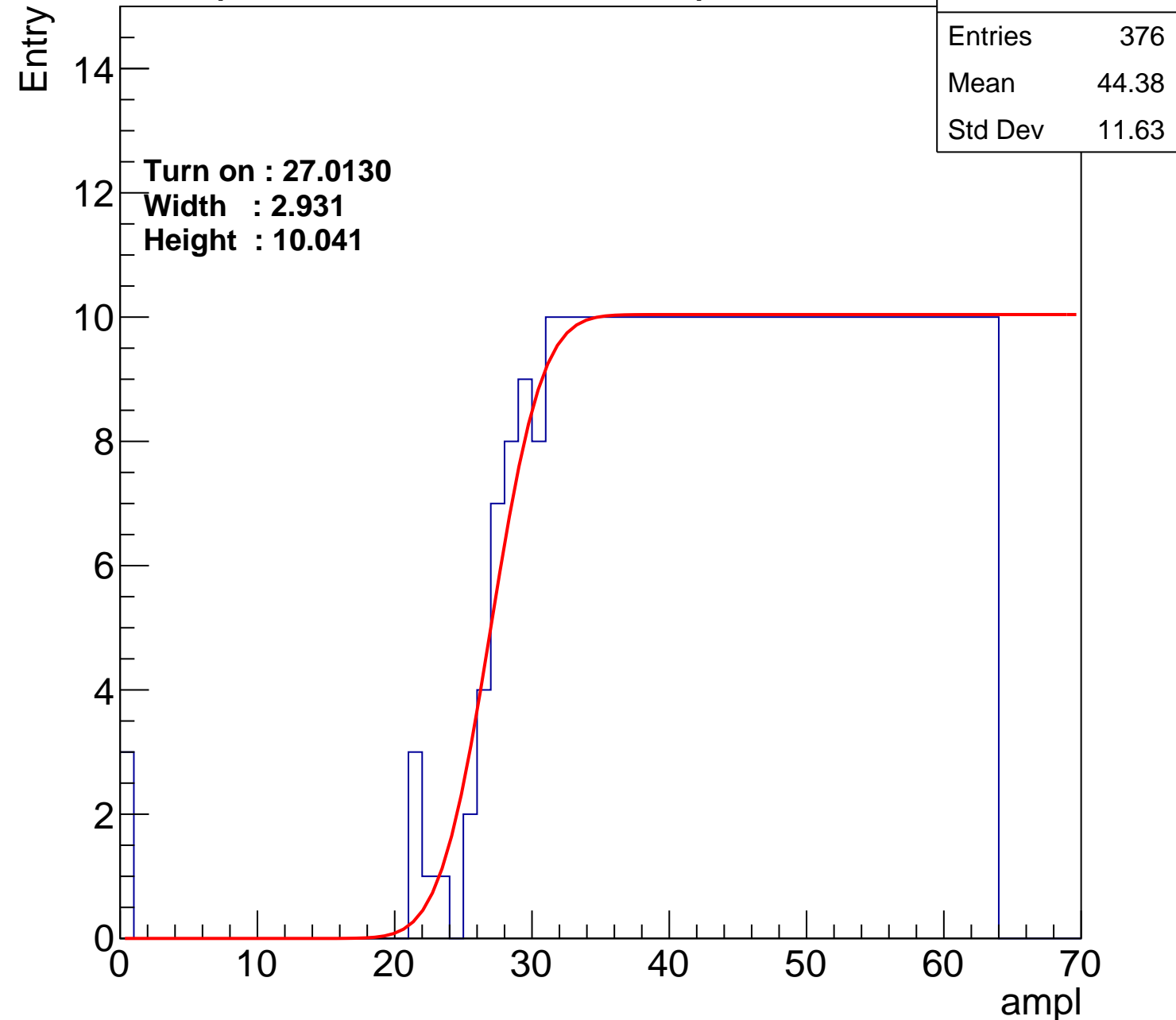
Width : 2.931

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.96
Std Dev	11.98

Turn on : 26.4072

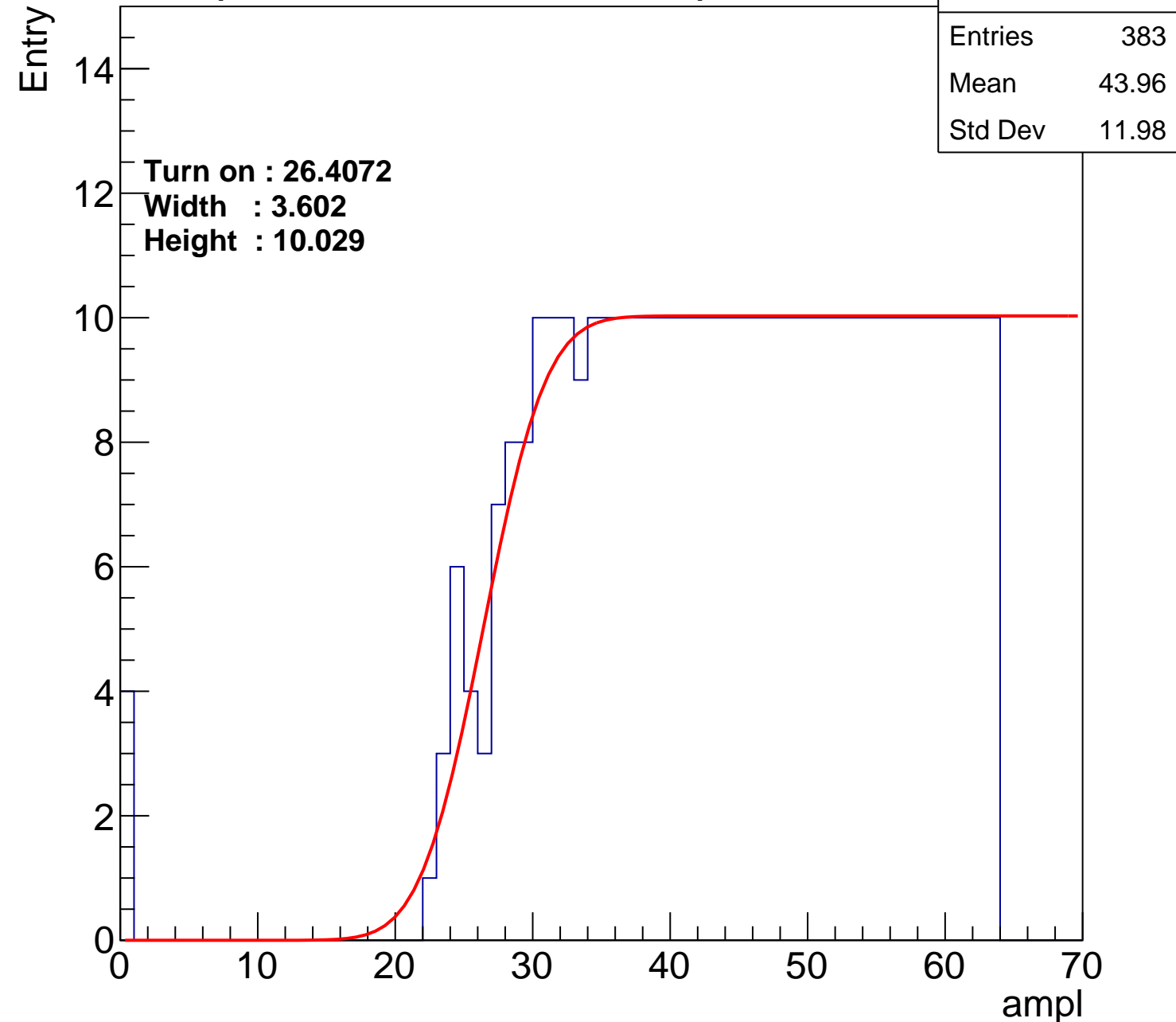
Width : 3.602

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 25.6725

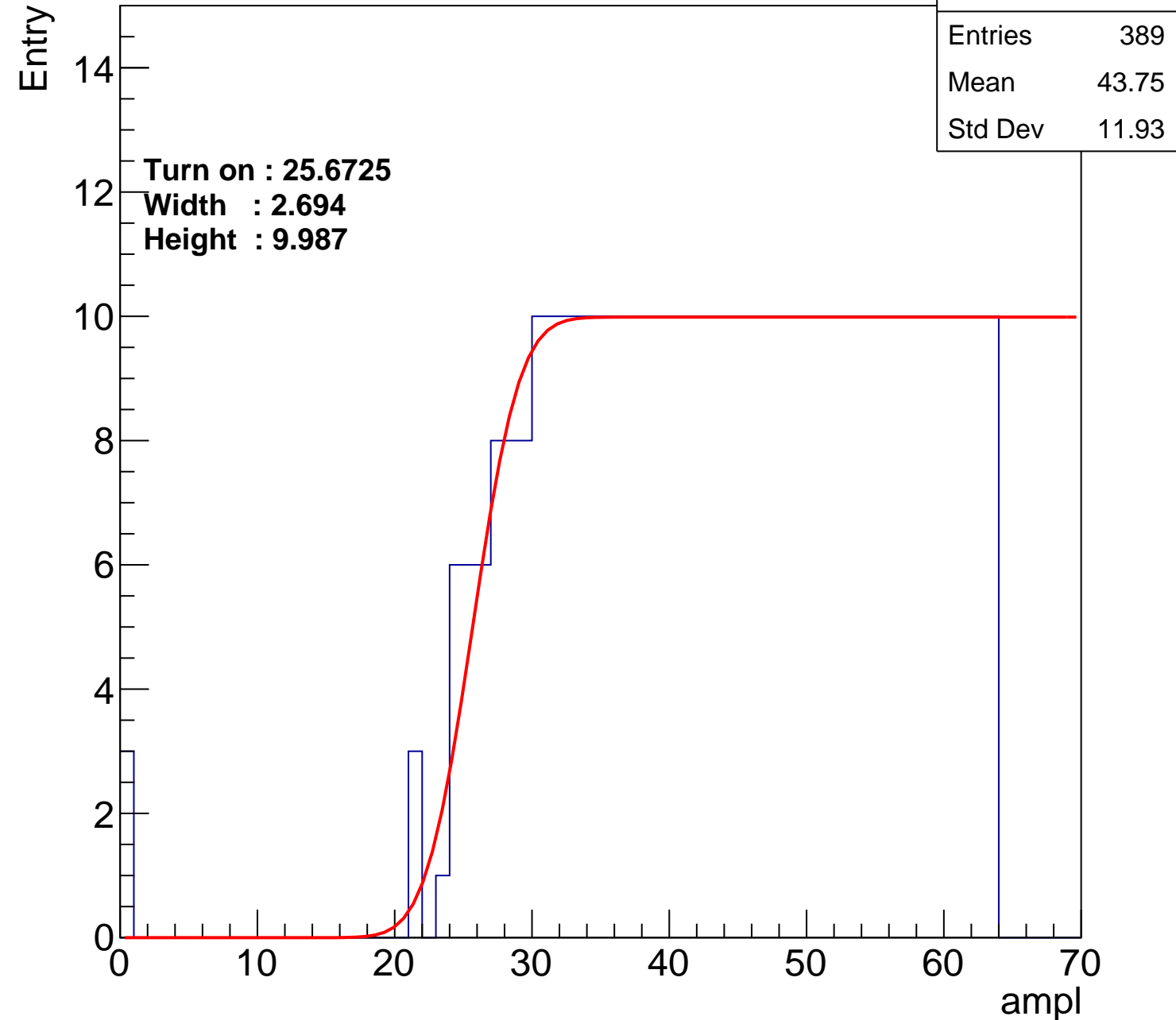
Width : 2.694

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch57

calib_packv5_042523_0143.root, FC#3, port B3

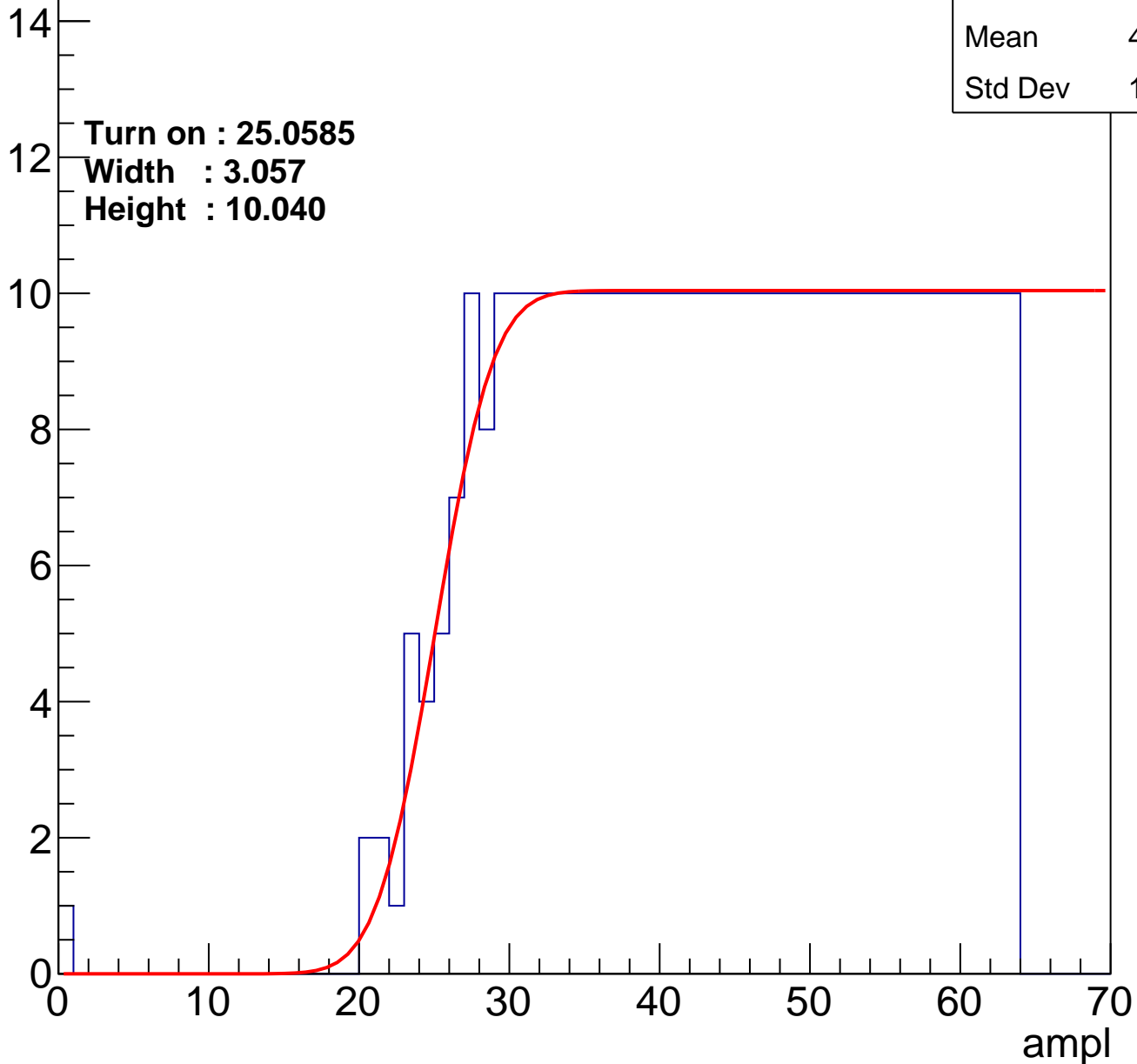
Entries	395
Mean	43.58
Std Dev	11.74

Turn on : 25.0585

Width : 3.057

Height : 10.040

Entry



B1L000S, U12-ch58

calib_packv5_042523_0143.root, FC#3, port B3

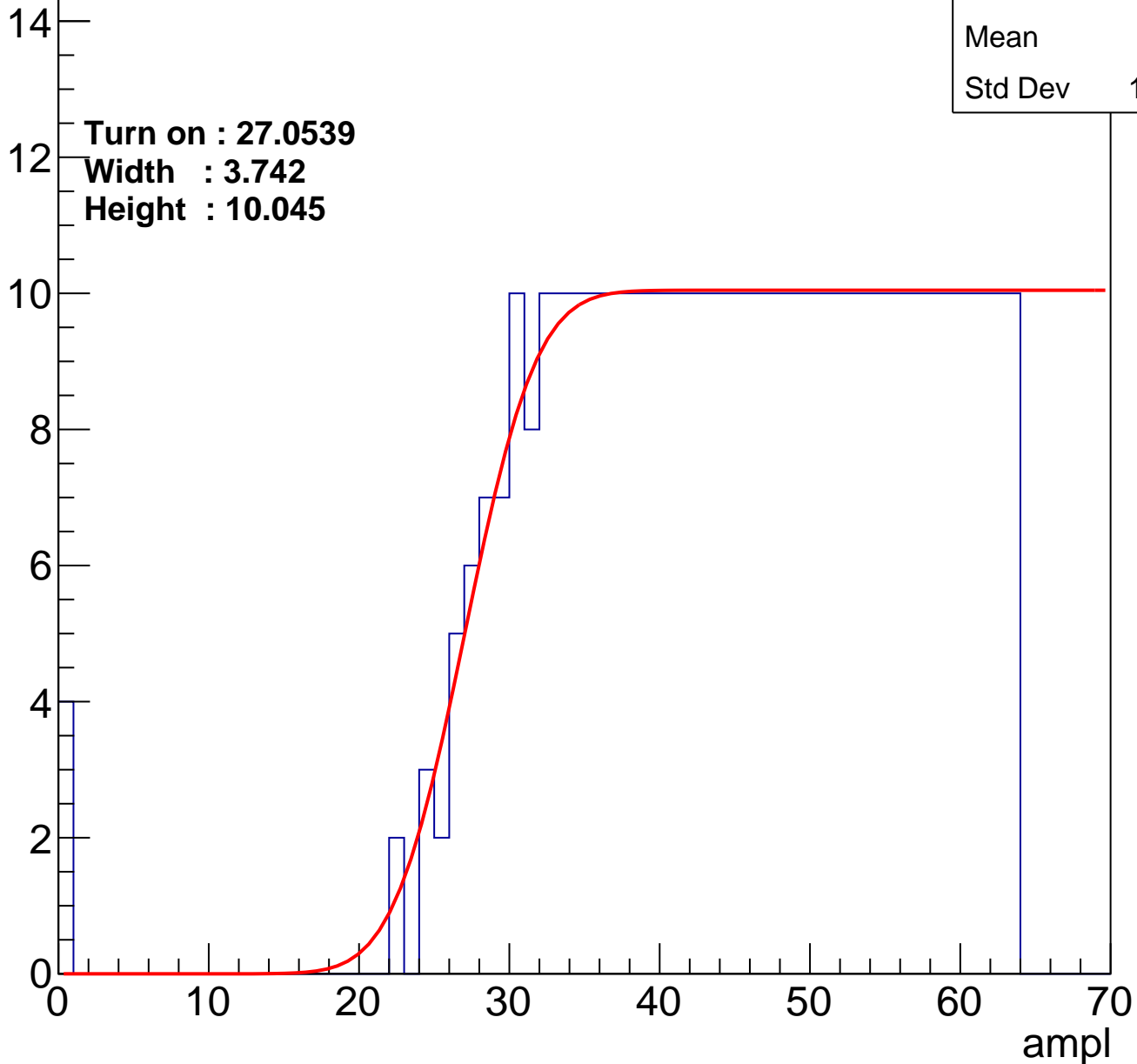
Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 27.0539

Width : 3.742

Height : 10.045

Entry



B1L000S, U12-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.87
Std Dev	11.03

Turn on : 27.0257

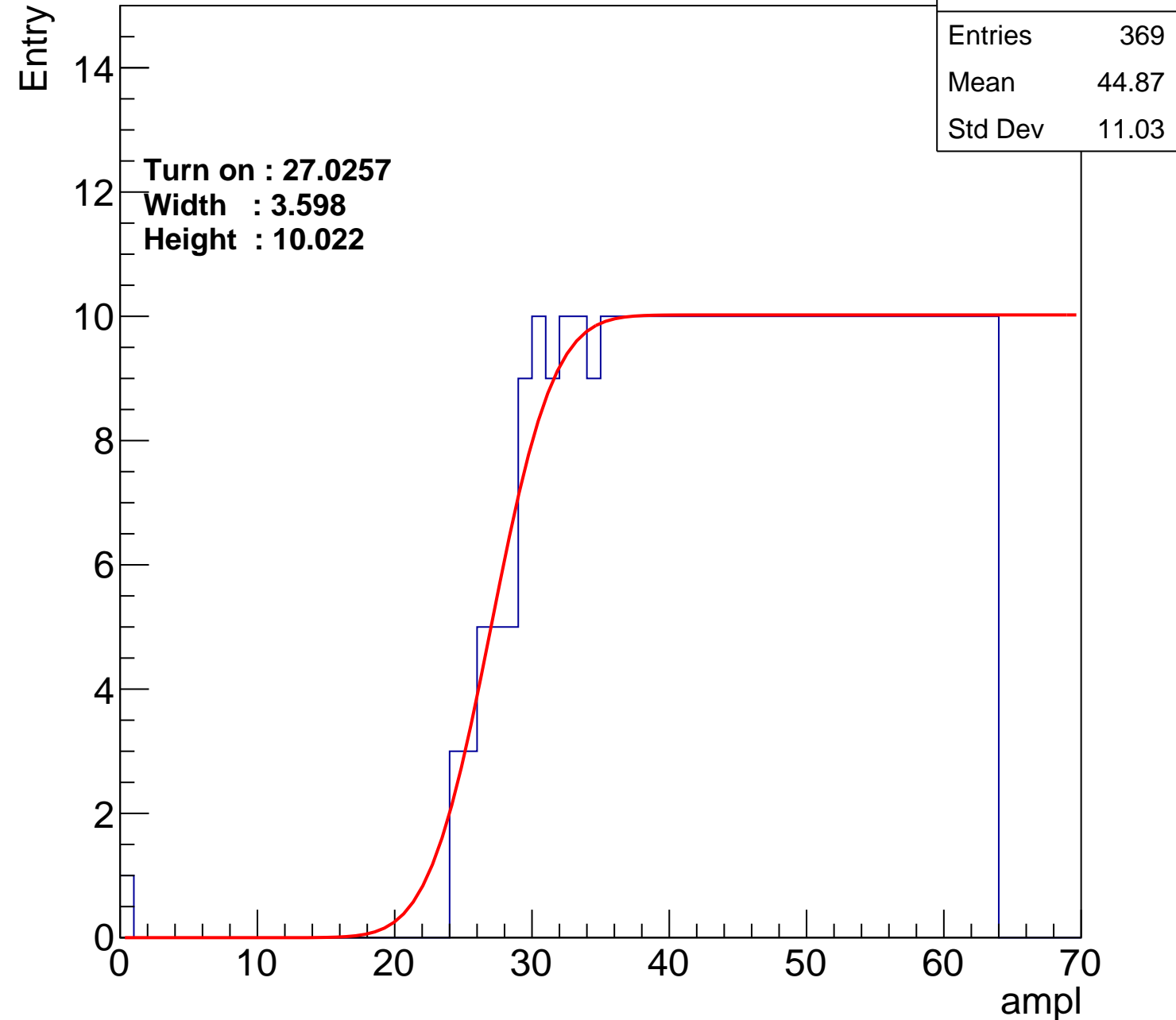
Width : 3.598

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch60

calib_packv5_042523_0143.root, FC#3, port B3

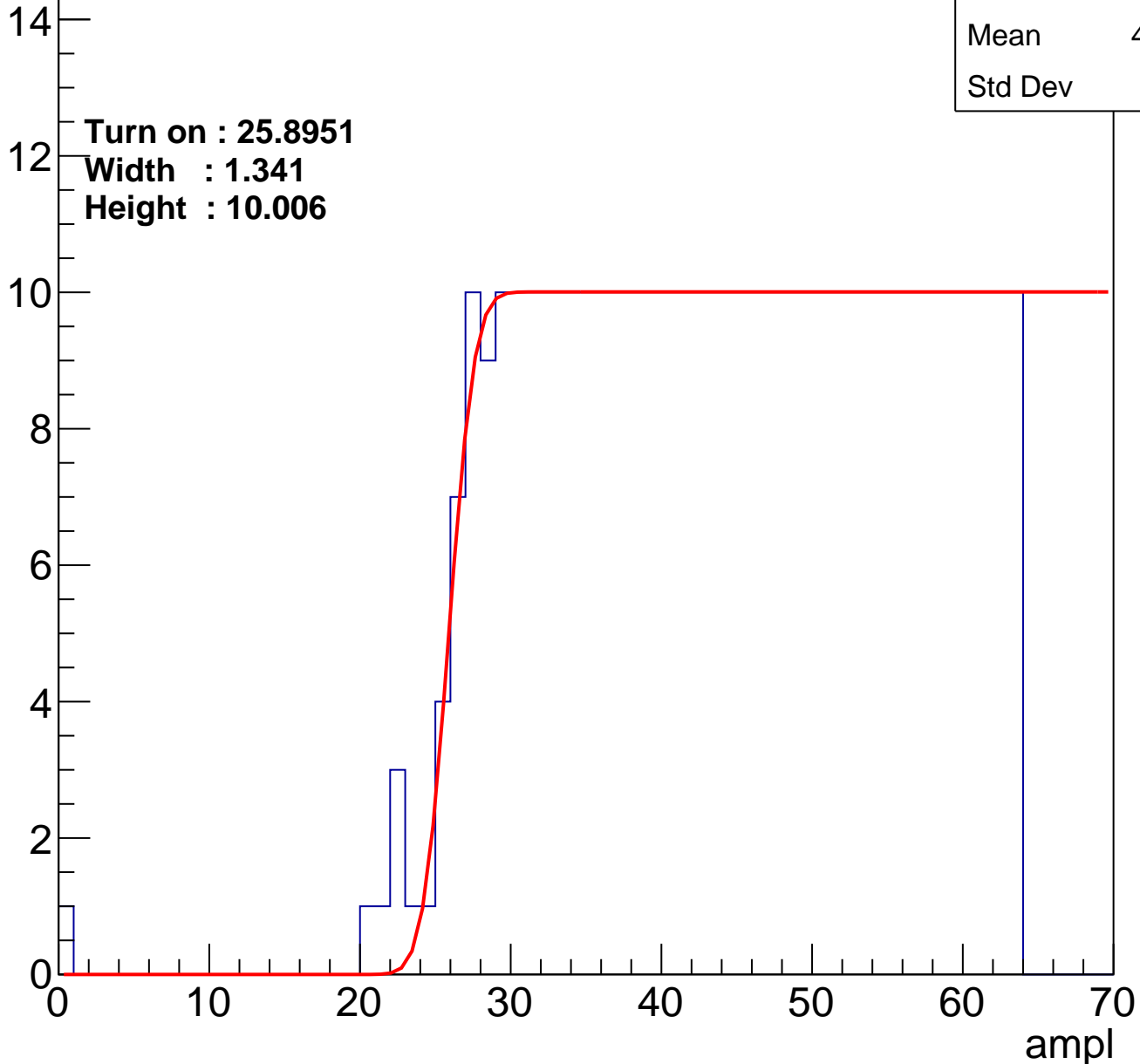
Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.8951

Width : 1.341

Height : 10.006

Entry



B1L000S, U12-ch61

calib_packv5_042523_0143.root, FC#3, port B3

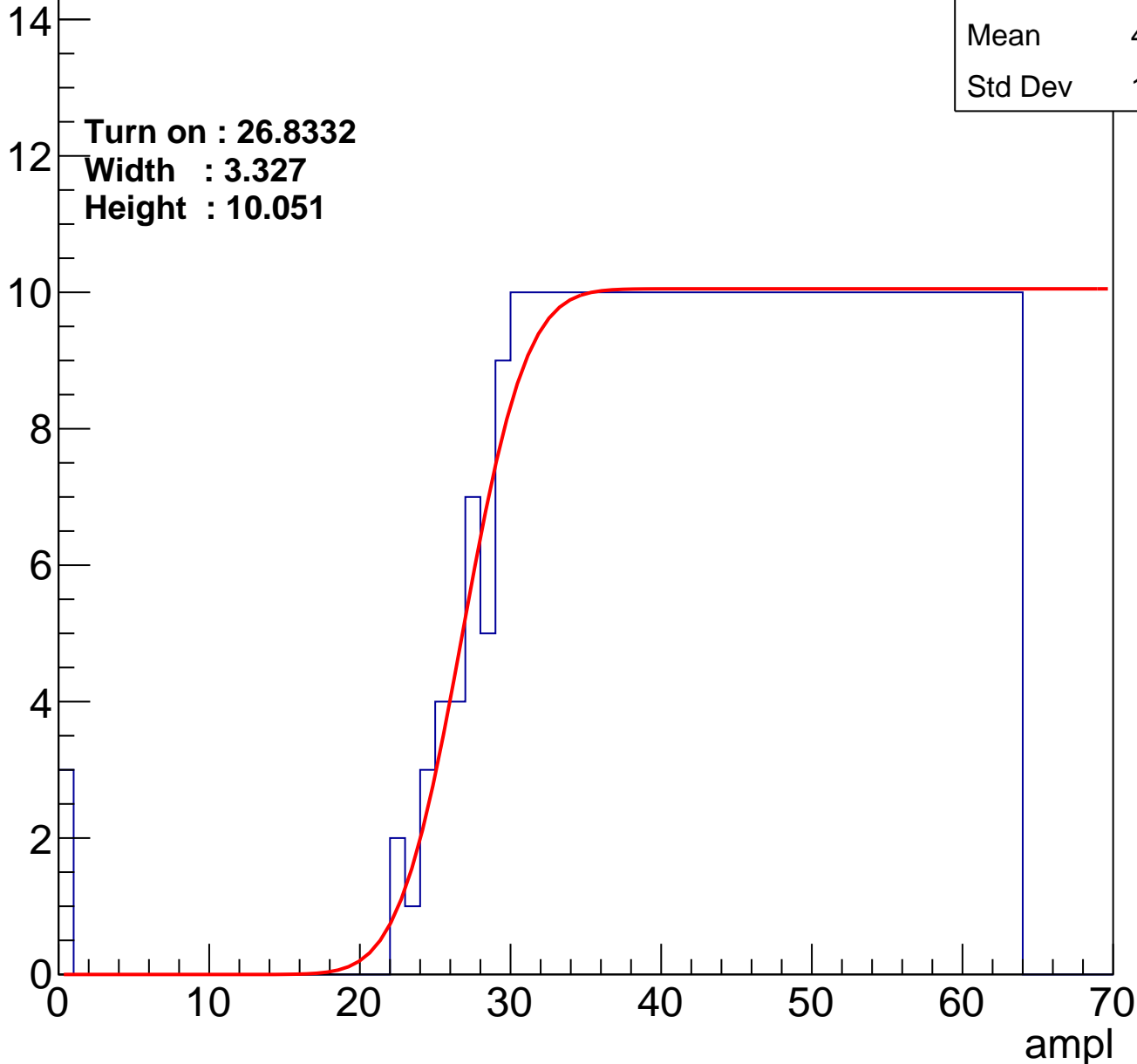
Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.8332

Width : 3.327

Height : 10.051

Entry



B1L000S, U12-ch62

calib_packv5_042523_0143.root, FC#3, port B3

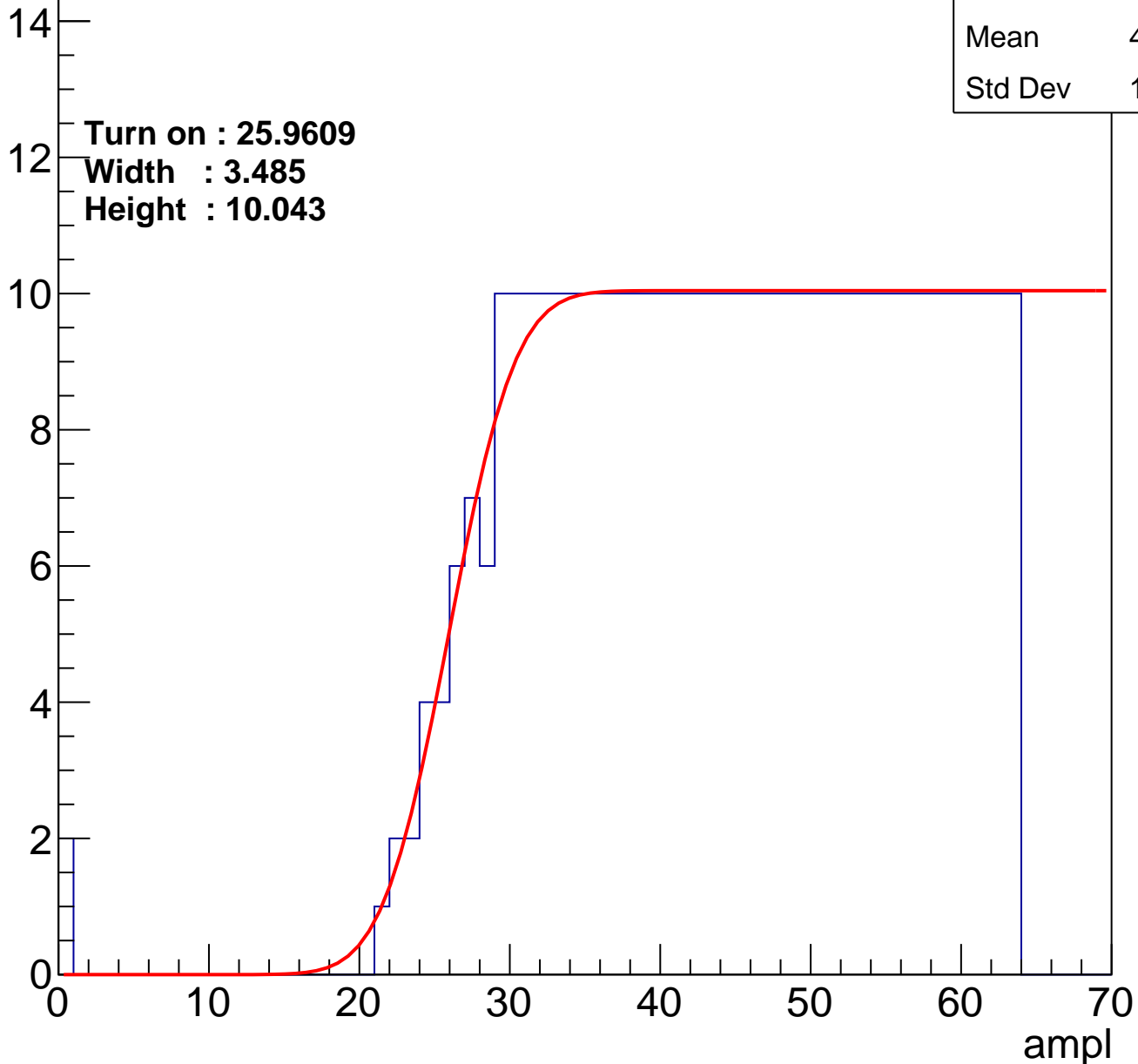
Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.9609

Width : 3.485

Height : 10.043

Entry



B1L000S, U12-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.38
Std Dev	11.98

Turn on : 25.0175

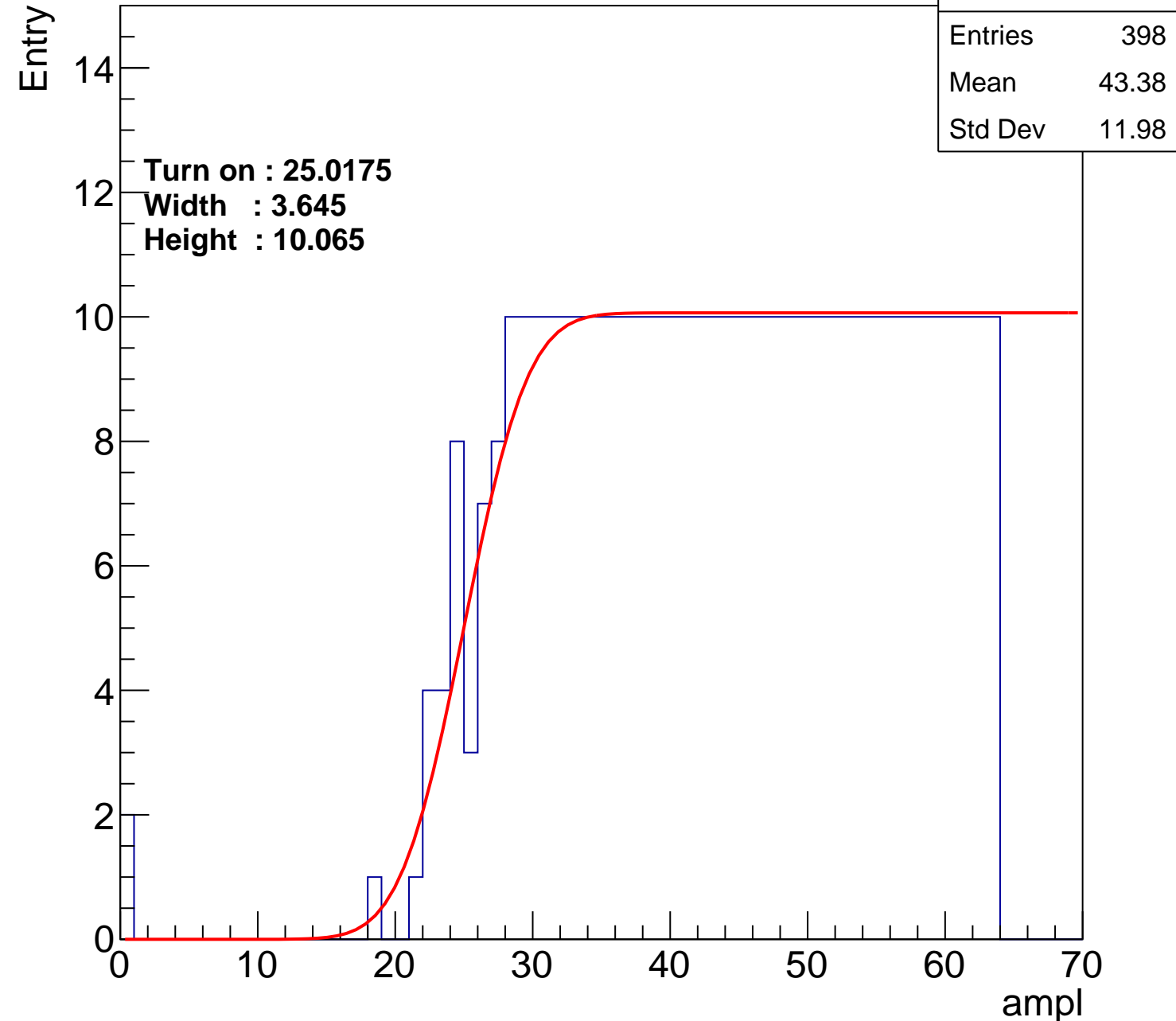
Width : 3.645

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch64

calib_packv5_042523_0143.root, FC#3, port B3

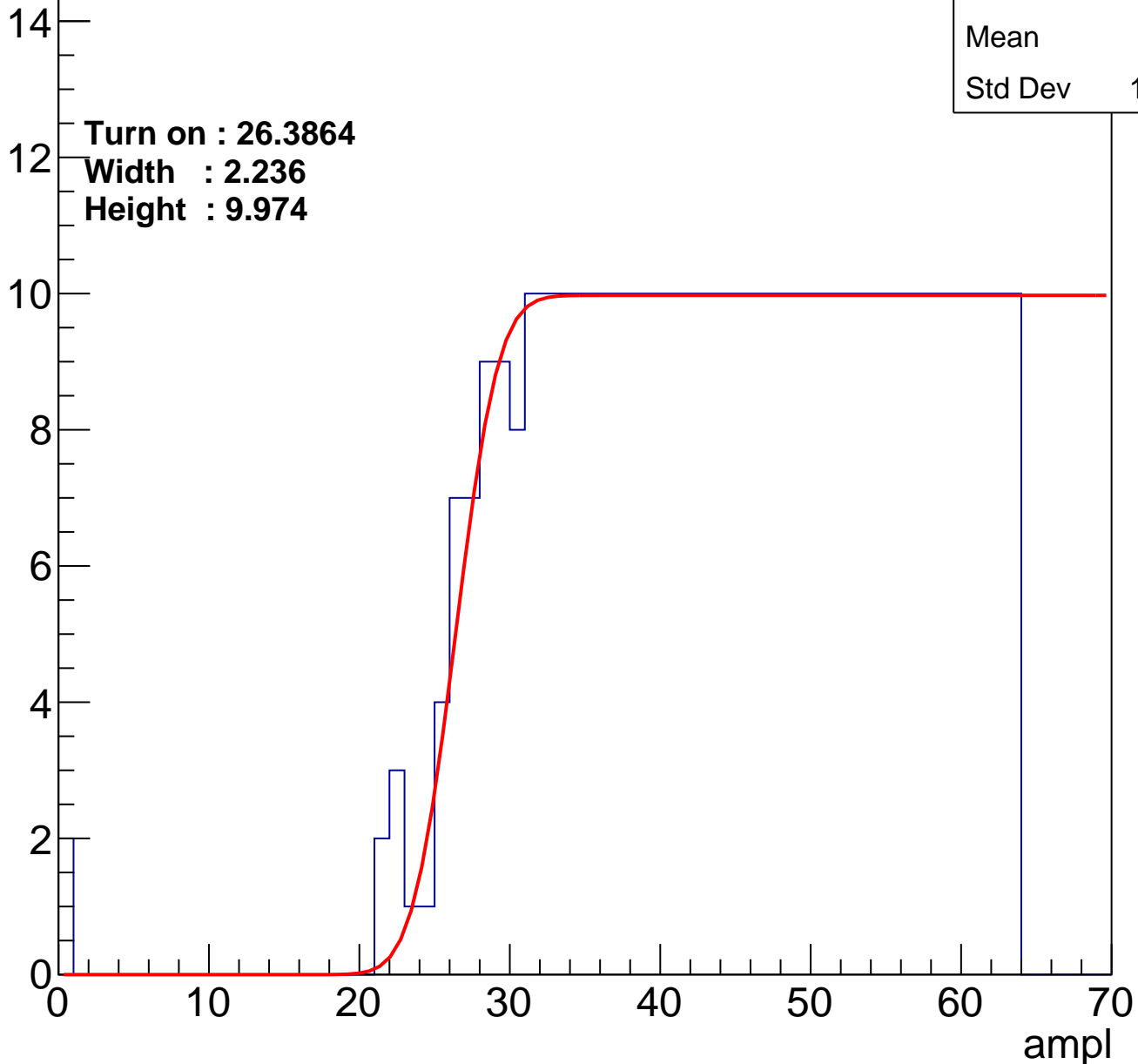
Entries	383
Mean	44.1
Std Dev	11.62

Turn on : 26.3864

Width : 2.236

Height : 9.974

Entry



B1L000S, U12-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.12
Std Dev	11.72

Turn on : 26.3959

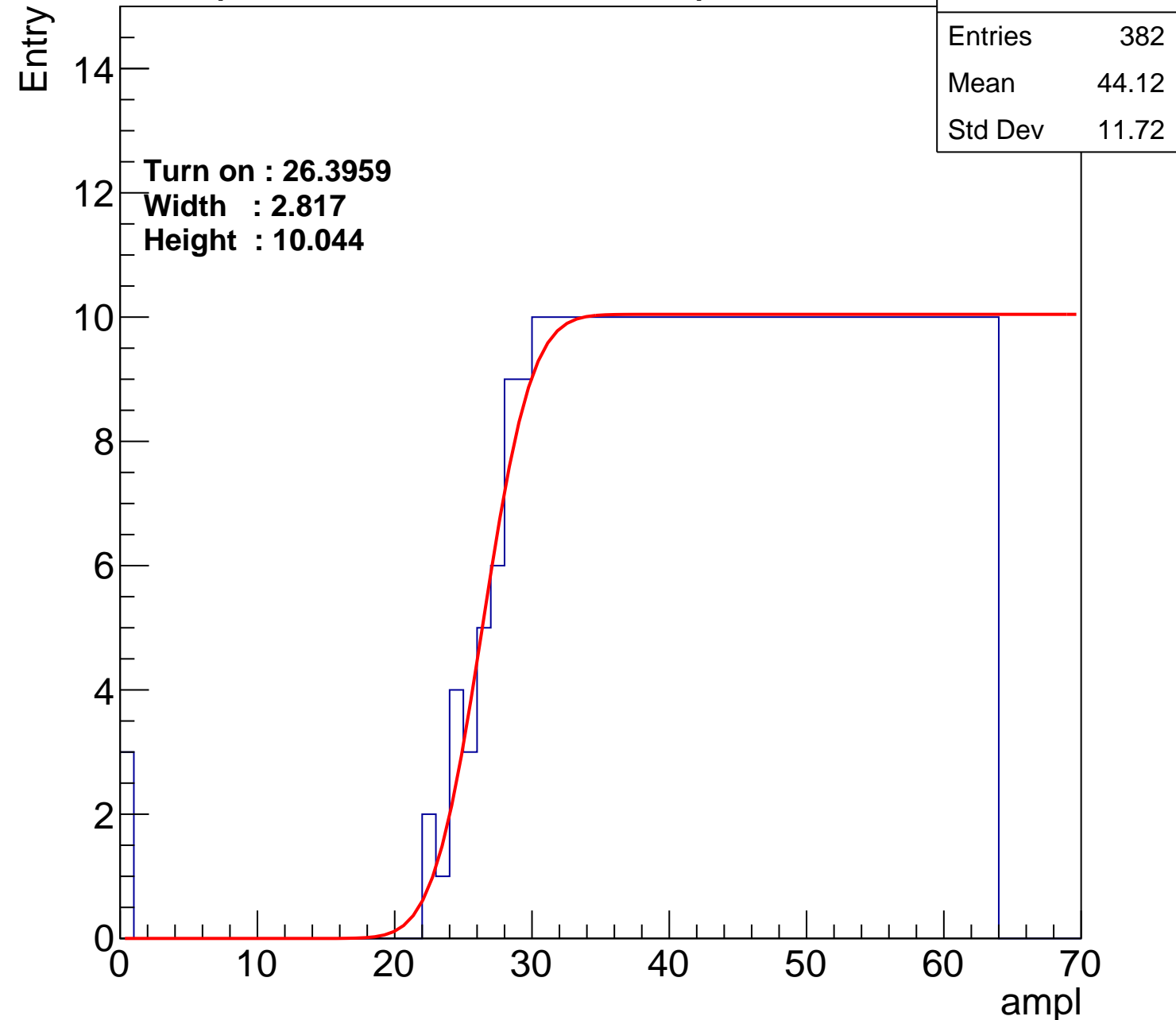
Width : 2.817

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.1882

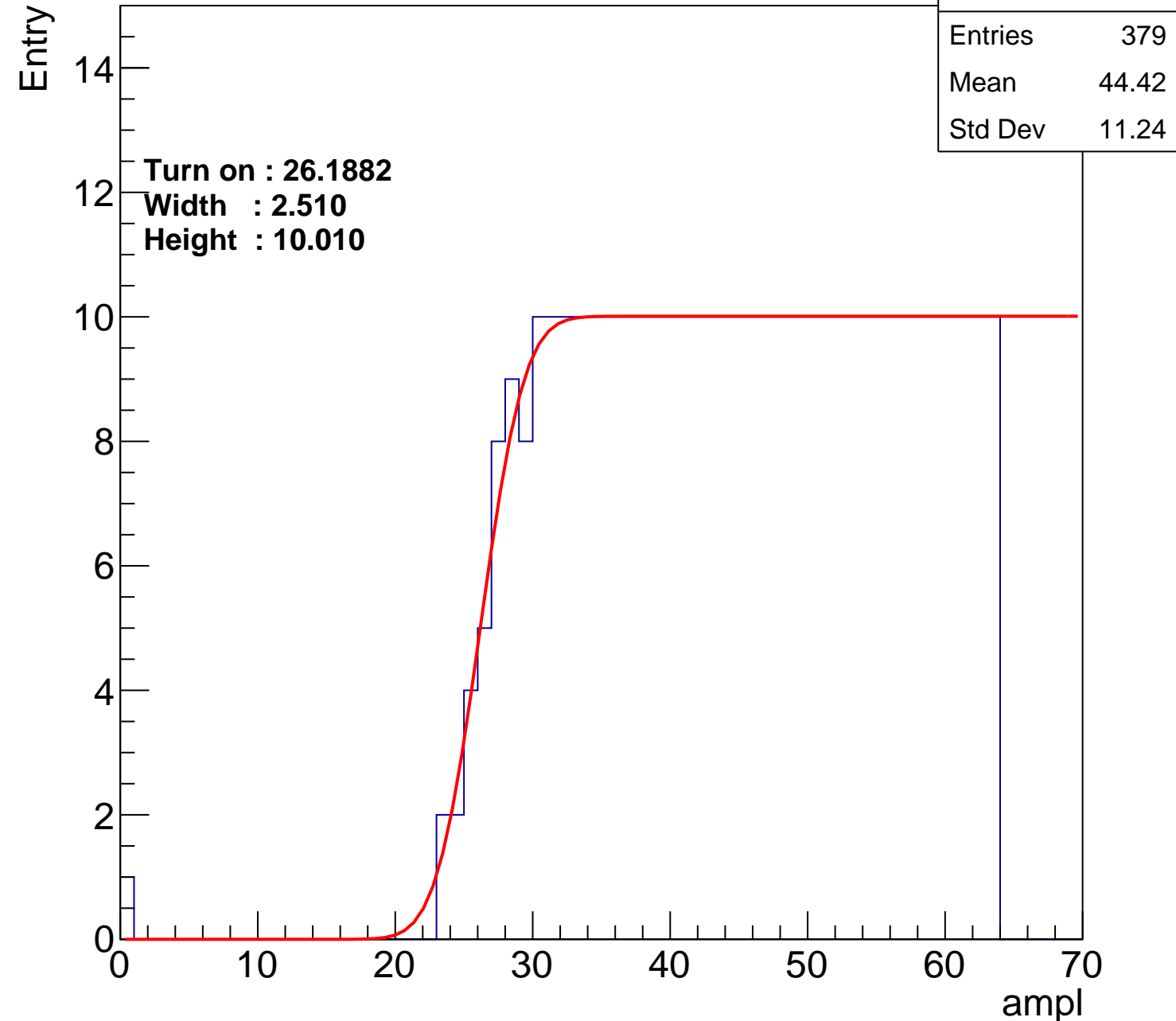
Width : 2.510

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch67

calib_packv5_042523_0143.root, FC#3, port B3

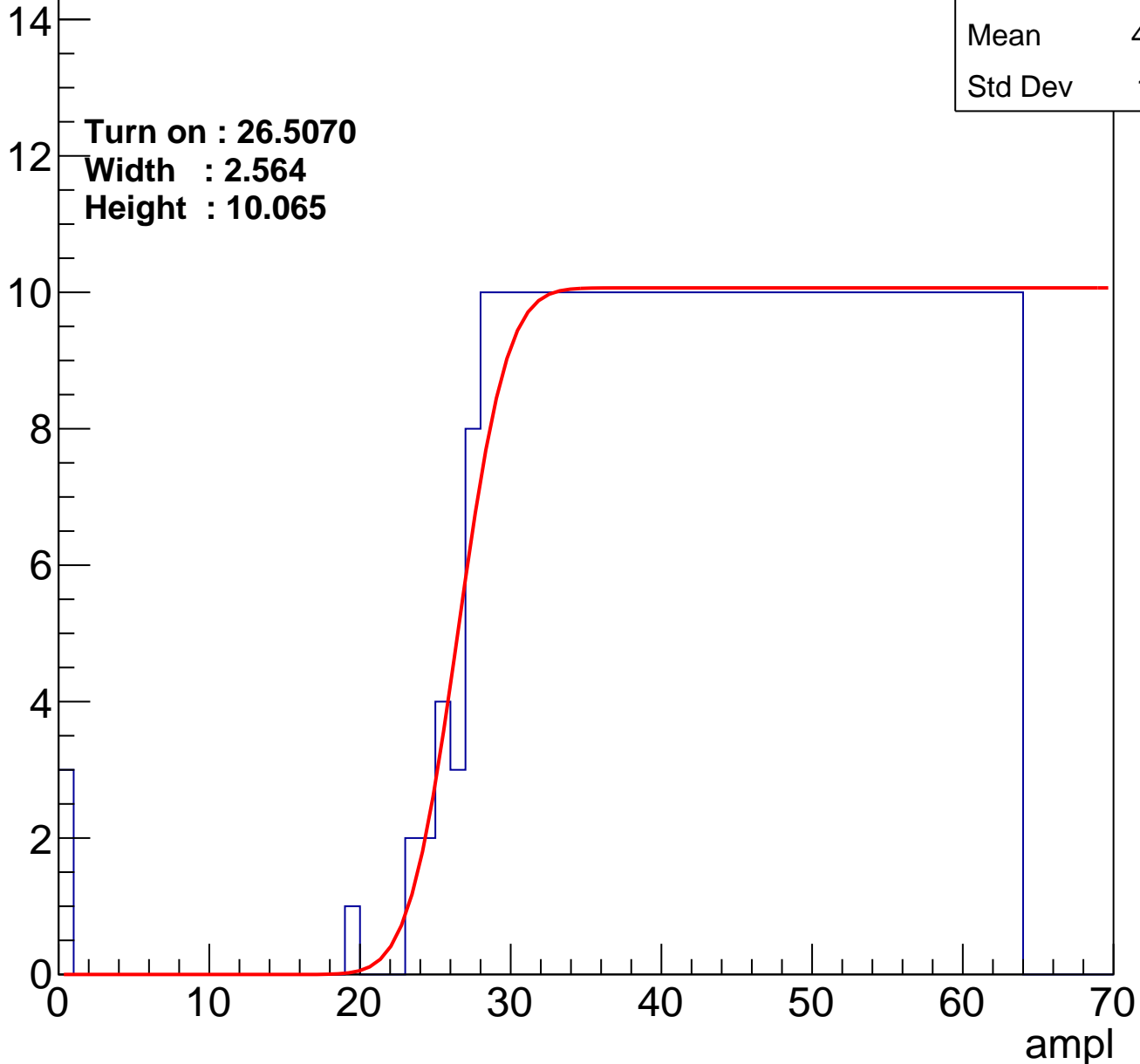
Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.5070

Width : 2.564

Height : 10.065

Entry



B1L000S, U12-ch68

calib_packv5_042523_0143.root, FC#3, port B3

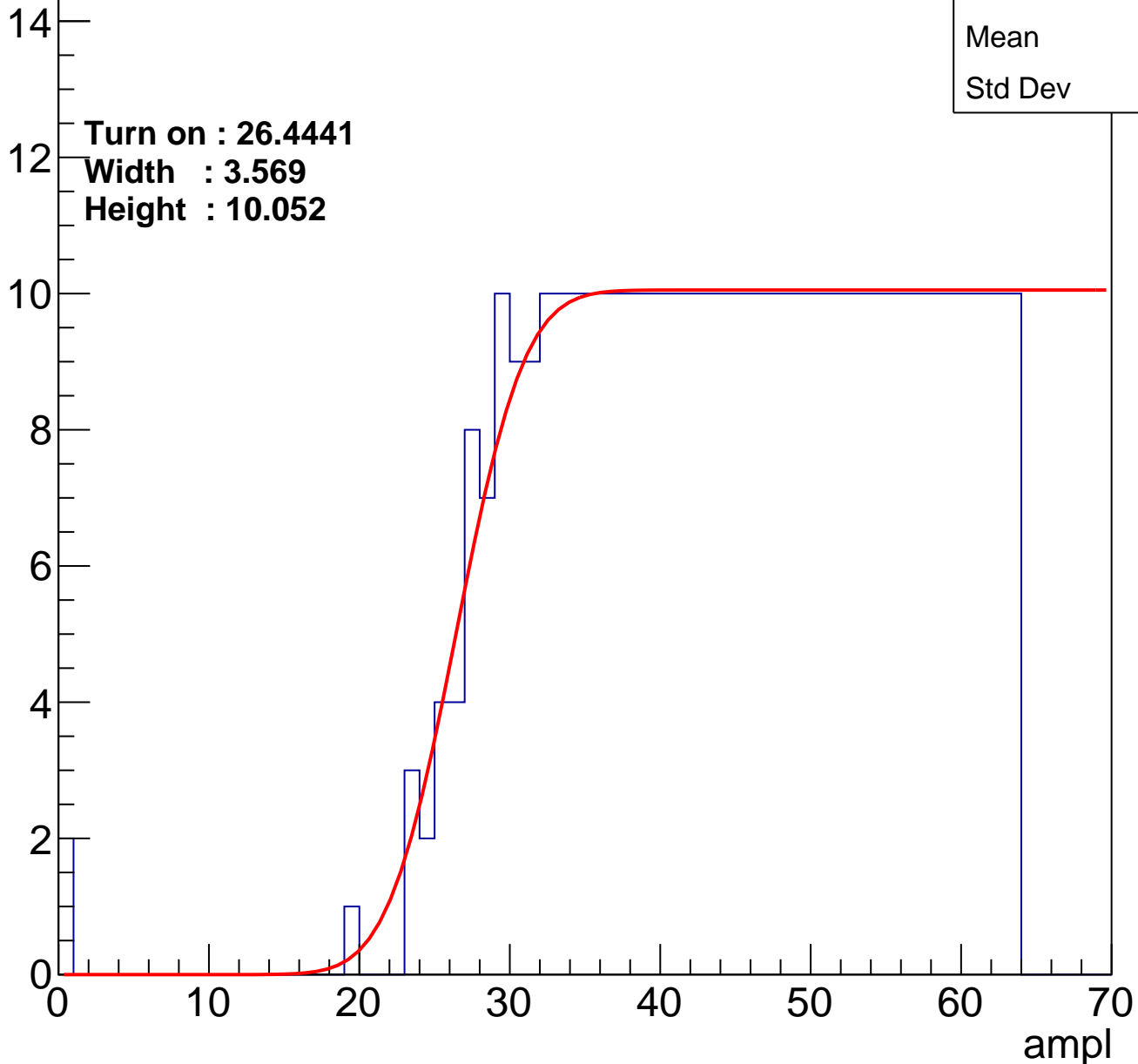
Entries	379
Mean	44.3
Std Dev	11.5

Turn on : 26.4441

Width : 3.569

Height : 10.052

Entry



B1L000S, U12-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.7605

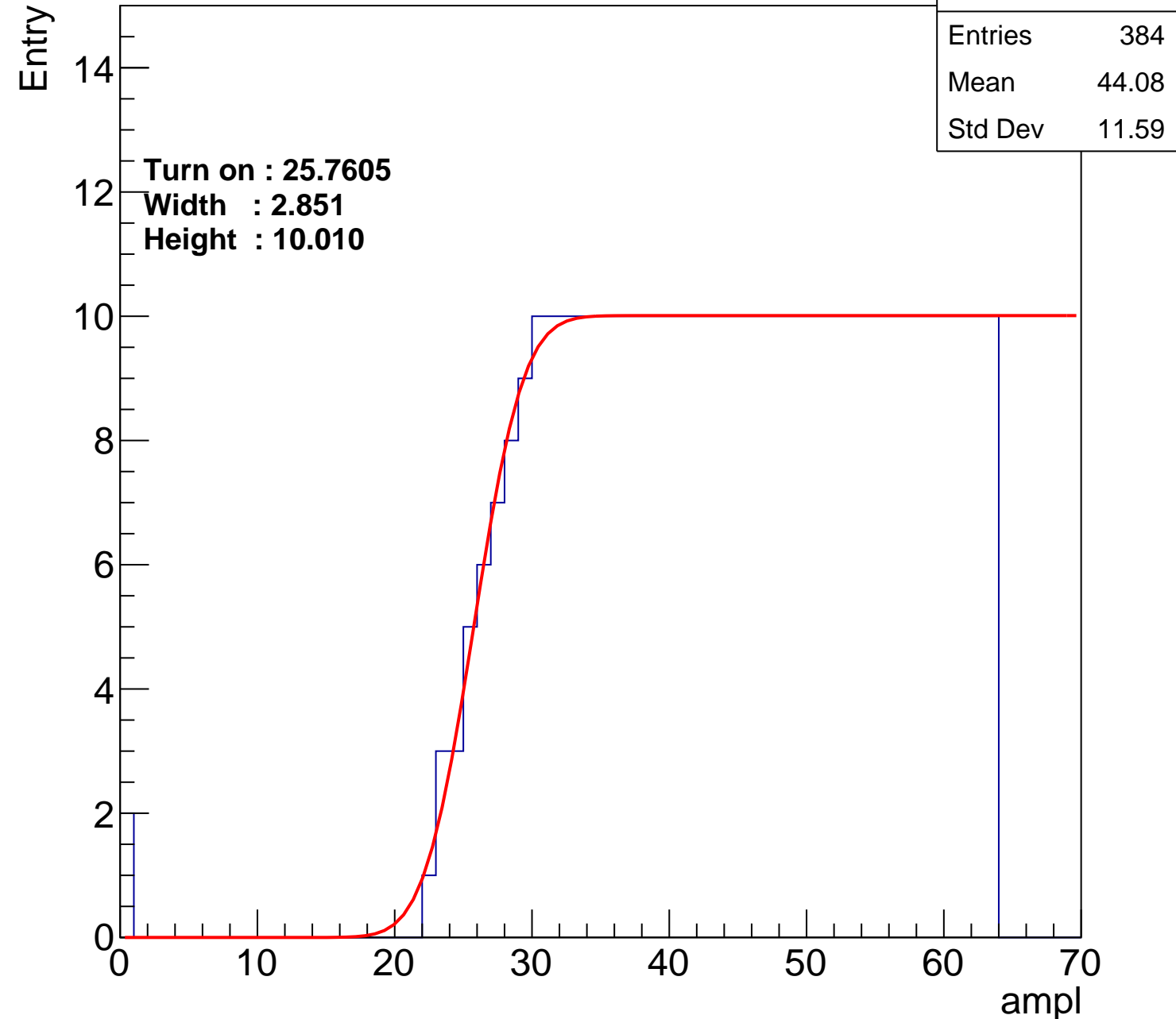
Width : 2.851

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.8915

Width : 3.898

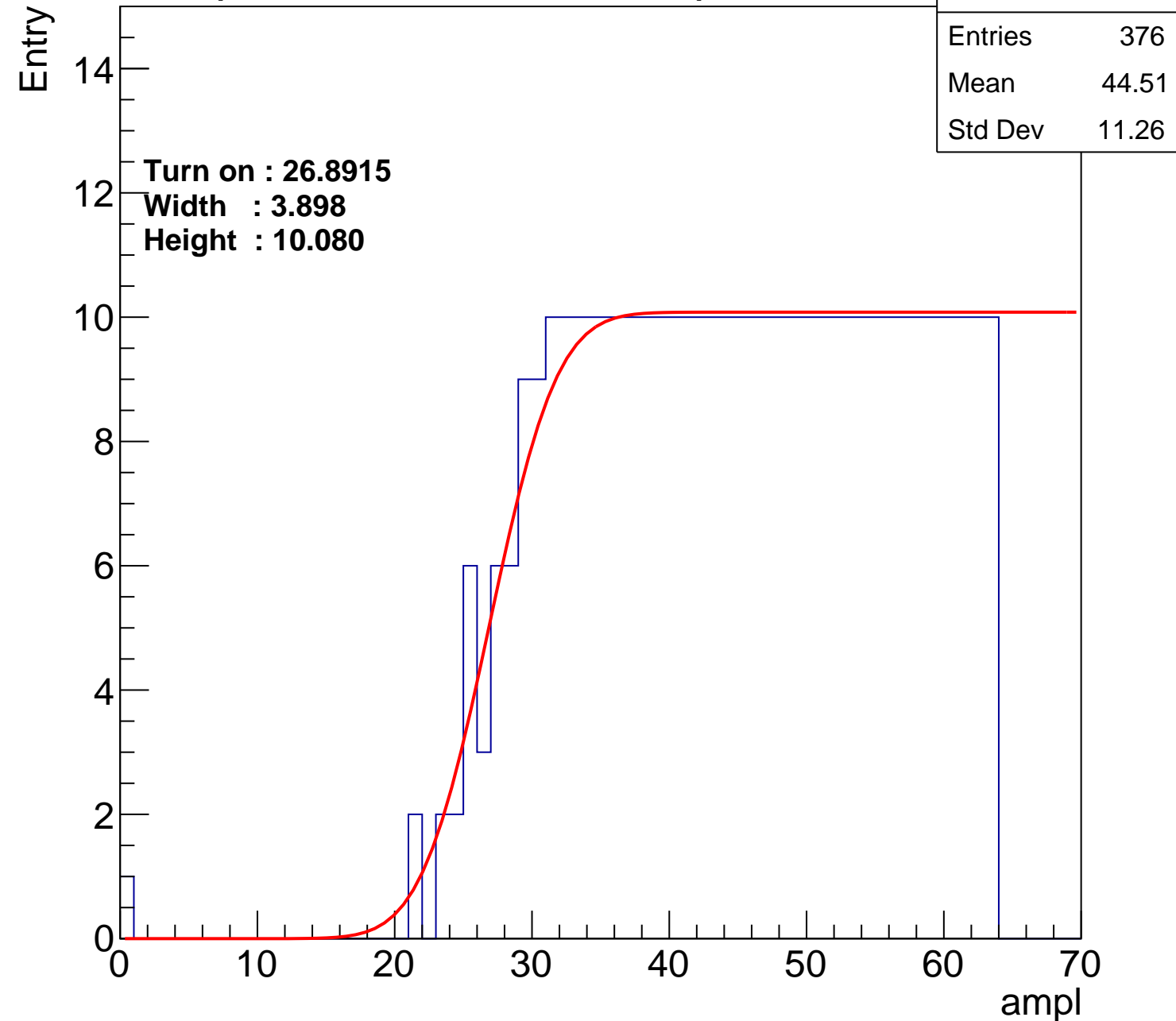
Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U12-ch71

calib_packv5_042523_0143.root, FC#3, port B3

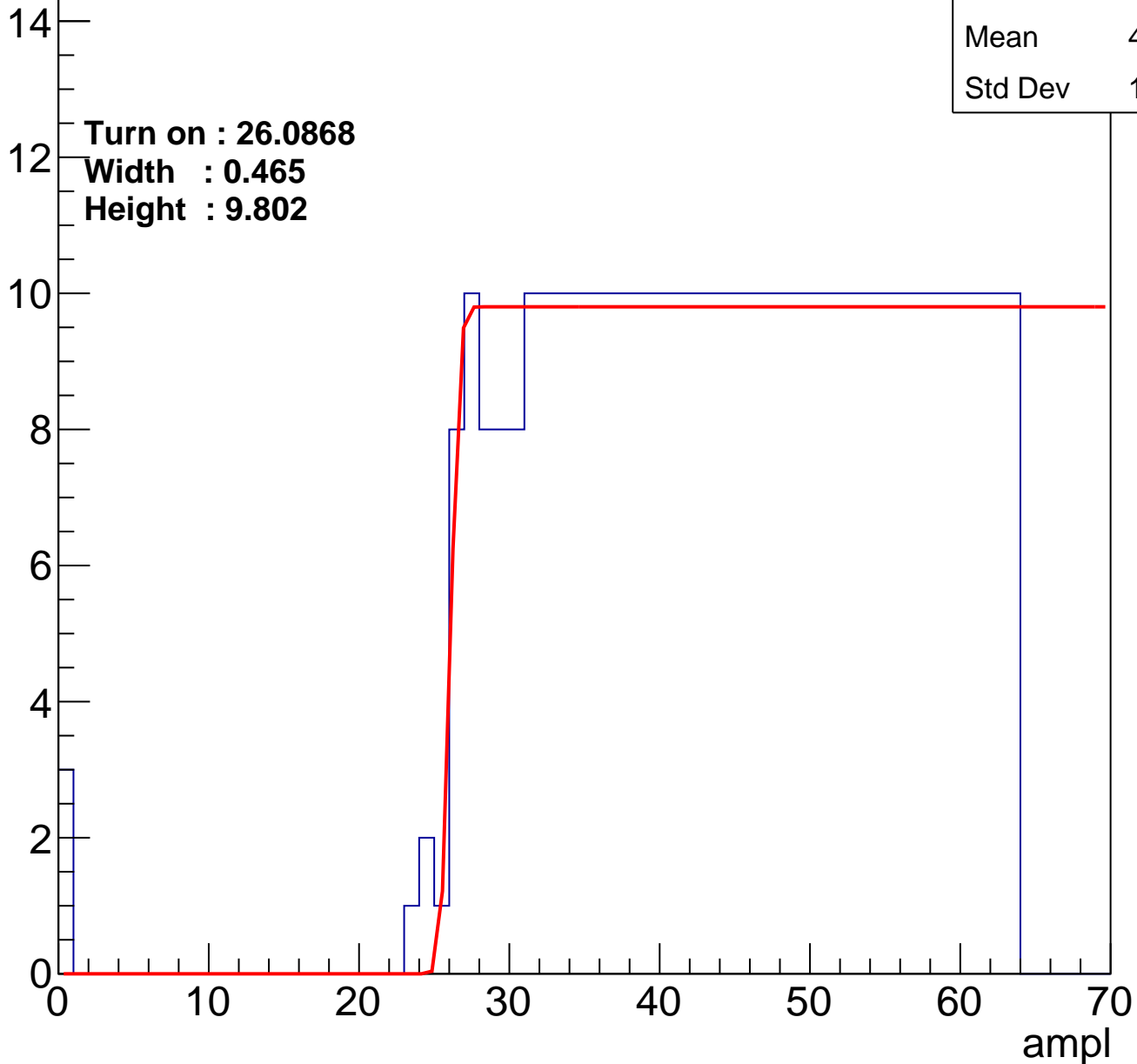
Entries	379
Mean	44.27
Std Dev	11.62

Turn on : 26.0868

Width : 0.465

Height : 9.802

Entry



B1L000S, U12-ch72

calib_packv5_042523_0143.root, FC#3, port B3

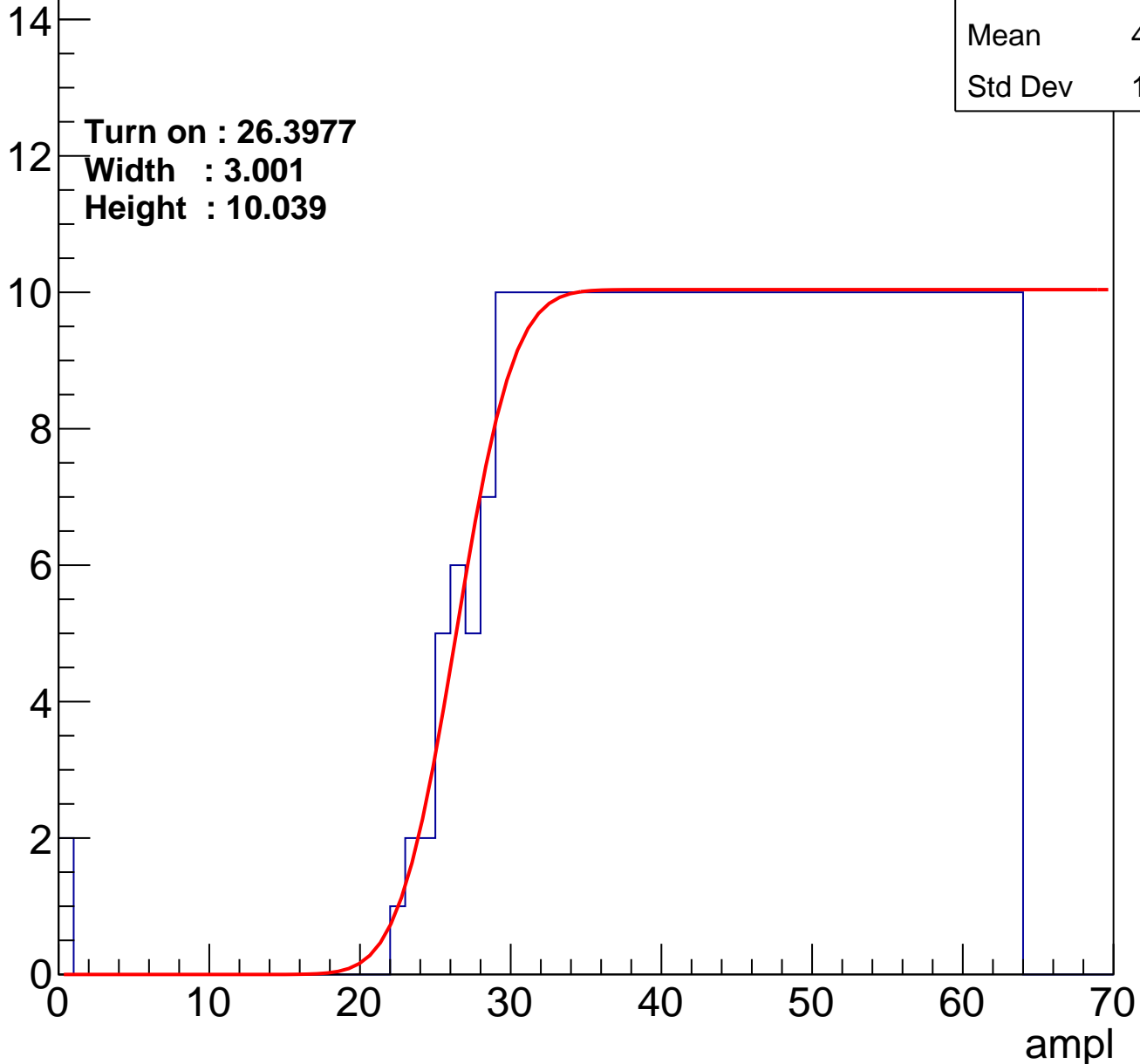
Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.3977

Width : 3.001

Height : 10.039

Entry



B1L000S, U12-ch73

calib_packv5_042523_0143.root, FC#3, port B3

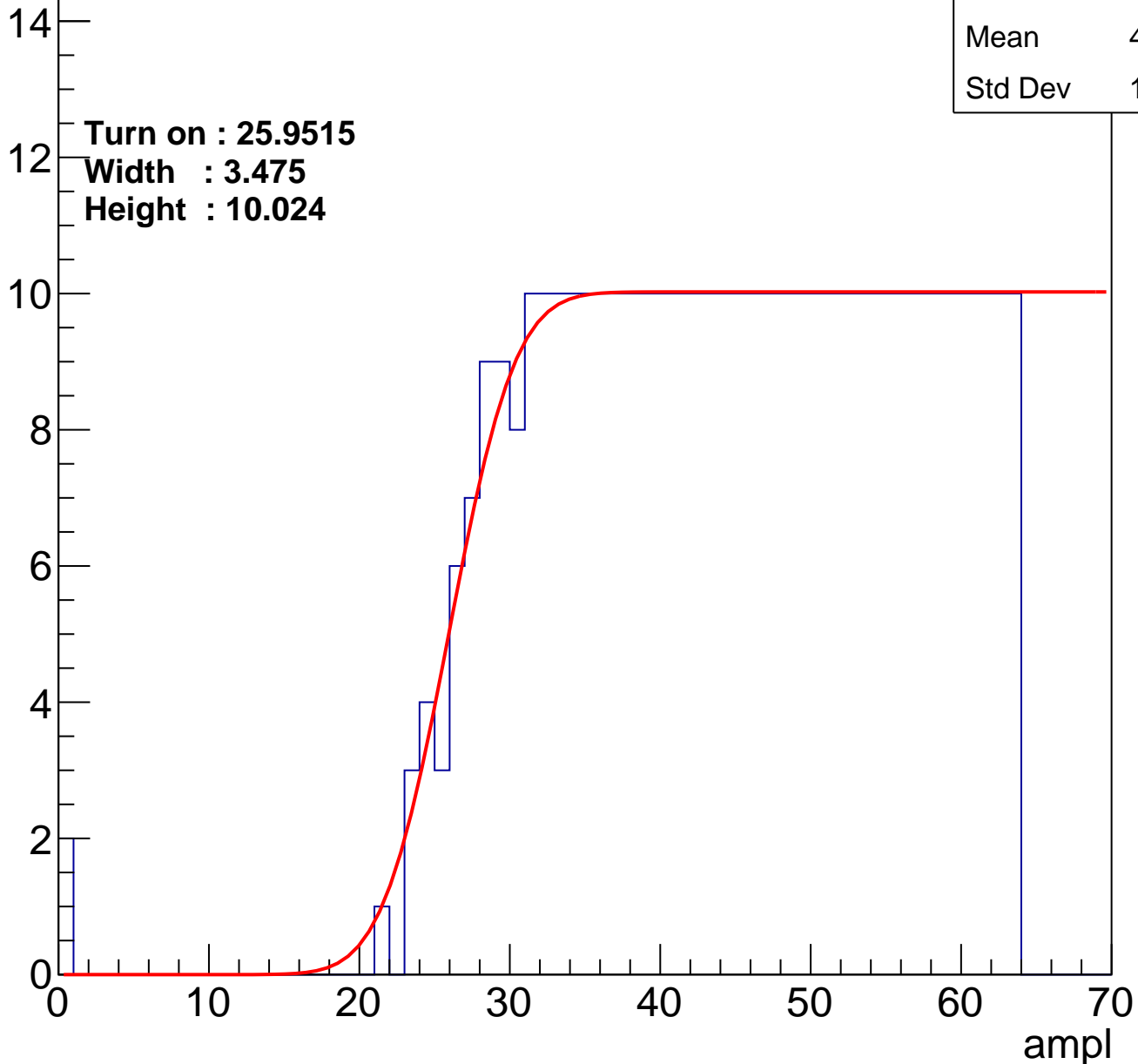
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 25.9515

Width : 3.475

Height : 10.024

Entry



B1L000S, U12-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.83
Std Dev	11.56

Turn on : 23.7732

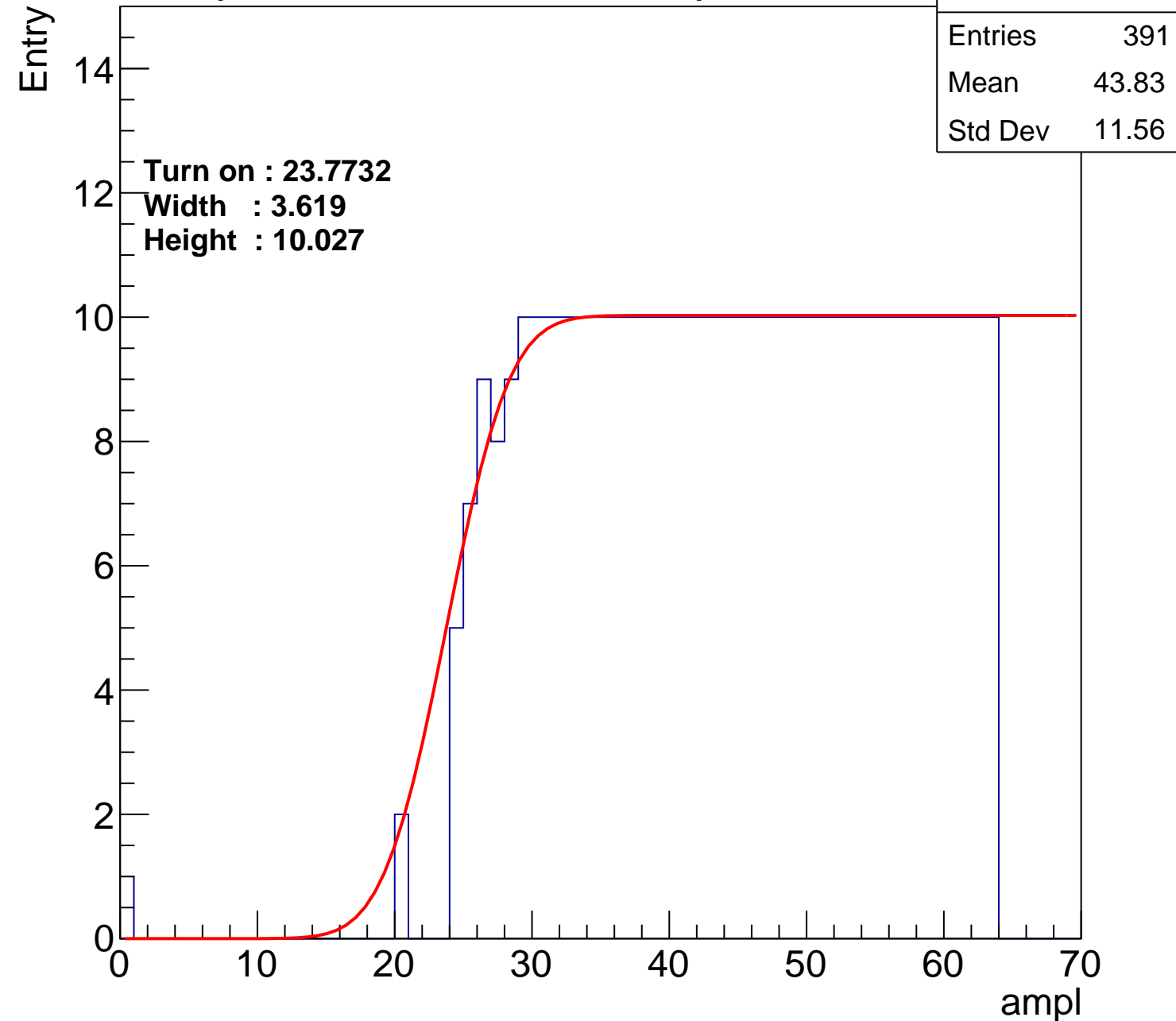
Width : 3.619

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch75

calib_packv5_042523_0143.root, FC#3, port B3

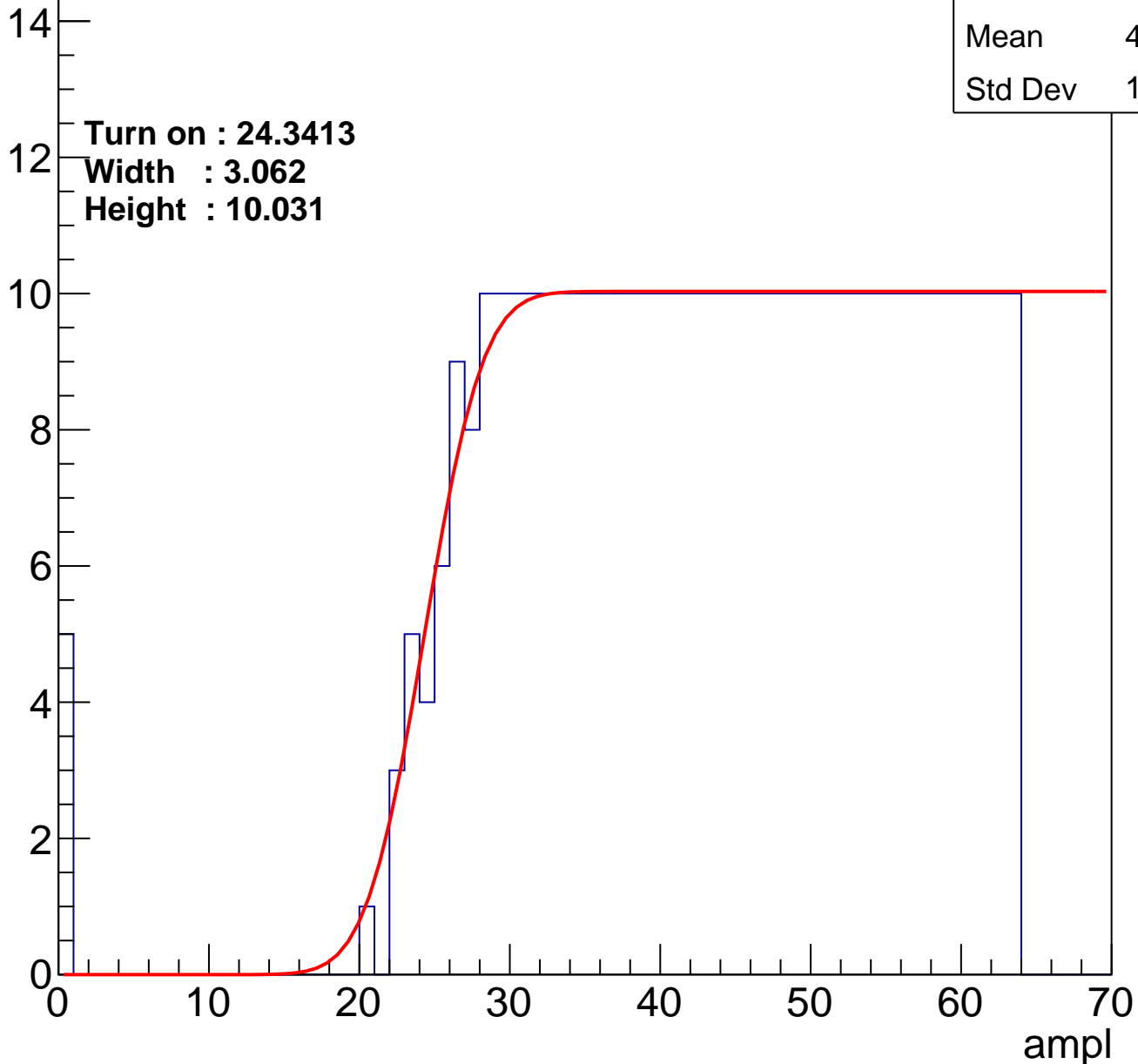
Entries	401
Mean	43.08
Std Dev	12.46

Turn on : 24.3413

Width : 3.062

Height : 10.031

Entry



B1L000S, U12-ch76

calib_packv5_042523_0143.root, FC#3, port B3

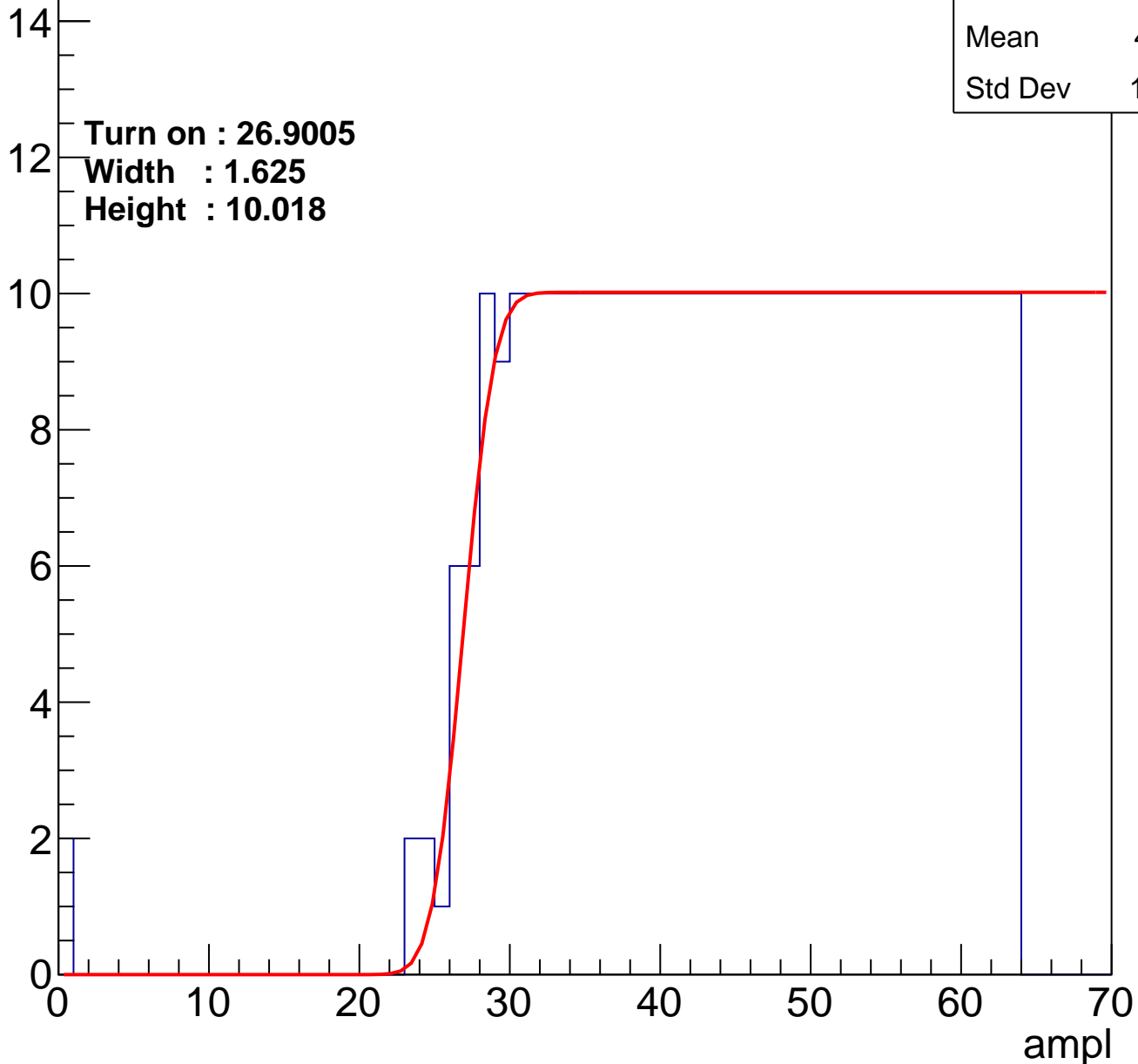
Entries	378
Mean	44.41
Std Dev	11.38

Turn on : 26.9005

Width : 1.625

Height : 10.018

Entry



B1L000S, U12-ch77

calib_packv5_042523_0143.root, FC#3, port B3

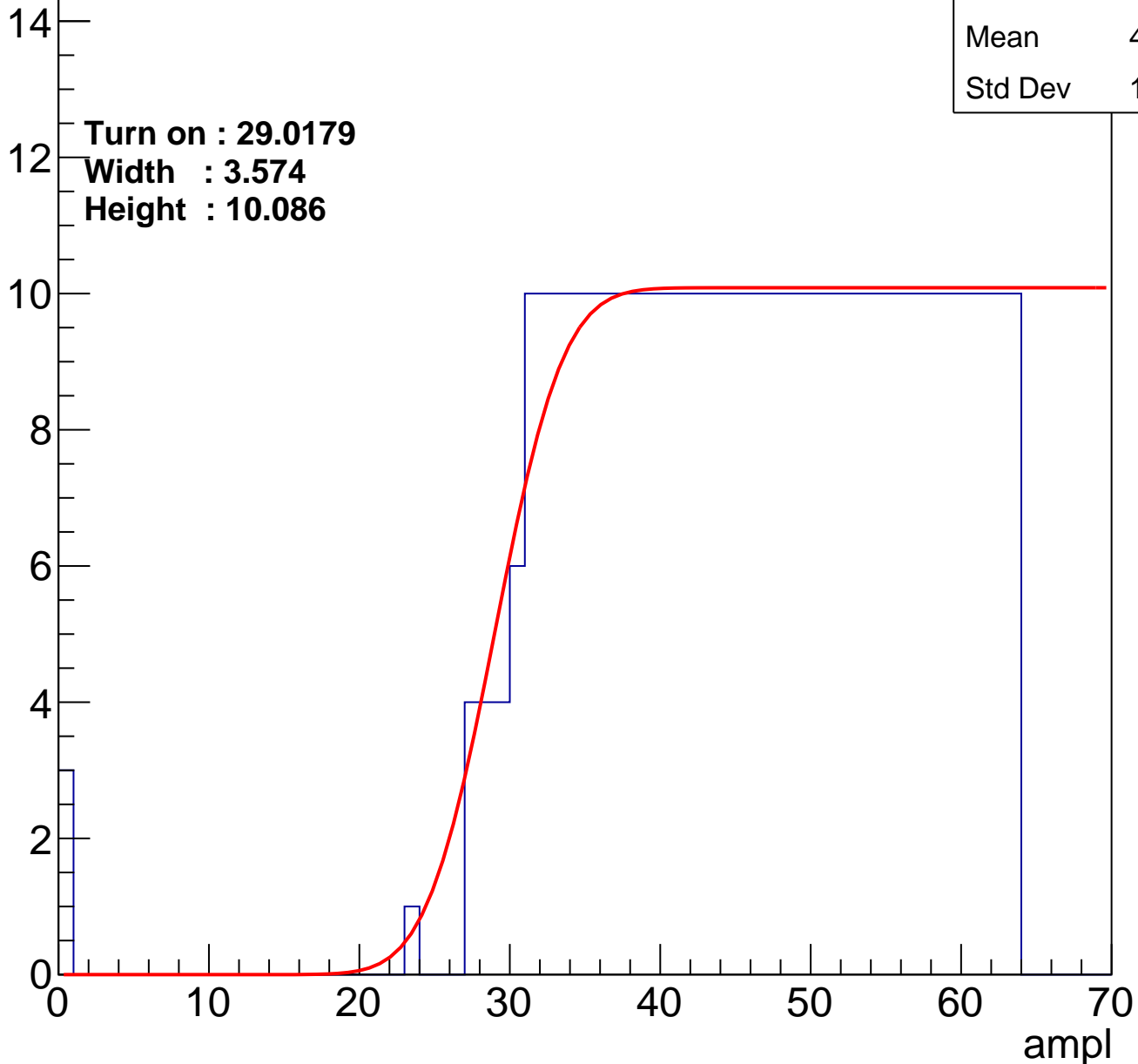
Entries	352
Mean	45.59
Std Dev	10.99

Turn on : 29.0179

Width : 3.574

Height : 10.086

Entry



B1L000S, U12-ch78

calib_packv5_042523_0143.root, FC#3, port B3

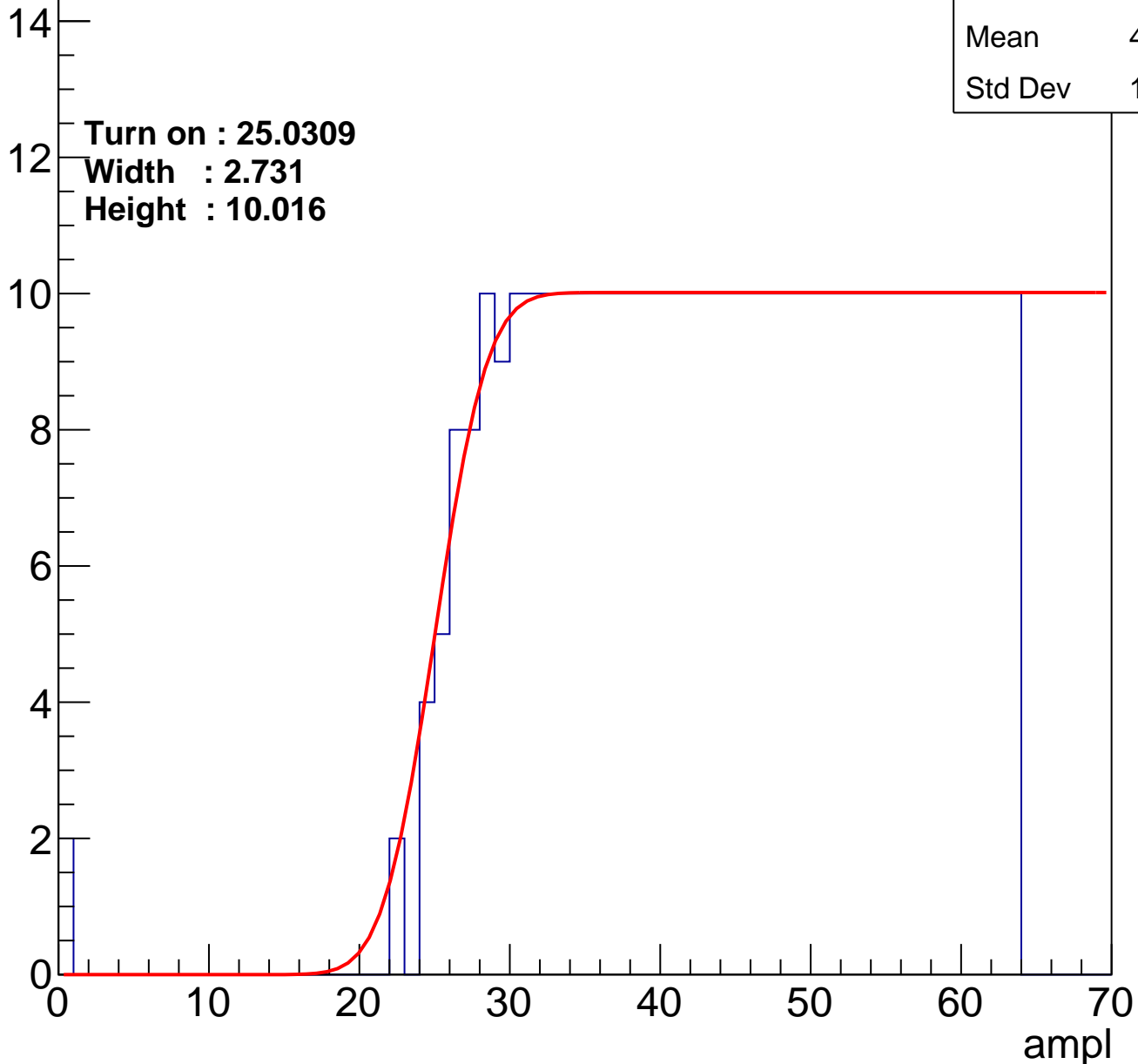
Entries	388
Mean	43.92
Std Dev	11.64

Turn on : 25.0309

Width : 2.731

Height : 10.016

Entry



B1L000S, U12-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.02
Std Dev	11.17

Turn on : 28.5310

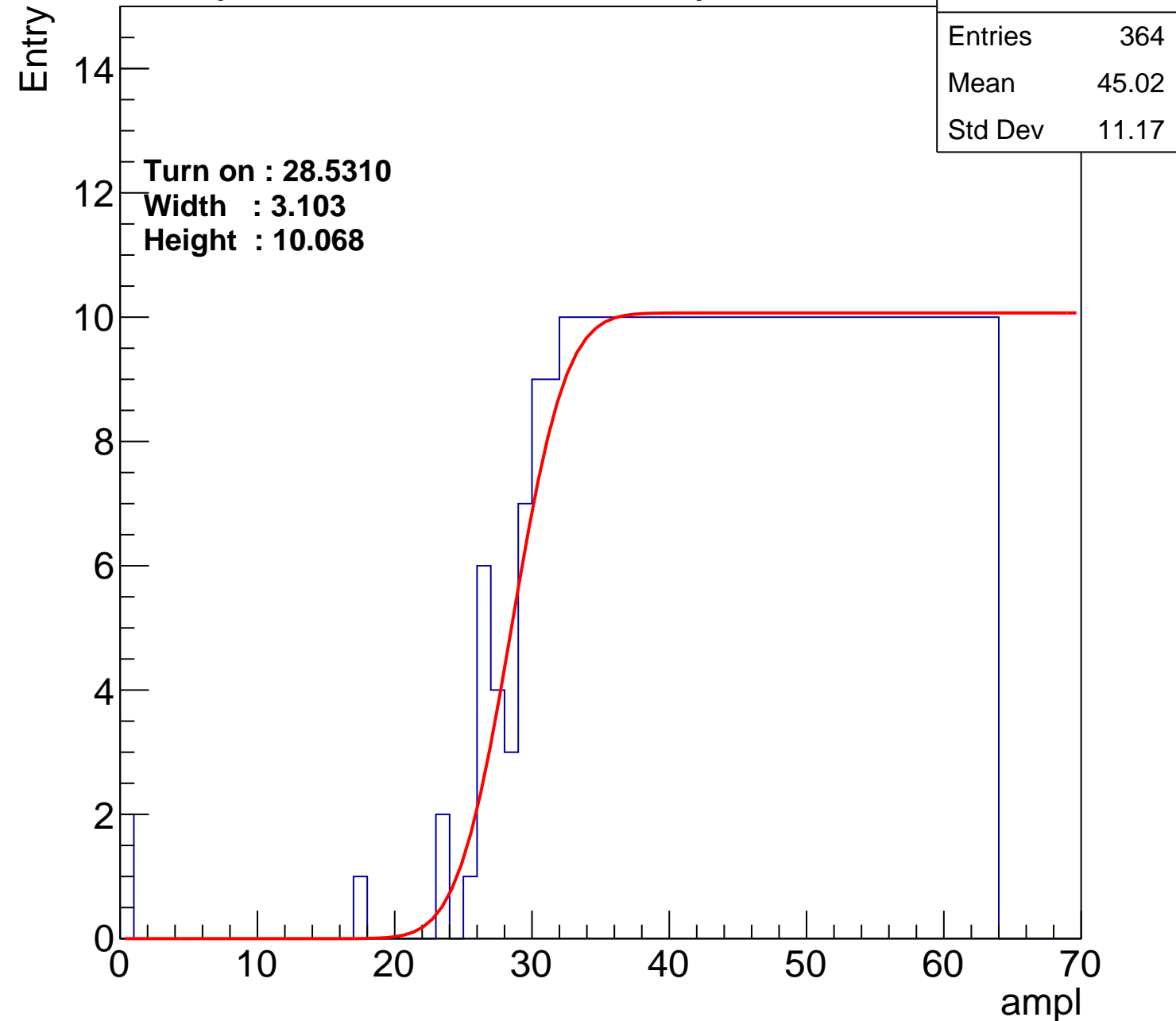
Width : 3.103

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.38
Std Dev	11.57

Turn on : 26.9111

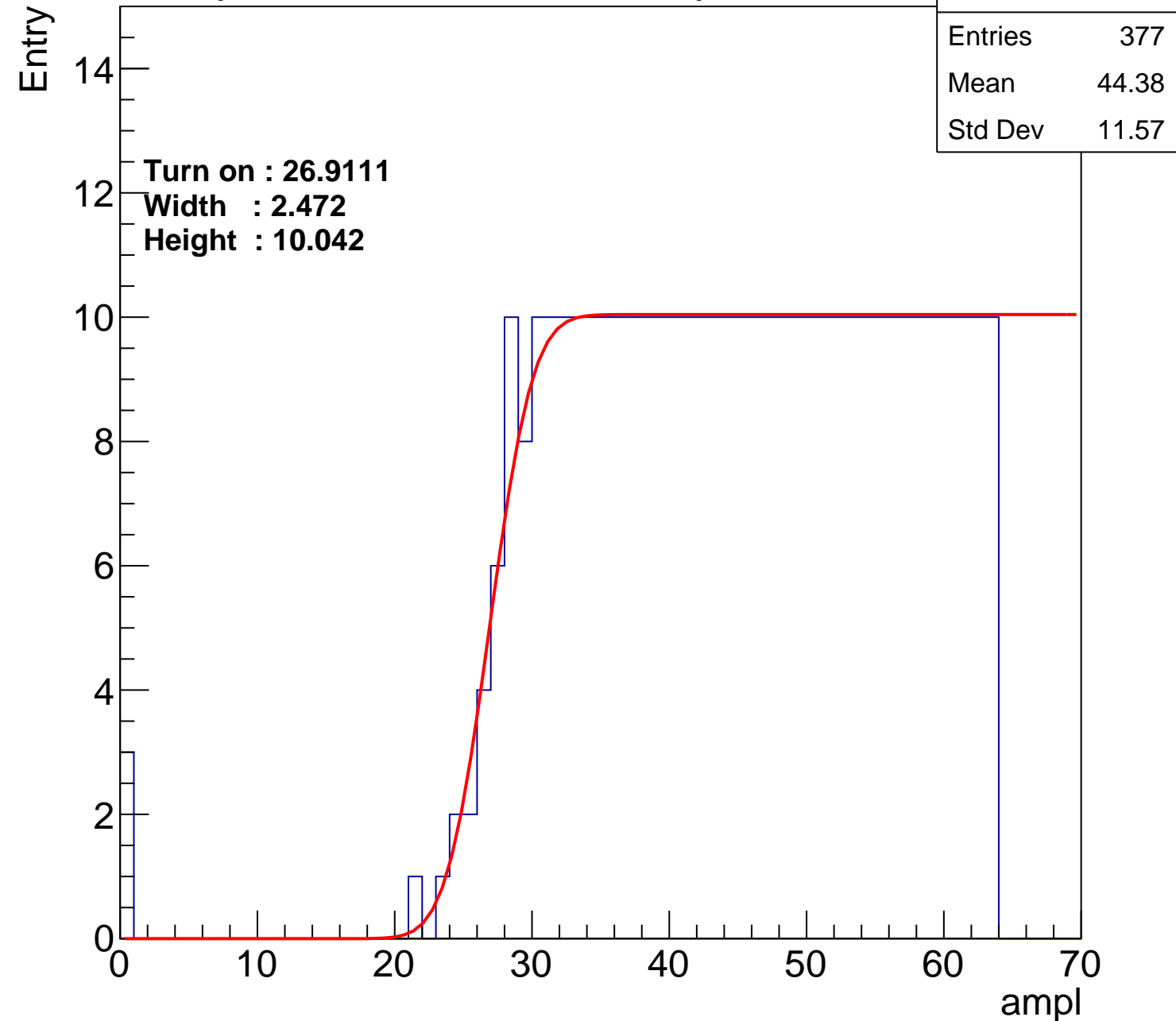
Width : 2.472

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch81

calib_packv5_042523_0143.root, FC#3, port B3

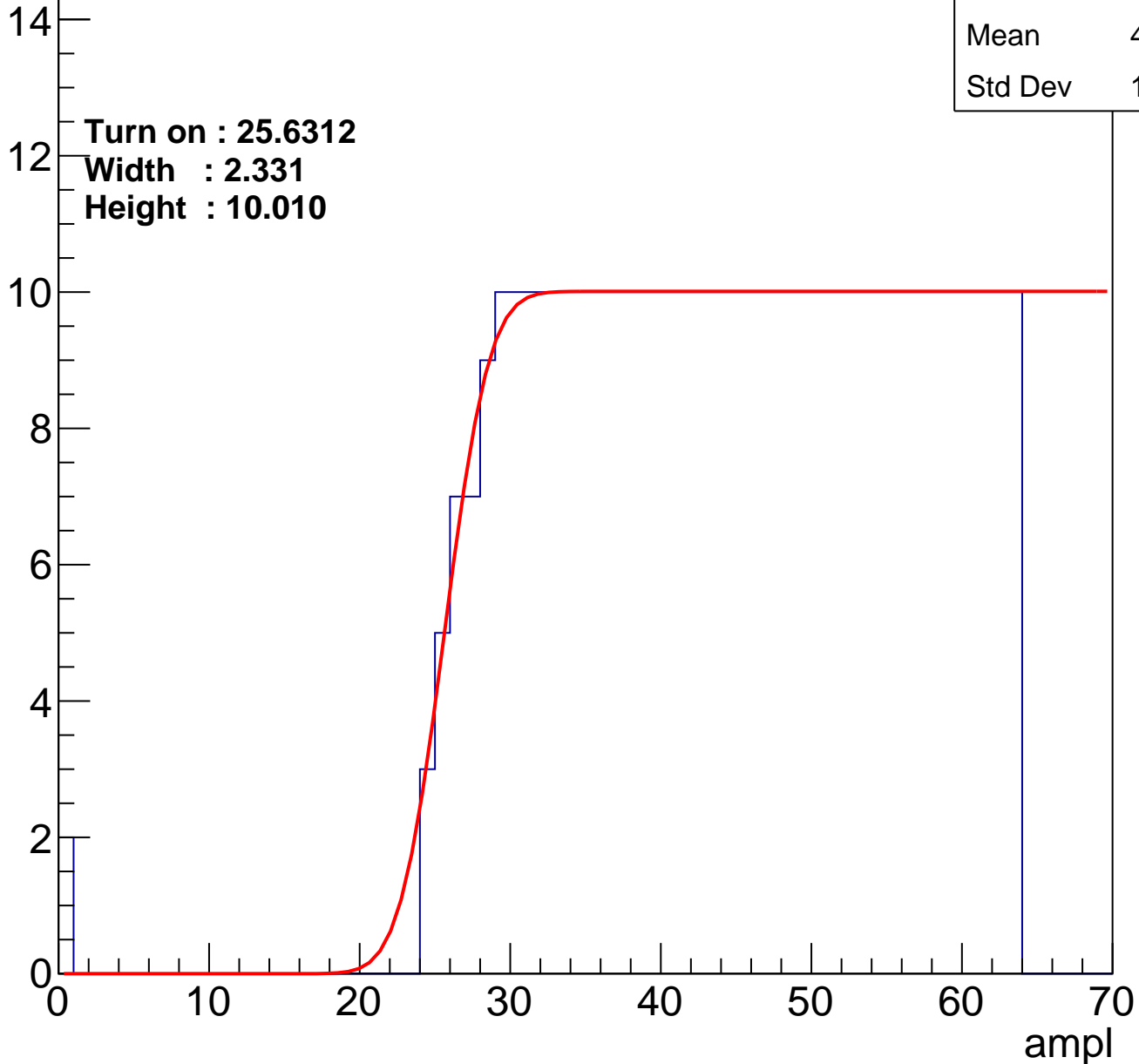
Entries	383
Mean	44.18
Std Dev	11.49

Turn on : 25.6312

Width : 2.331

Height : 10.010

Entry



B1L000S, U12-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.7
Std Dev	11.95

Turn on : 25.4628

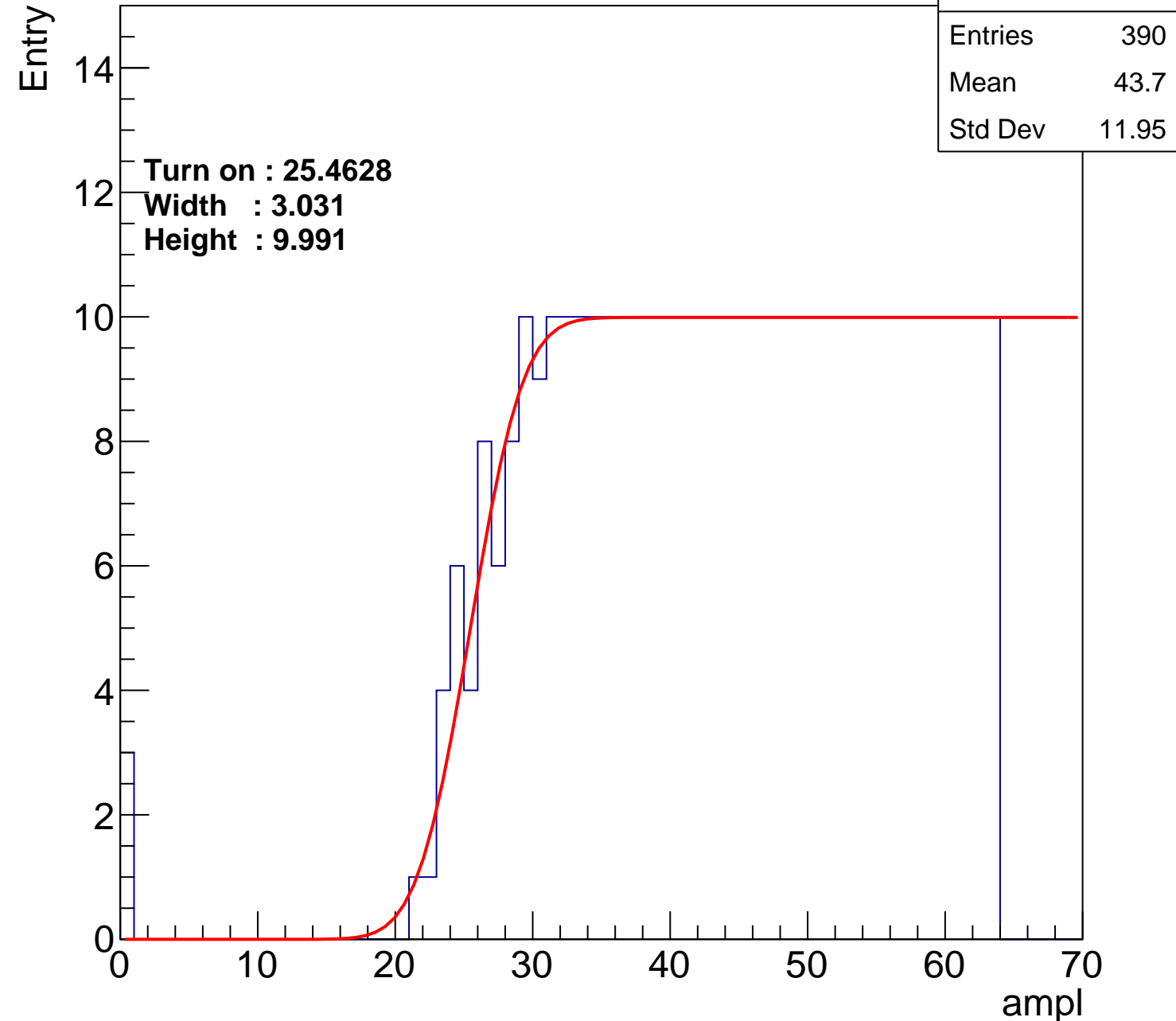
Width : 3.031

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.62
Std Dev	11.83

Turn on : 28.2343

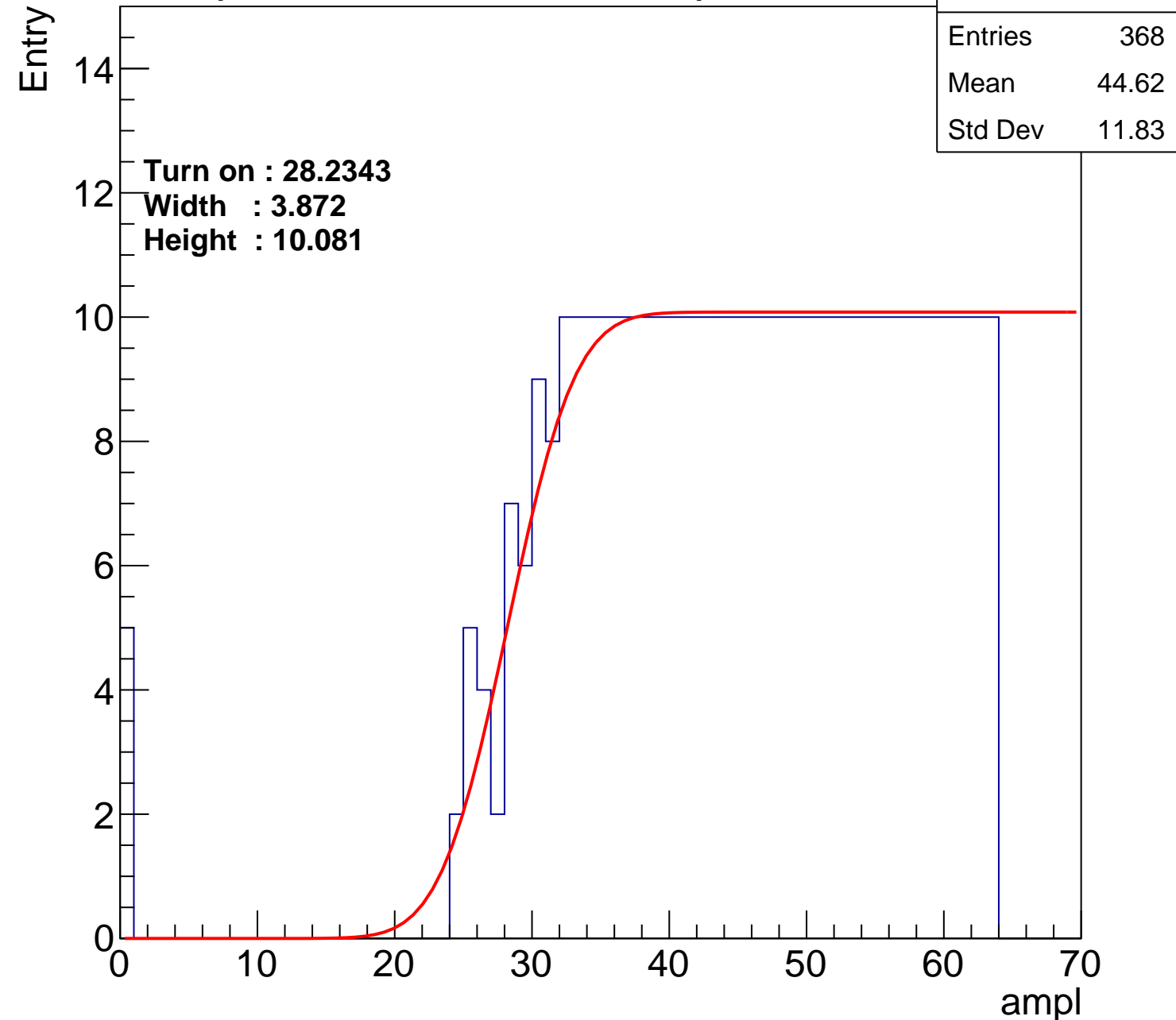
Width : 3.872

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.17
Std Dev	11.38

Turn on : 25.7896

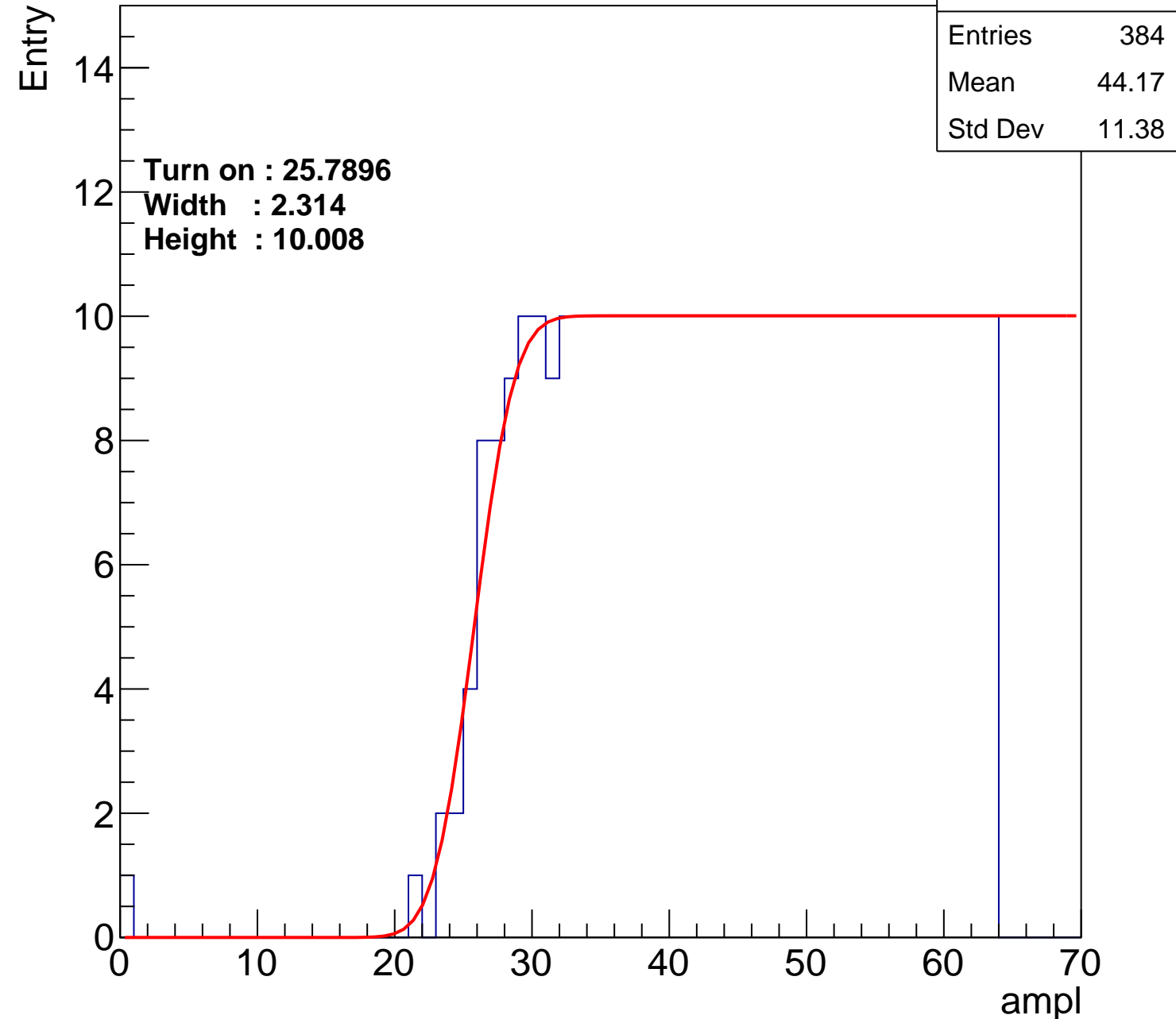
Width : 2.314

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.79
Std Dev	11.07

Turn on : 27.4600

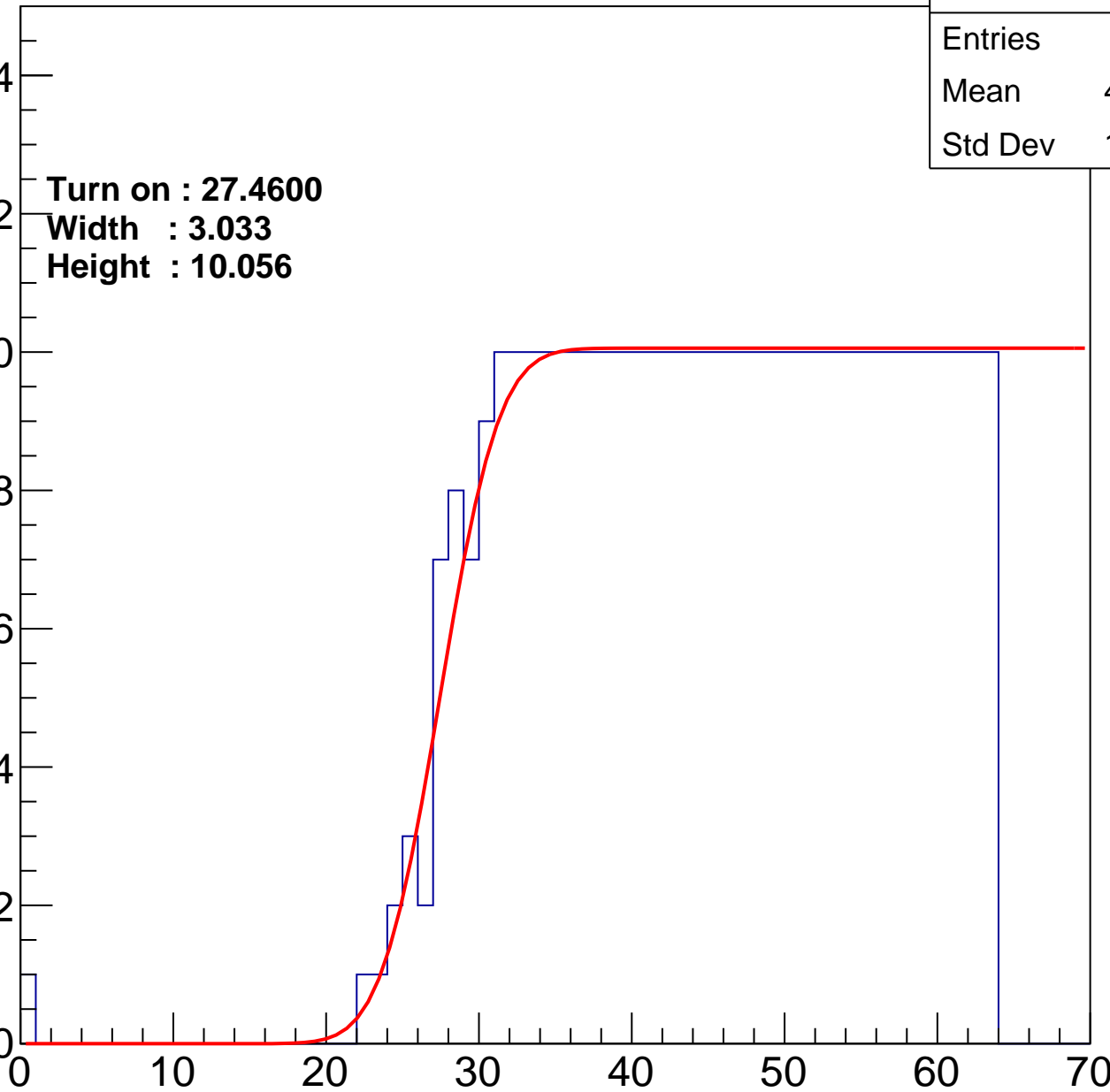
Width : 3.033

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch86

calib_packv5_042523_0143.root, FC#3, port B3

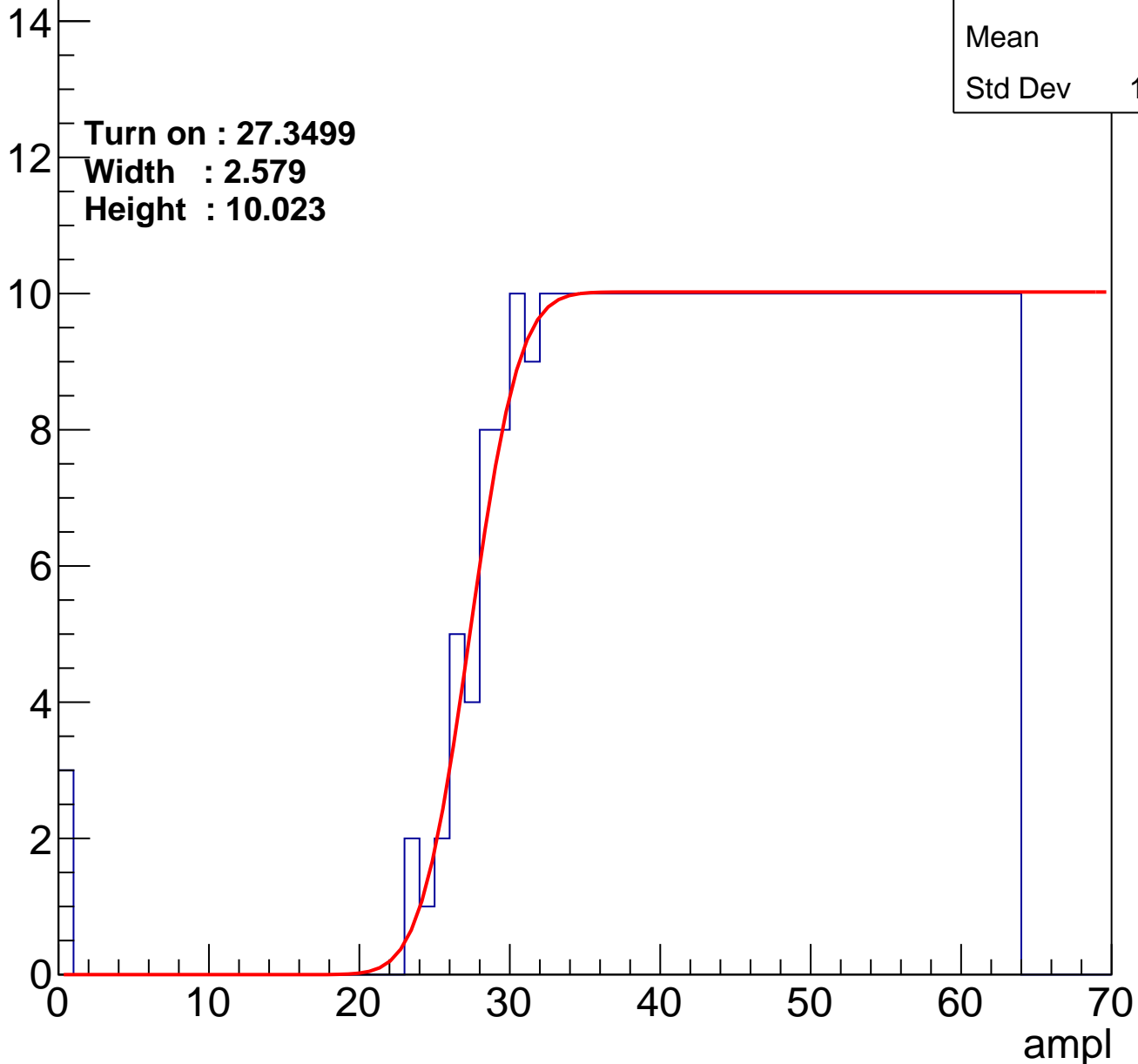
Entries	372
Mean	44.6
Std Dev	11.47

Turn on : 27.3499

Width : 2.579

Height : 10.023

Entry



B1L000S, U12-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.89
Std Dev	11.02

Turn on : 27.3064

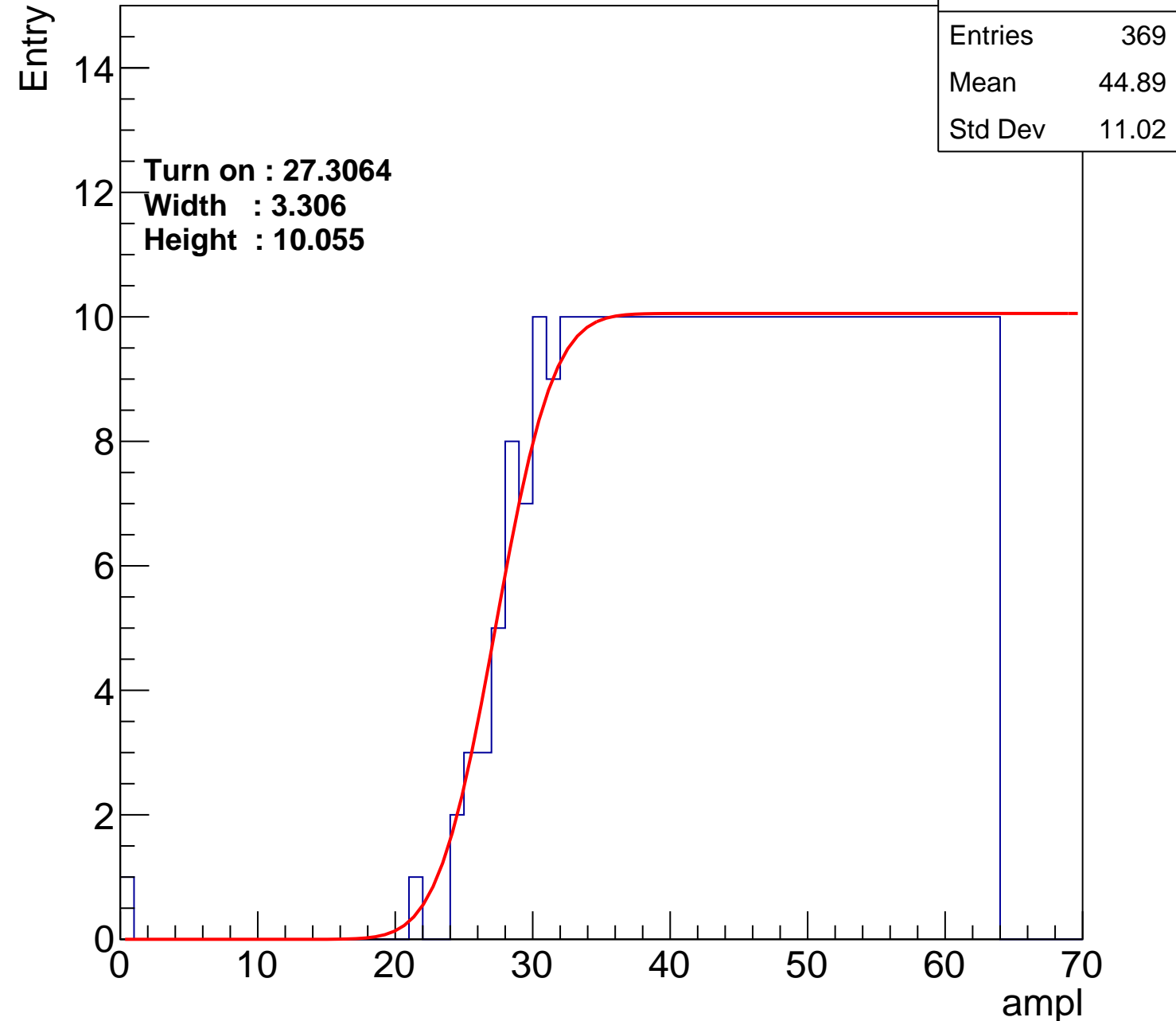
Width : 3.306

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch88

calib_packv5_042523_0143.root, FC#3, port B3

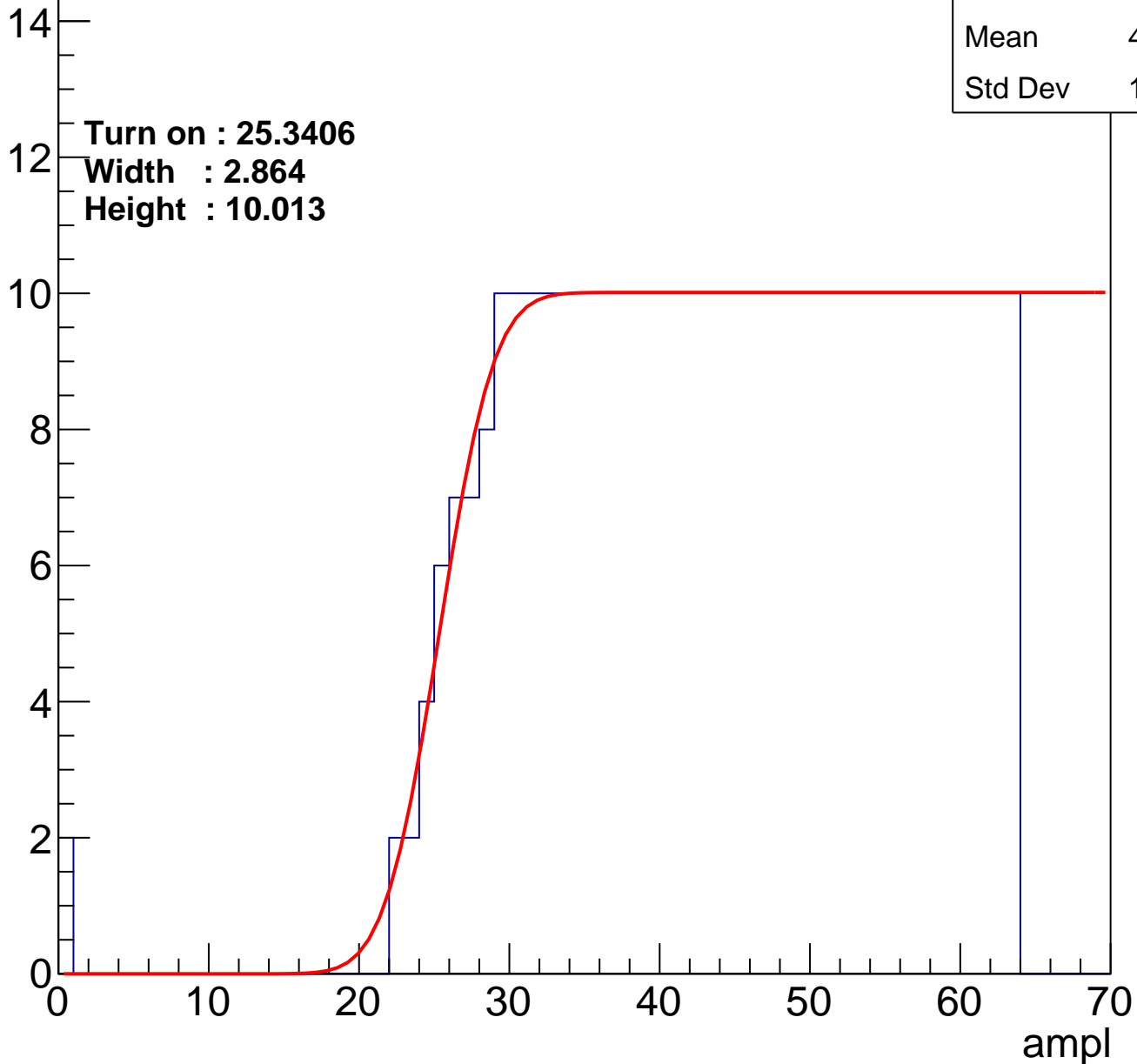
Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.3406

Width : 2.864

Height : 10.013

Entry



B1L000S, U12-ch89

calib_packv5_042523_0143.root, FC#3, port B3

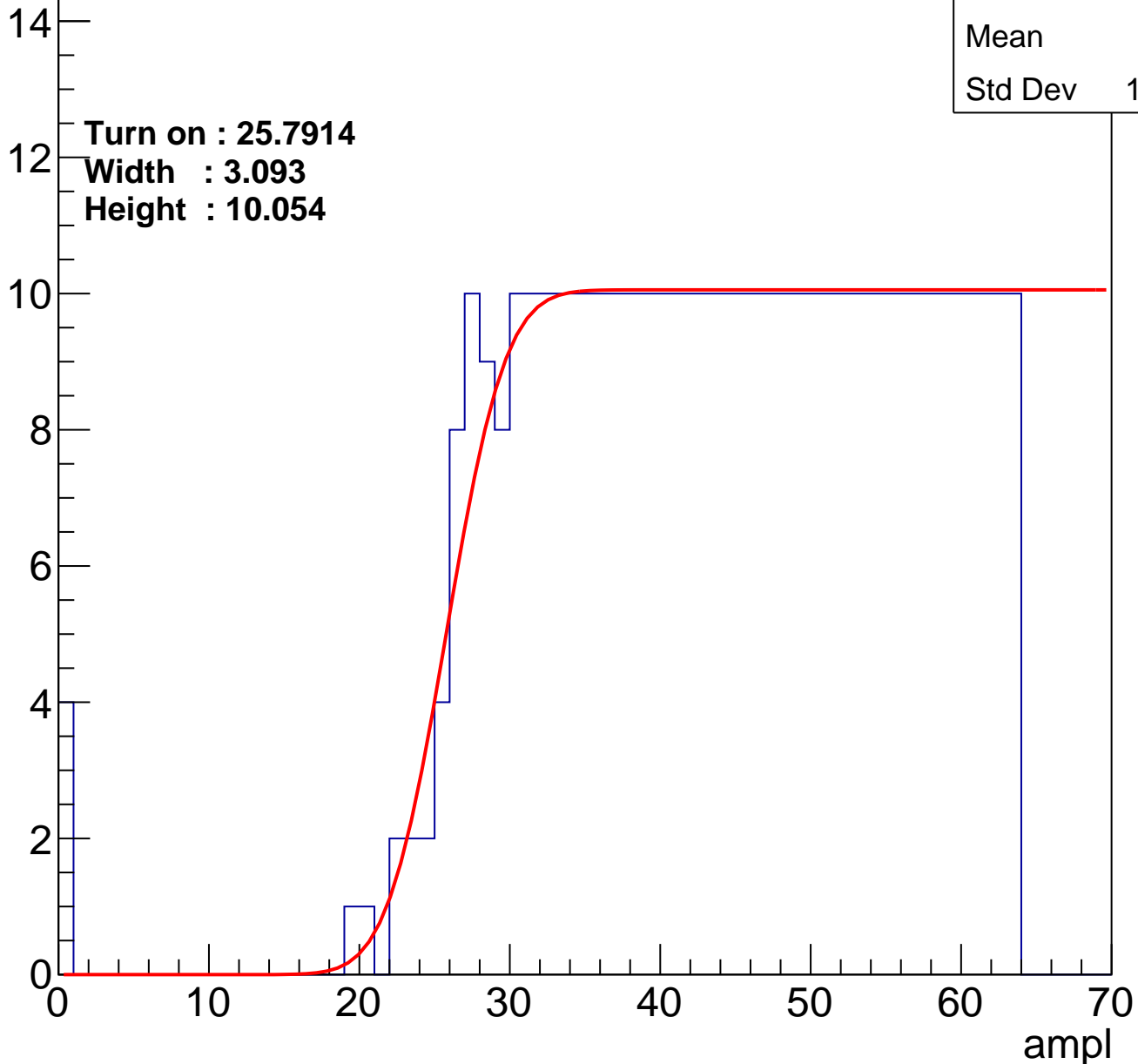
Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 25.7914

Width : 3.093

Height : 10.054

Entry



B1L000S, U12-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.2145

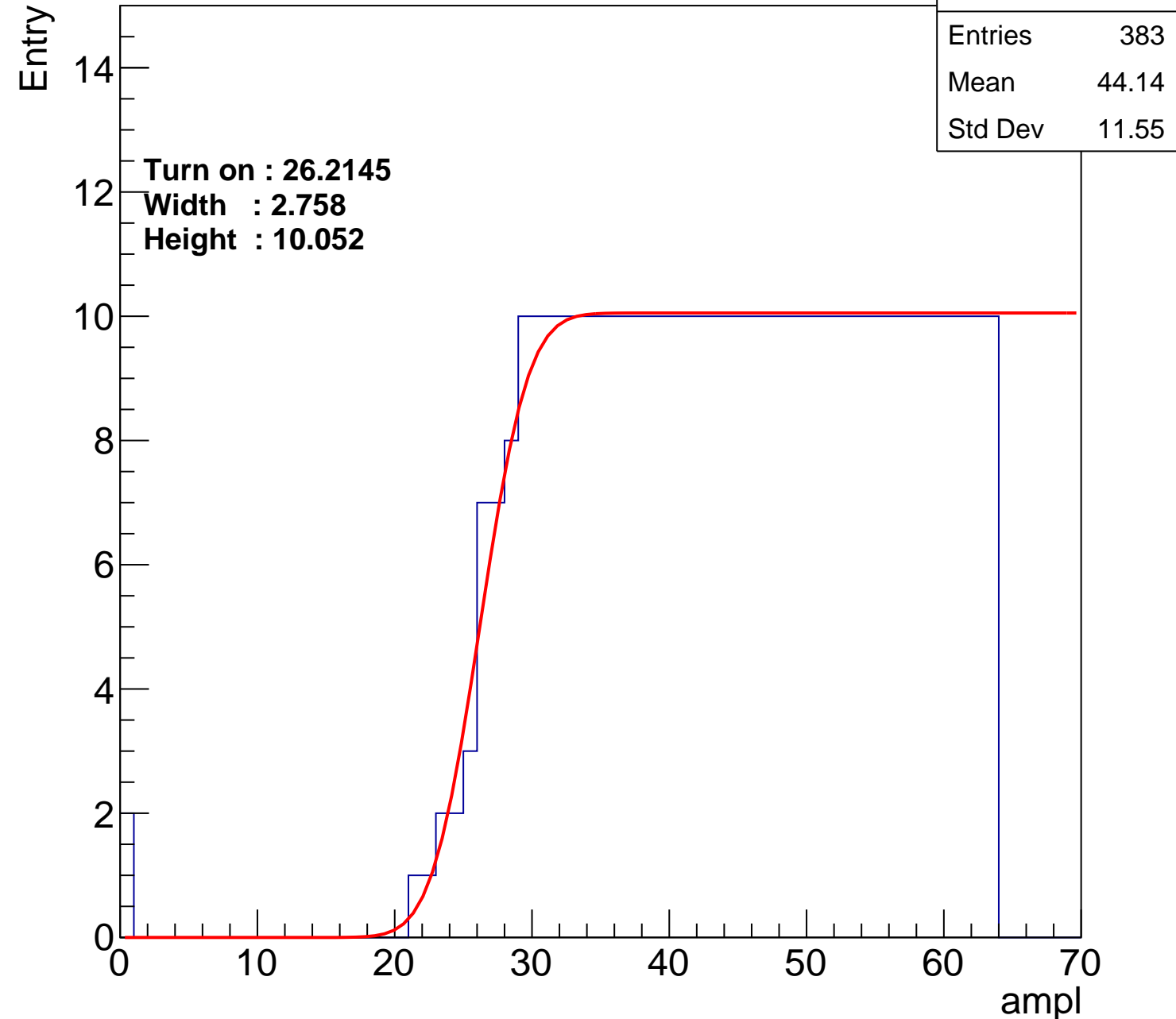
Width : 2.758

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.04

Turn on : 26.9420

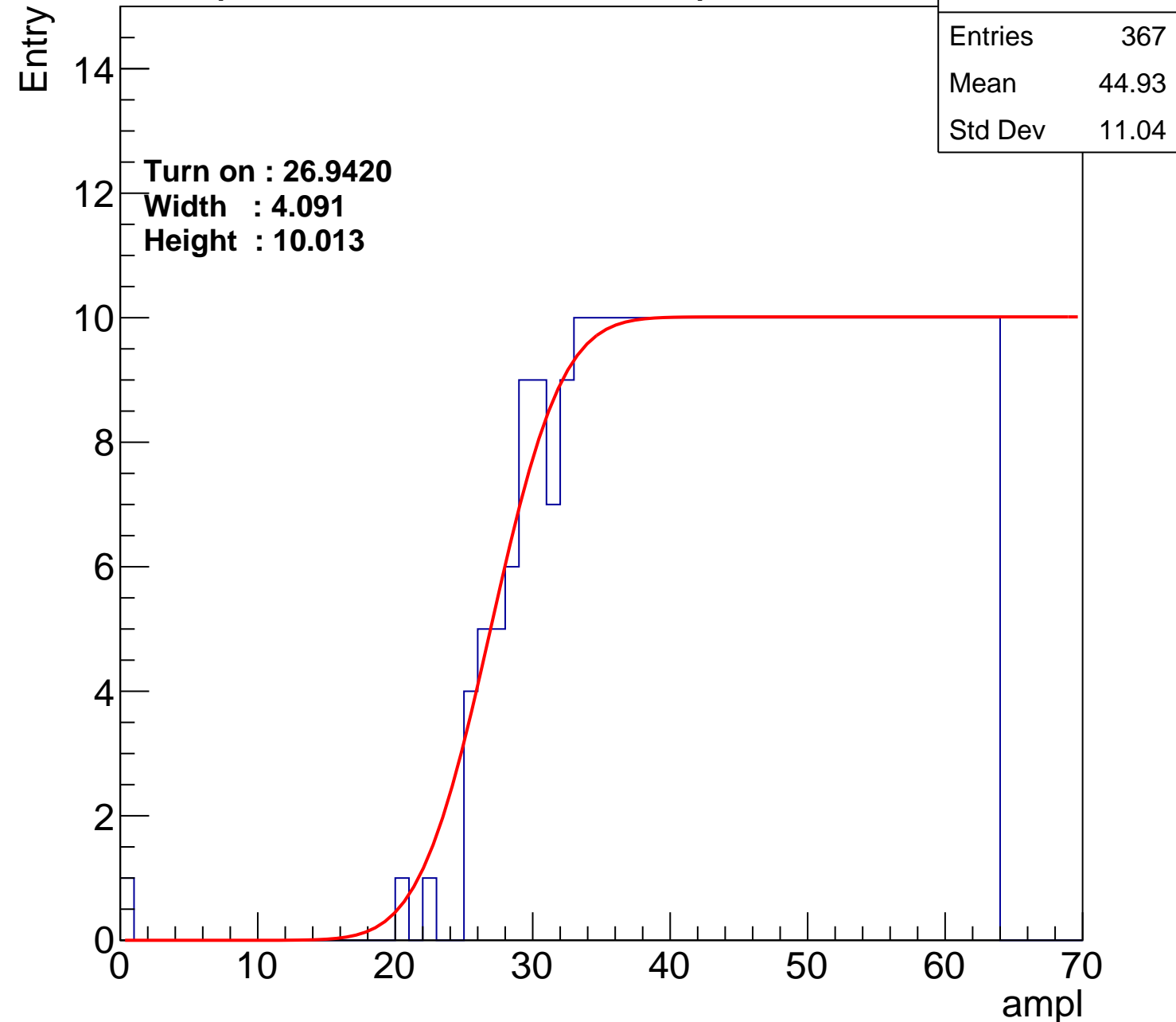
Width : 4.091

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch92

calib_packv5_042523_0143.root, FC#3, port B3

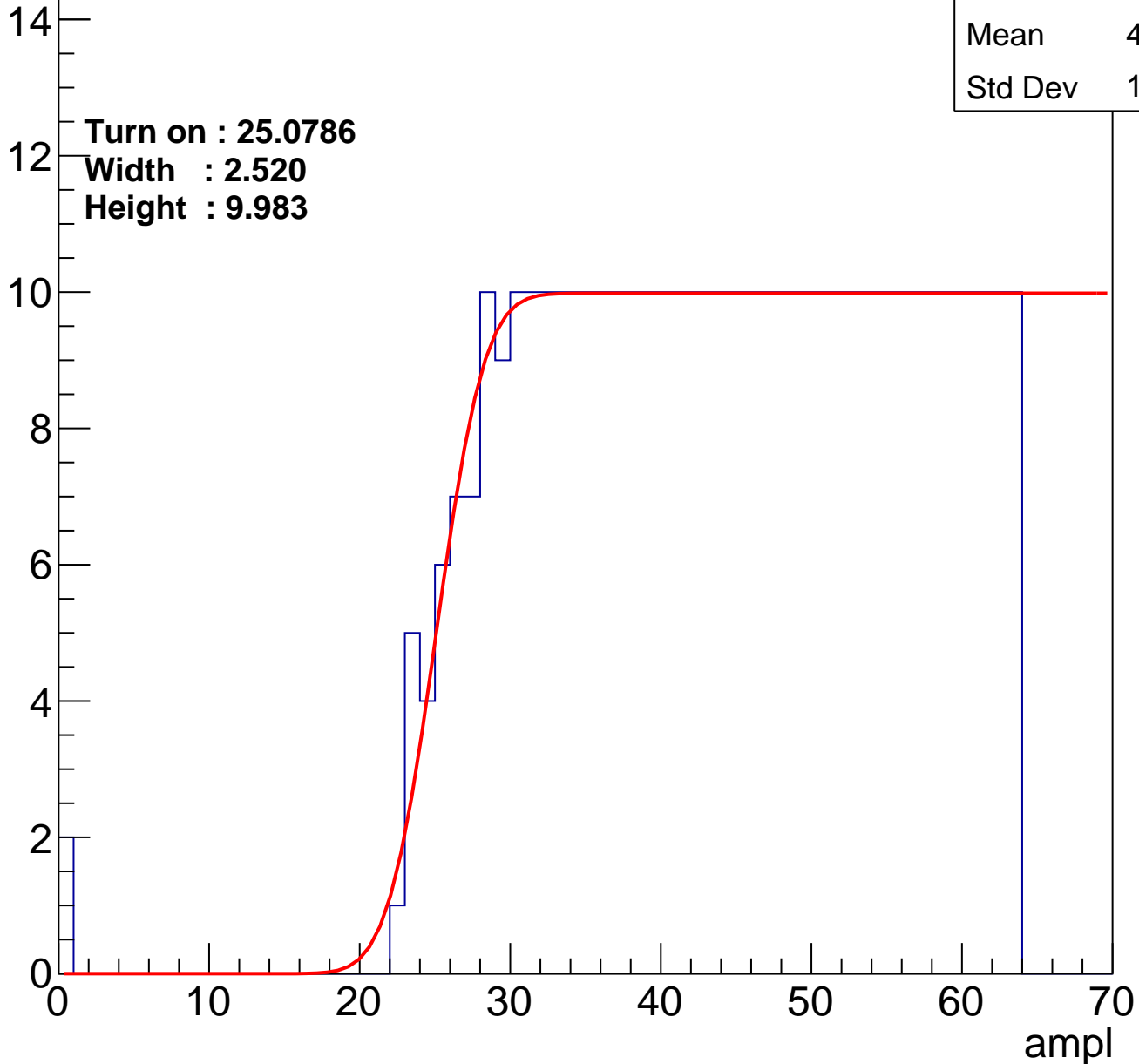
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.0786

Width : 2.520

Height : 9.983

Entry



B1L000S, U12-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.67
Std Dev	11.3

Turn on : 27.1603

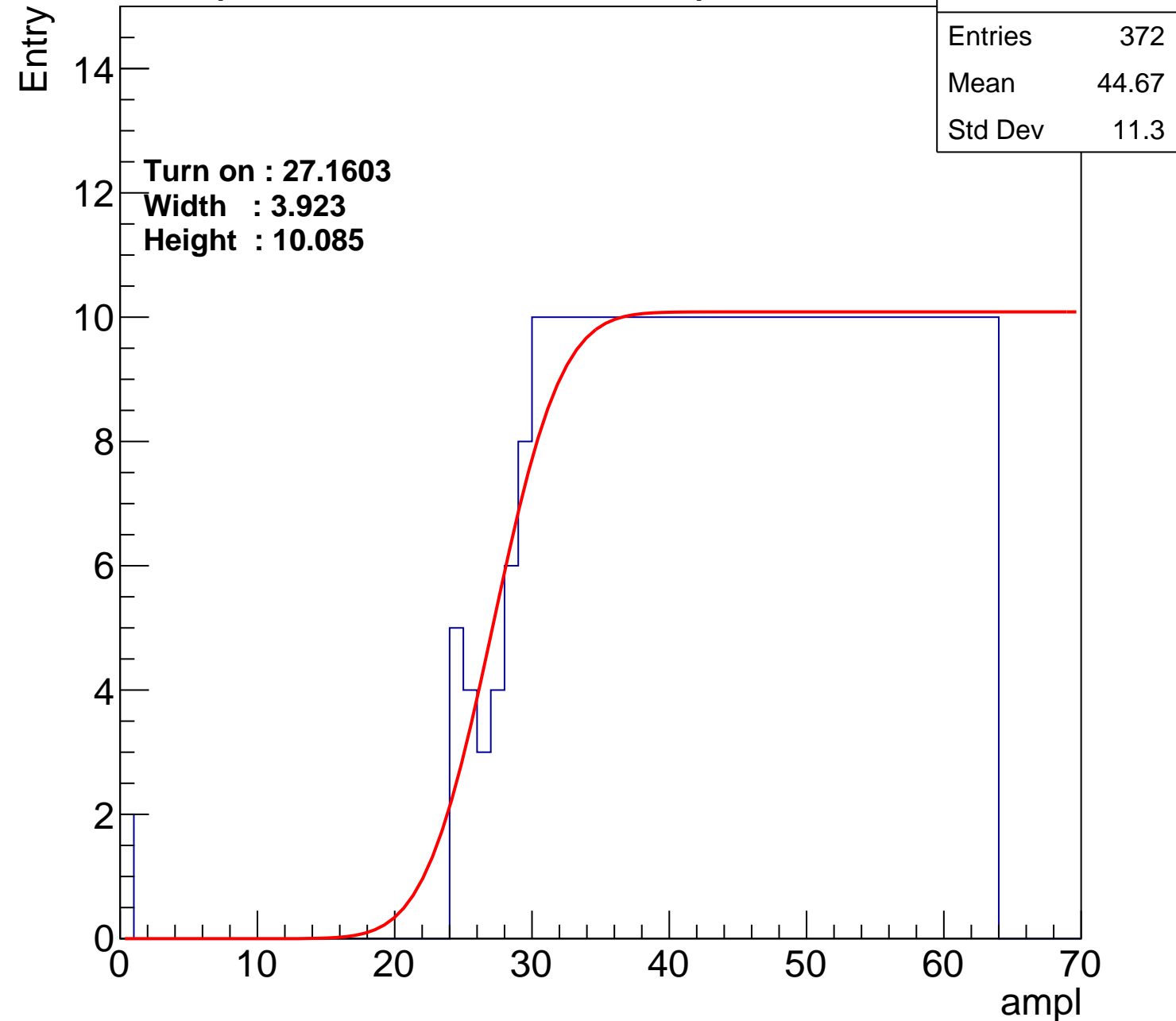
Width : 3.923

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 25.7447

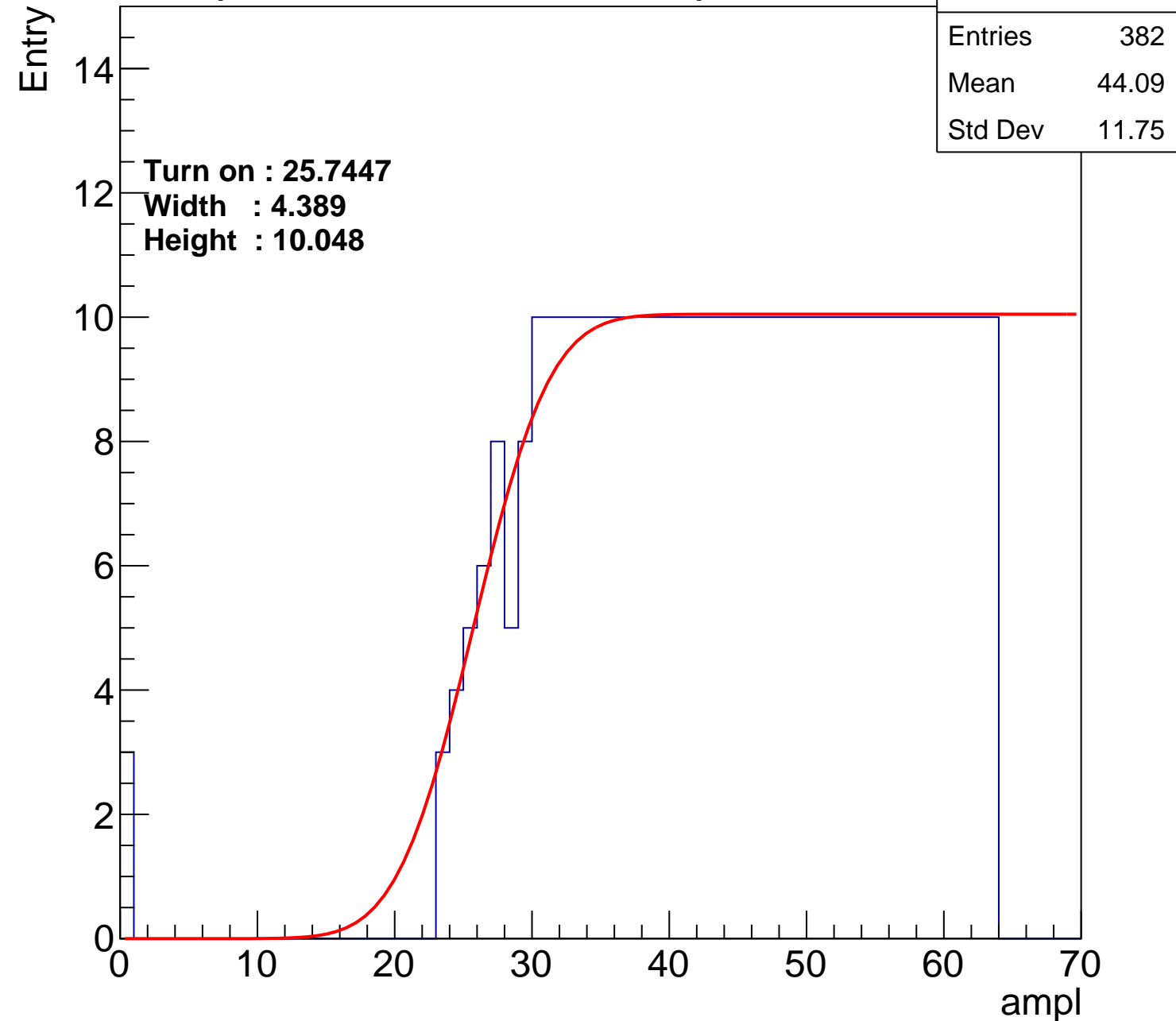
Width : 4.389

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.23
Std Dev	11.76

Turn on : 26.2973

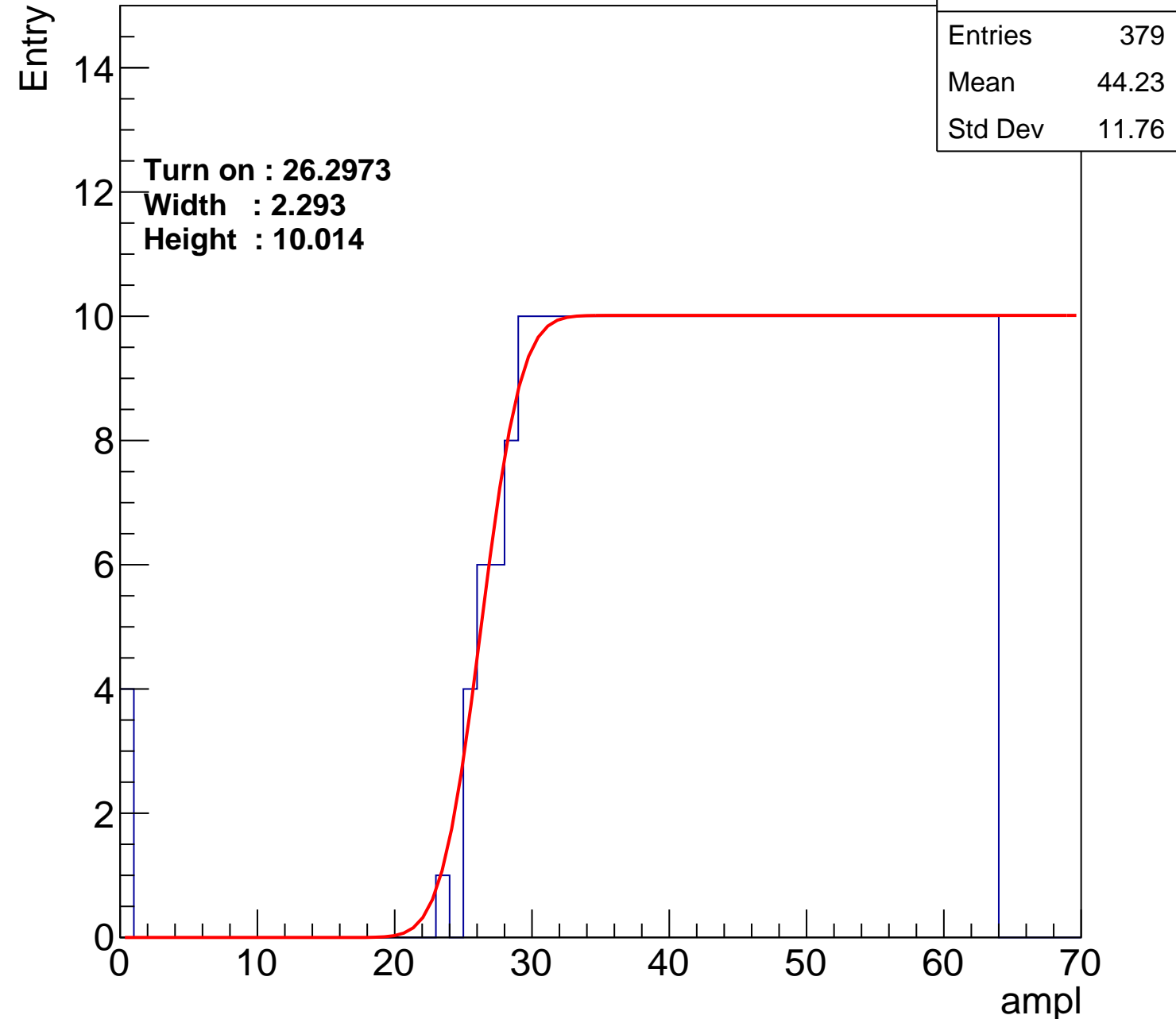
Width : 2.293

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch96

calib_packv5_042523_0143.root, FC#3, port B3

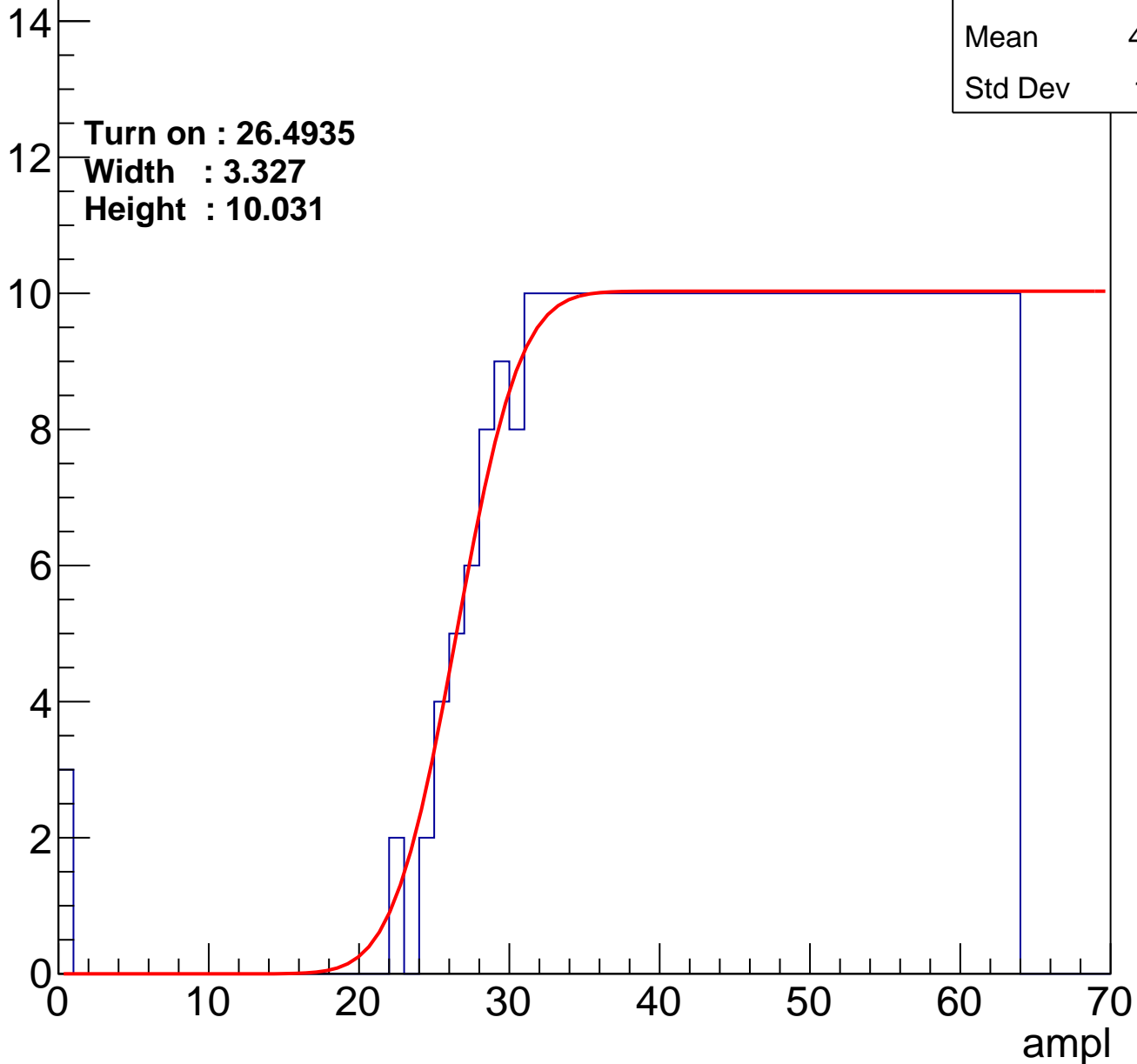
Entries	377
Mean	44.35
Std Dev	11.61

Turn on : 26.4935

Width : 3.327

Height : 10.031

Entry



B1L000S, U12-ch97

calib_packv5_042523_0143.root, FC#3, port B3

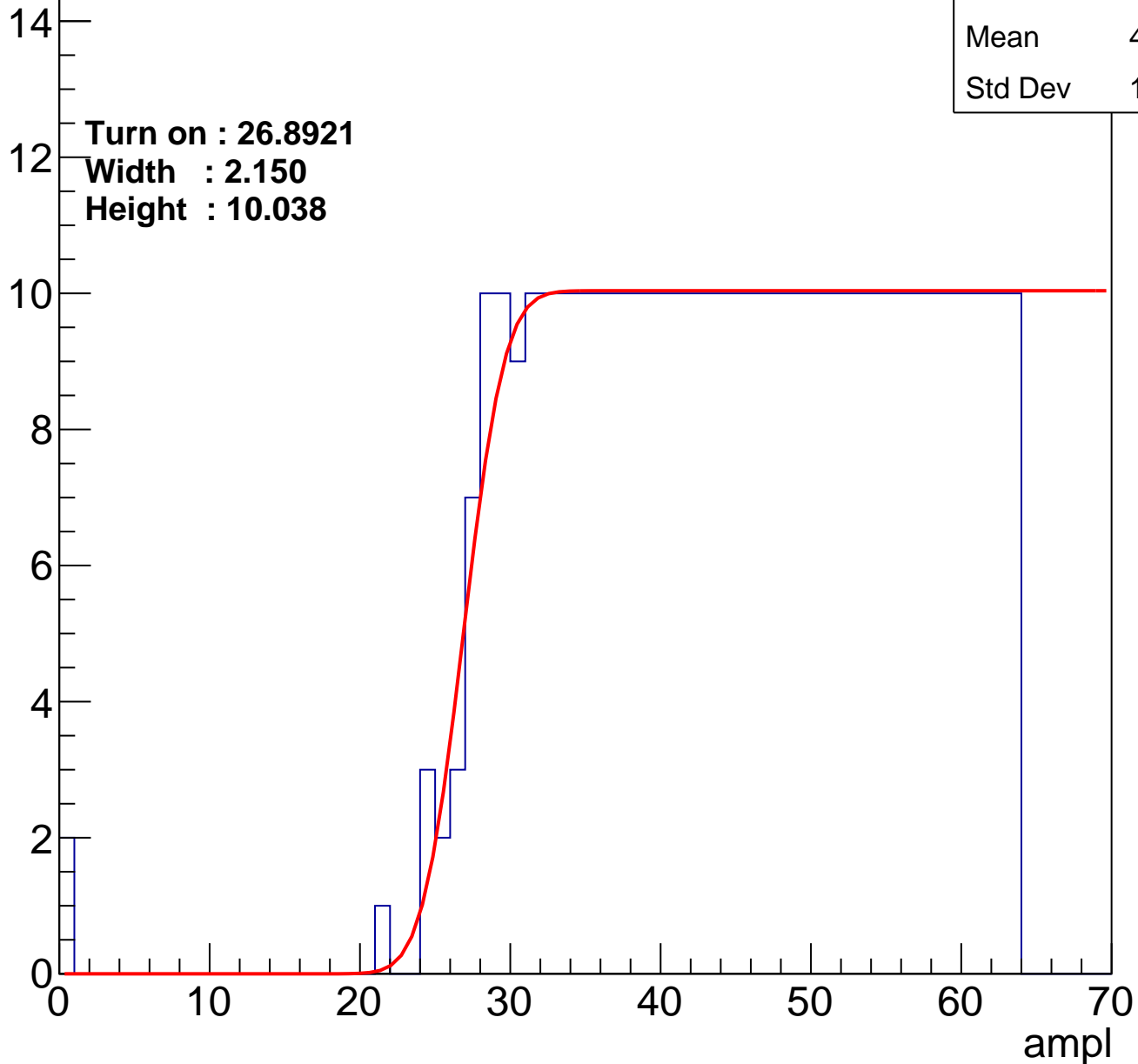
Entries	377
Mean	44.46
Std Dev	11.37

Turn on : 26.8921

Width : 2.150

Height : 10.038

Entry



B1L000S, U12-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.19
Std Dev	11.66

Turn on : 25.9694

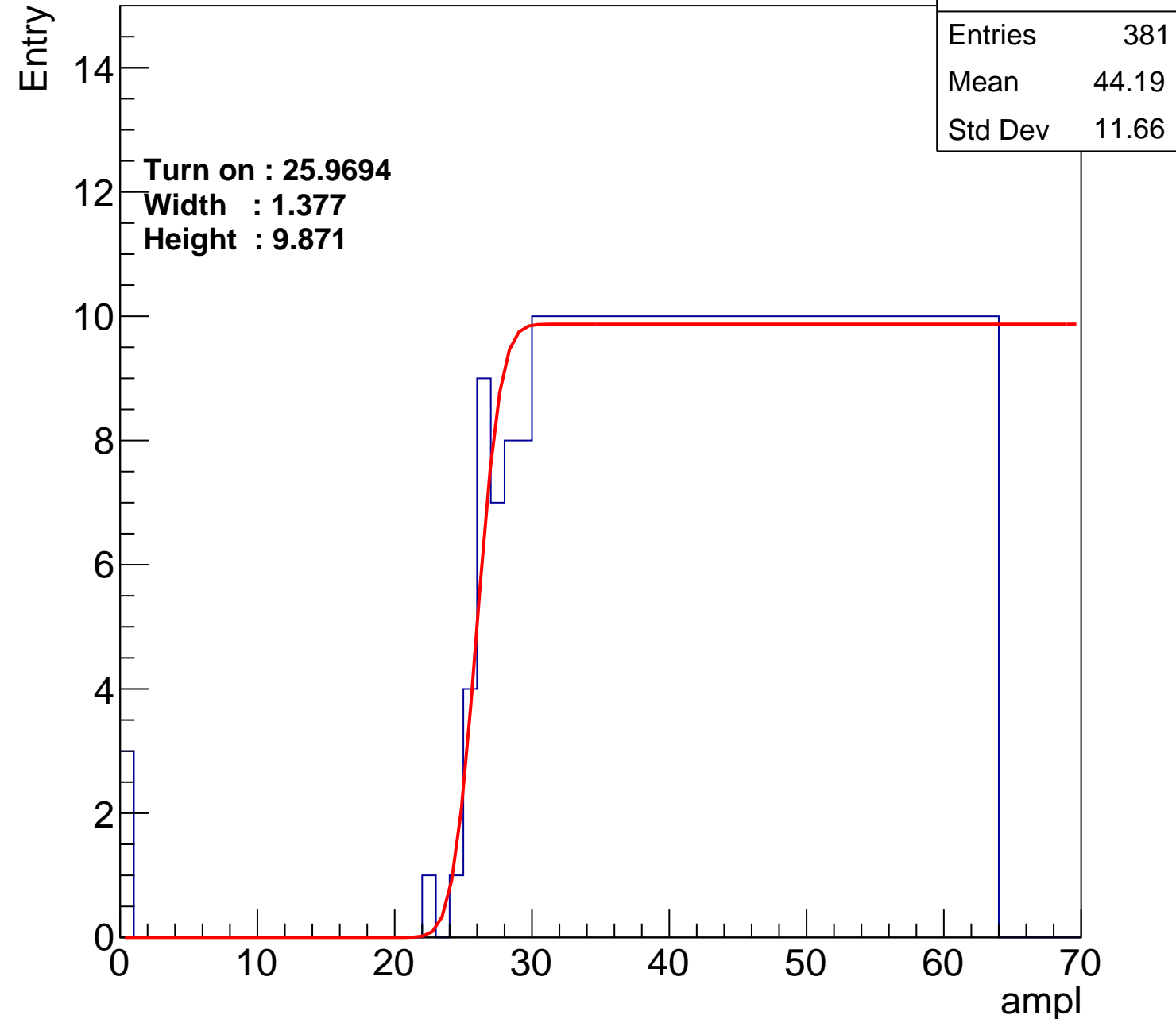
Width : 1.377

Height : 9.871

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch99

calib_packv5_042523_0143.root, FC#3, port B3

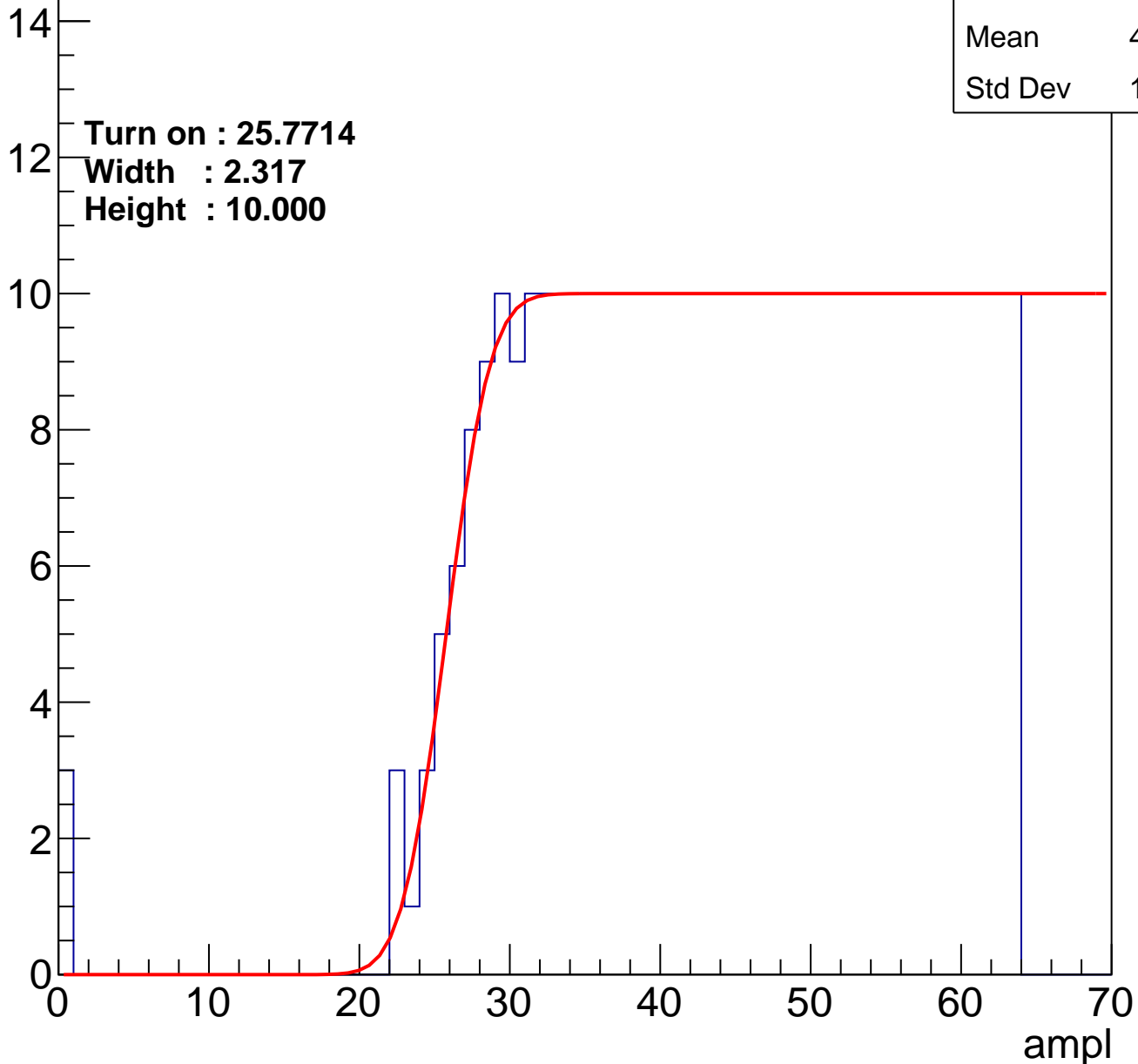
Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.7714

Width : 2.317

Height : 10.000

Entry



B1L000S, U12-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.78
Std Dev	11.71

Turn on : 25.3789

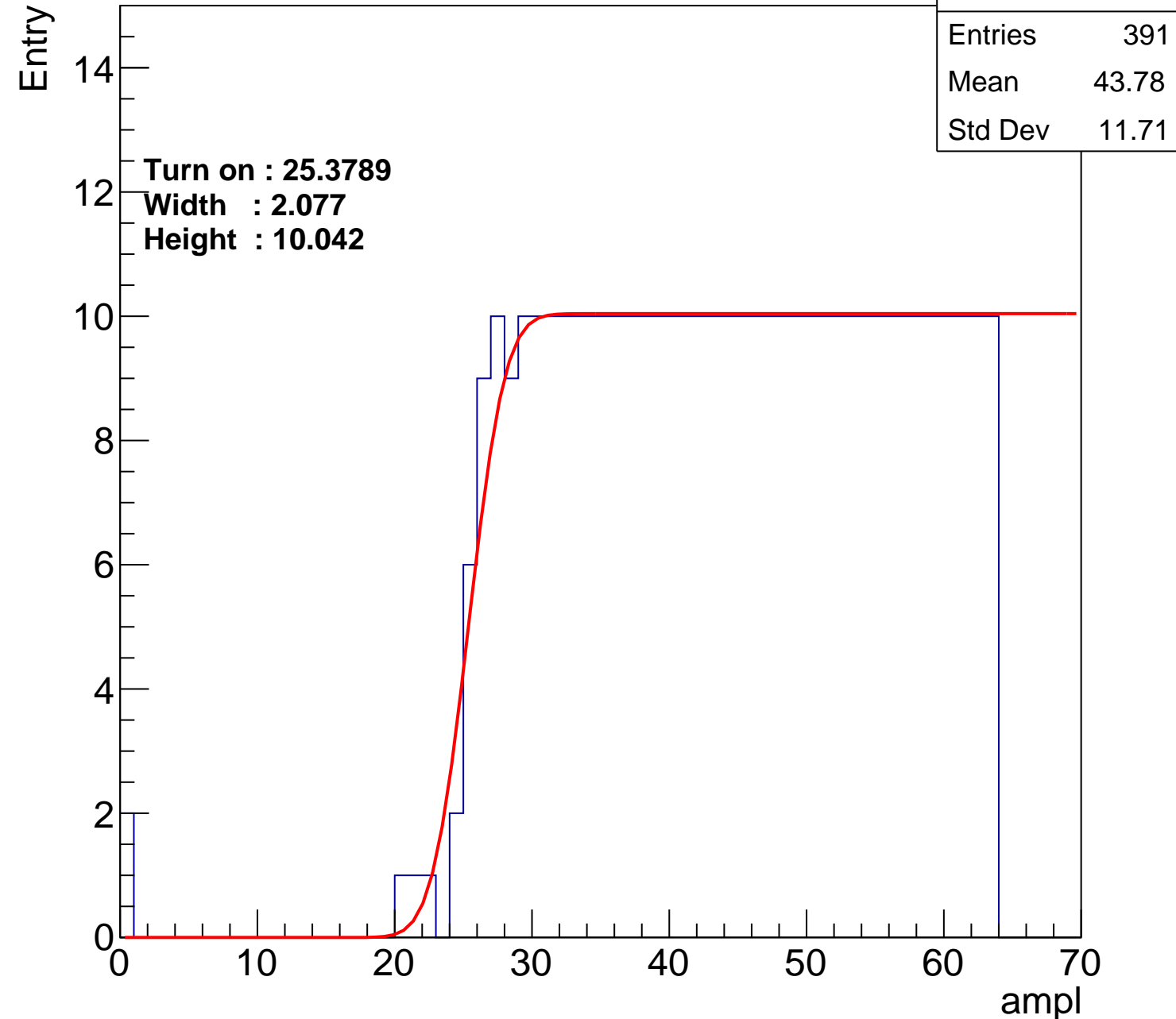
Width : 2.077

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.78
Std Dev	12.06

Turn on : 25.7683

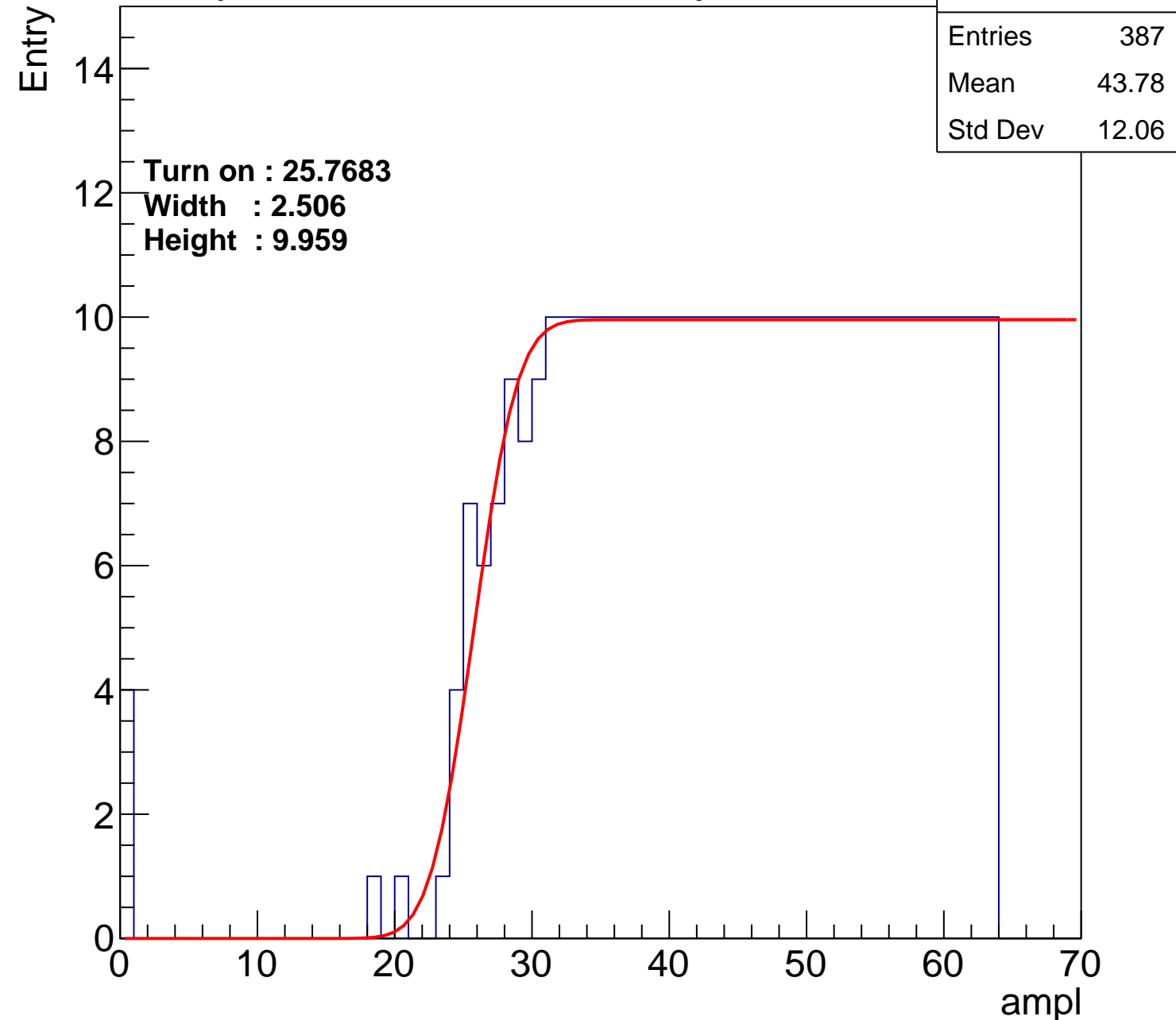
Width : 2.506

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.43
Std Dev	11.4

Turn on : 26.4569

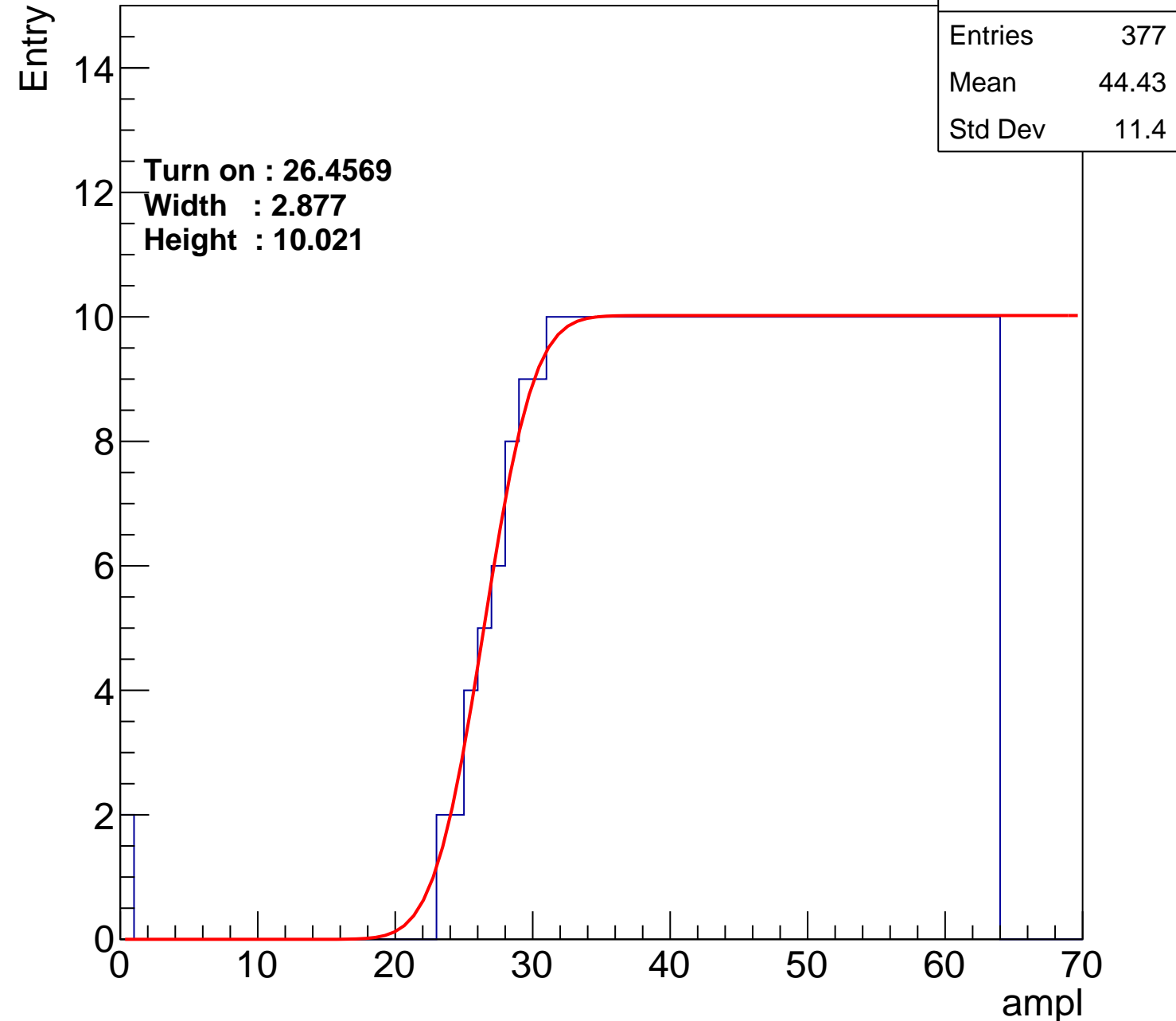
Width : 2.877

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.19
Std Dev	11.57

Turn on : 26.2391

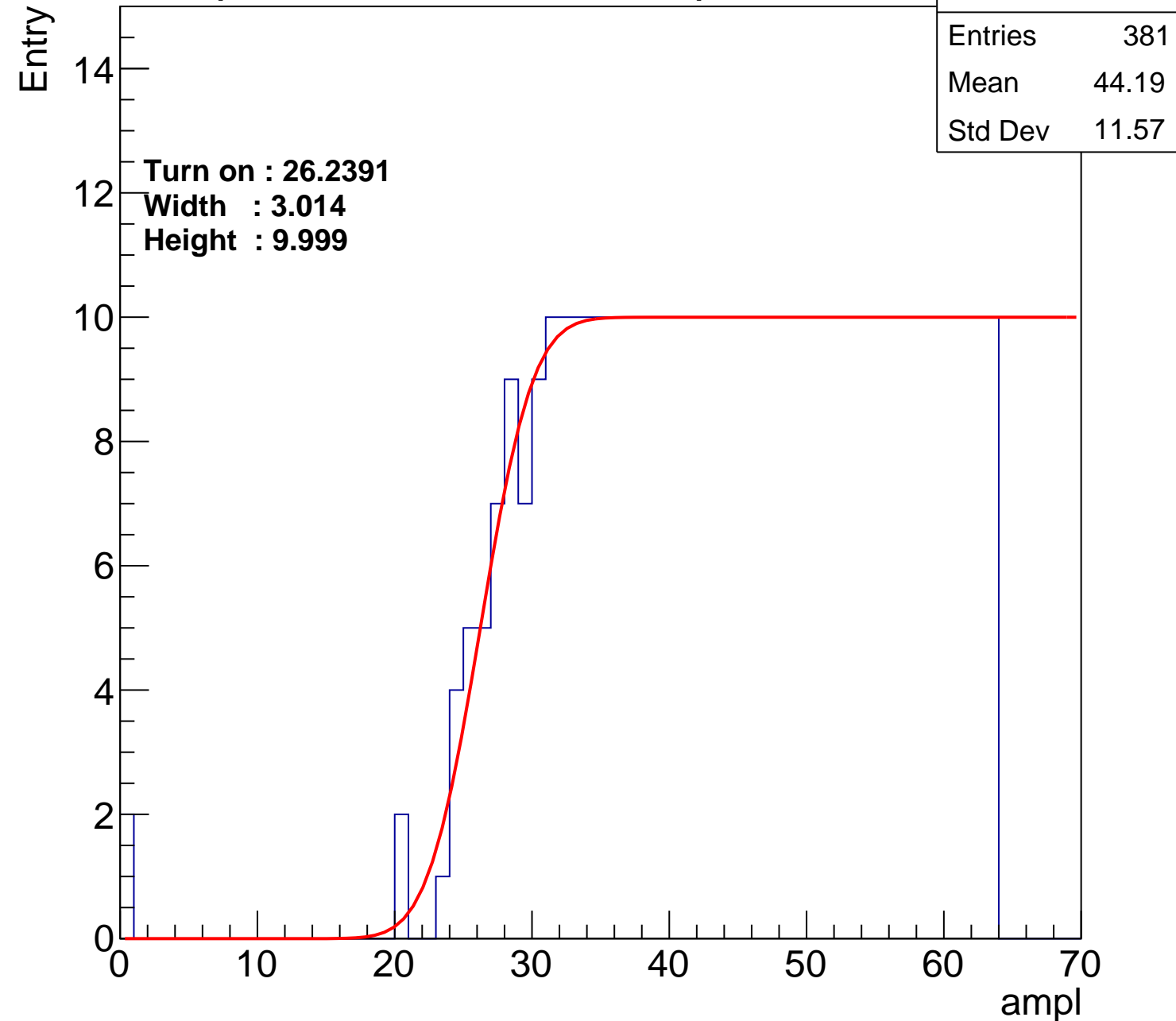
Width : 3.014

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch104

calib_packv5_042523_0143.root, FC#3, port B3

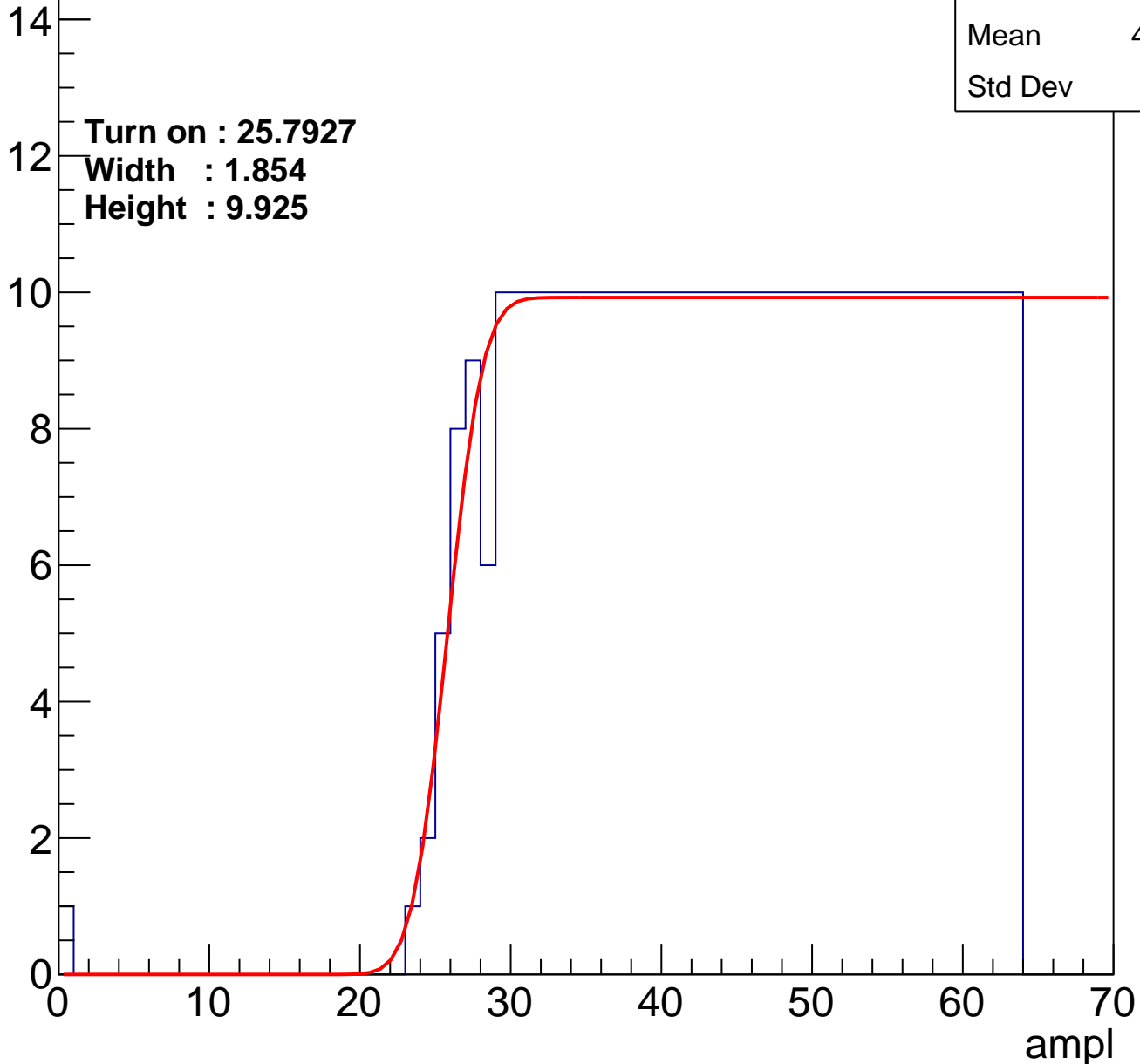
Entries	382
Mean	44.28
Std Dev	11.3

Turn on : 25.7927

Width : 1.854

Height : 9.925

Entry



B1L000S, U12-ch105

calib_packv5_042523_0143.root, FC#3, port B3

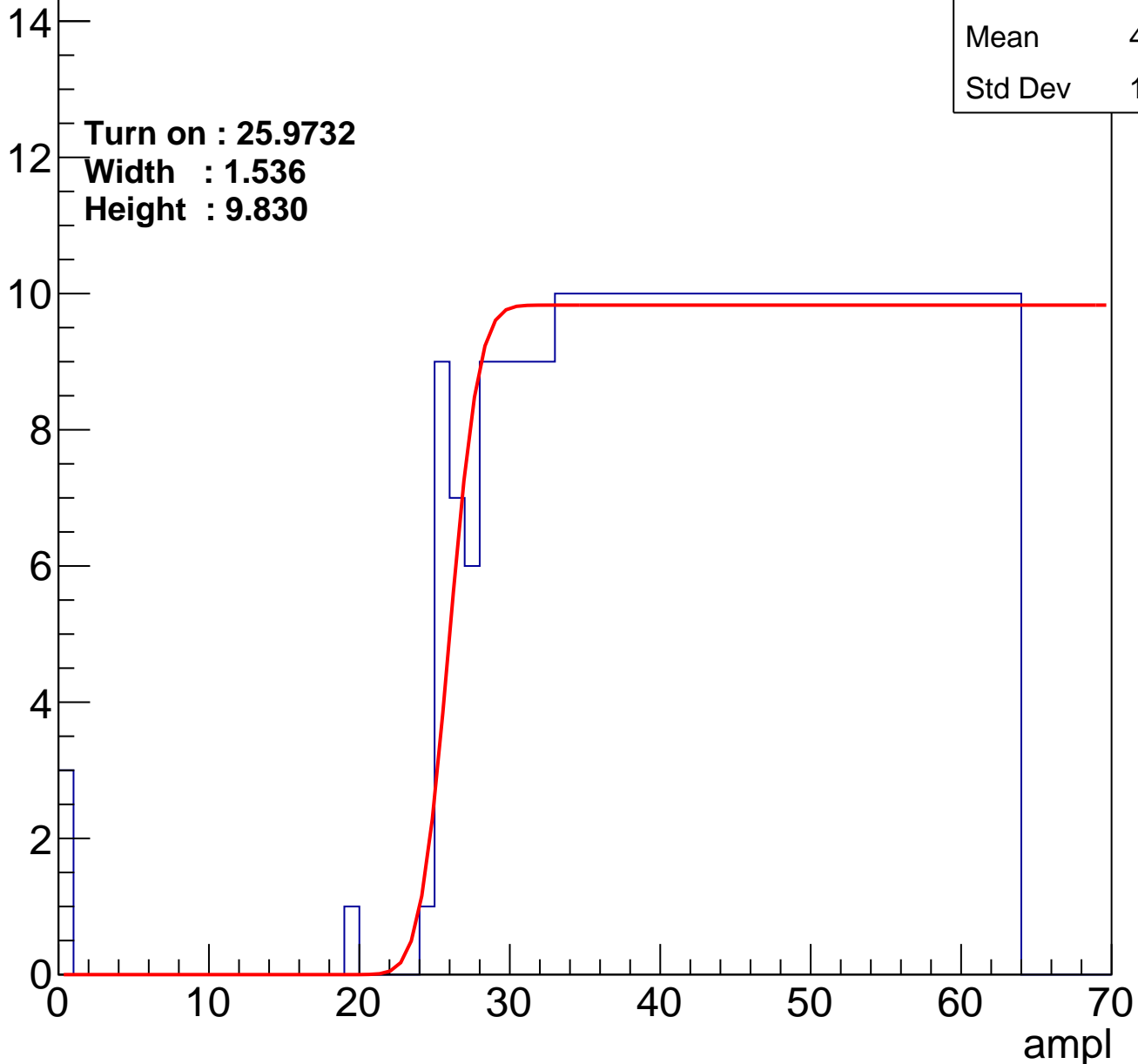
Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 25.9732

Width : 1.536

Height : 9.830

Entry



B1L000S, U12-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.61
Std Dev	11.48

Turn on : 27.7703

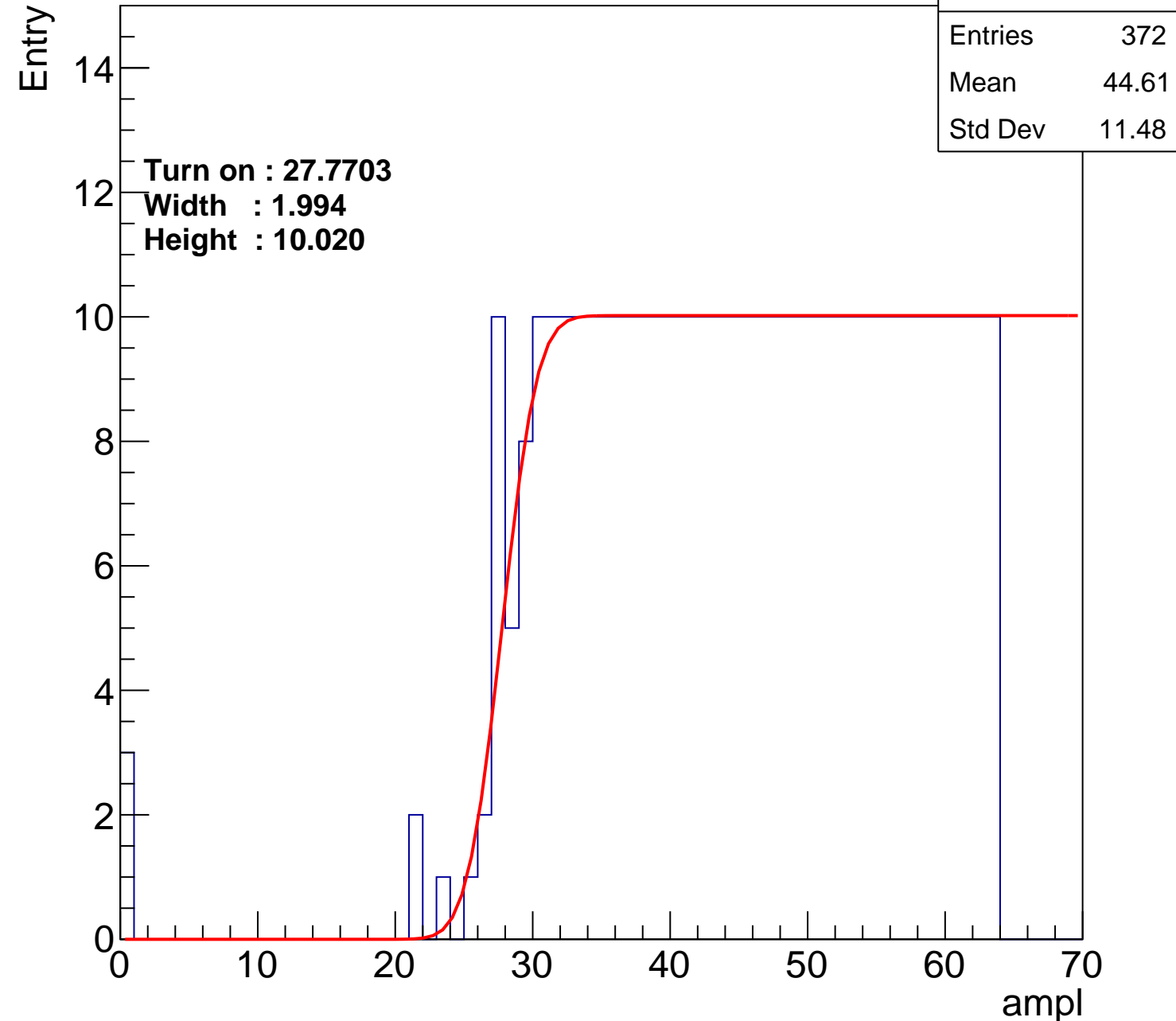
Width : 1.994

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch107

calib_packv5_042523_0143.root, FC#3, port B3

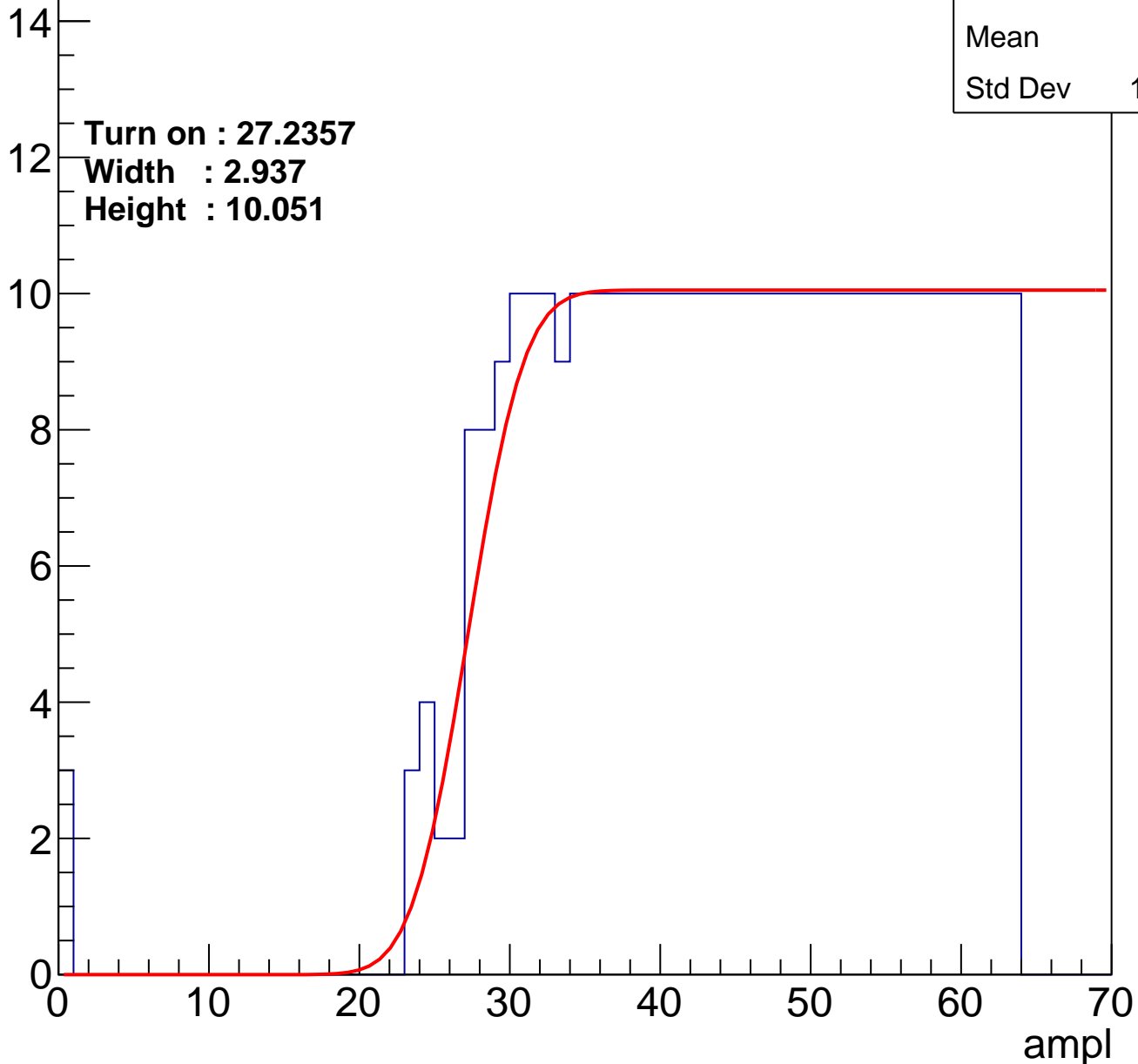
Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 27.2357

Width : 2.937

Height : 10.051

Entry



B1L000S, U12-ch108

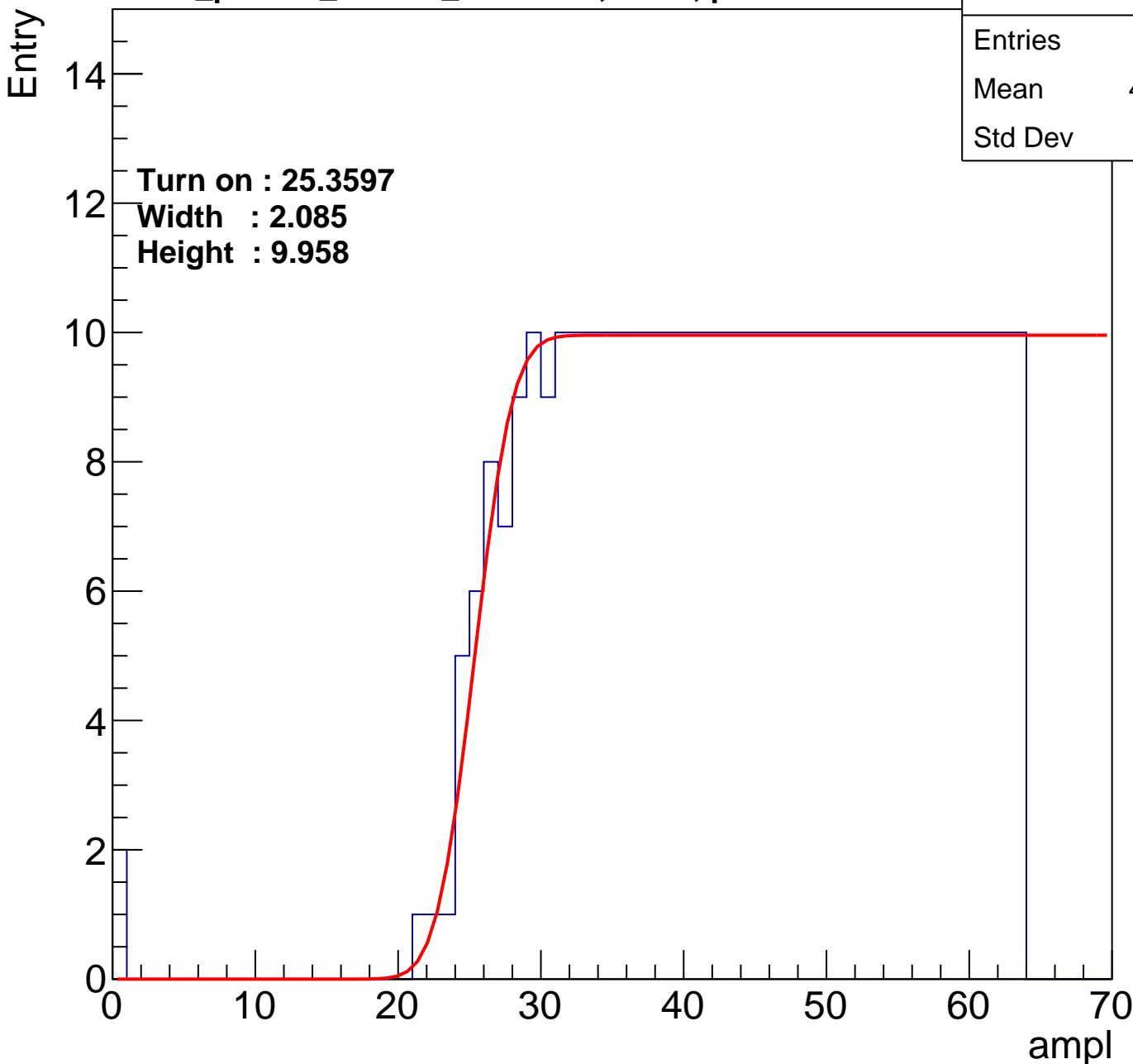
calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.3597

Width : 2.085

Height : 9.958



B1L000S, U12-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.97
Std Dev	11.62

Turn on : 26.1029

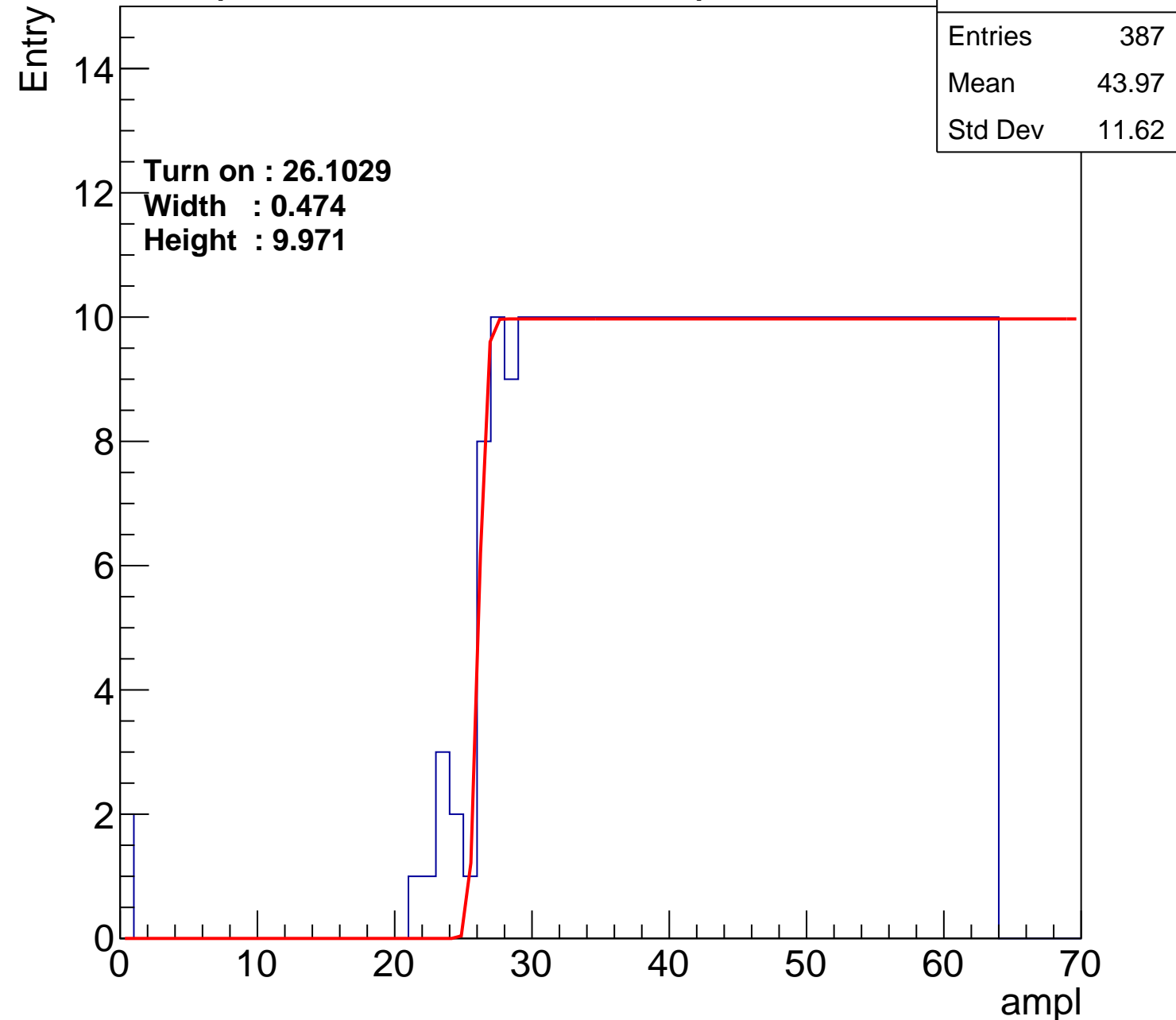
Width : 0.474

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.18
Std Dev	12.41

Turn on : 25.3209

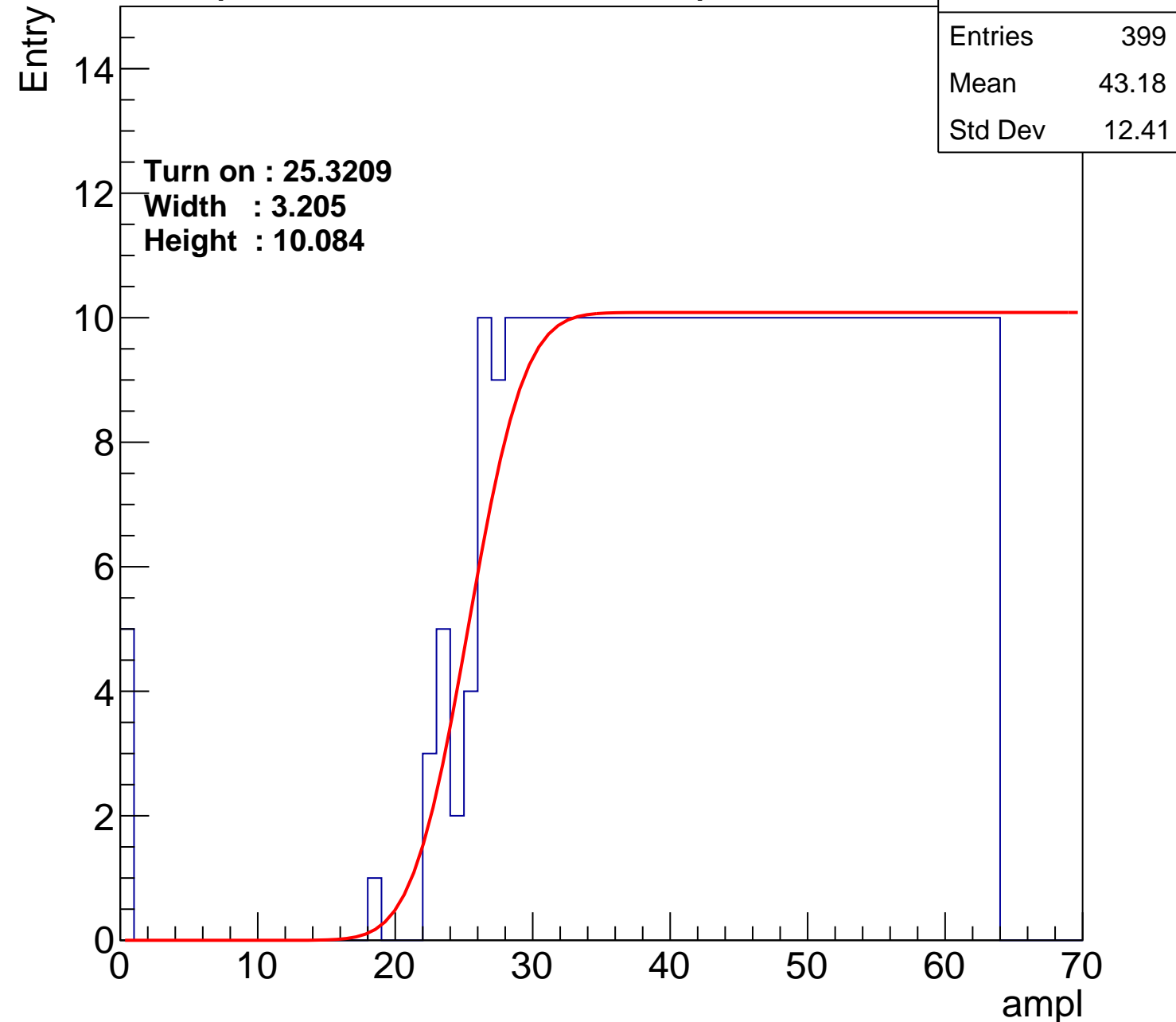
Width : 3.205

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.13
Std Dev	11.62

Turn on : 26.2360

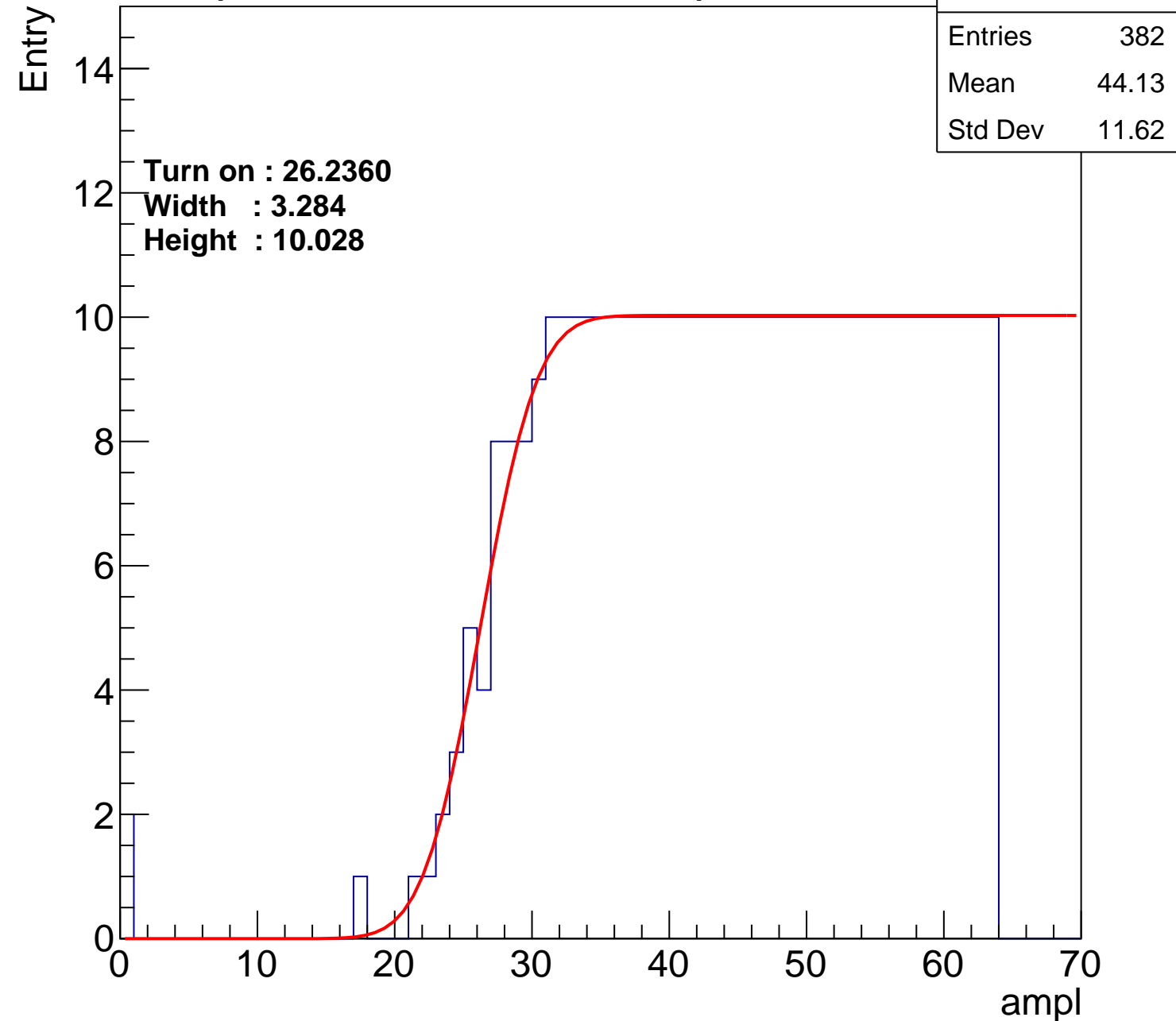
Width : 3.284

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.8
Std Dev	11.78

Turn on : 25.6347

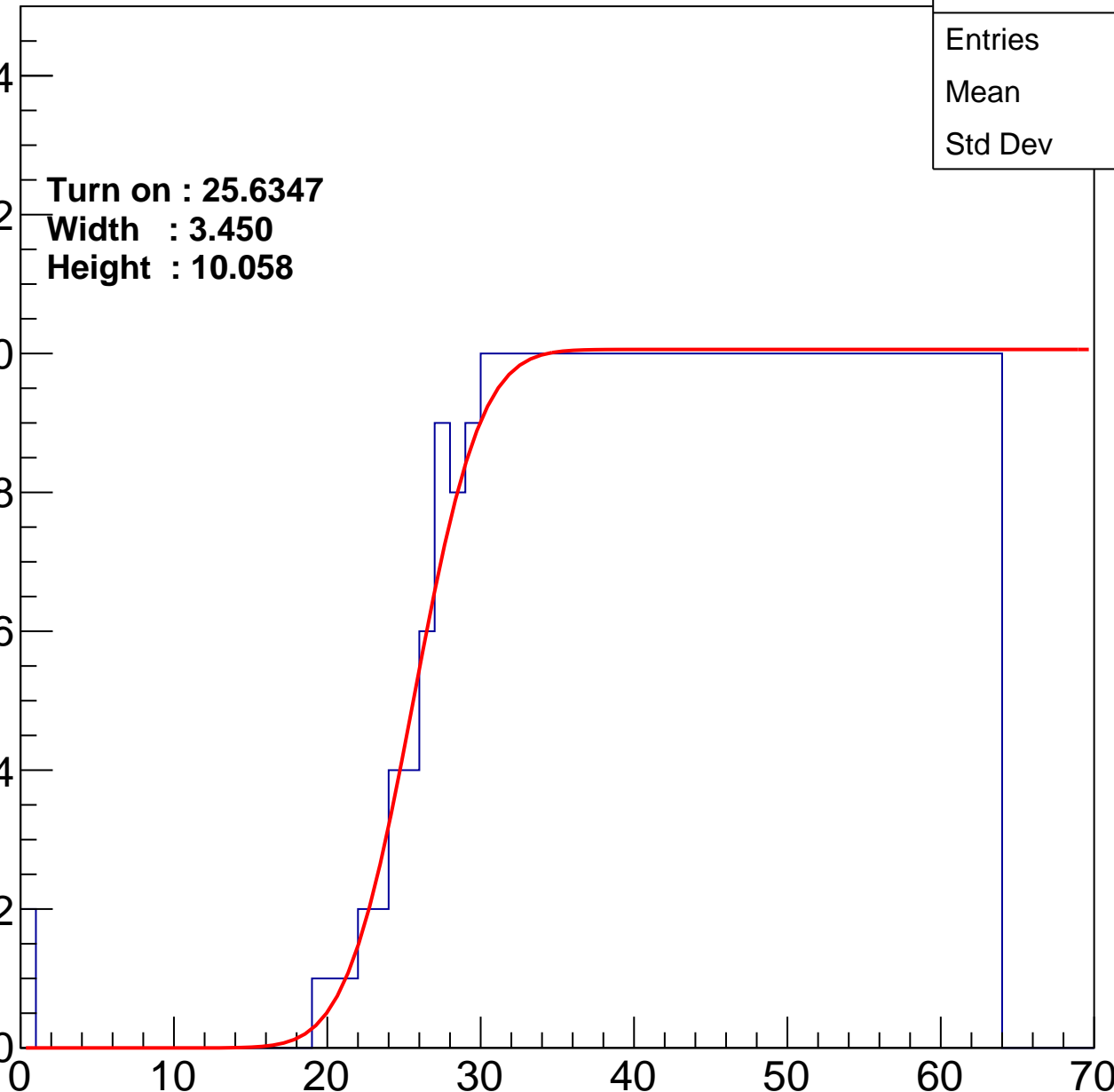
Width : 3.450

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch113

calib_packv5_042523_0143.root, FC#3, port B3

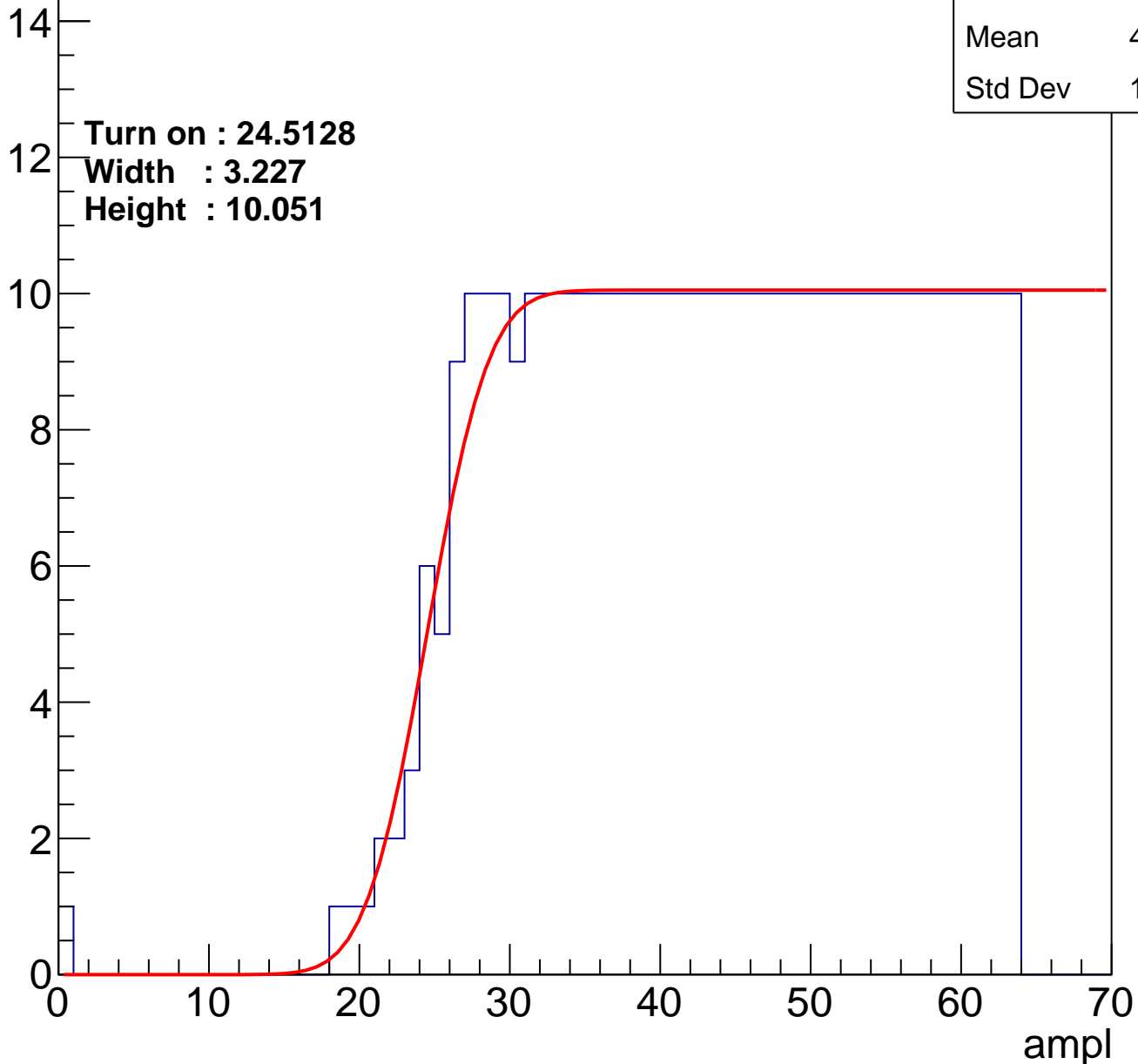
Entries	400
Mean	43.34
Std Dev	11.88

Turn on : 24.5128

Width : 3.227

Height : 10.051

Entry



B1L000S, U12-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.09
Std Dev	11.7

Turn on : 26.0733

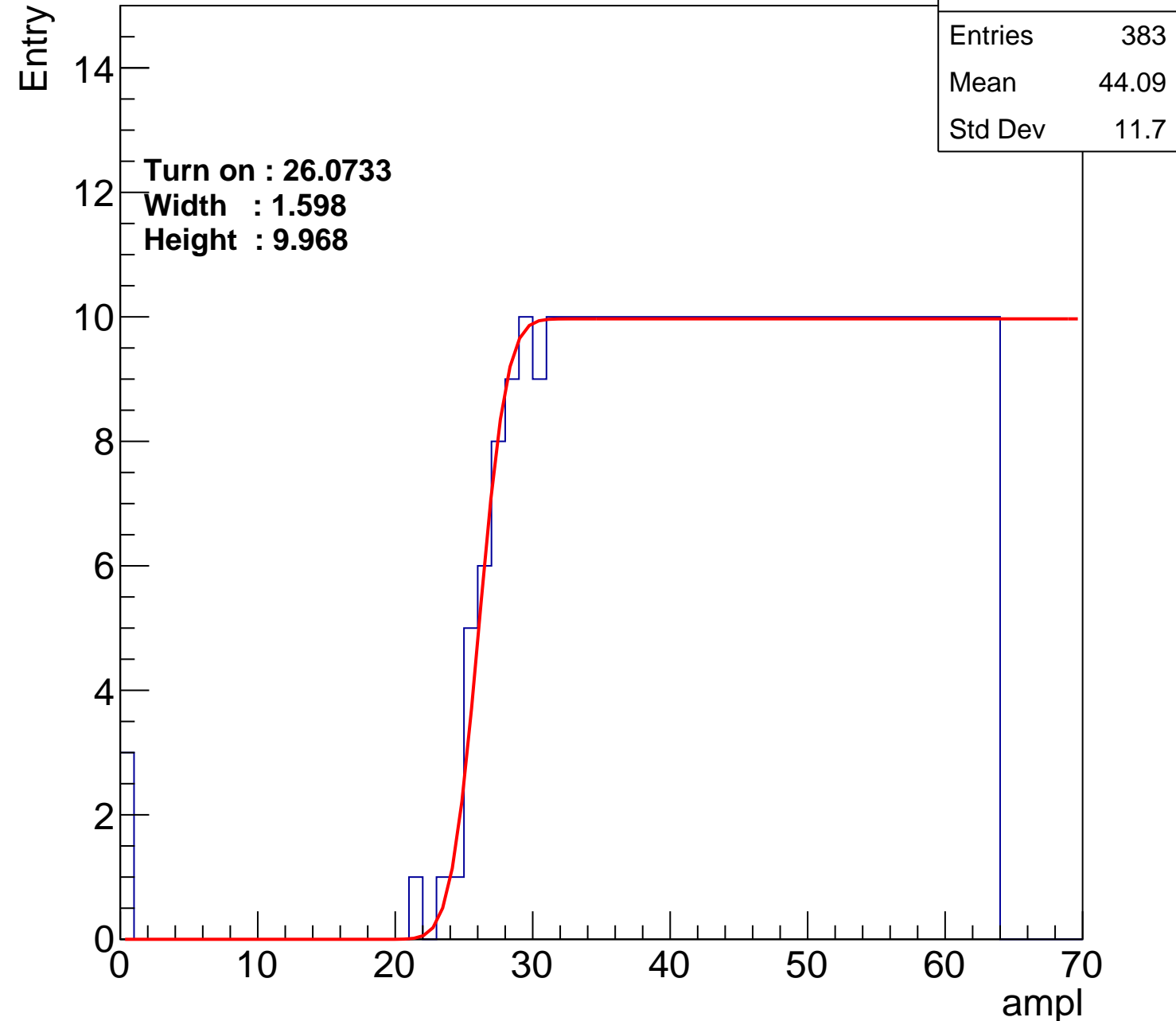
Width : 1.598

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch115

calib_packv5_042523_0143.root, FC#3, port B3

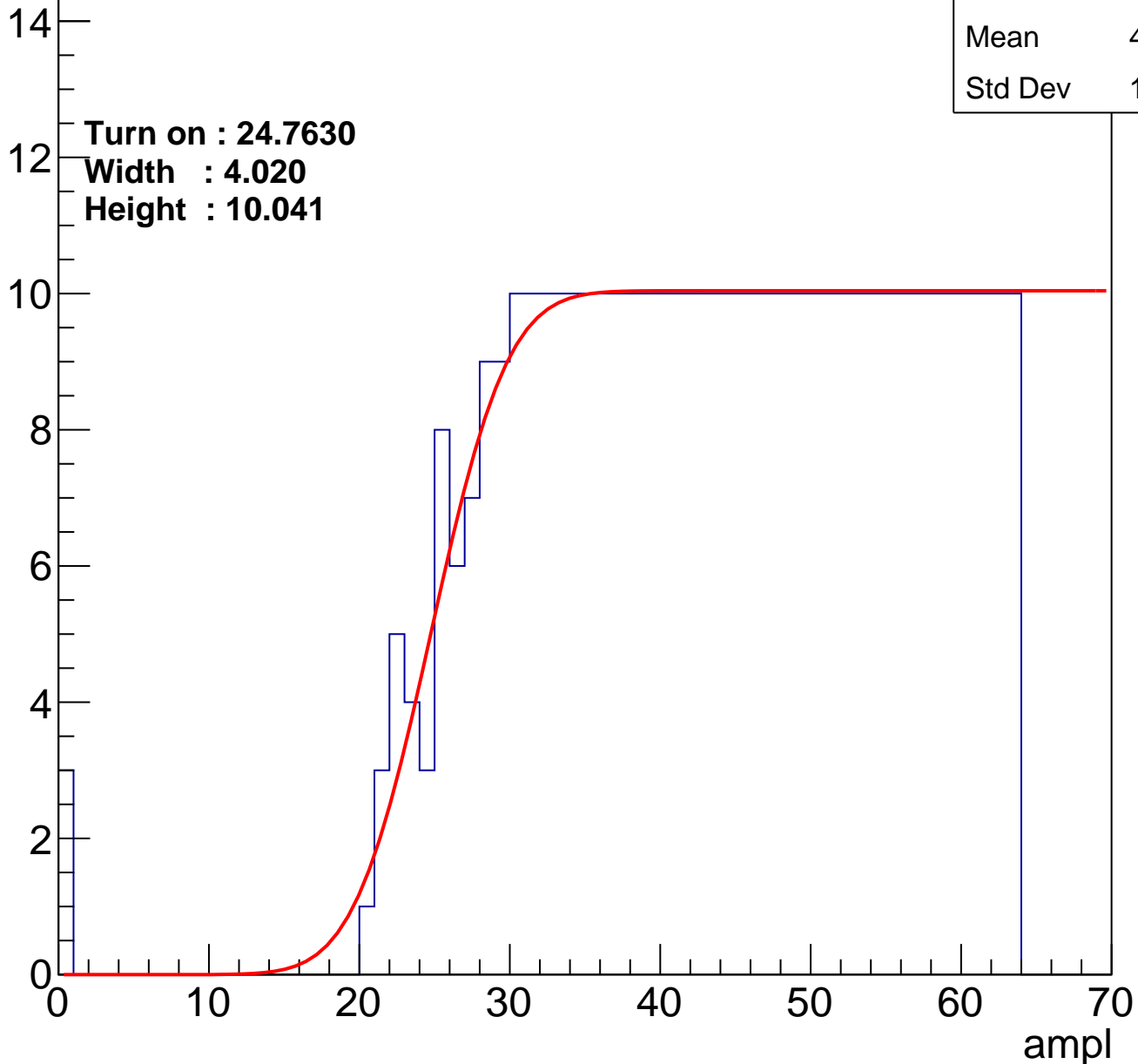
Entries	398
Mean	43.28
Std Dev	12.19

Turn on : 24.7630

Width : 4.020

Height : 10.041

Entry



B1L000S, U12-ch116

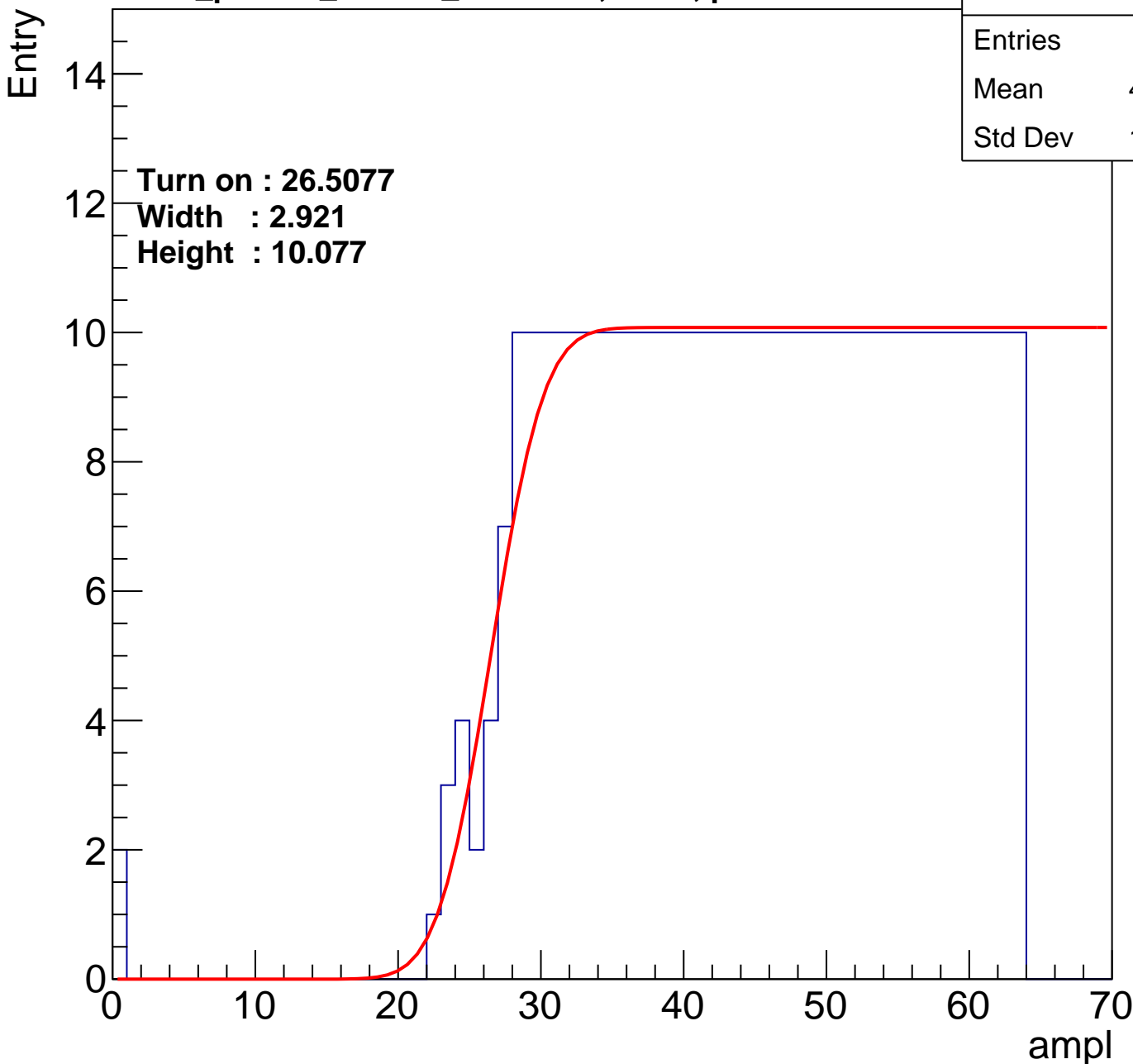
calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.5077

Width : 2.921

Height : 10.077



B1L000S, U12-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.73
Std Dev	11.67

Turn on : 25.7020

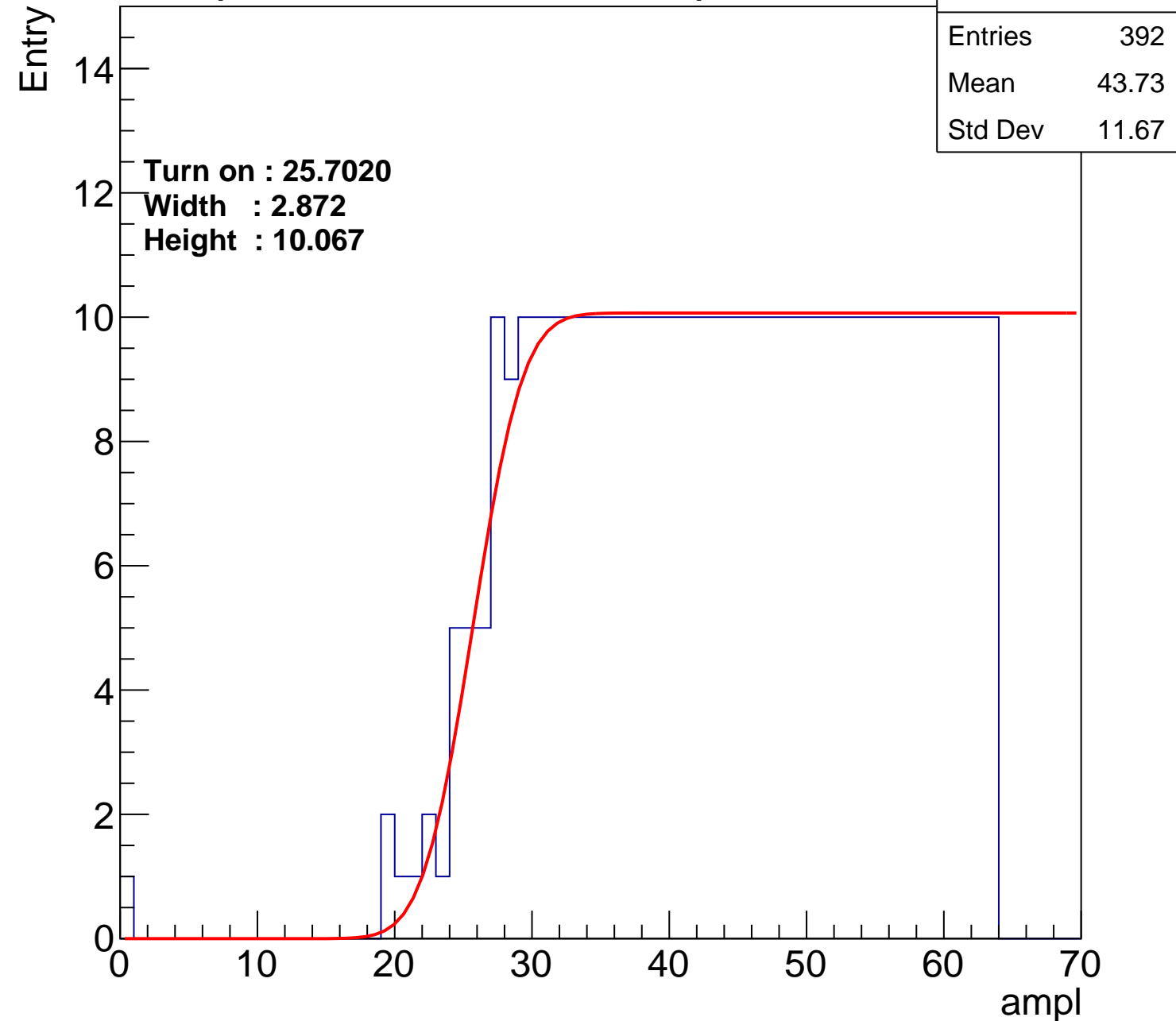
Width : 2.872

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.33
Std Dev	12.11

Turn on : 24.9183

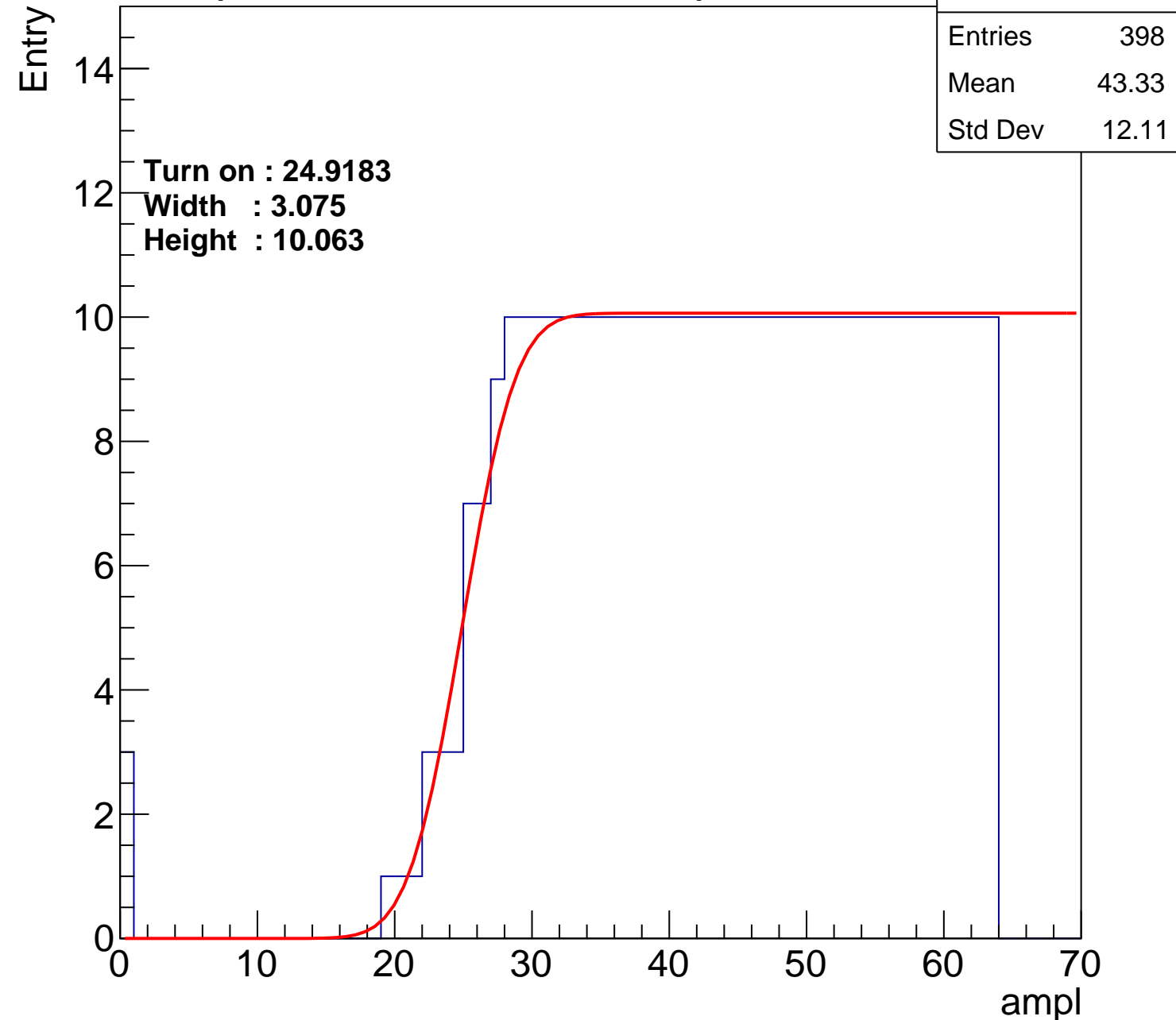
Width : 3.075

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.68
Std Dev	12.22

Turn on : 25.8751

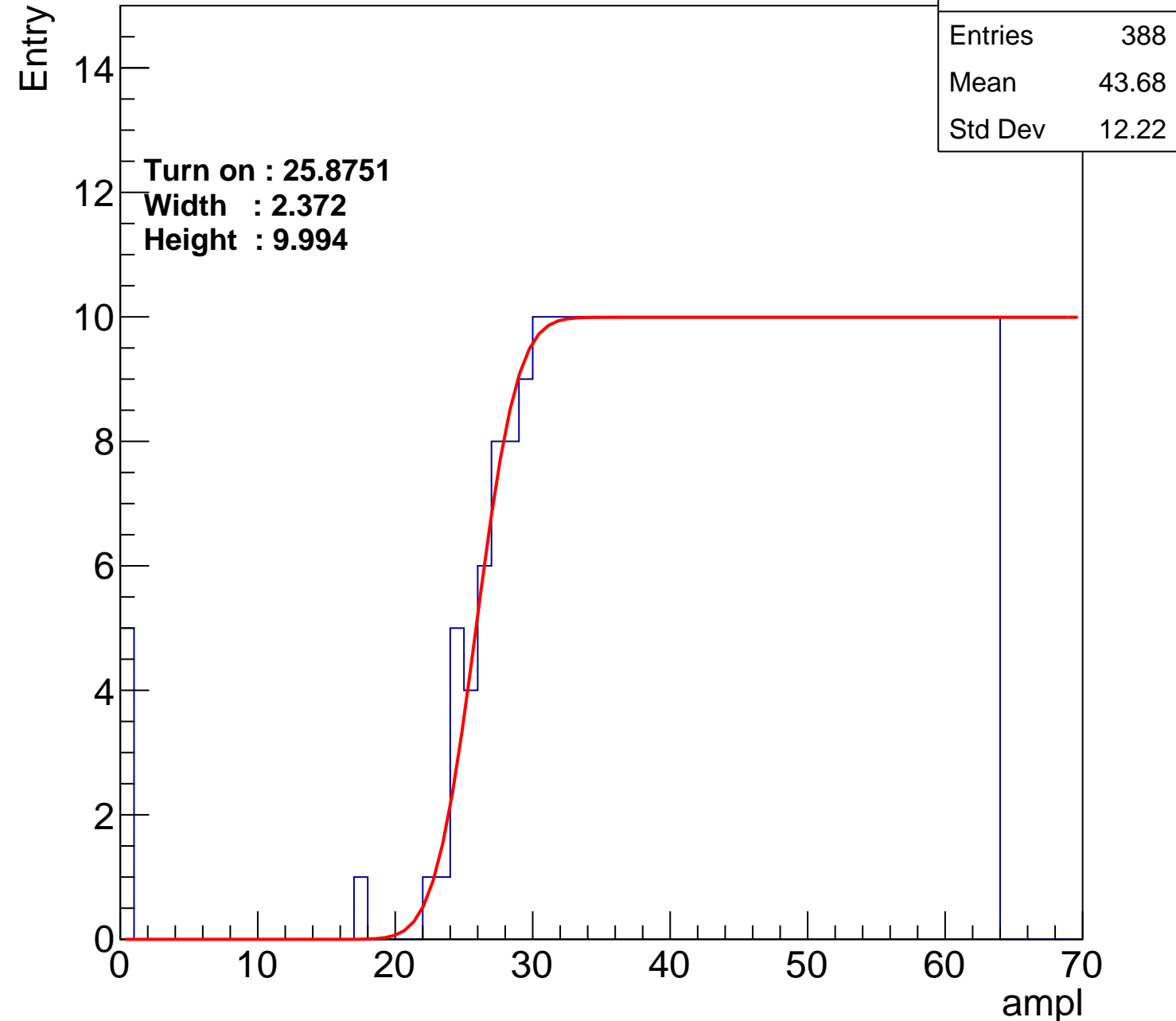
Width : 2.372

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch120

calib_packv5_042523_0143.root, FC#3, port B3

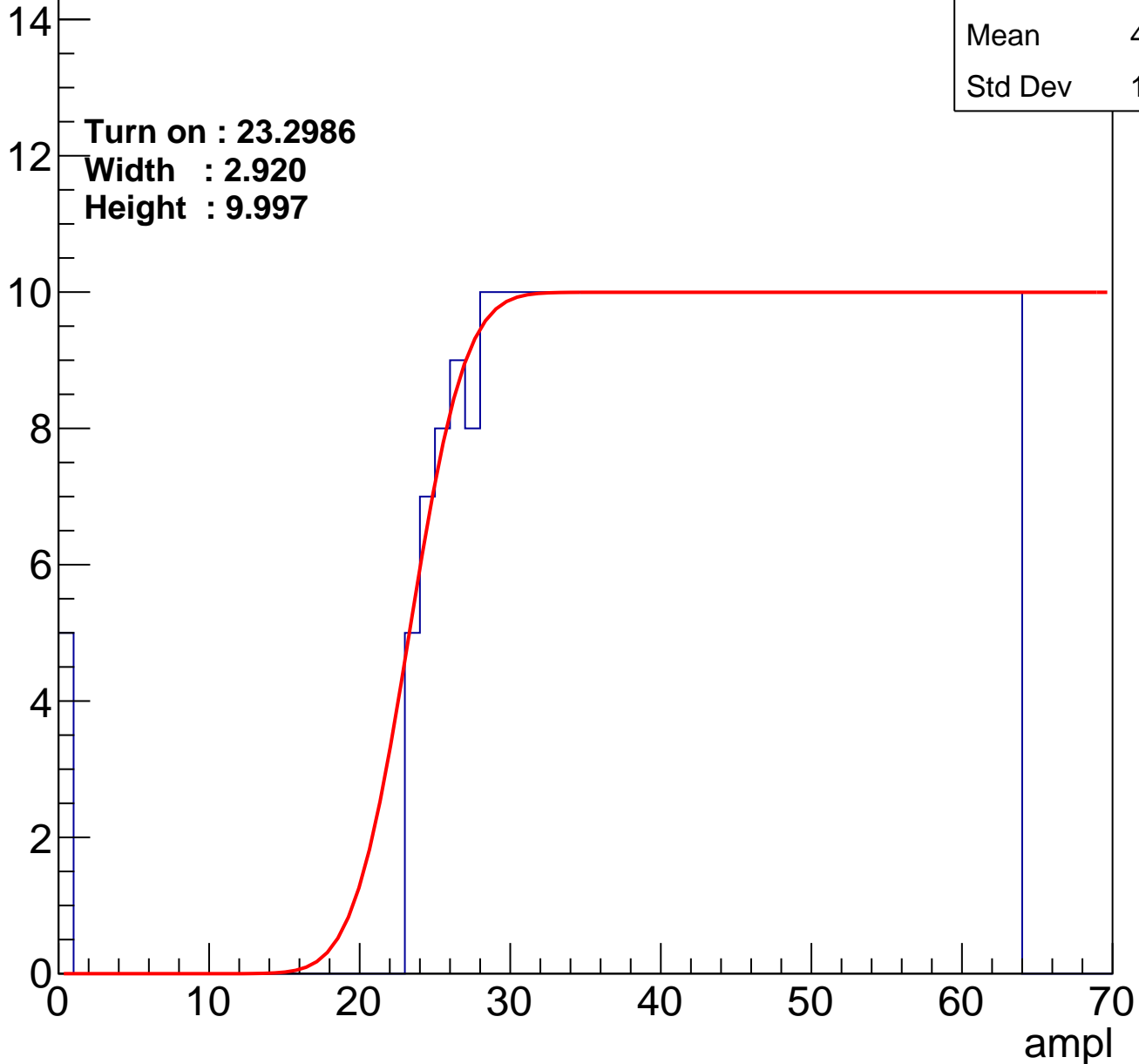
Entries	402
Mean	43.07
Std Dev	12.43

Turn on : 23.2986

Width : 2.920

Height : 9.997

Entry



B1L000S, U12-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.42
Std Dev	11.3

Turn on : 26.2242

Width : 2.845

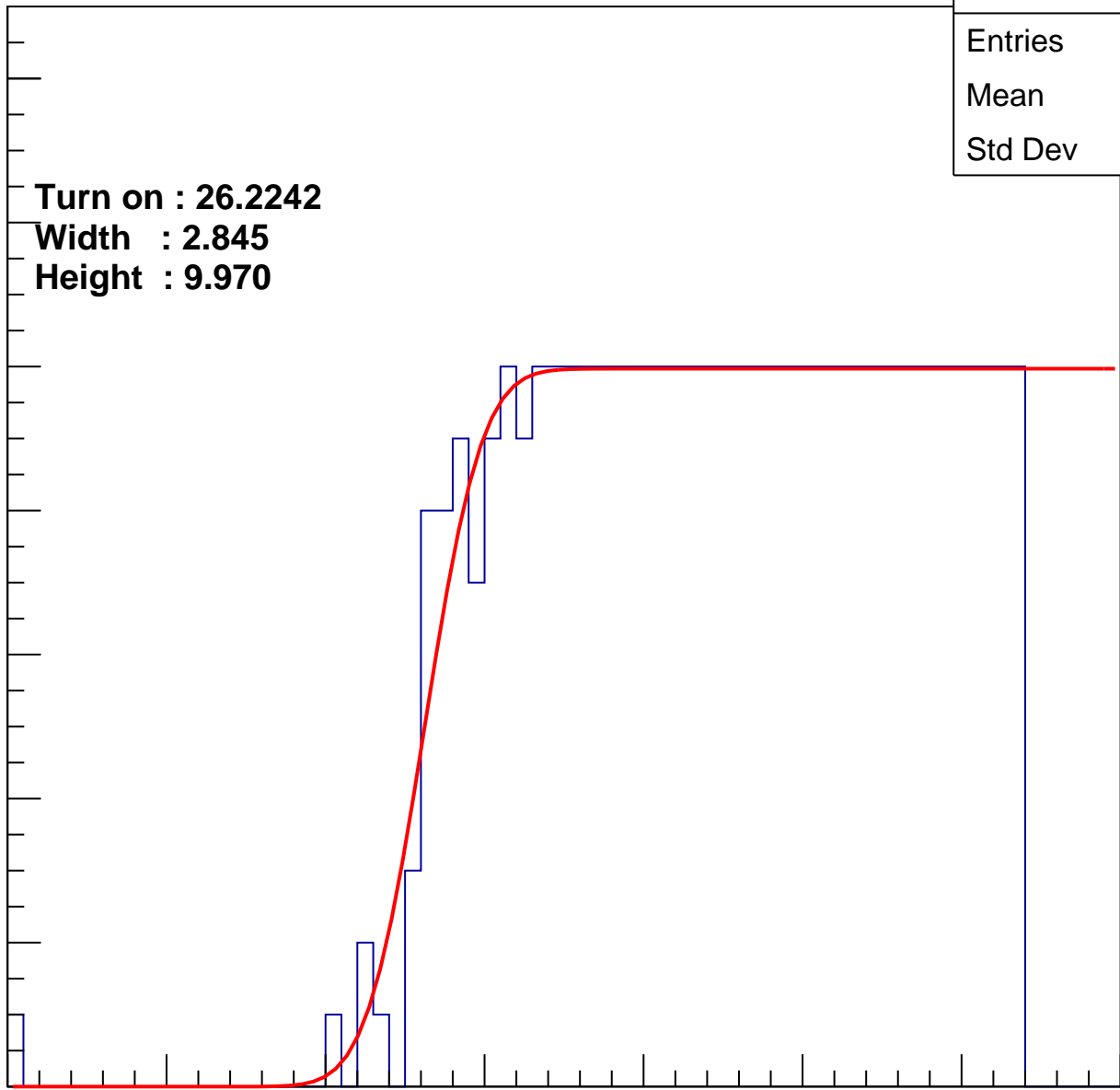
Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U12-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.83
Std Dev	11.72

Turn on : 25.3933

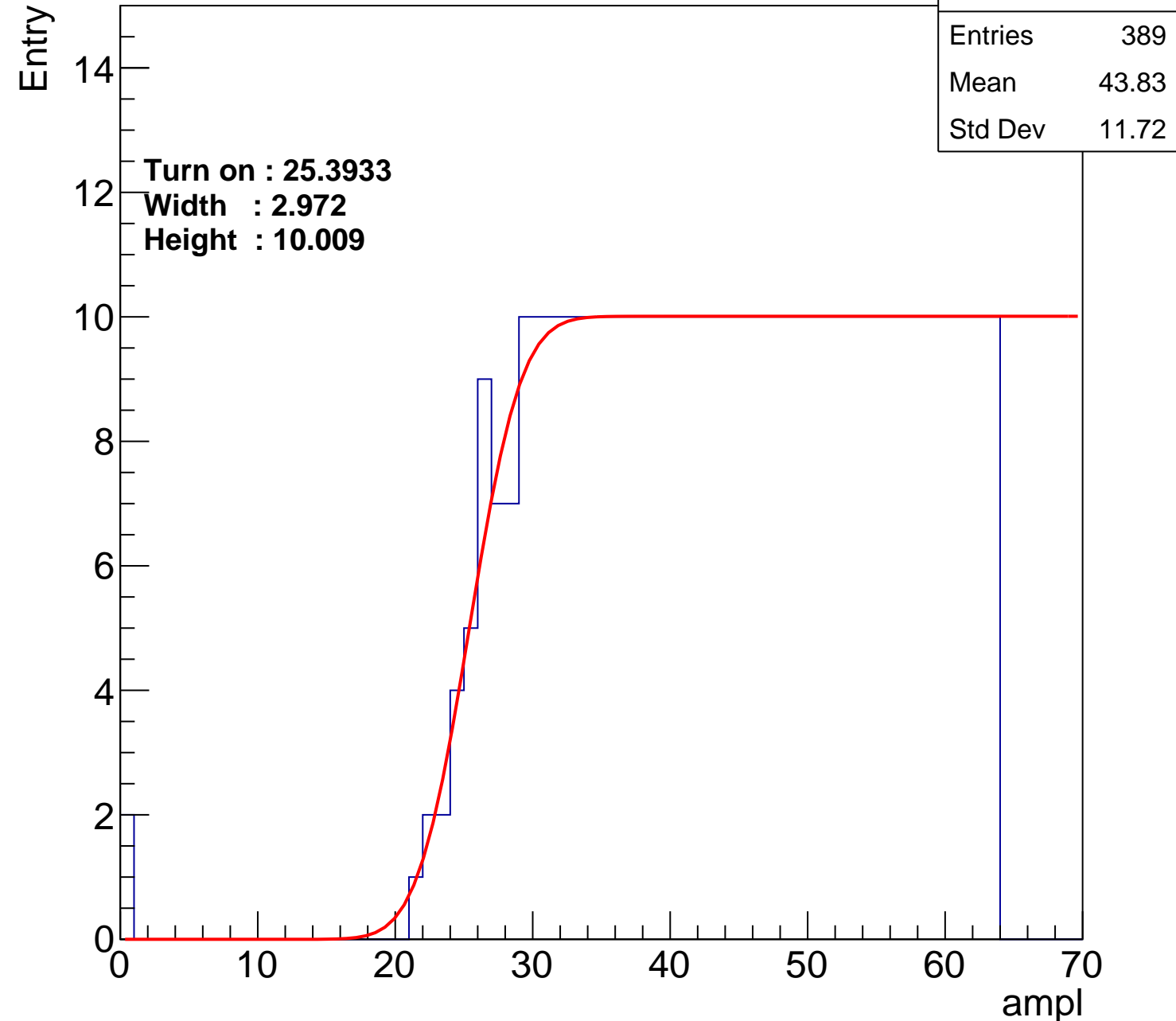
Width : 2.972

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.3738

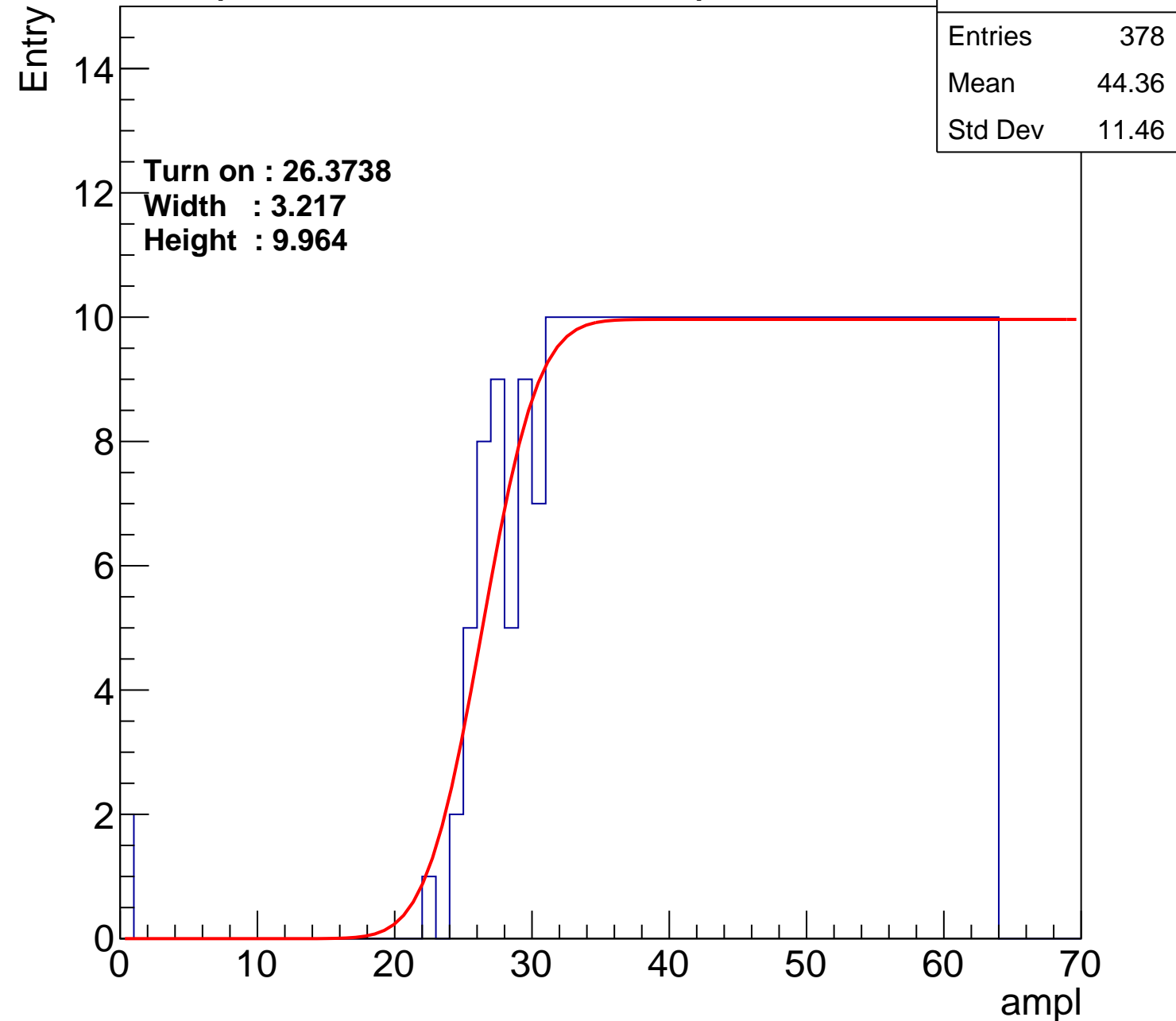
Width : 3.217

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.79
Std Dev	11.85

Turn on : 25.1835

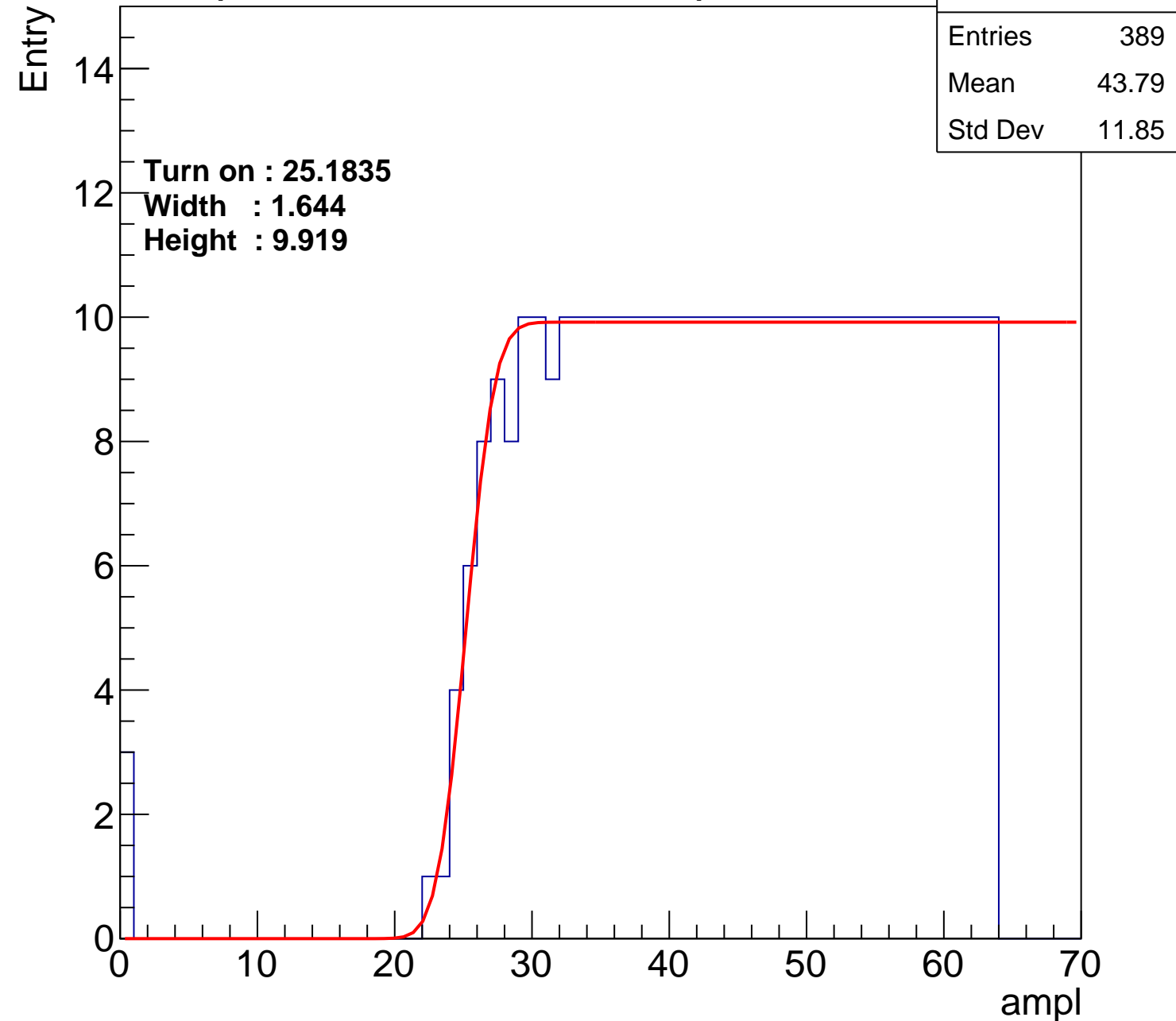
Width : 1.644

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.5957

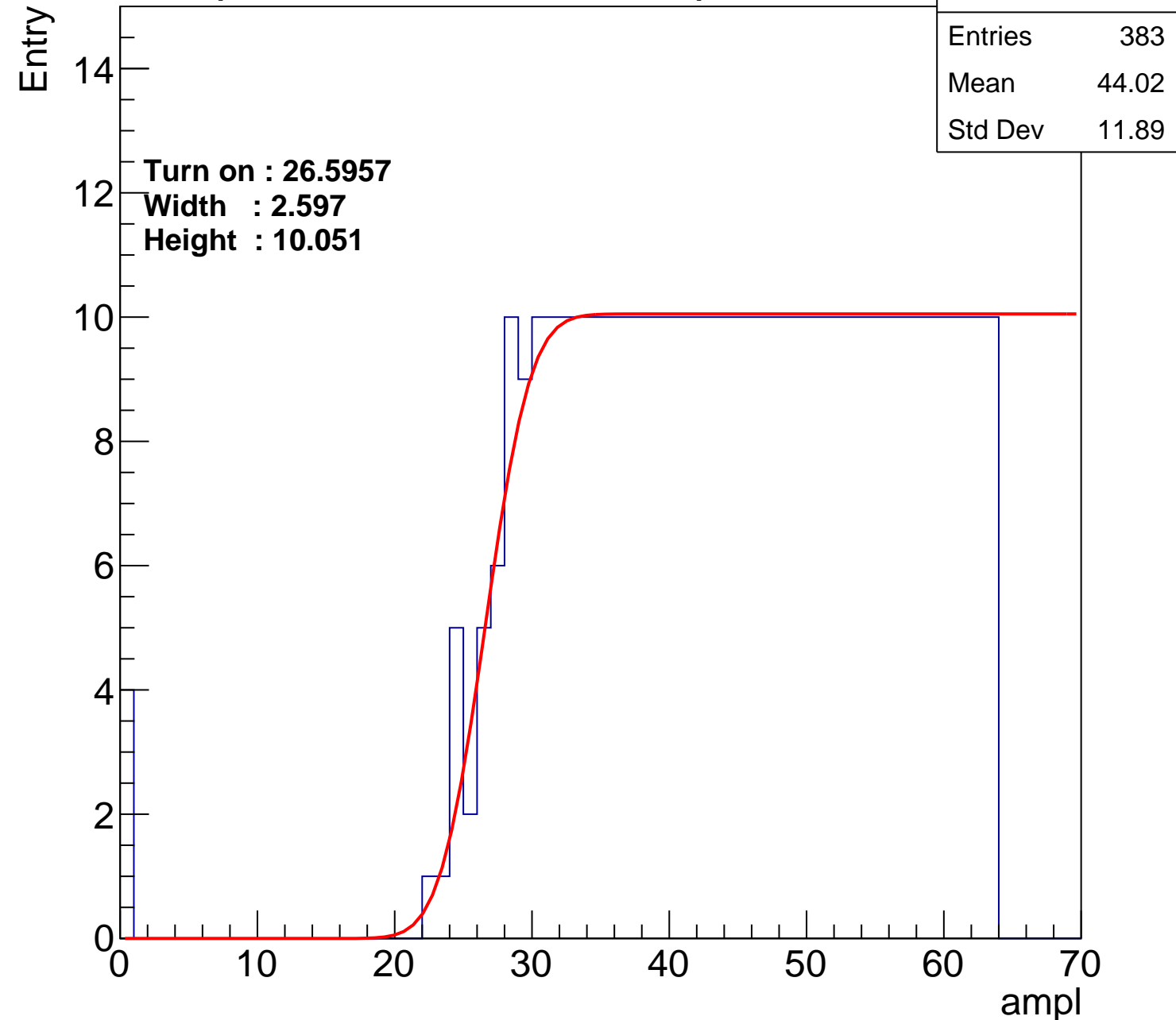
Width : 2.597

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.37
Std Dev	11.46

Turn on : 26.7697

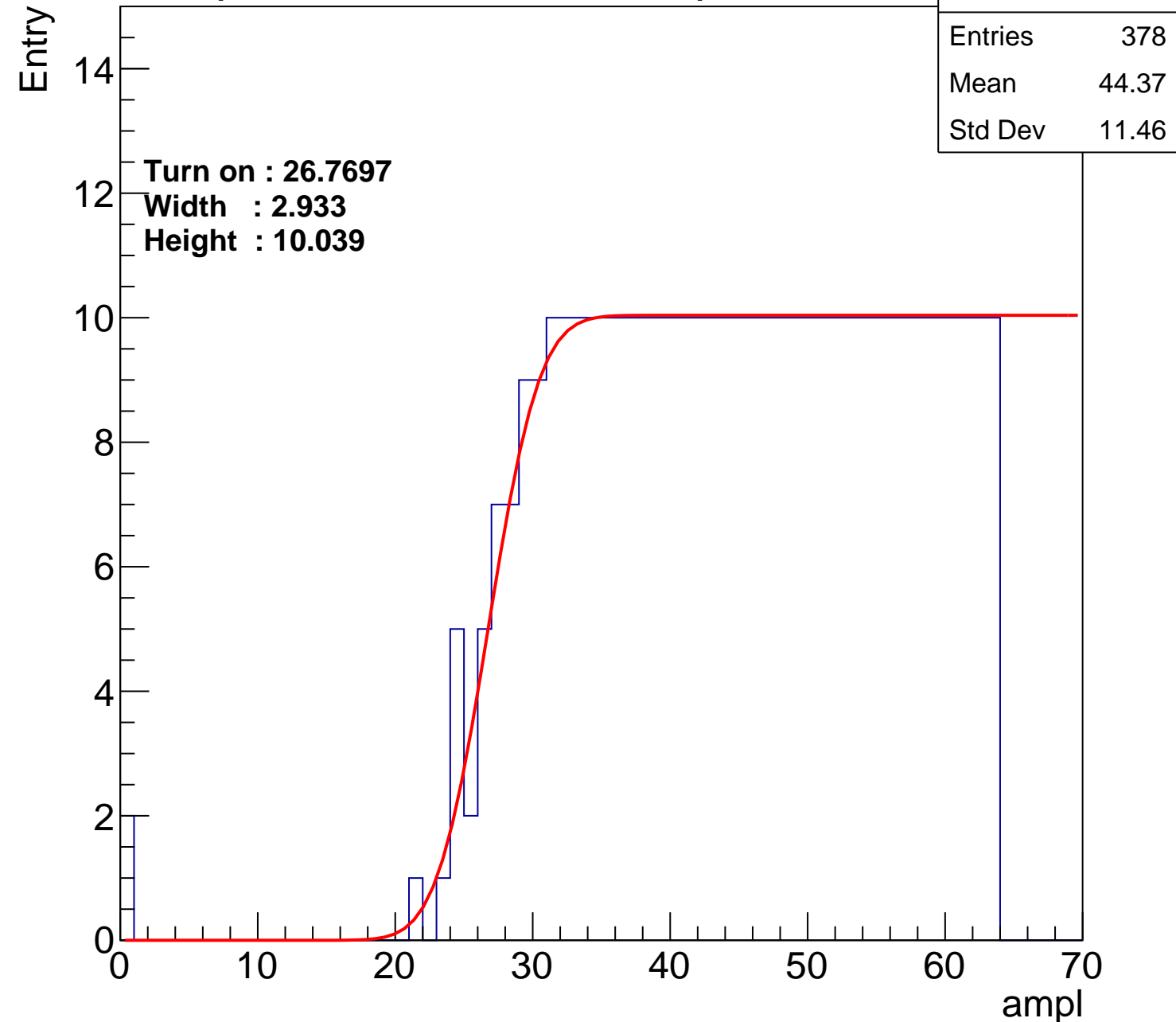
Width : 2.933

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.7930

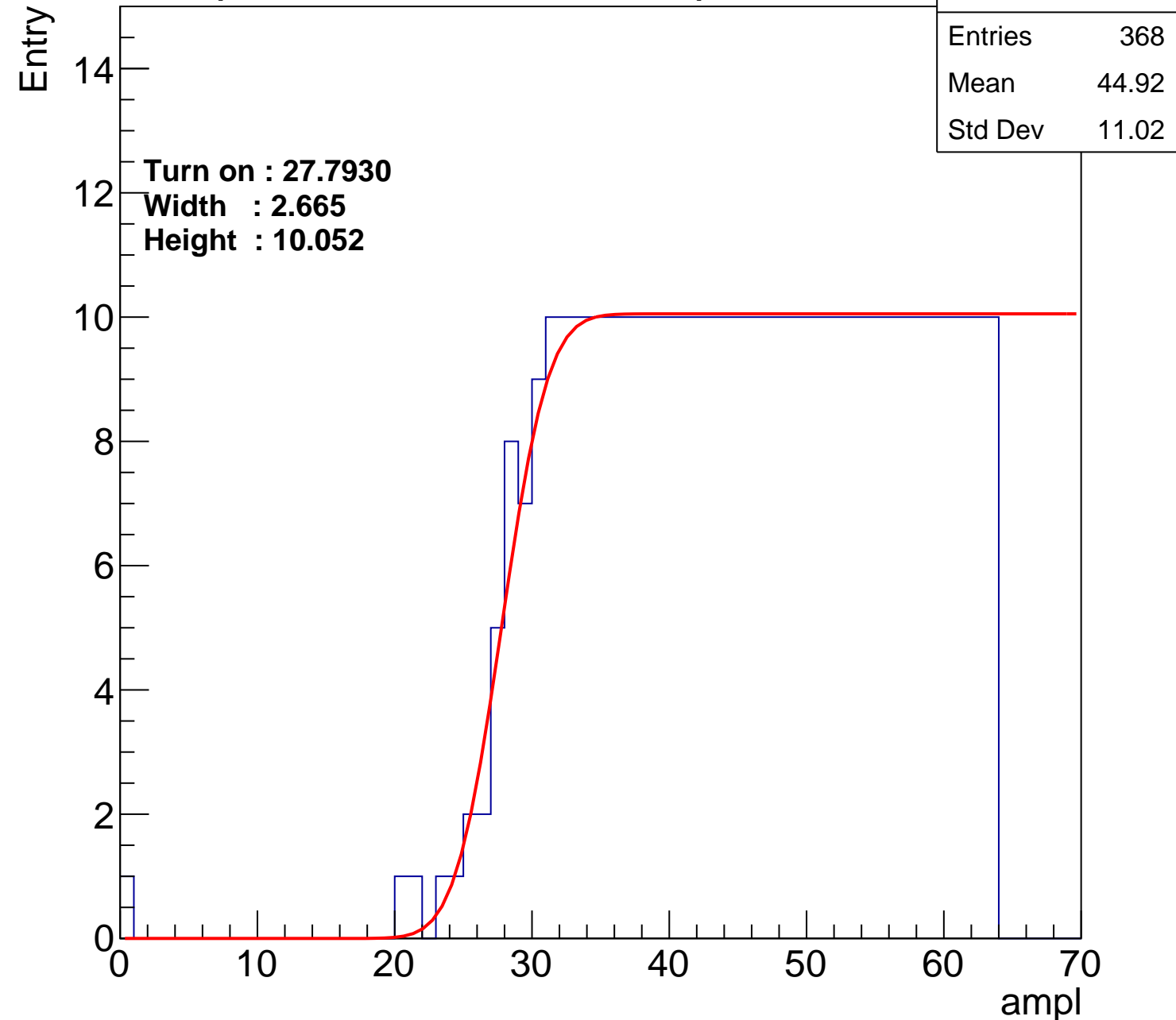
Width : 2.665

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U12-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.7930

Width : 2.665

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

