

B1L103S, U1-ch0, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	21.47
Std Dev	10.93

Gaus mean : 27.0369

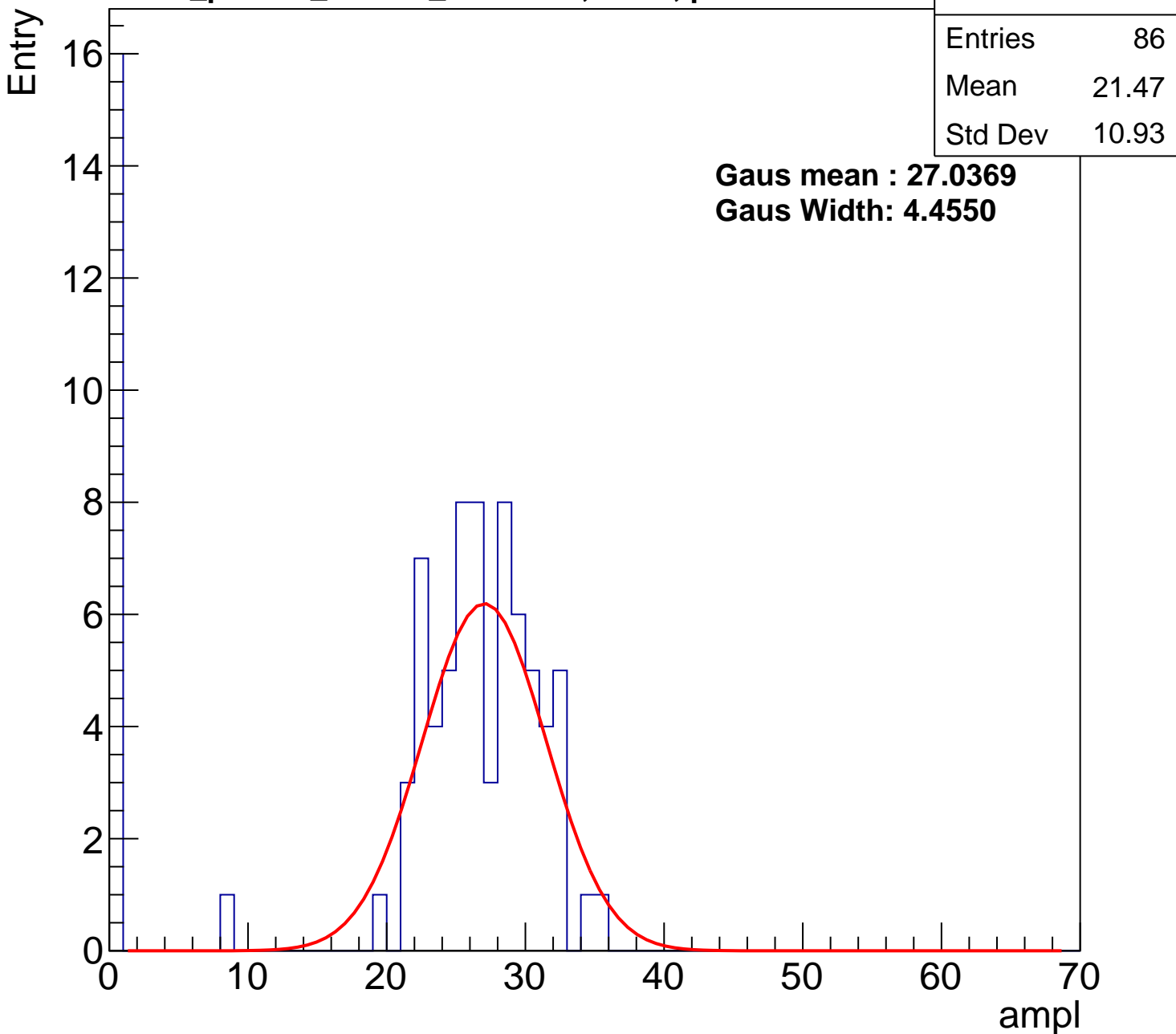
Gaus Width: 4.4550

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



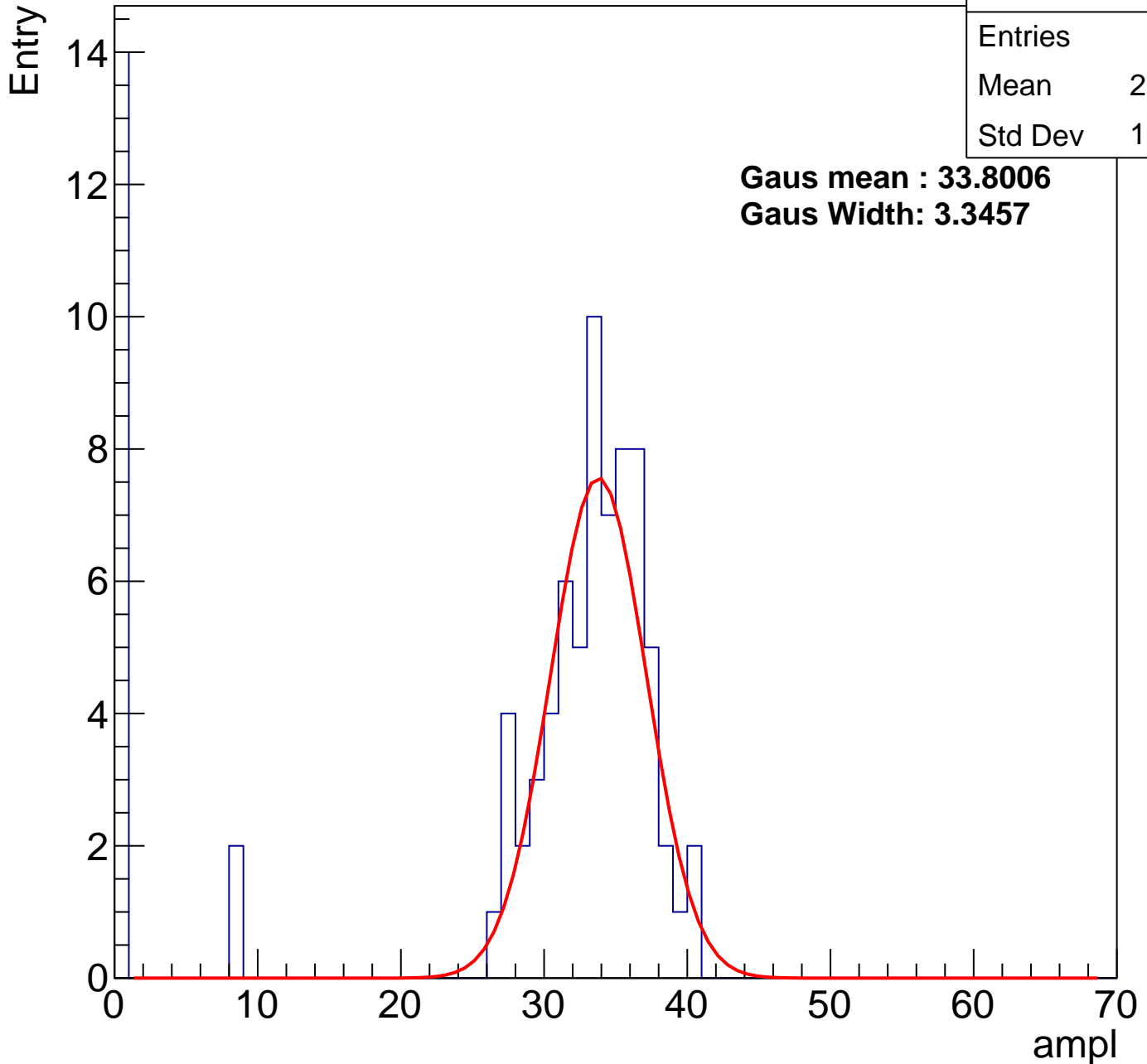
B1L103S, U1-ch0, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	27.08
Std Dev	13.04

Gaus mean : 33.8006

Gaus Width: 3.3457



B1L103S, U1-ch0, adc2

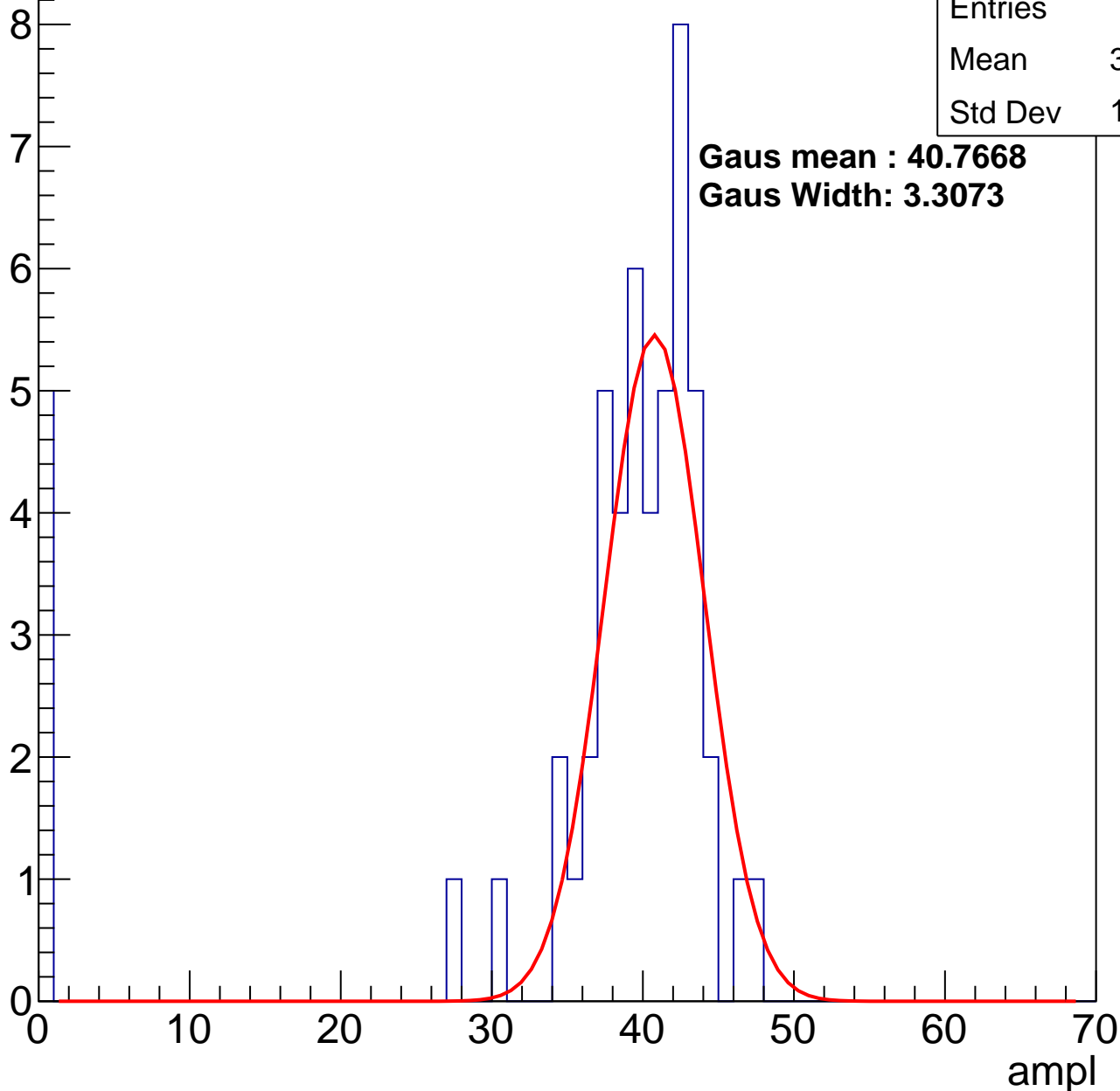
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	35.85
Std Dev	12.09

Gaus mean : 40.7668

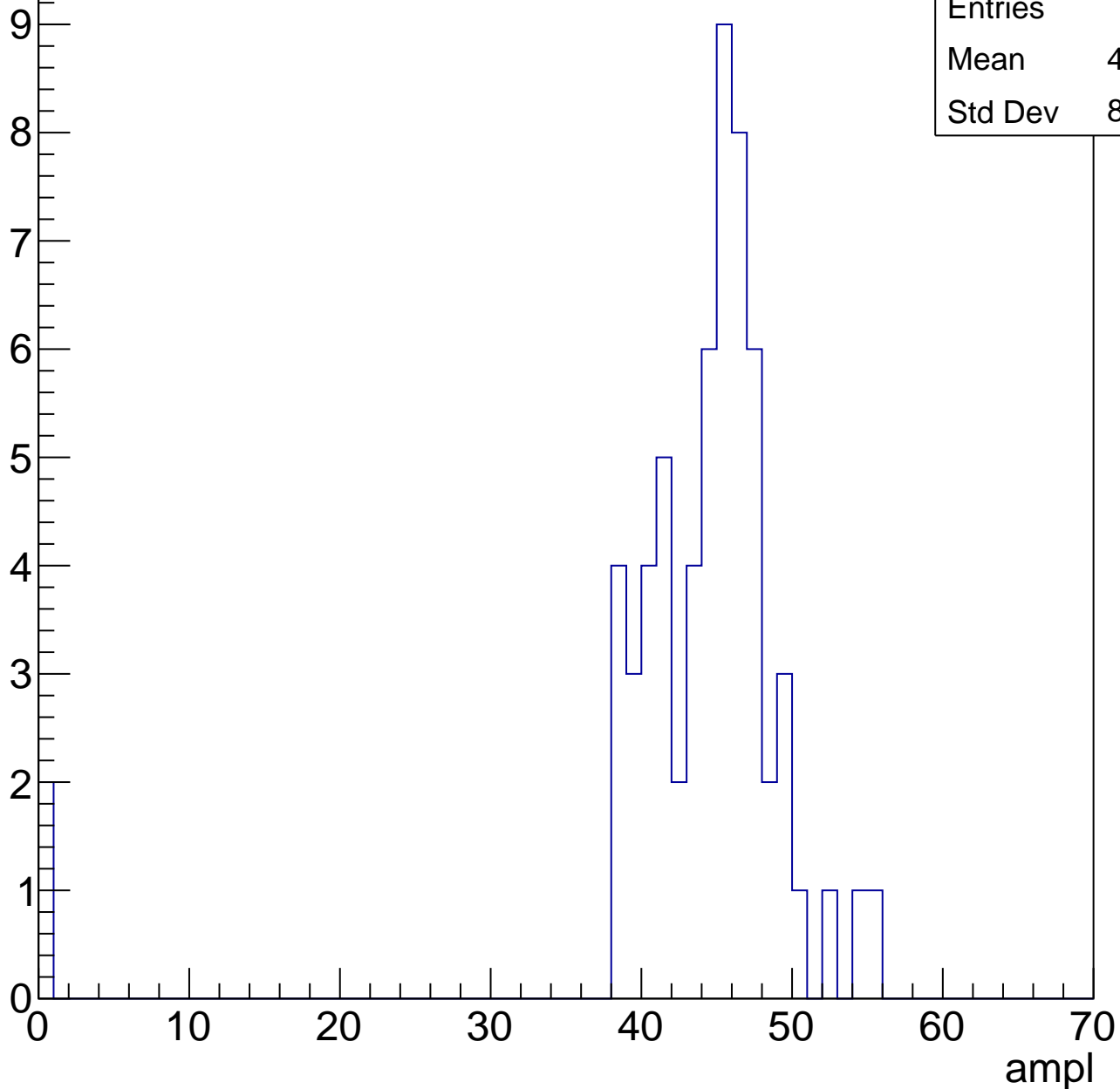
Gaus Width: 3.3073



B1L103S, U1-ch0, adc3

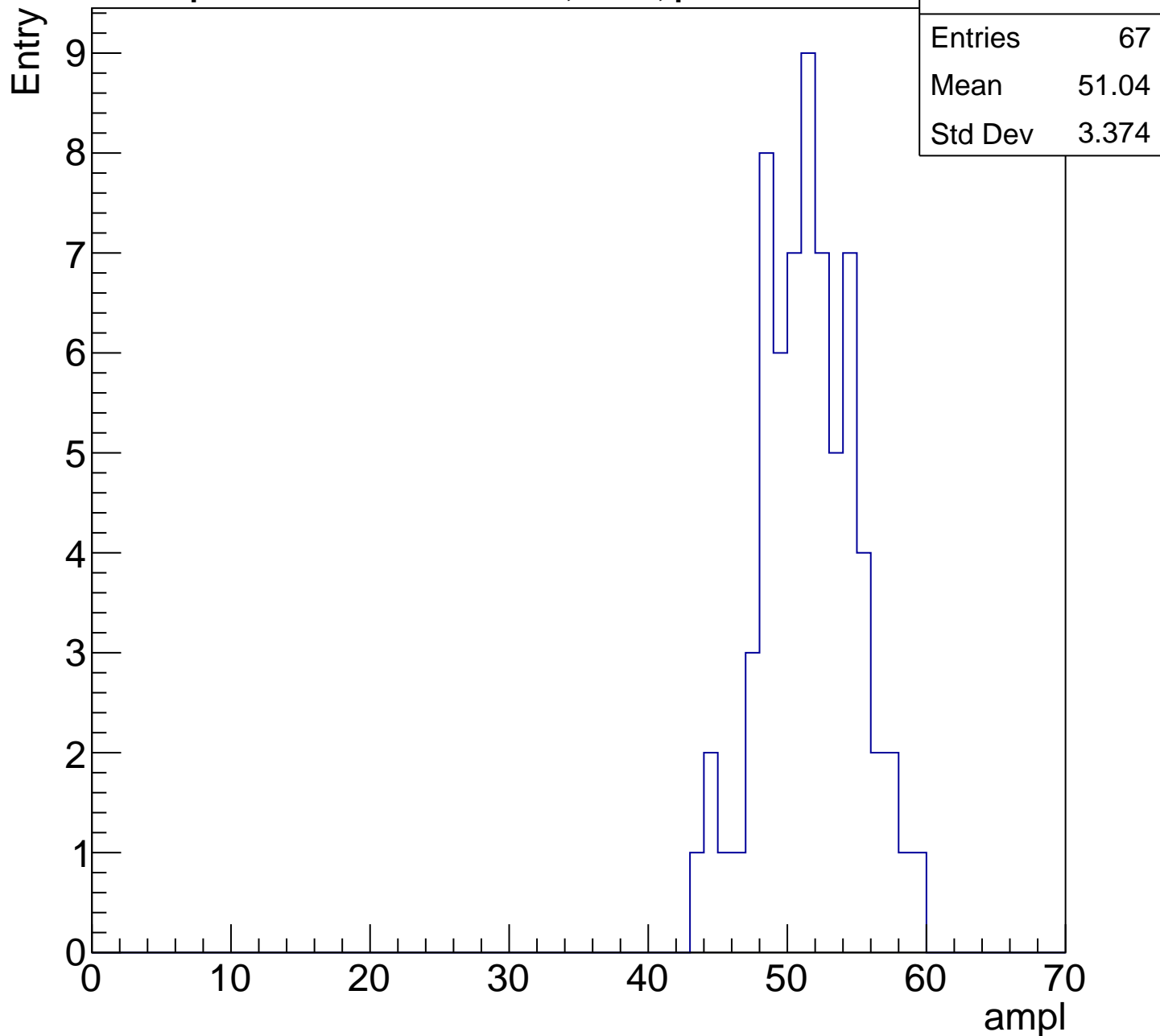
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

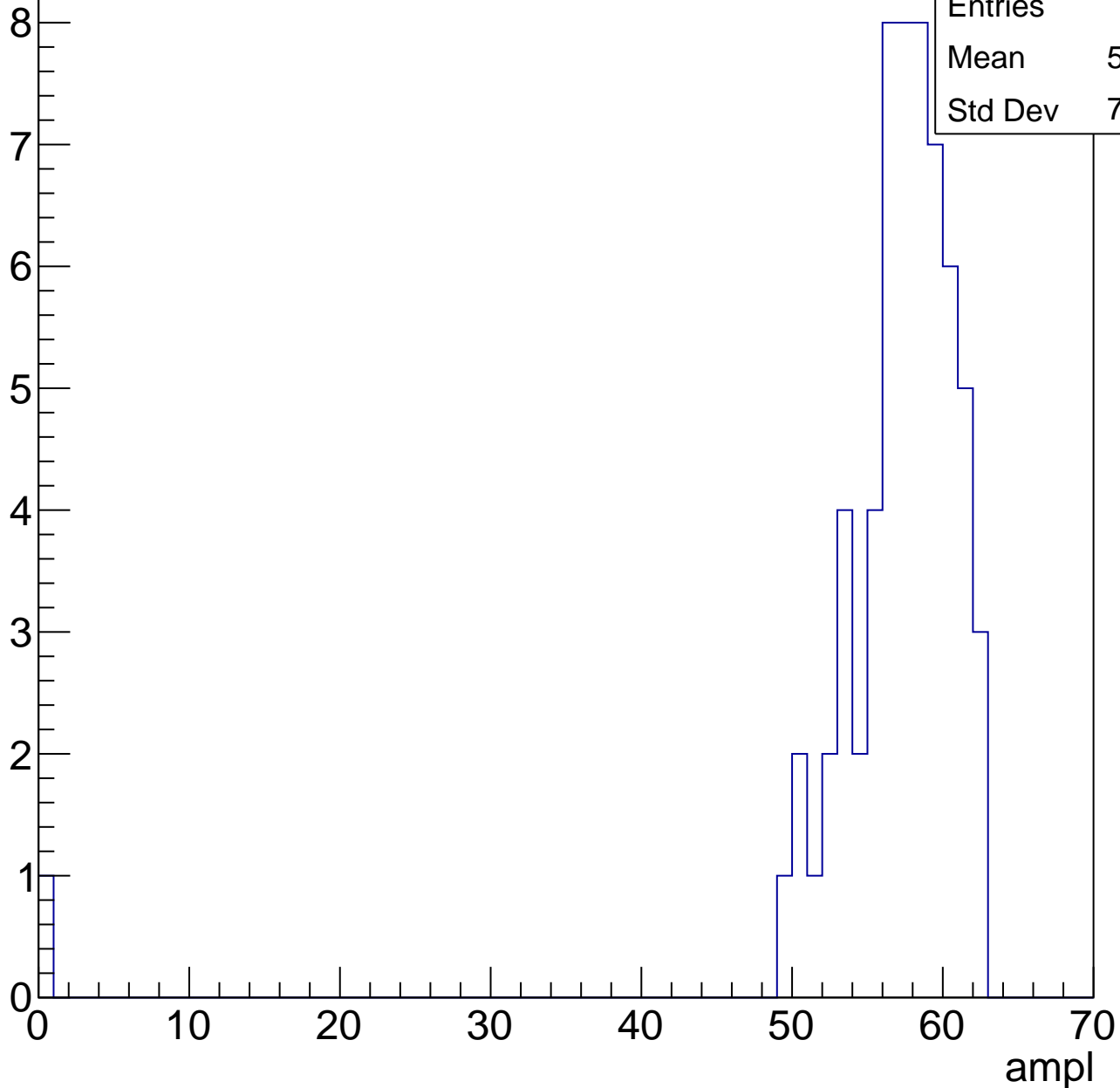


B1L103S, U1-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	56.06
Std Dev	7.822

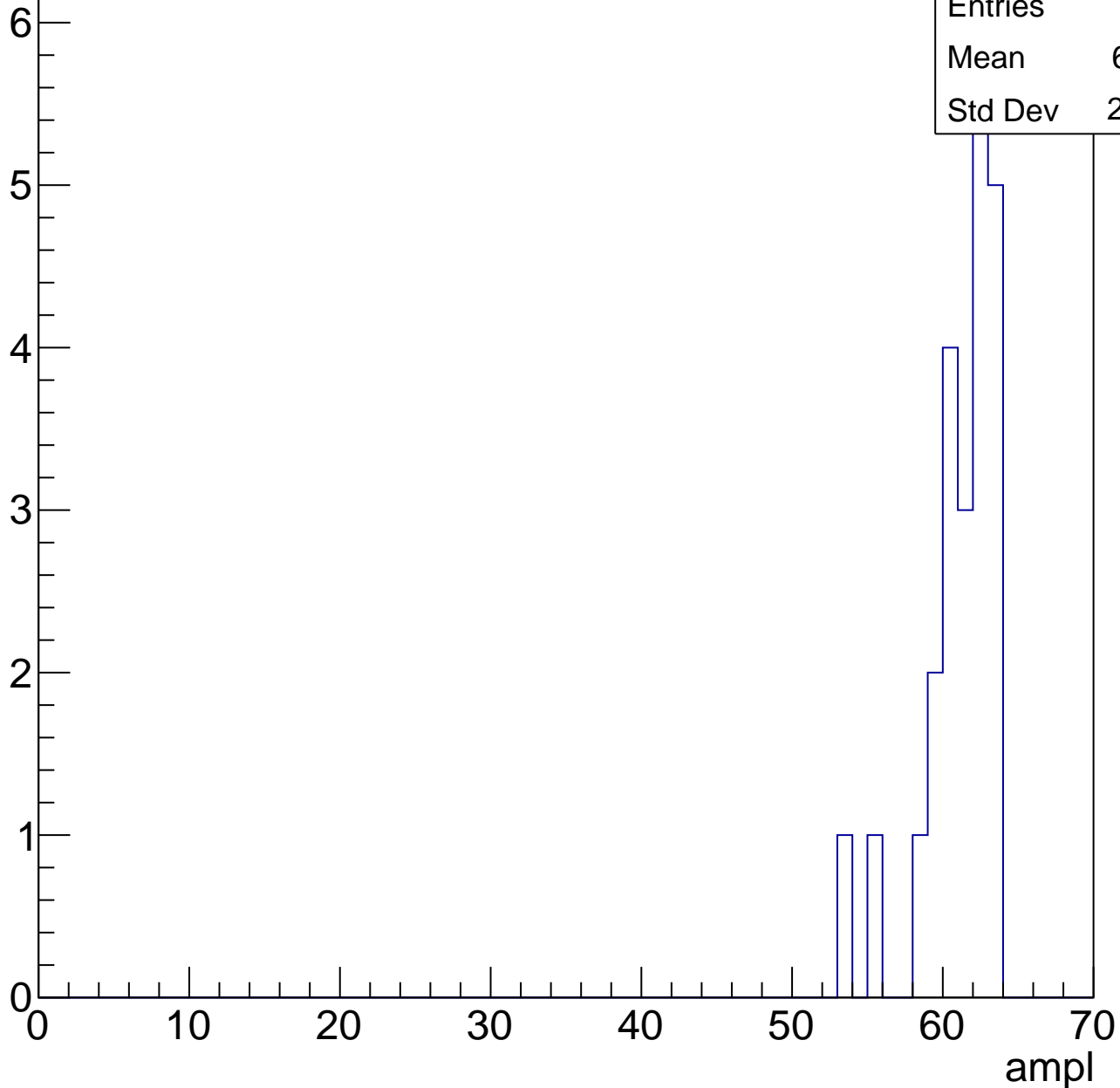


B1L103S, U1-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

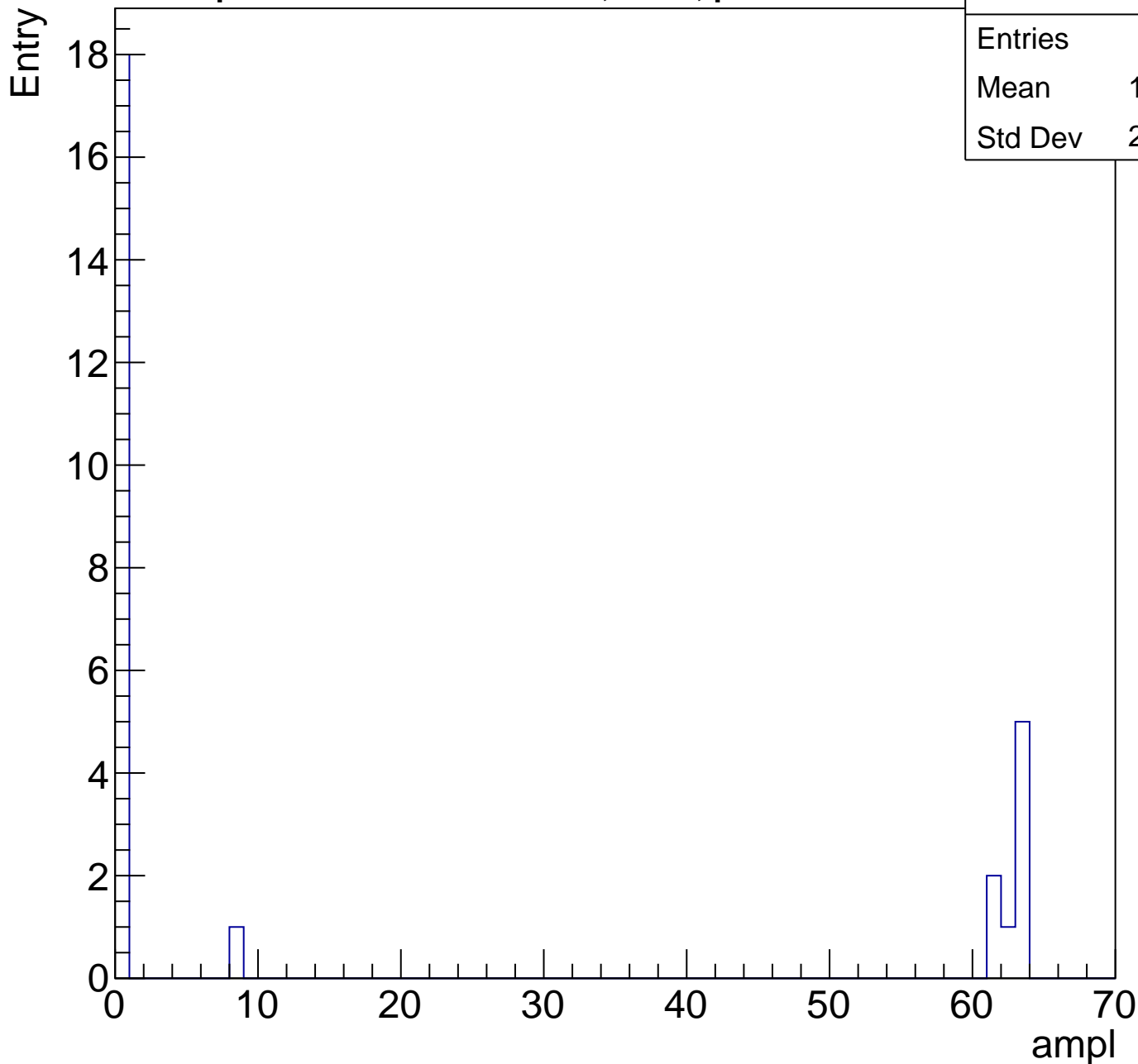
Entries	23
Mean	60.61
Std Dev	2.498



B1L103S, U1-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	27
Mean	18.78
Std Dev	28.33



B1L103S, U1-ch1, adc0

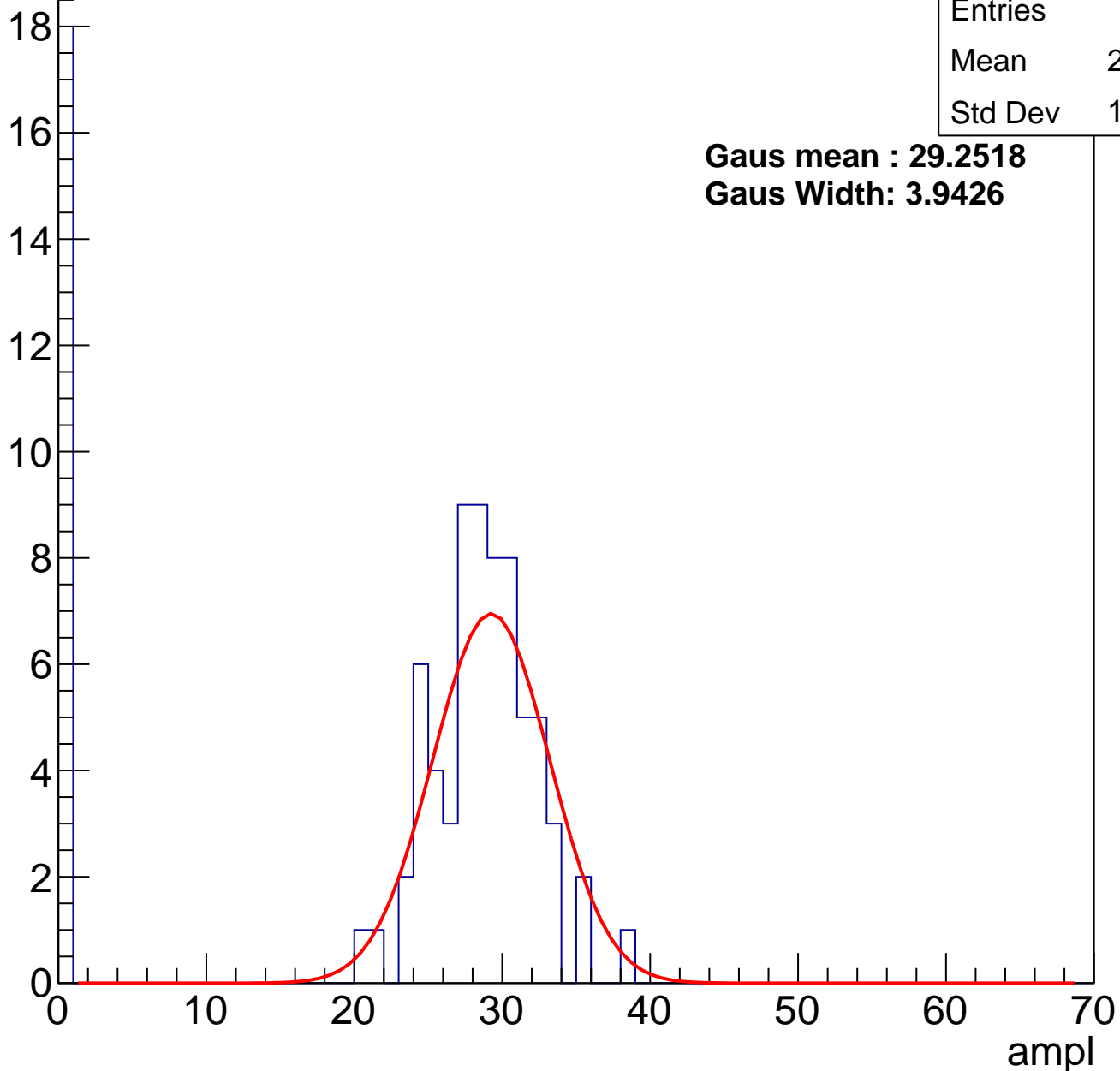
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.33
Std Dev	11.95

Gaus mean : 29.2518

Gaus Width: 3.9426

Entry



B1L103S, U1-ch1, adc1

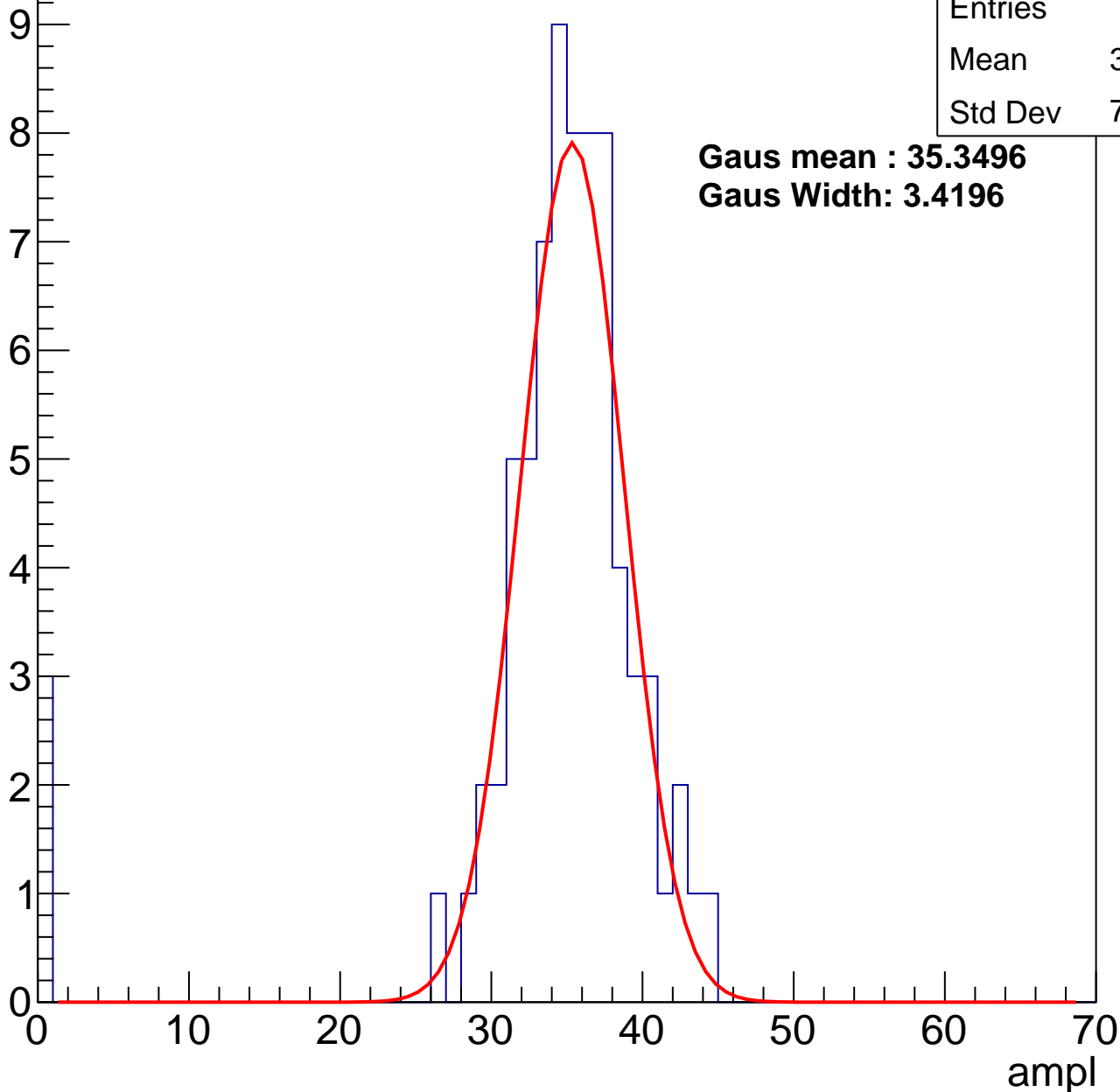
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	33.64
Std Dev	7.736

Gaus mean : 35.3496

Gaus Width: 3.4196



B1L103S, U1-ch1, adc2

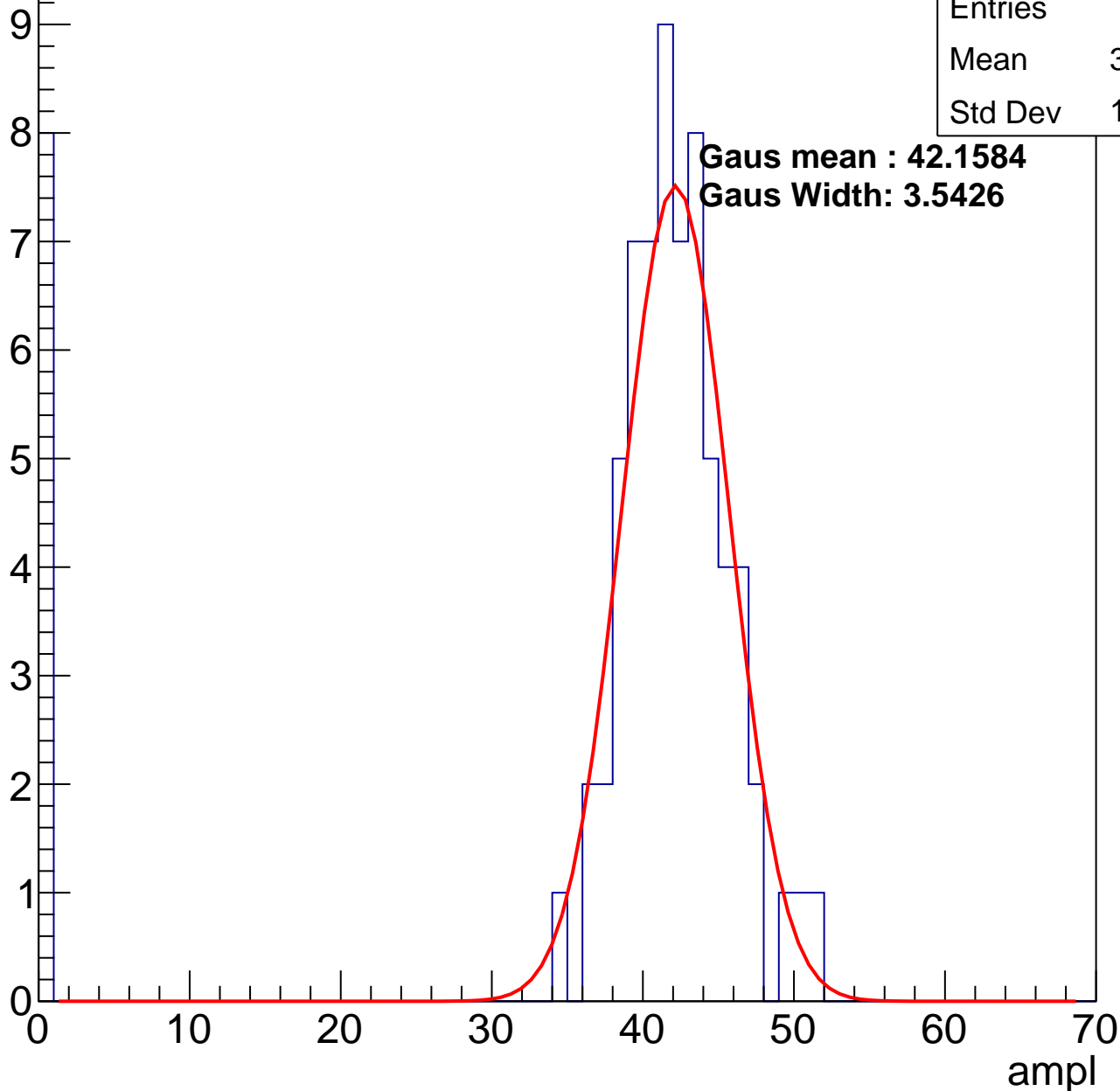
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	37.27
Std Dev	13.35

Gaus mean : 42.1584

Gaus Width: 3.5426

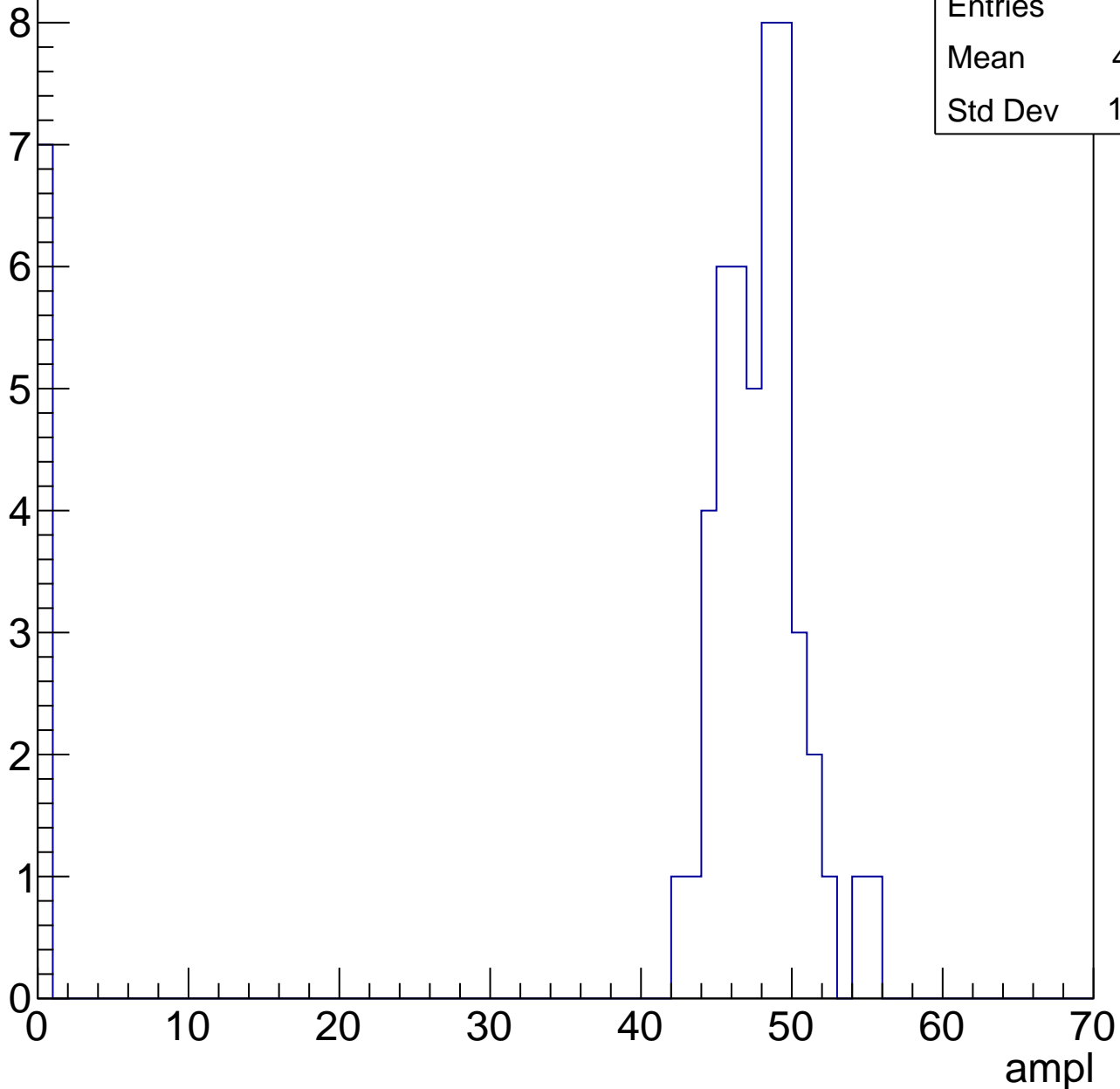


B1L103S, U1-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	41.31
Std Dev	16.14

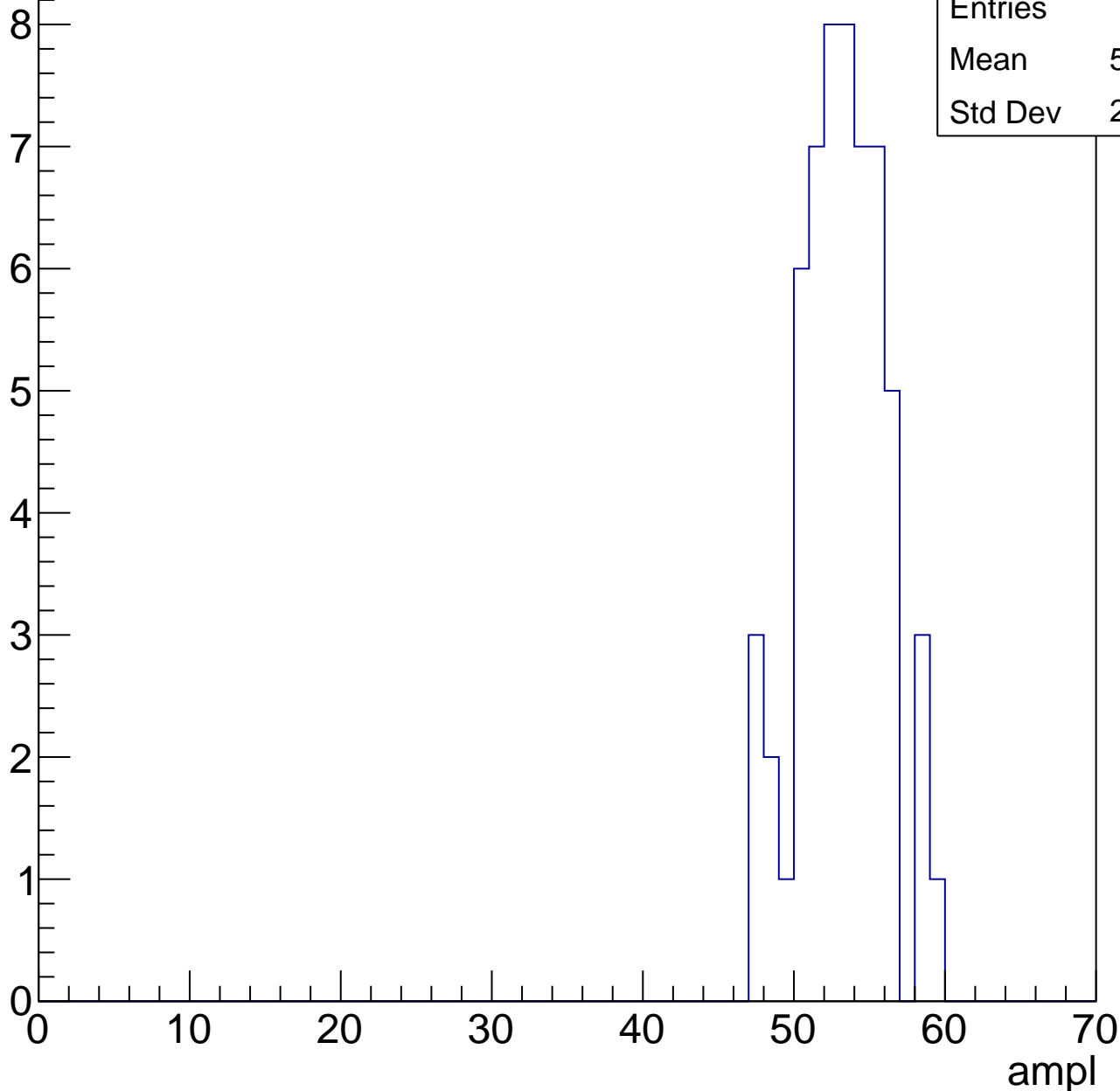


B1L103S, U1-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.74
Std Dev	2.789

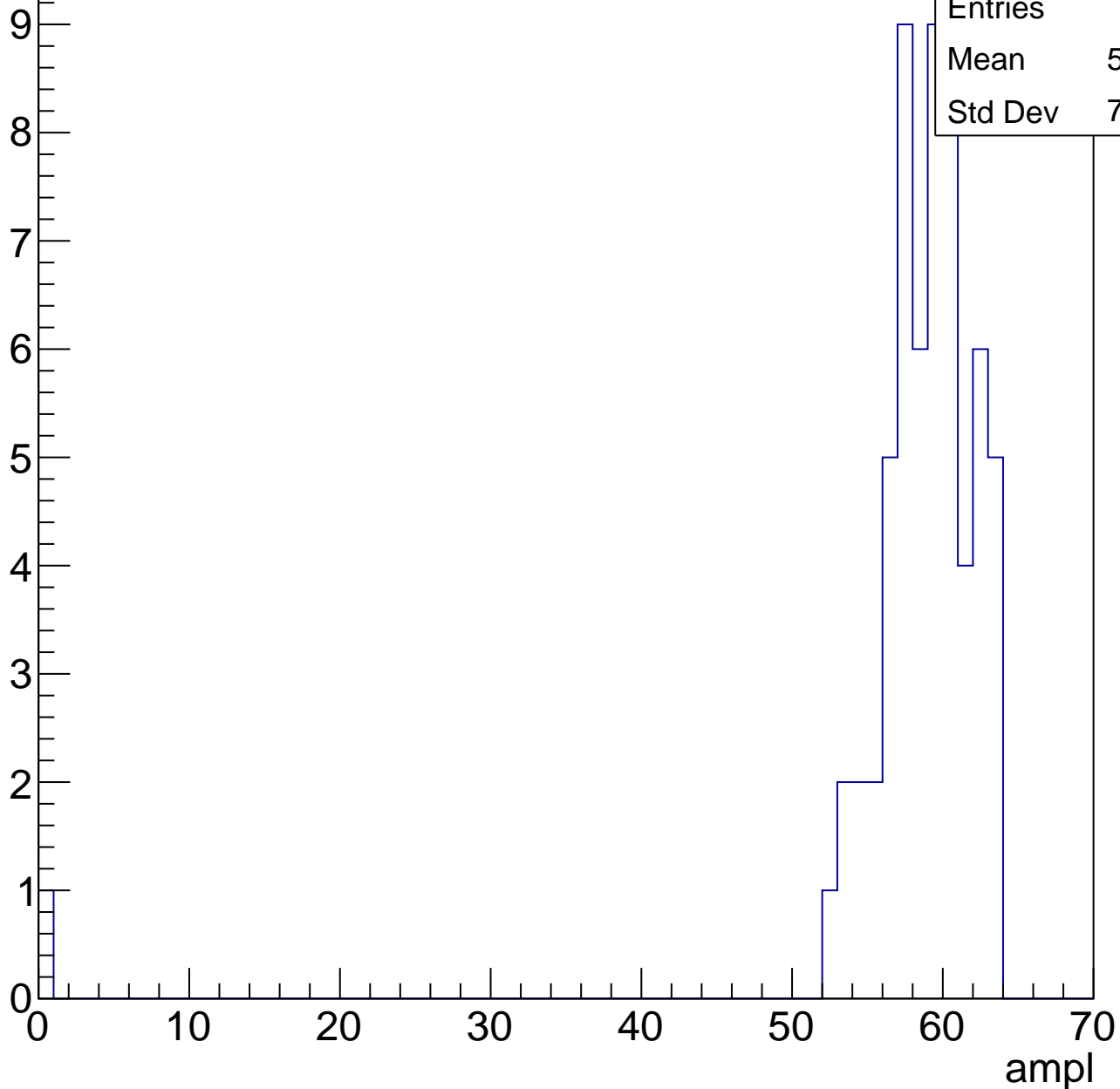


B1L103S, U1-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.69
Std Dev	7.919

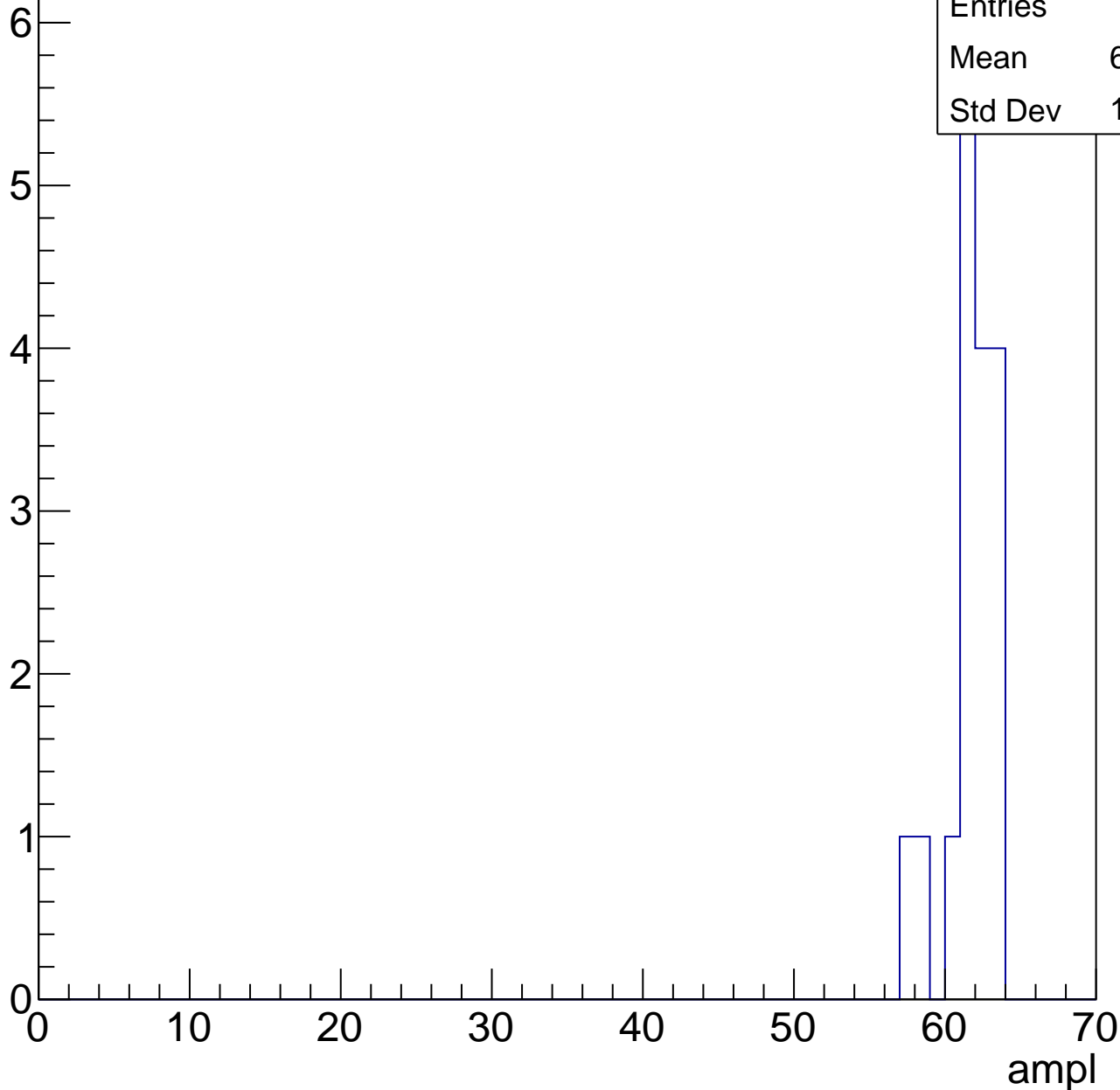


B1L103S, U1-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

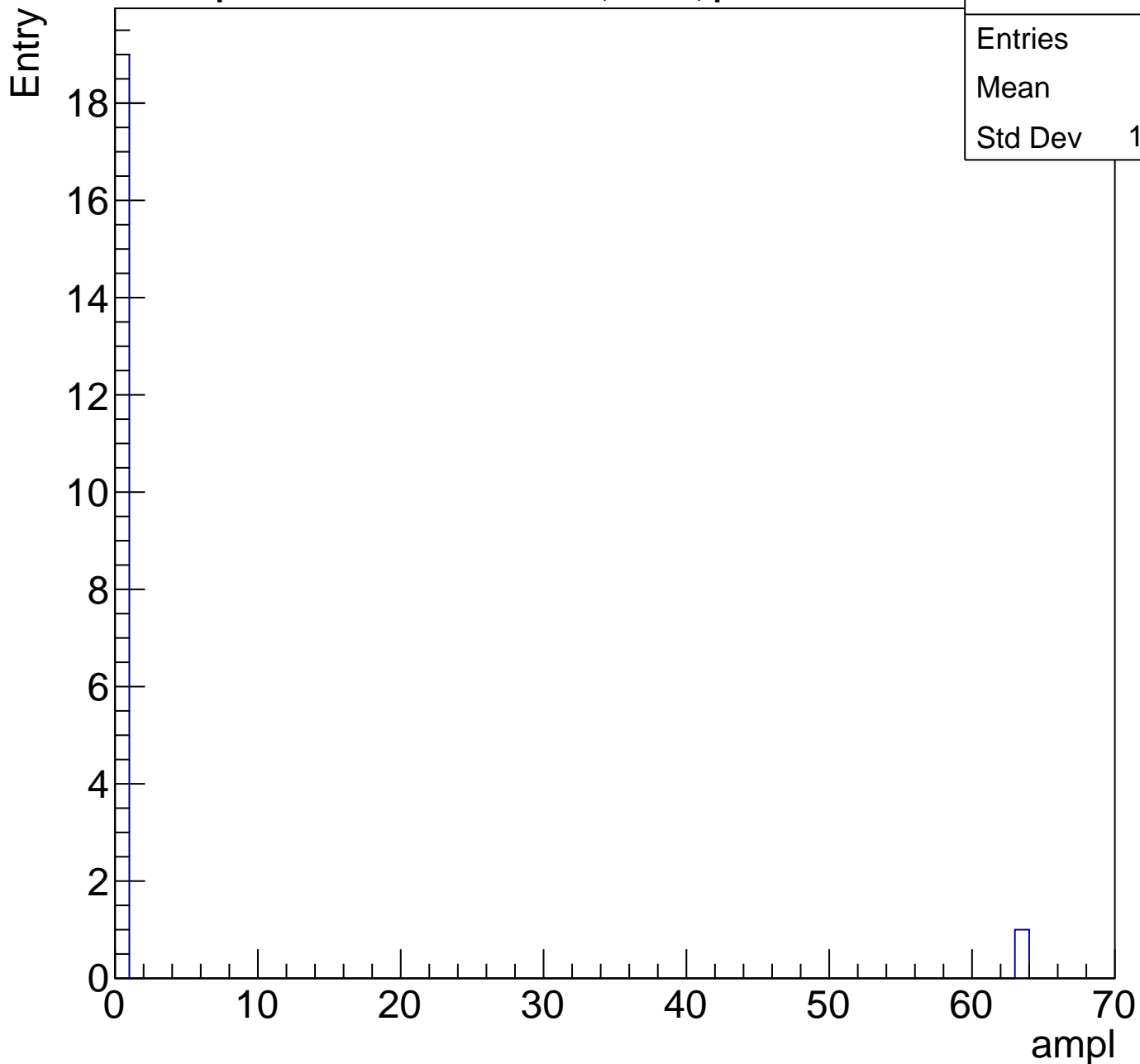
Entries	17
Mean	61.24
Std Dev	1.628



B1L103S, U1-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



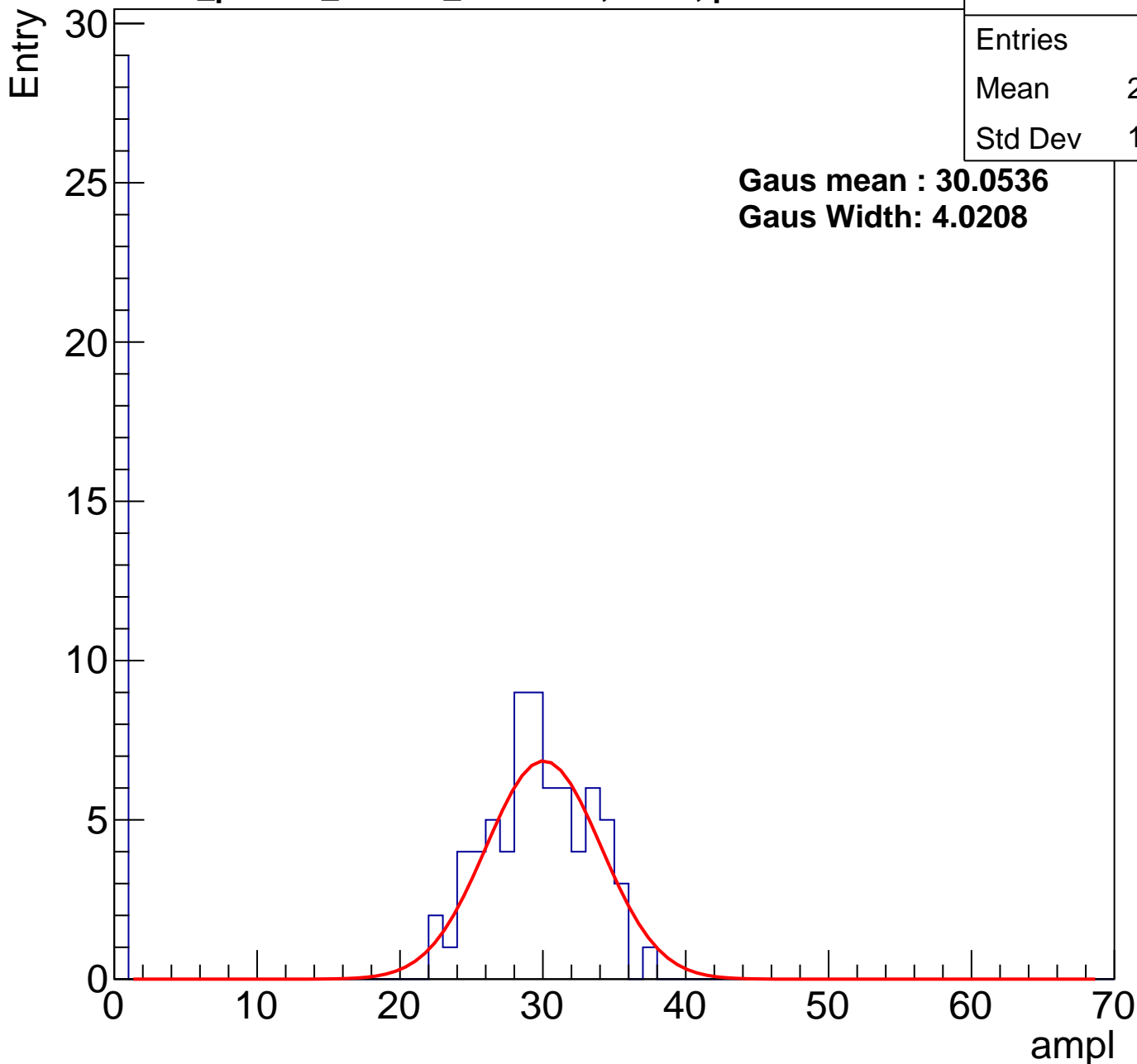
B1L103S, U1-ch2, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	20.59
Std Dev	13.66

Gaus mean : 30.0536

Gaus Width: 4.0208



B1L103S, U1-ch2, adc1

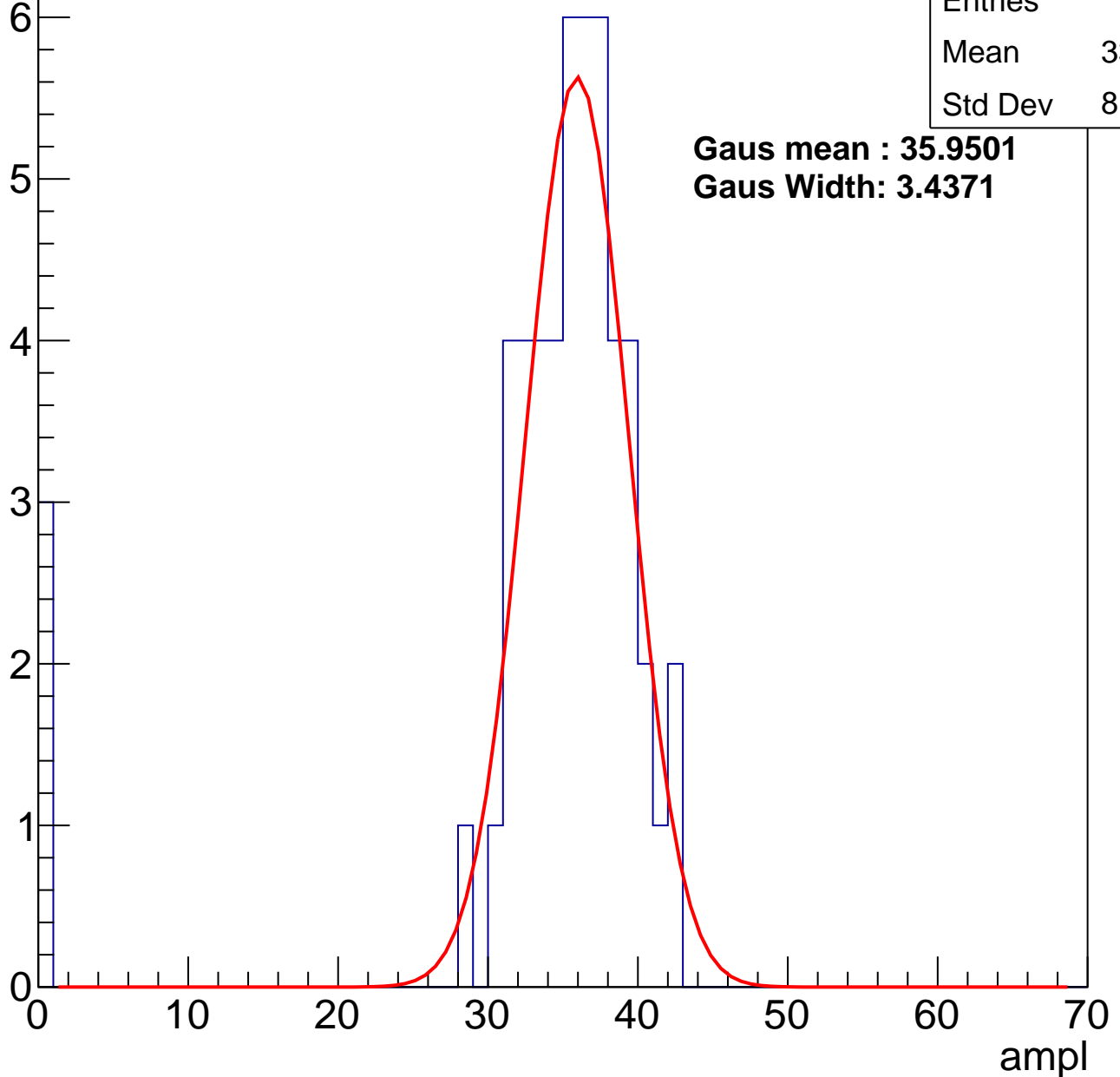
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	33.44
Std Dev	8.833

Gaus mean : 35.9501

Gaus Width: 3.4371



B1L103S, U1-ch2, adc2

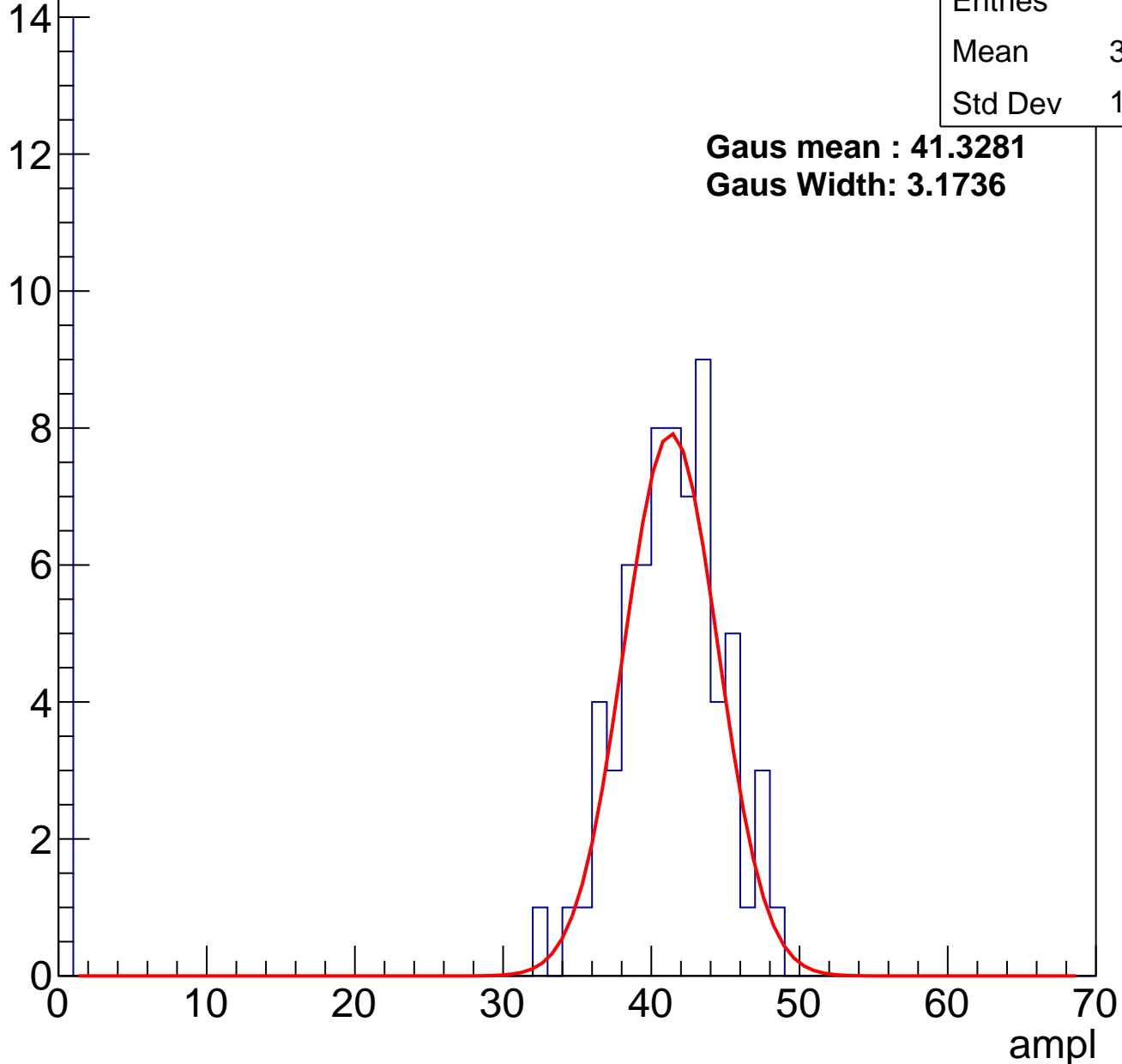
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	33.94
Std Dev	15.69

Gaus mean : 41.3281

Gaus Width: 3.1736

Entry

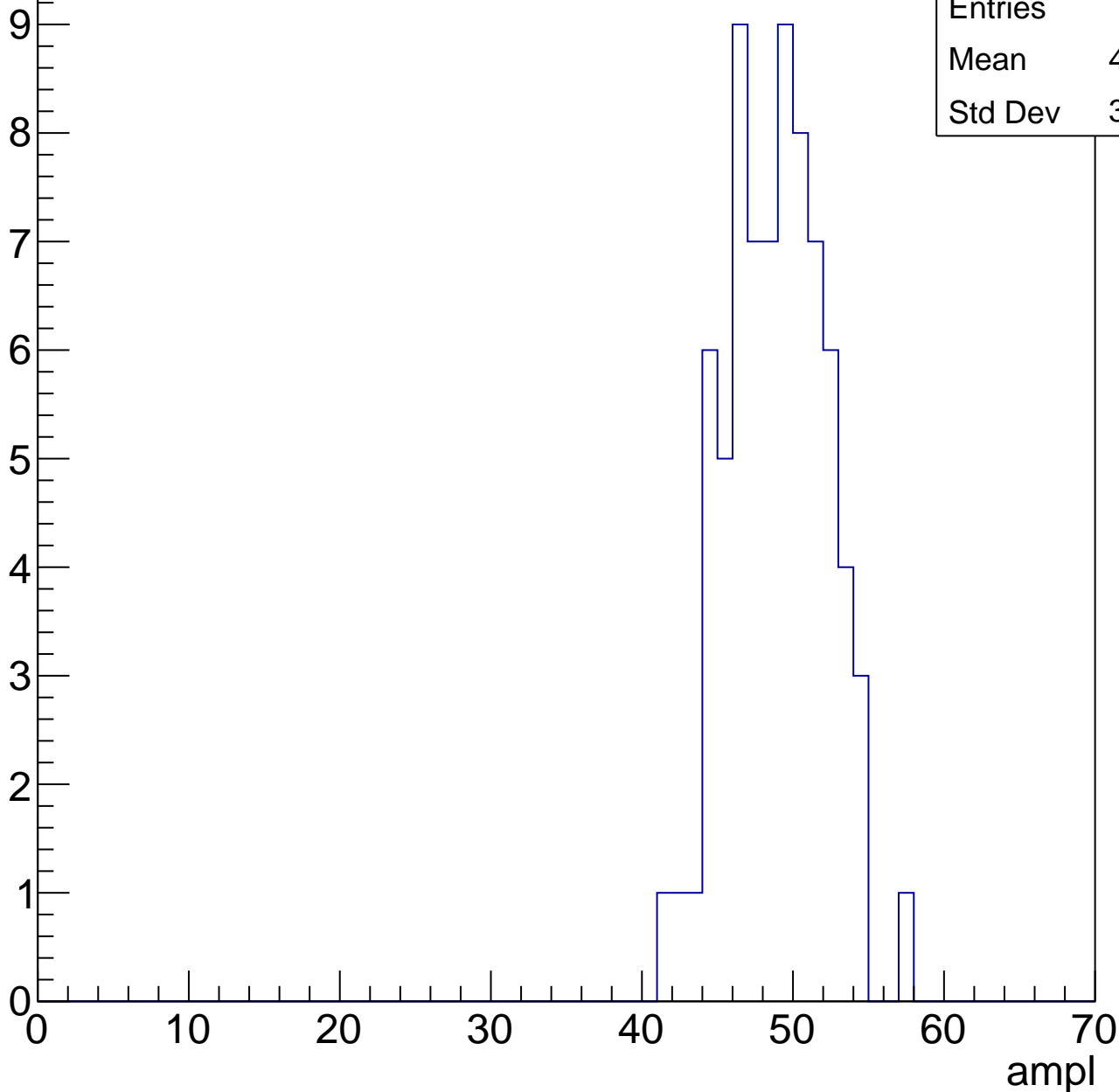


B1L103S, U1-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	48.47
Std Dev	3.197

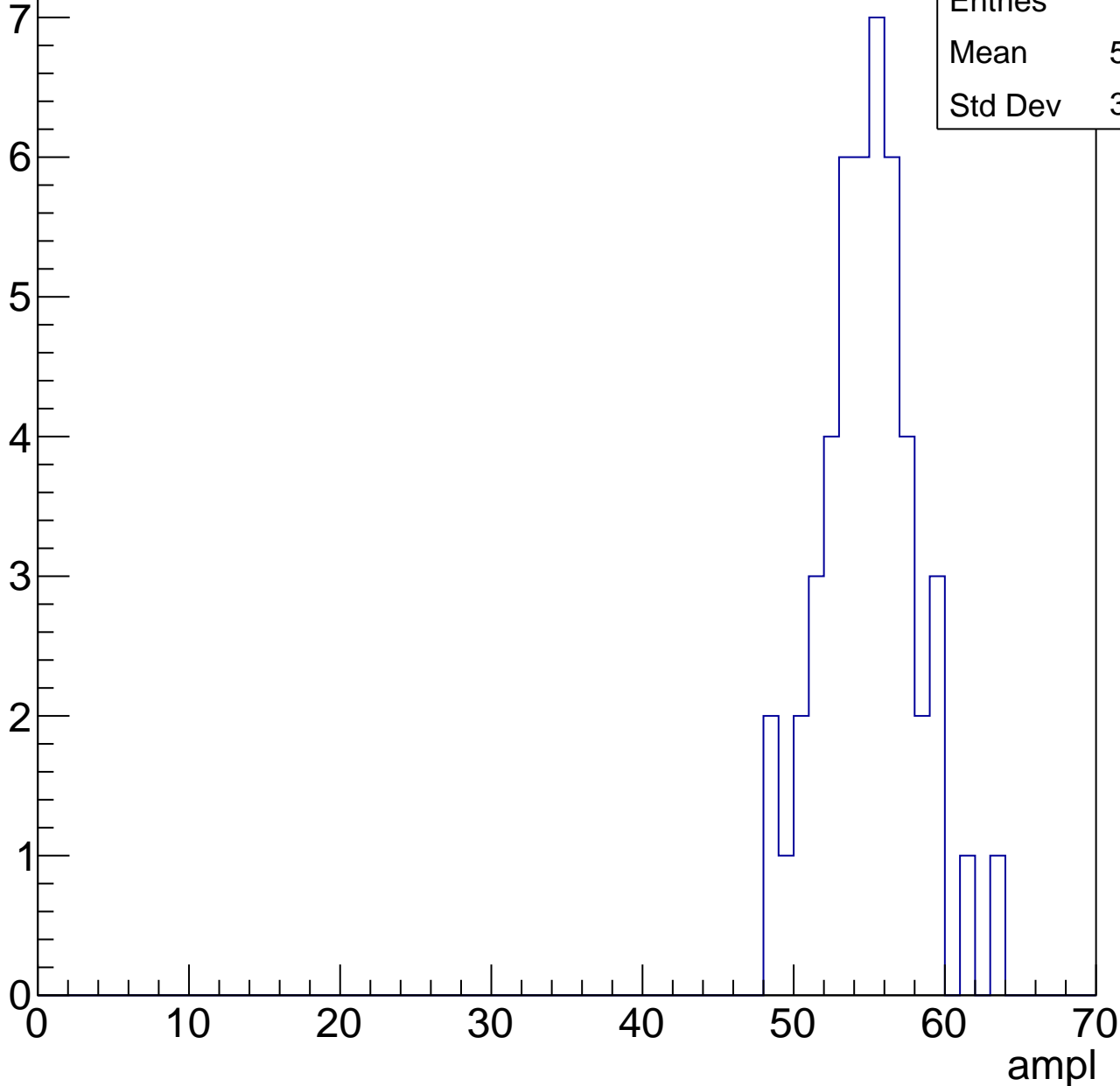


B1L103S, U1-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.46
Std Dev	3.136

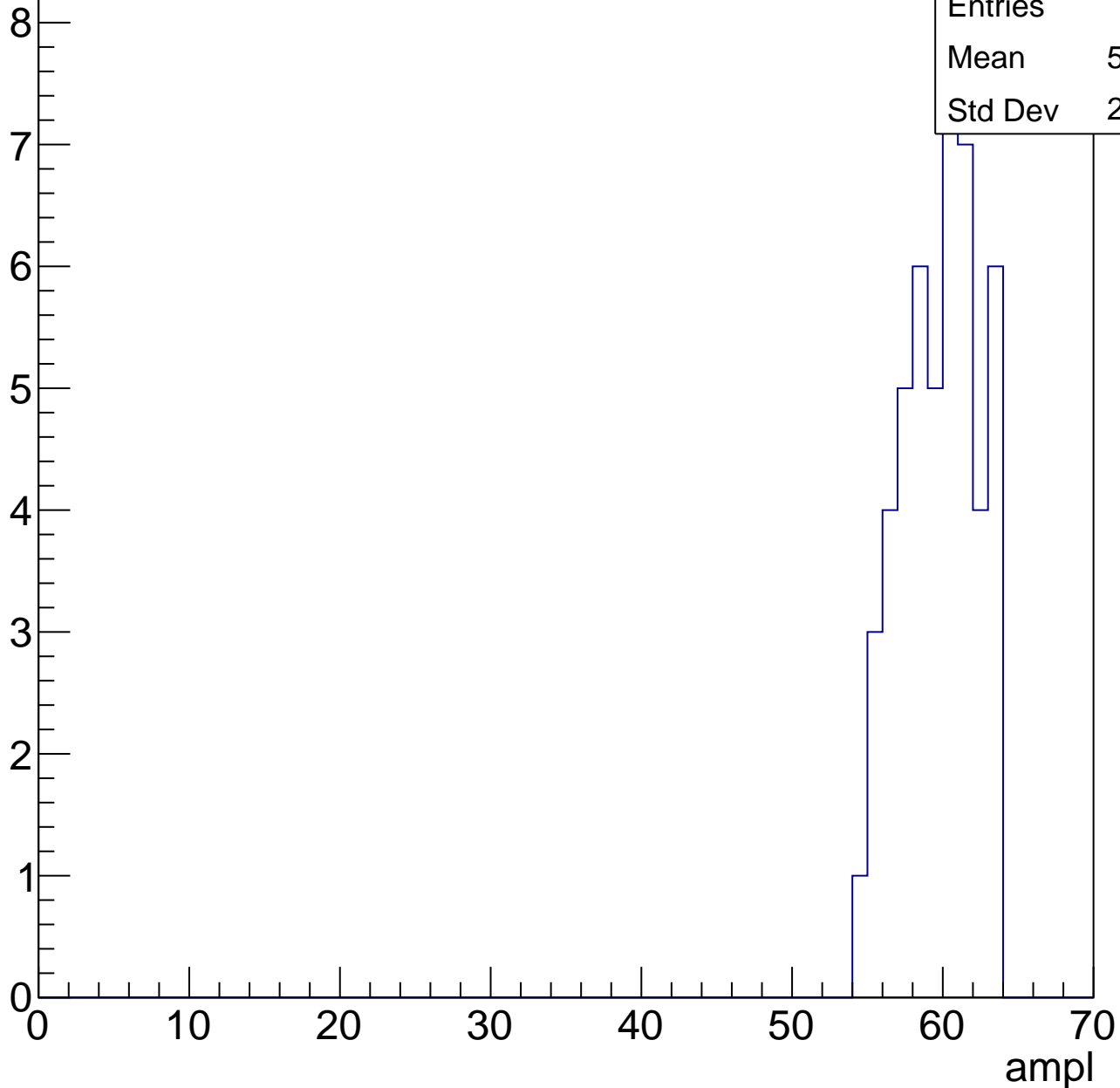


B1L103S, U1-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	59.27
Std Dev	2.473

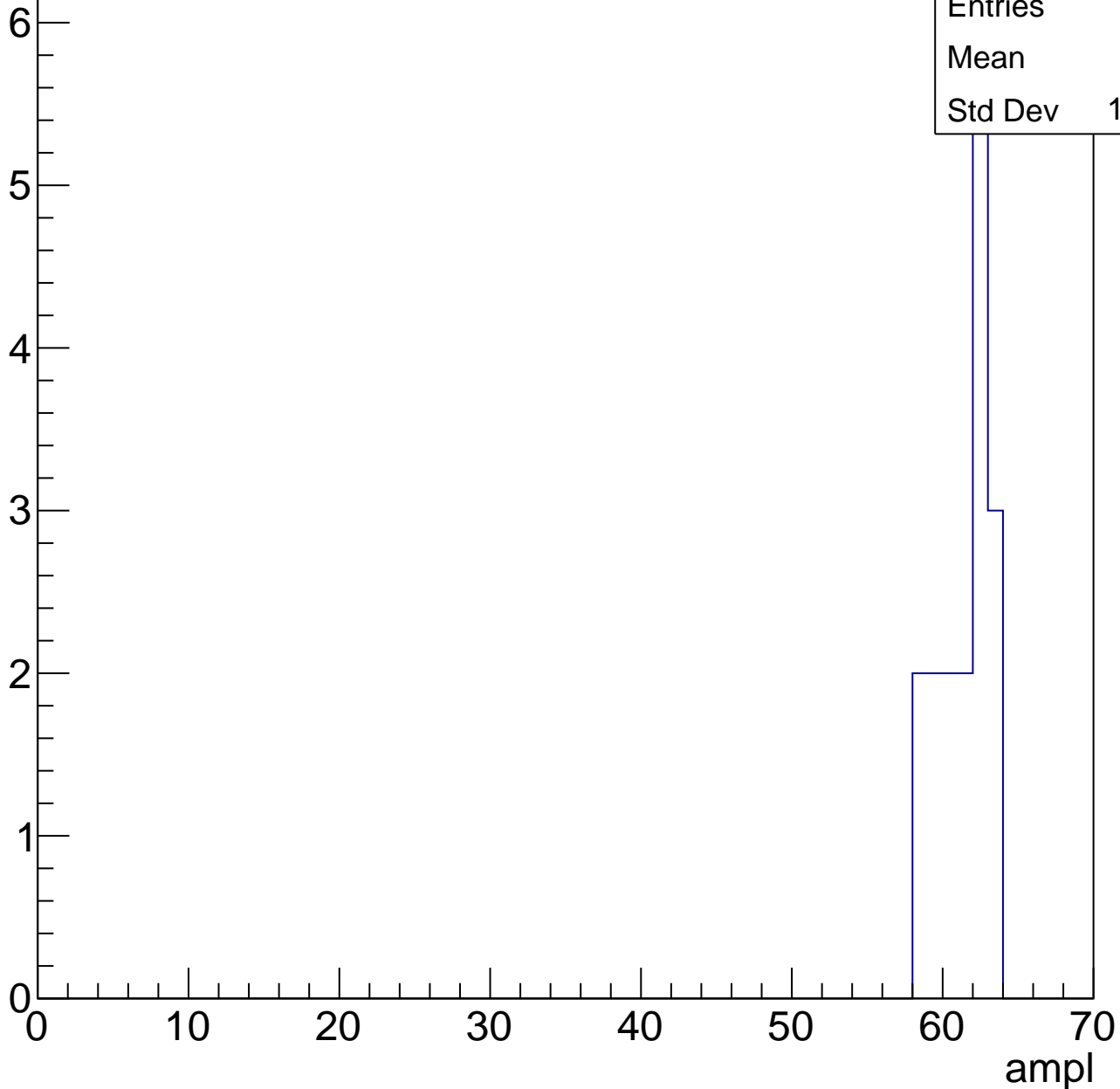


B1L103S, U1-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

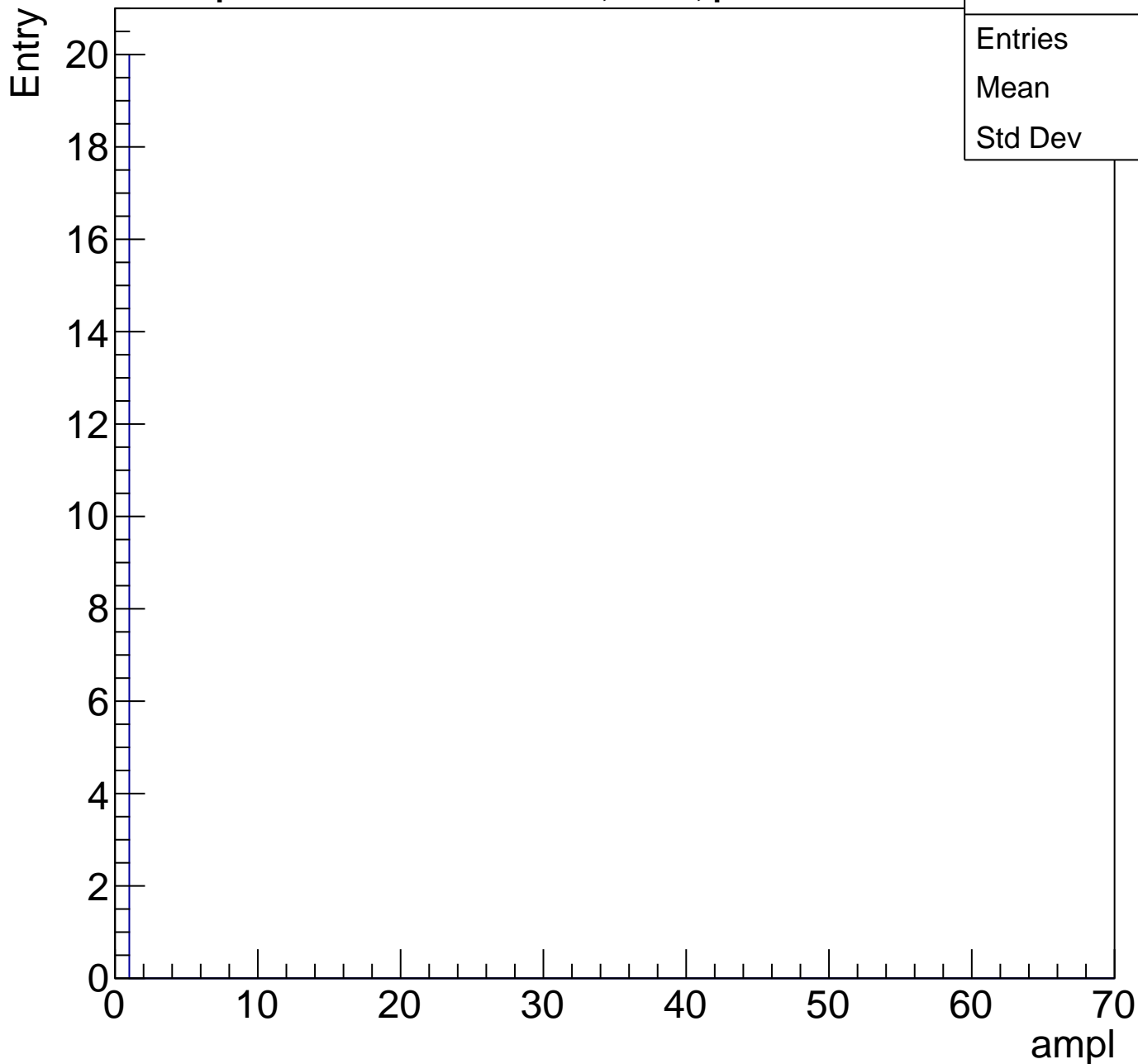
Entries	17
Mean	61
Std Dev	1.645



B1L103S, U1-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0
Std Dev	0



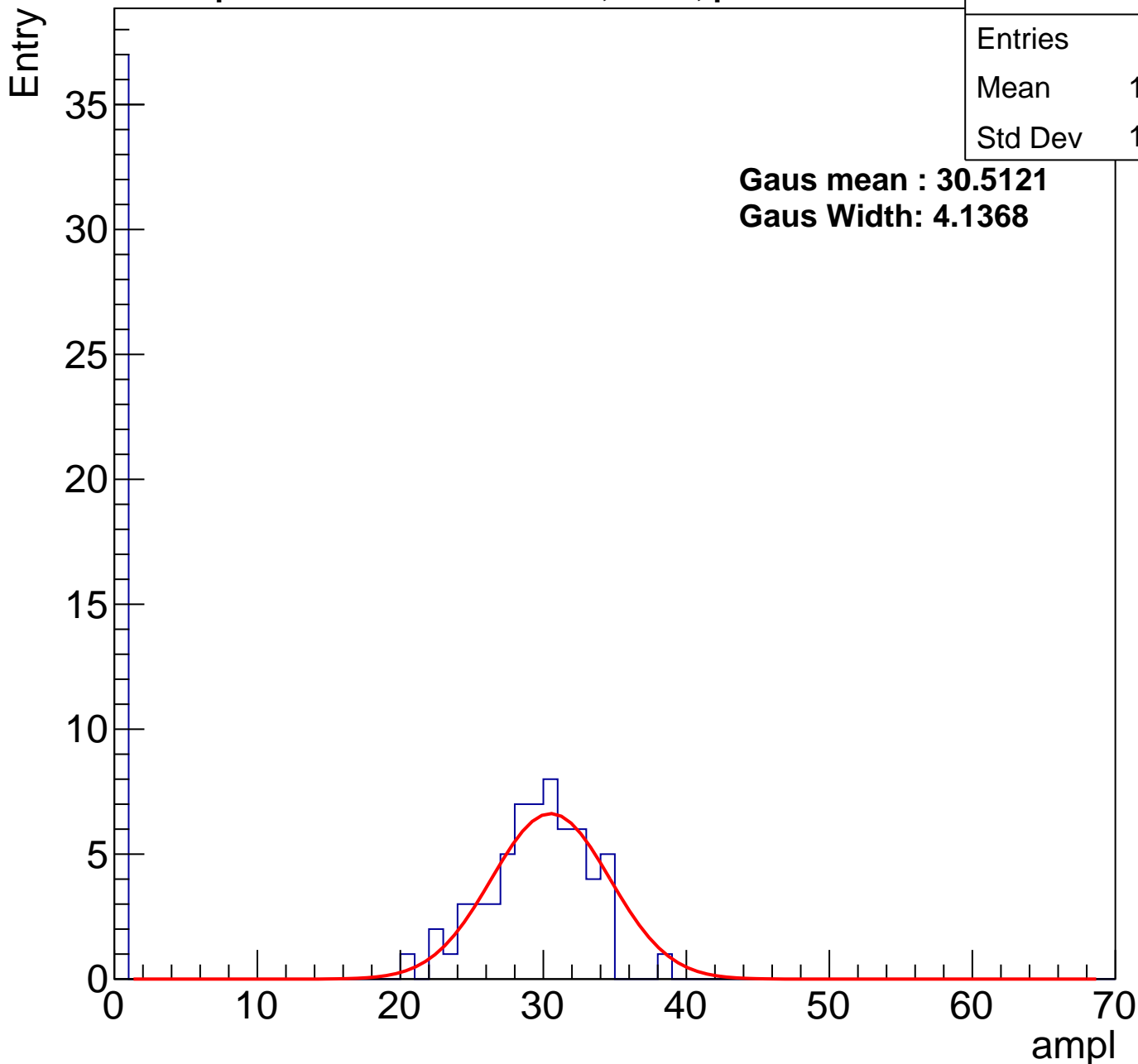
B1L103S, U1-ch3, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	18.22
Std Dev	14.34

Gaus mean : 30.5121

Gaus Width: 4.1368



B1L103S, U1-ch3, adc1

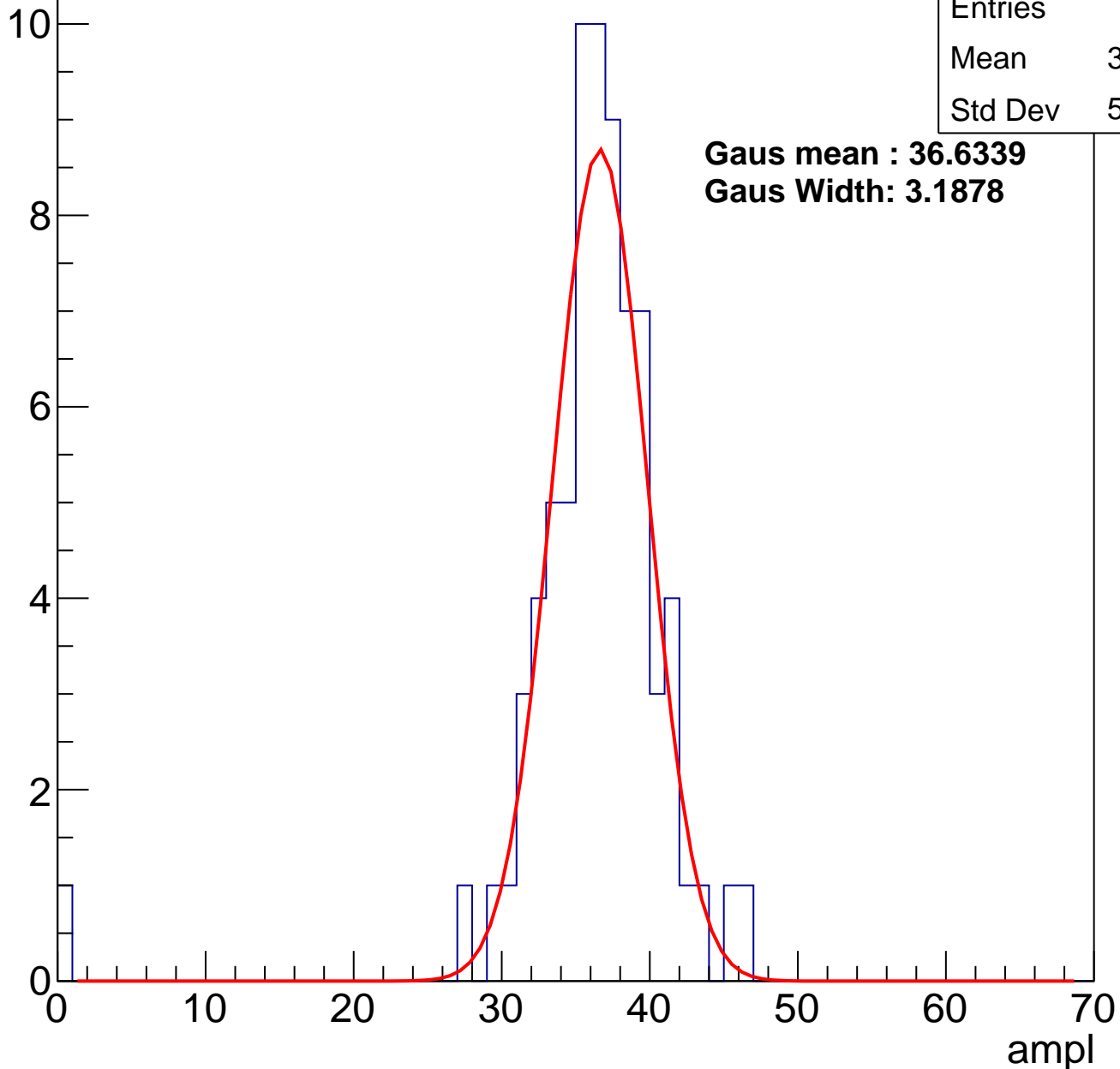
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	35.79
Std Dev	5.399

Gaus mean : 36.6339

Gaus Width: 3.1878

Entry



B1L103S, U1-ch3, adc2

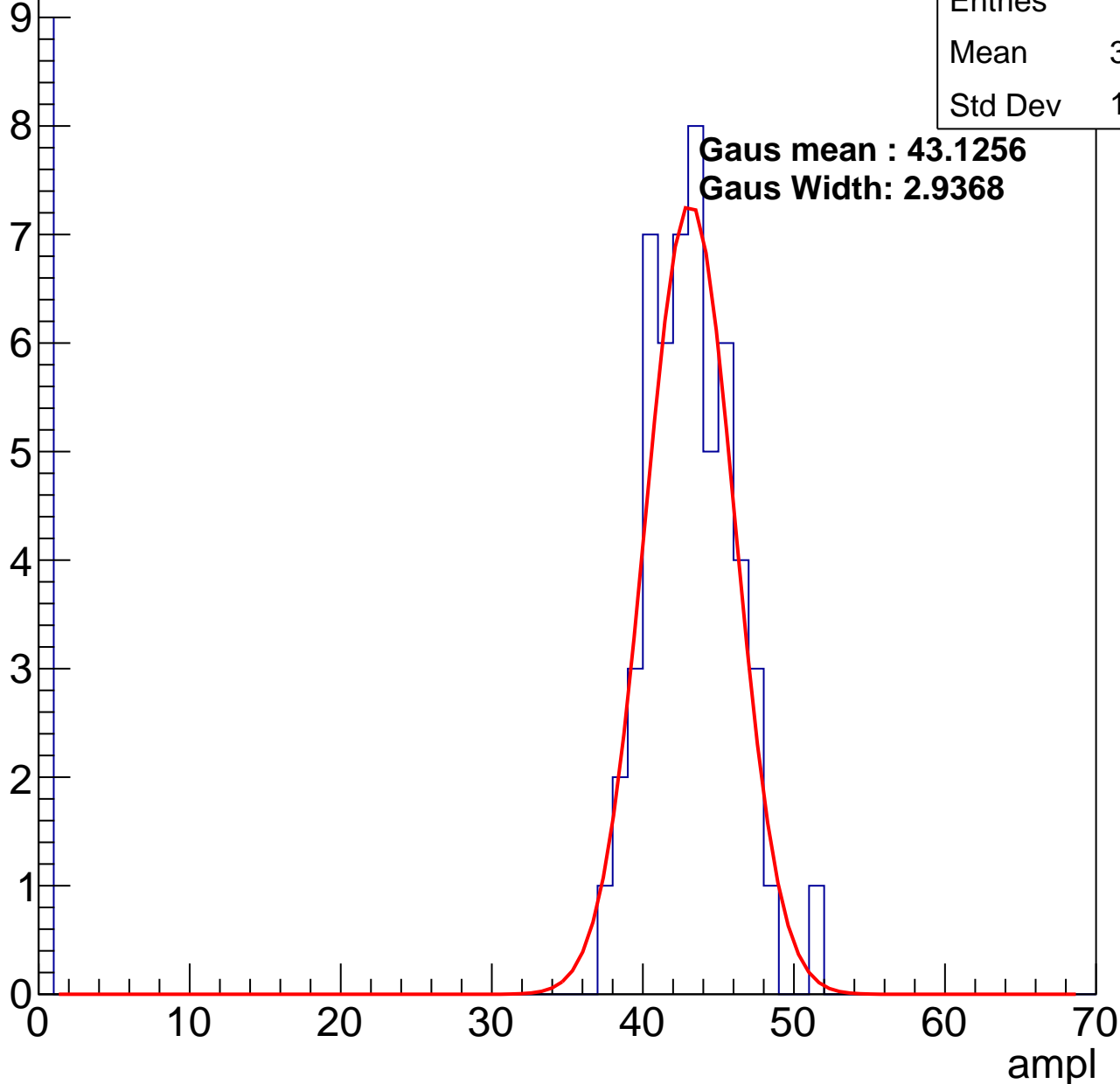
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.63
Std Dev	15.18

Gaus mean : 43.1256

Gaus Width: 2.9368



B1L103S, U1-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

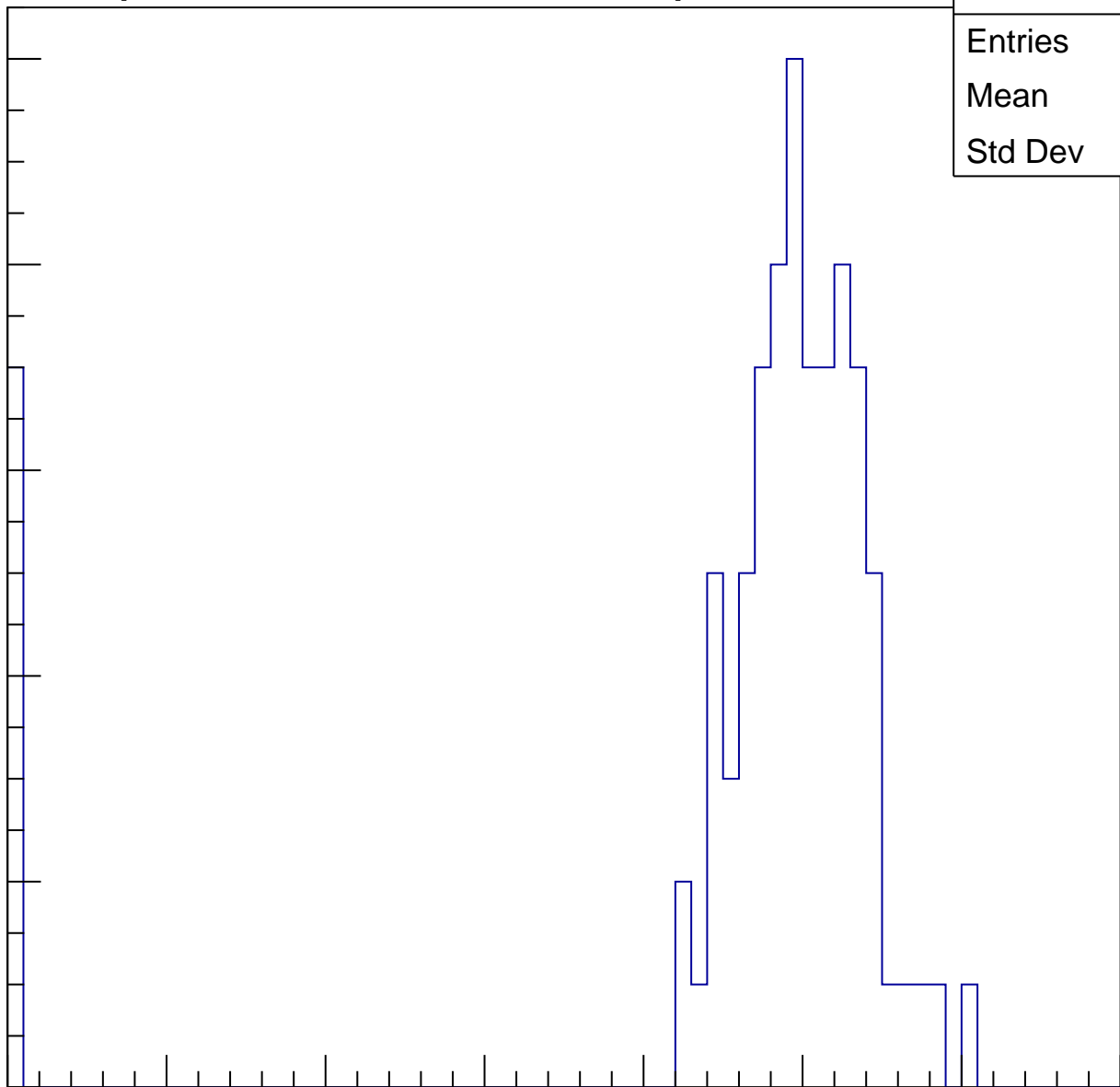
Entries	87
Mean	45.57
Std Dev	13.92

Entry

10
8
6
4
2
0

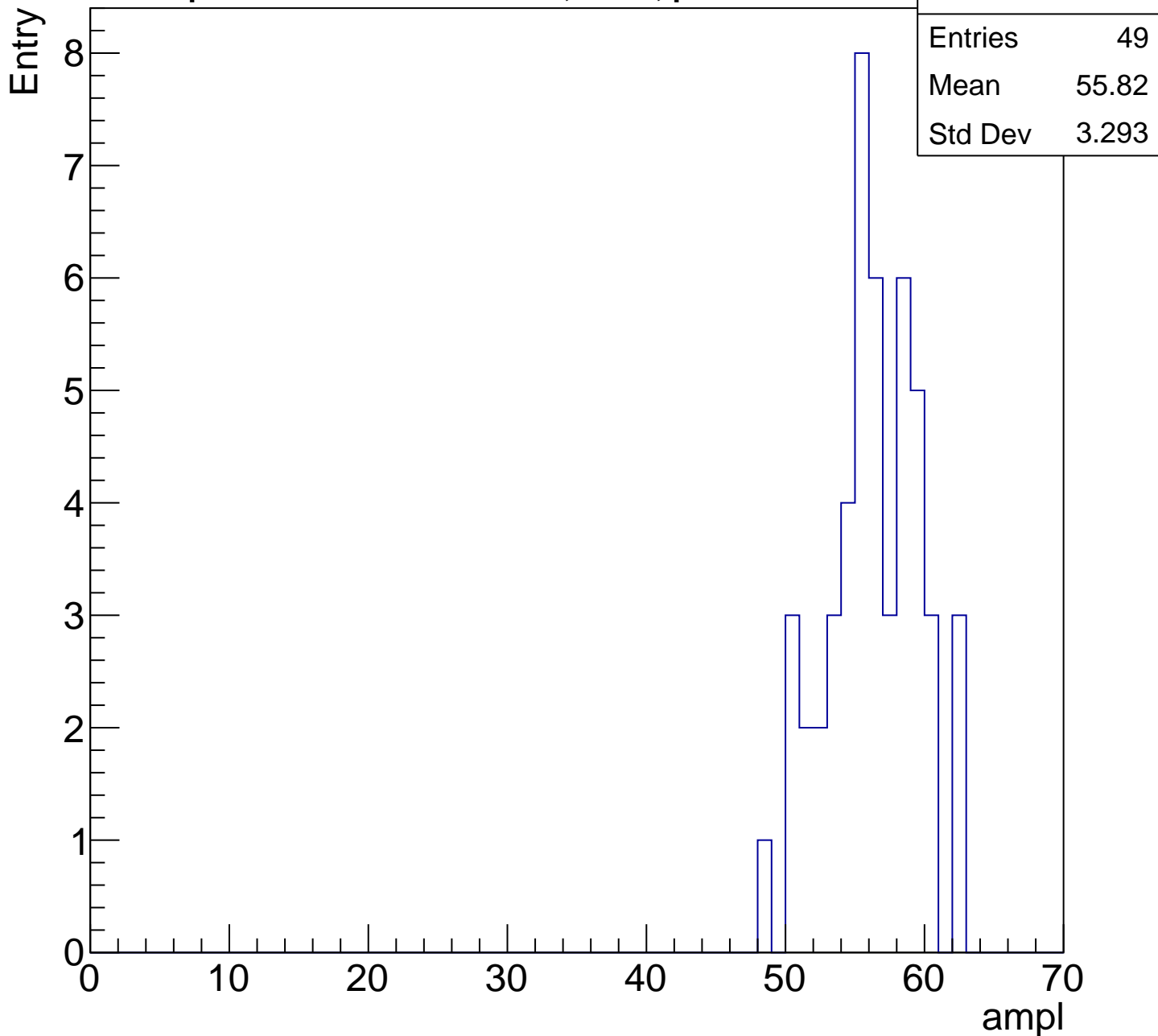
0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

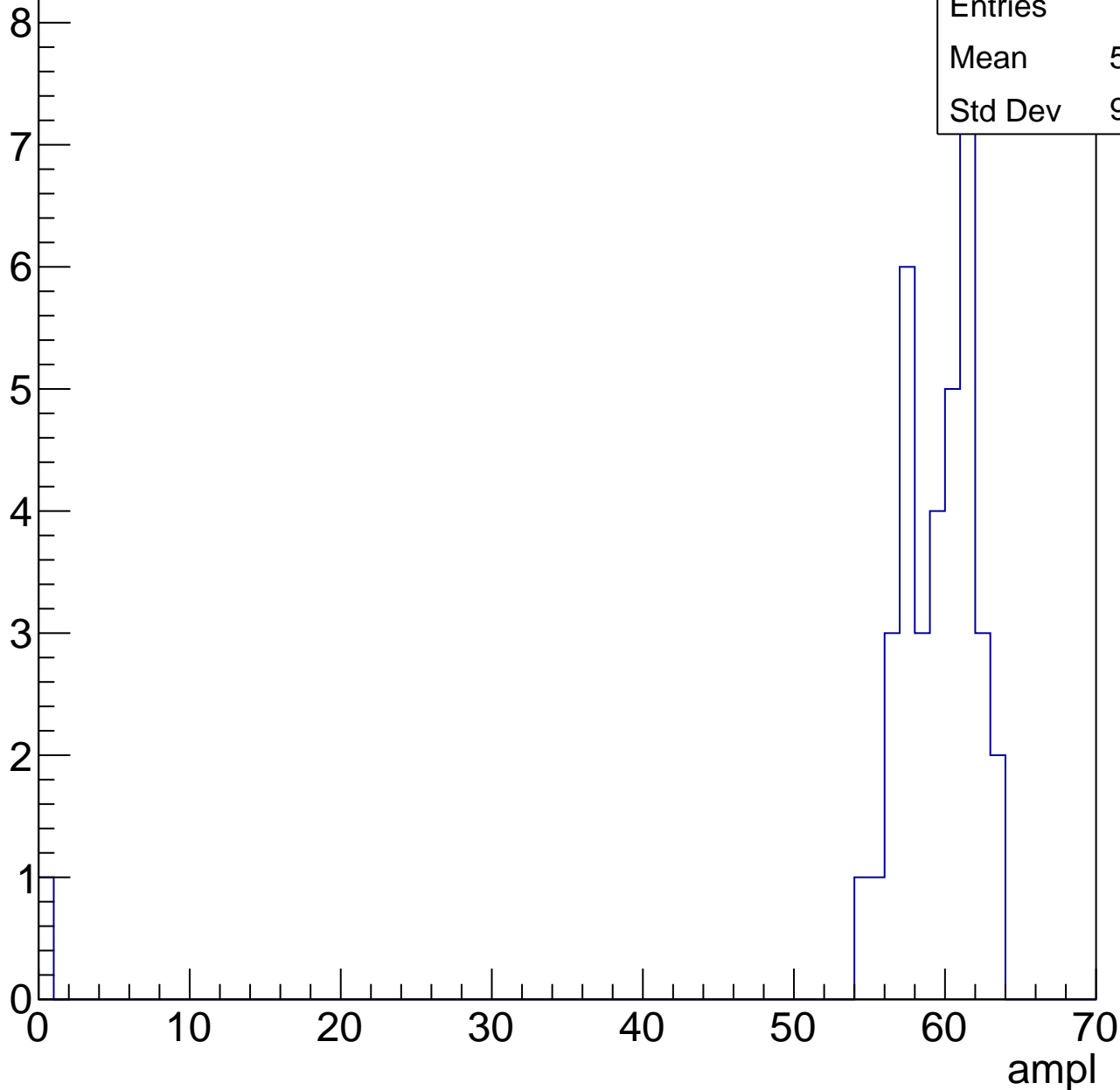


B1L103S, U1-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	57.54
Std Dev	9.855

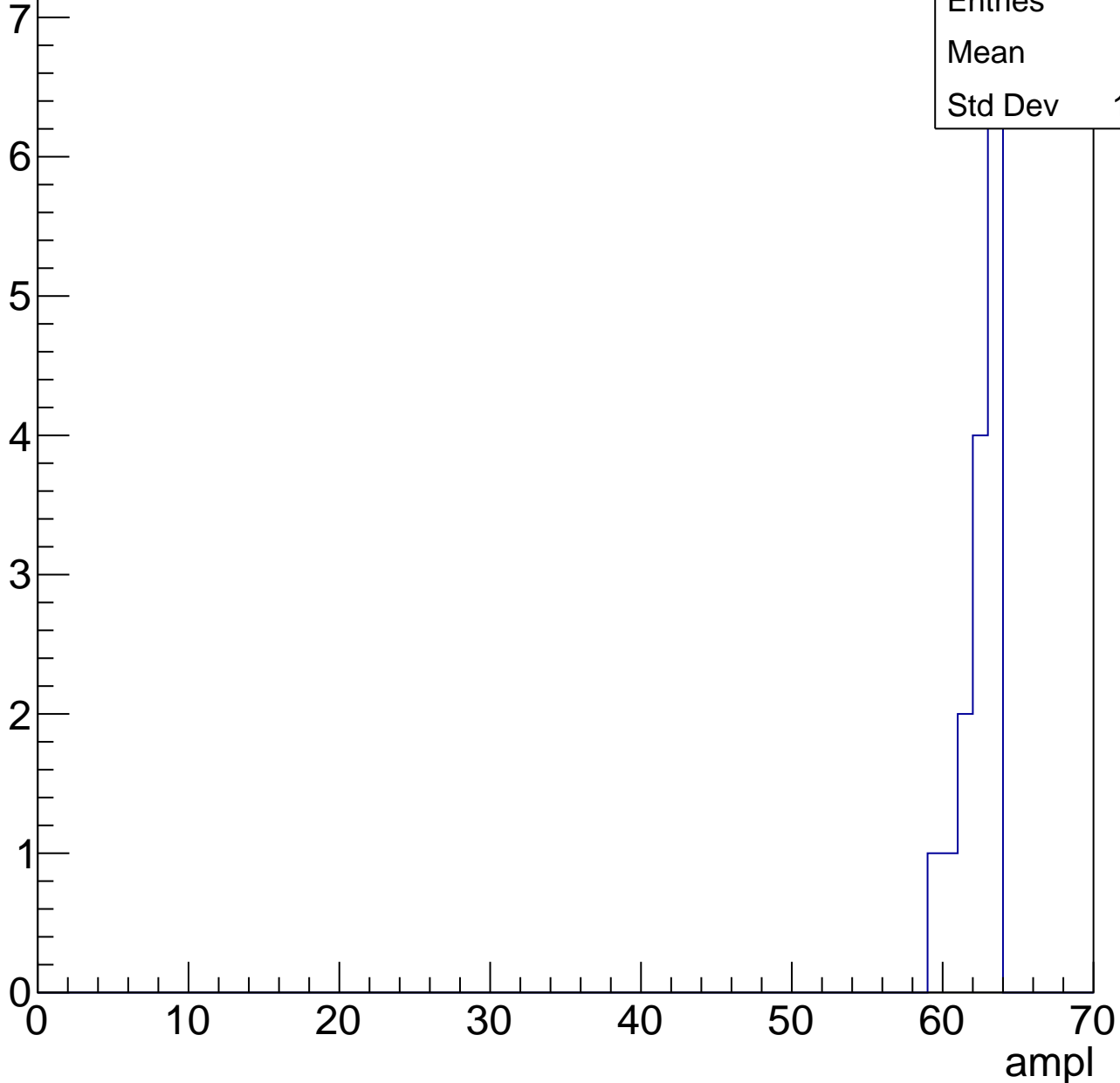


B1L103S, U1-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	62
Std Dev	1.211



B1L103S, U1-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U1-ch4, adc0

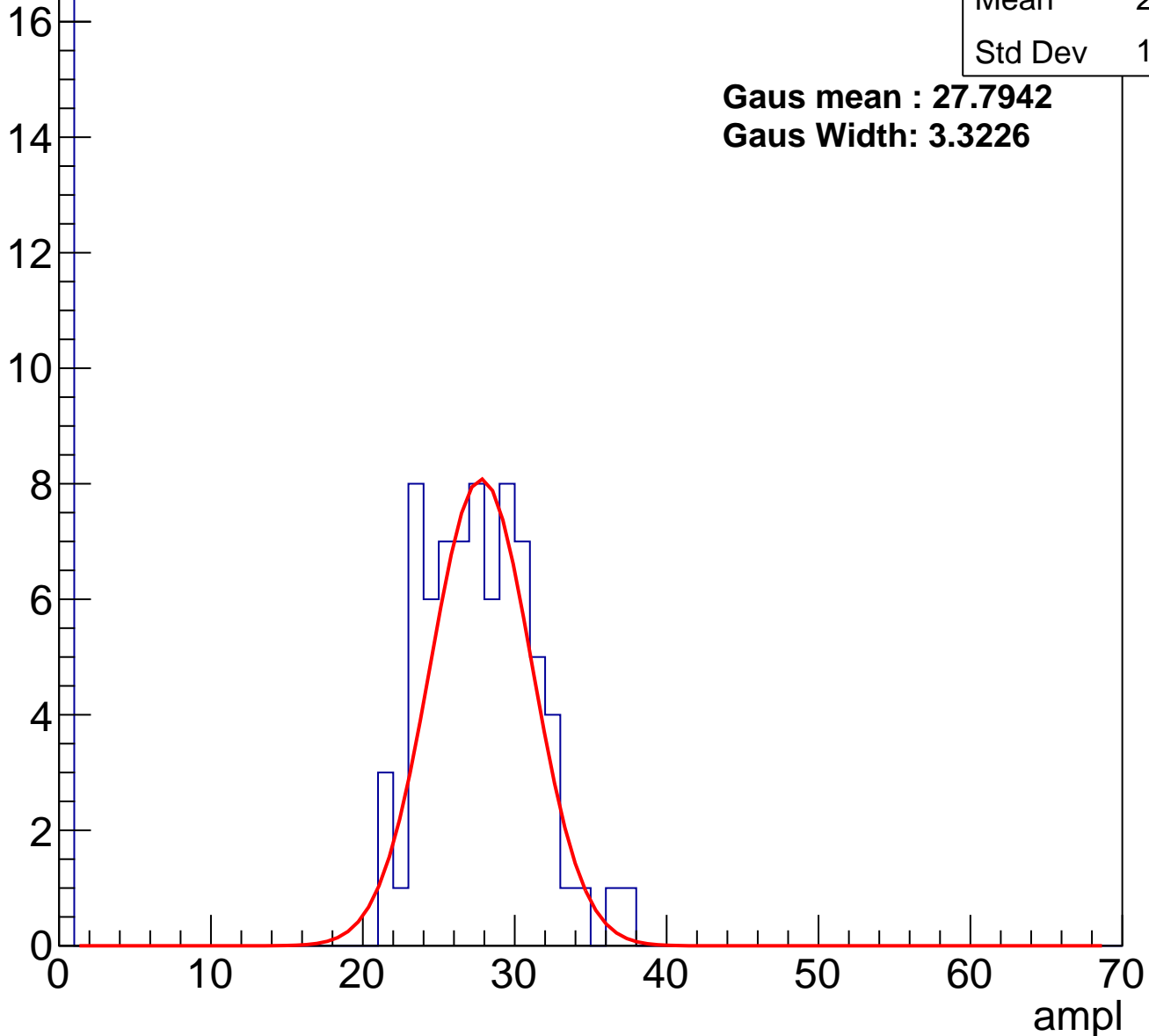
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.19
Std Dev	11.08

Gaus mean : 27.7942

Gaus Width: 3.3226

Entry



B1L103S, U1-ch4, adc1

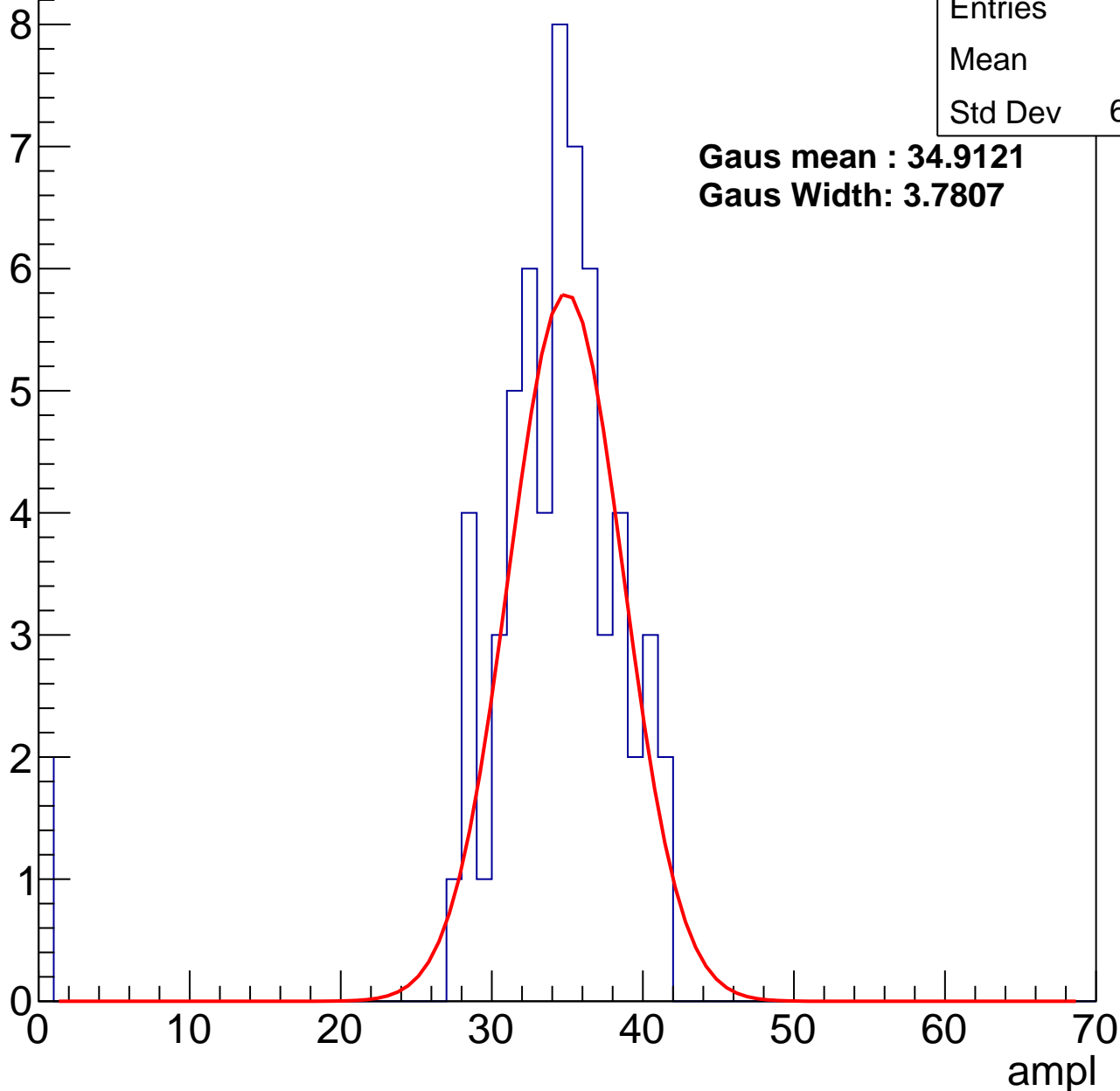
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33
Std Dev	6.975

Gaus mean : 34.9121

Gaus Width: 3.7807



B1L103S, U1-ch4, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	35.21
Std Dev	13.84

Gaus mean : 41.1091

Gaus Width: 3.8402

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

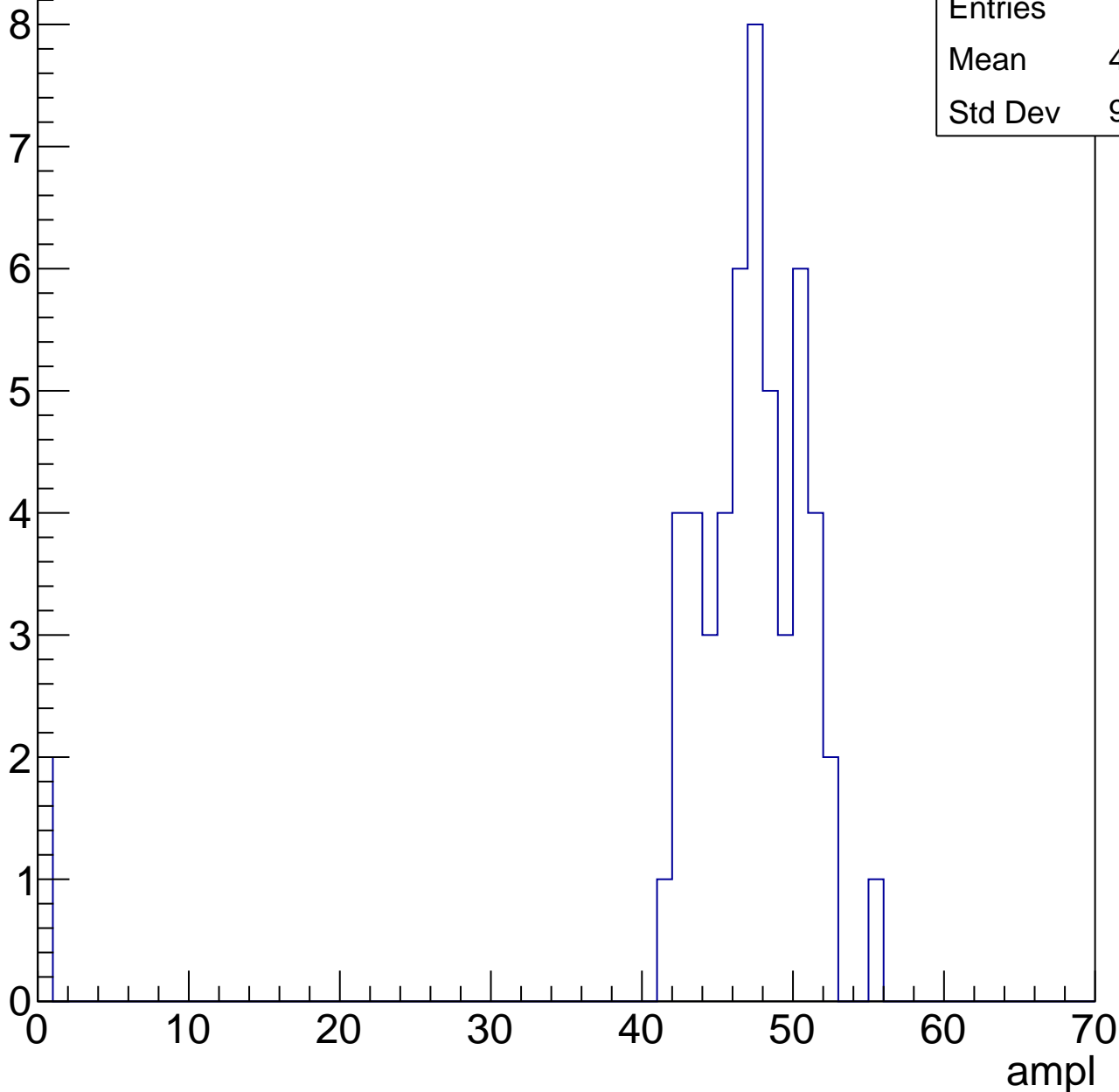
10

B1L103S, U1-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	45.19
Std Dev	9.459

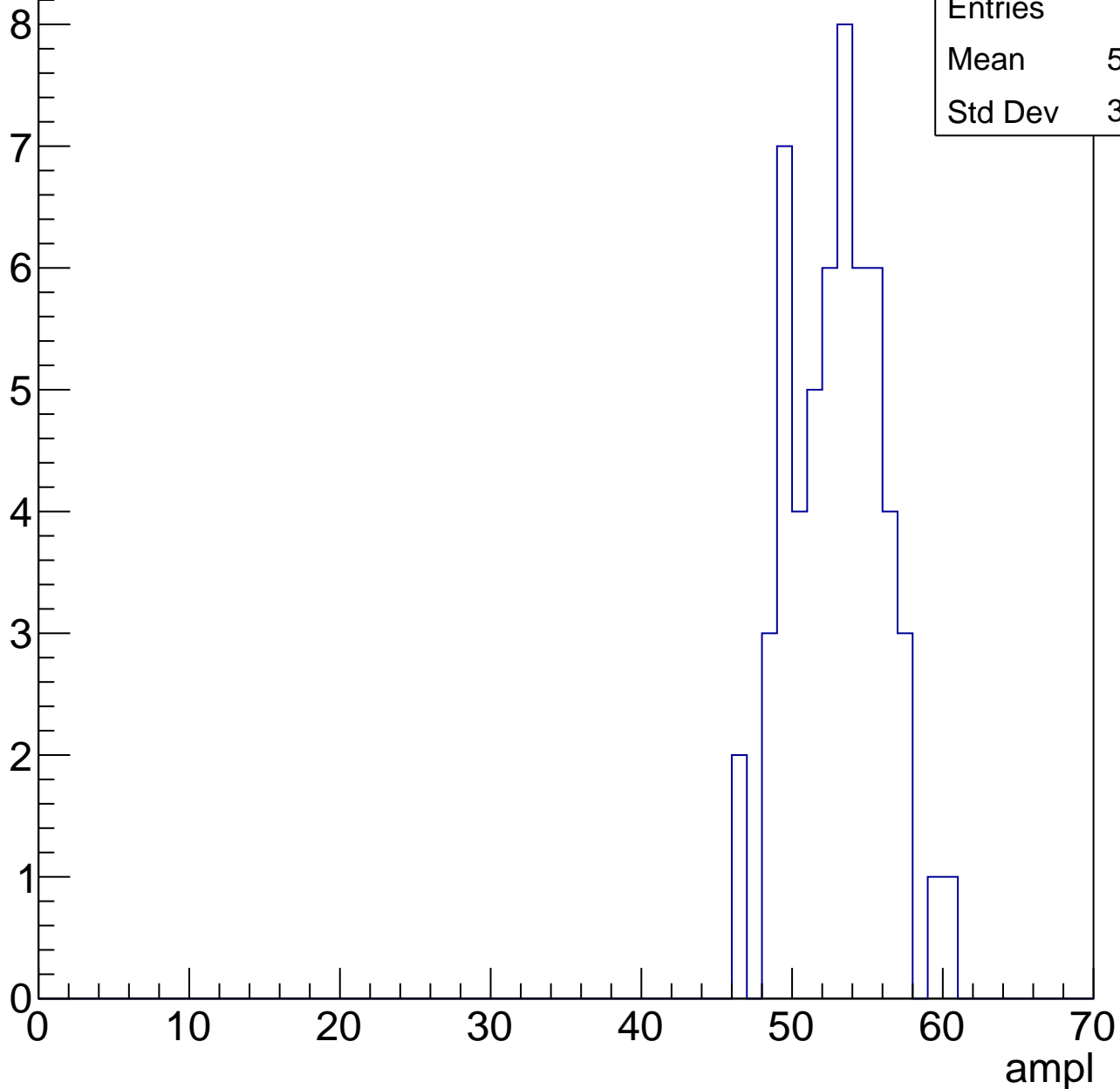


B1L103S, U1-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

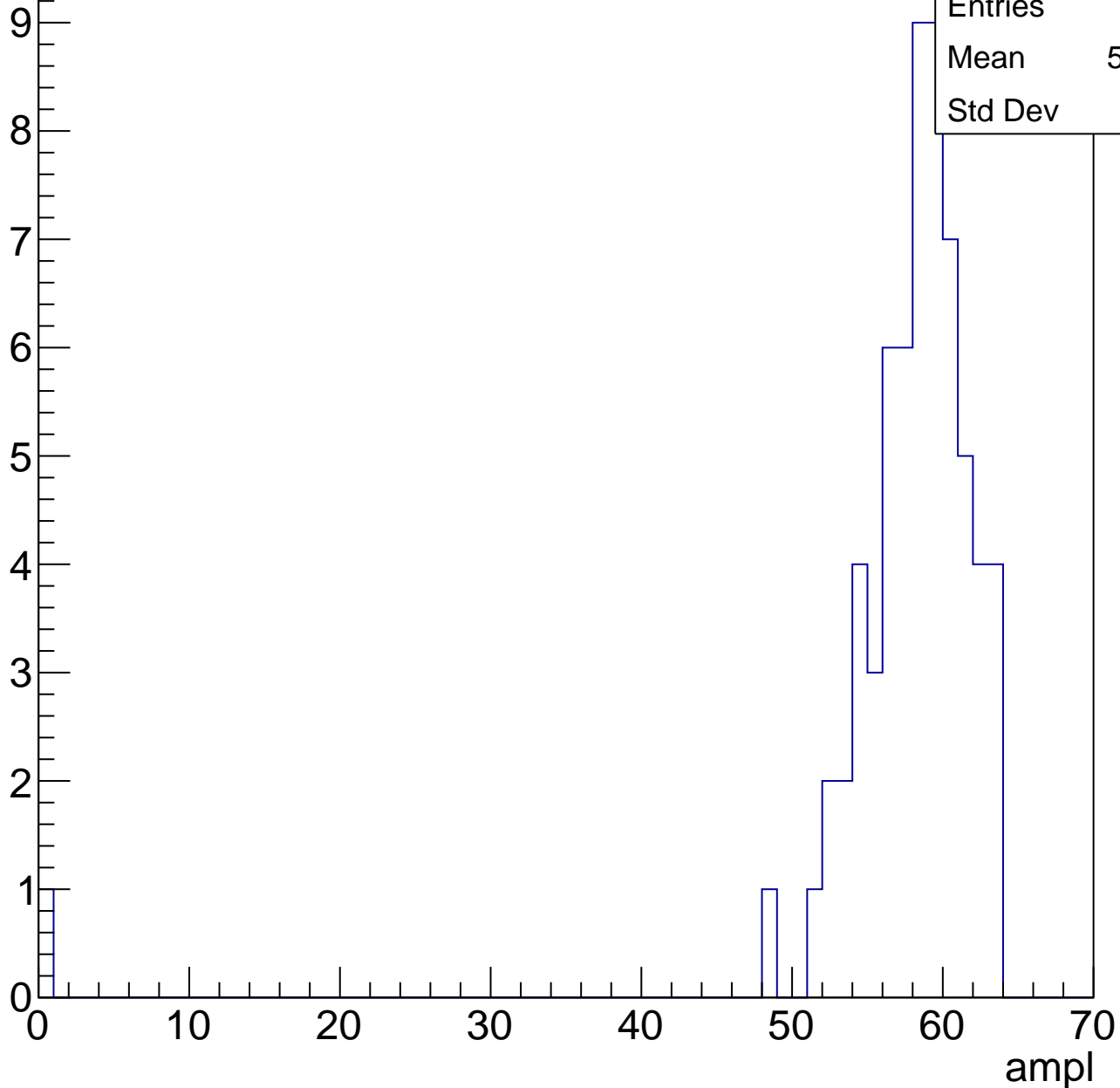
Entries	56
Mean	52.46
Std Dev	3.076



B1L103S, U1-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

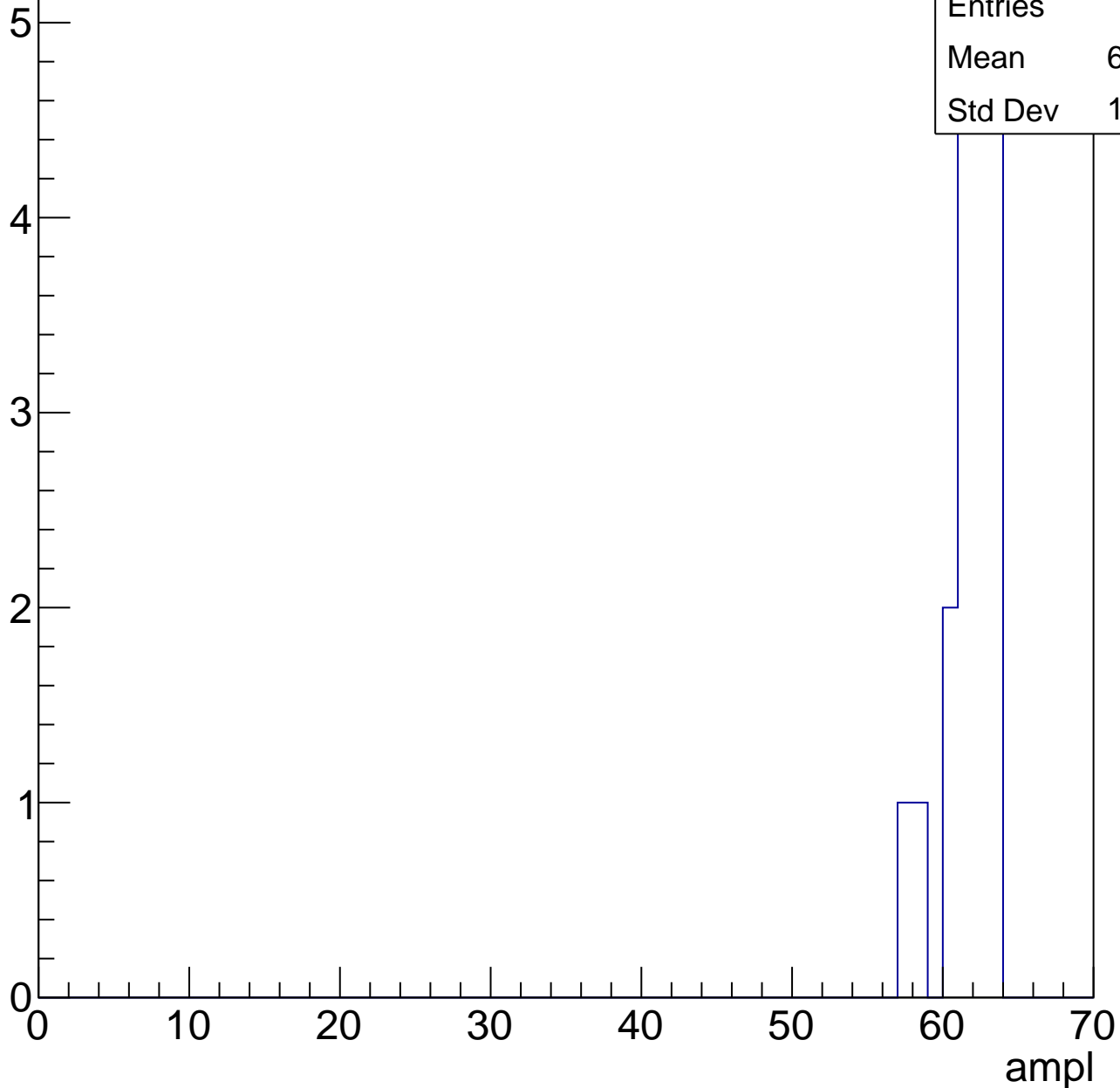


B1L103S, U1-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

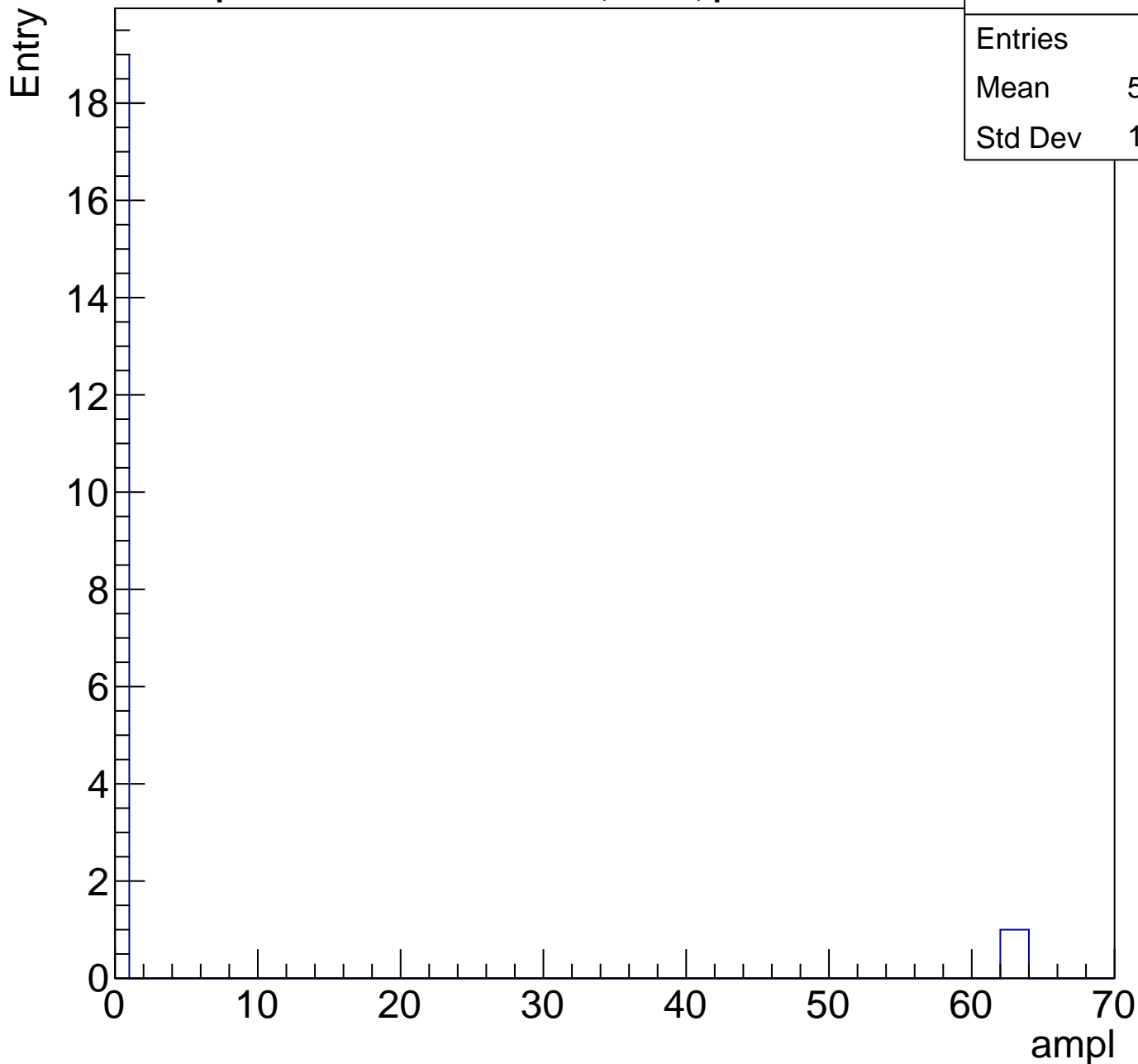
Entries	19
Mean	61.32
Std Dev	1.624



B1L103S, U1-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



B1L103S, U1-ch5, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	21.49
Std Dev	12.82

Gaus mean : 29.3617

Gaus Width: 3.5435

Entry

25

20

15

10

5

0

0

10

20

30

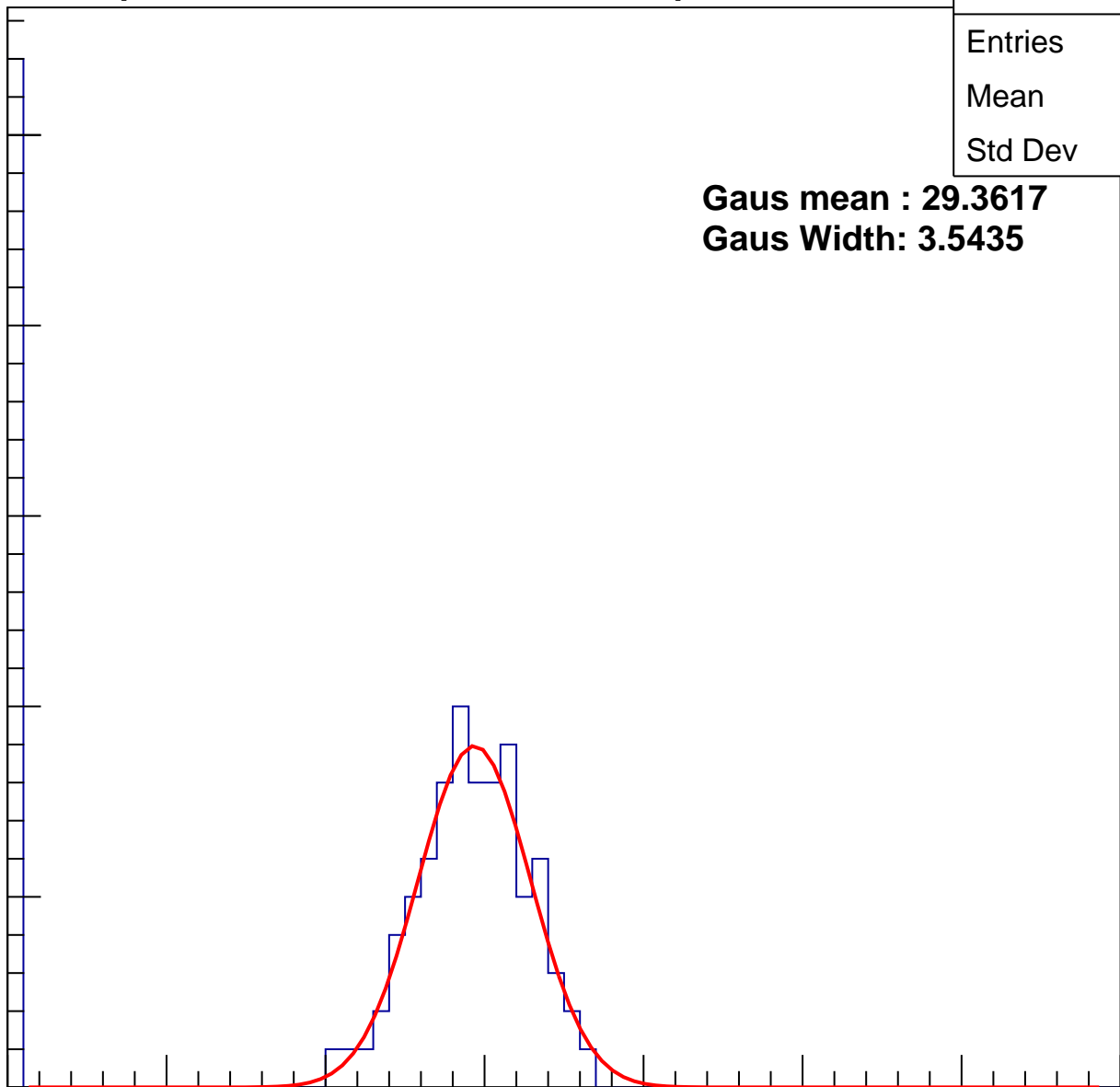
40

50

60

70

ampl



B1L103S, U1-ch5, adc1

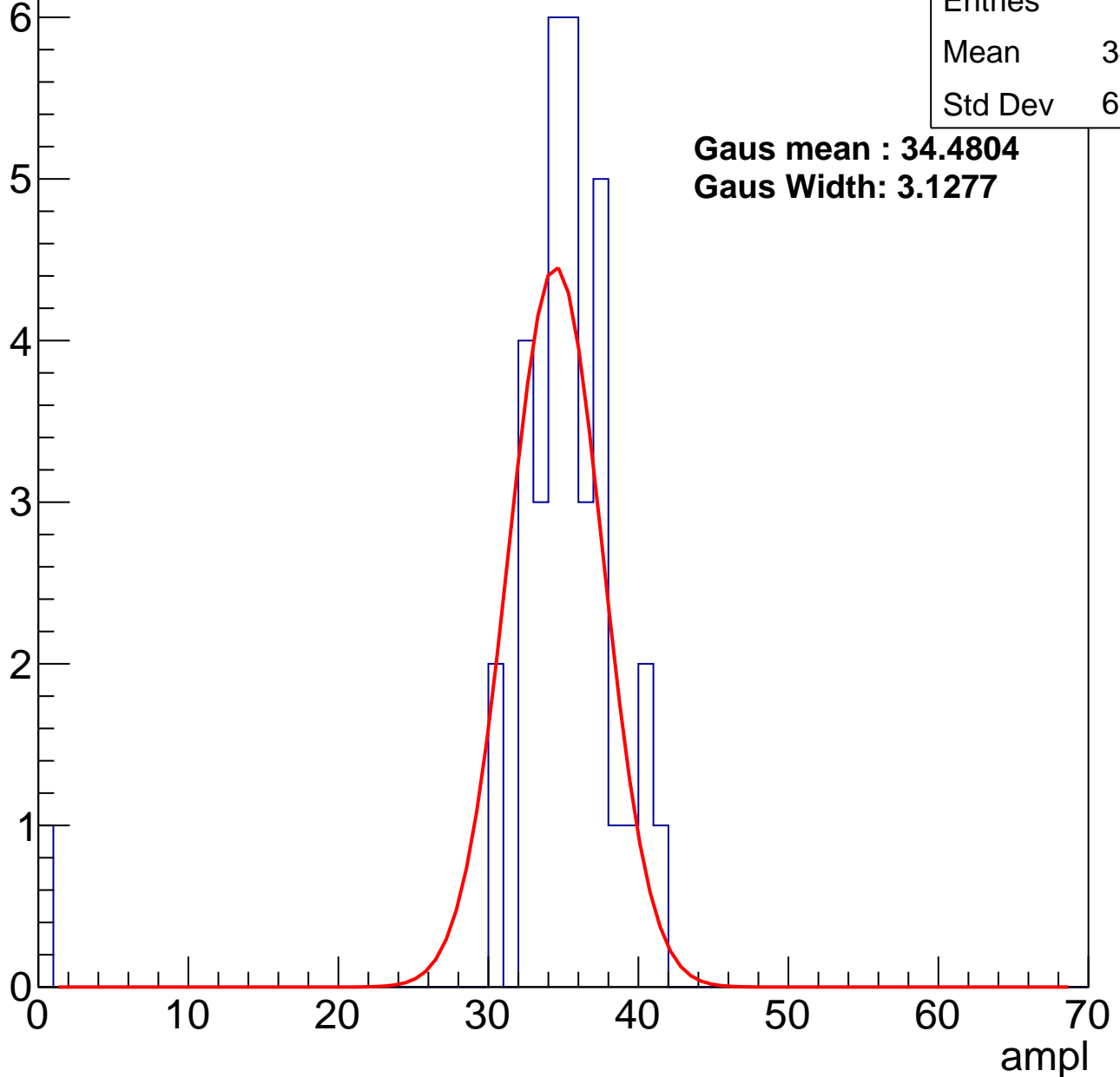
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	34.06
Std Dev	6.396

Gaus mean : 34.4804

Gaus Width: 3.1277



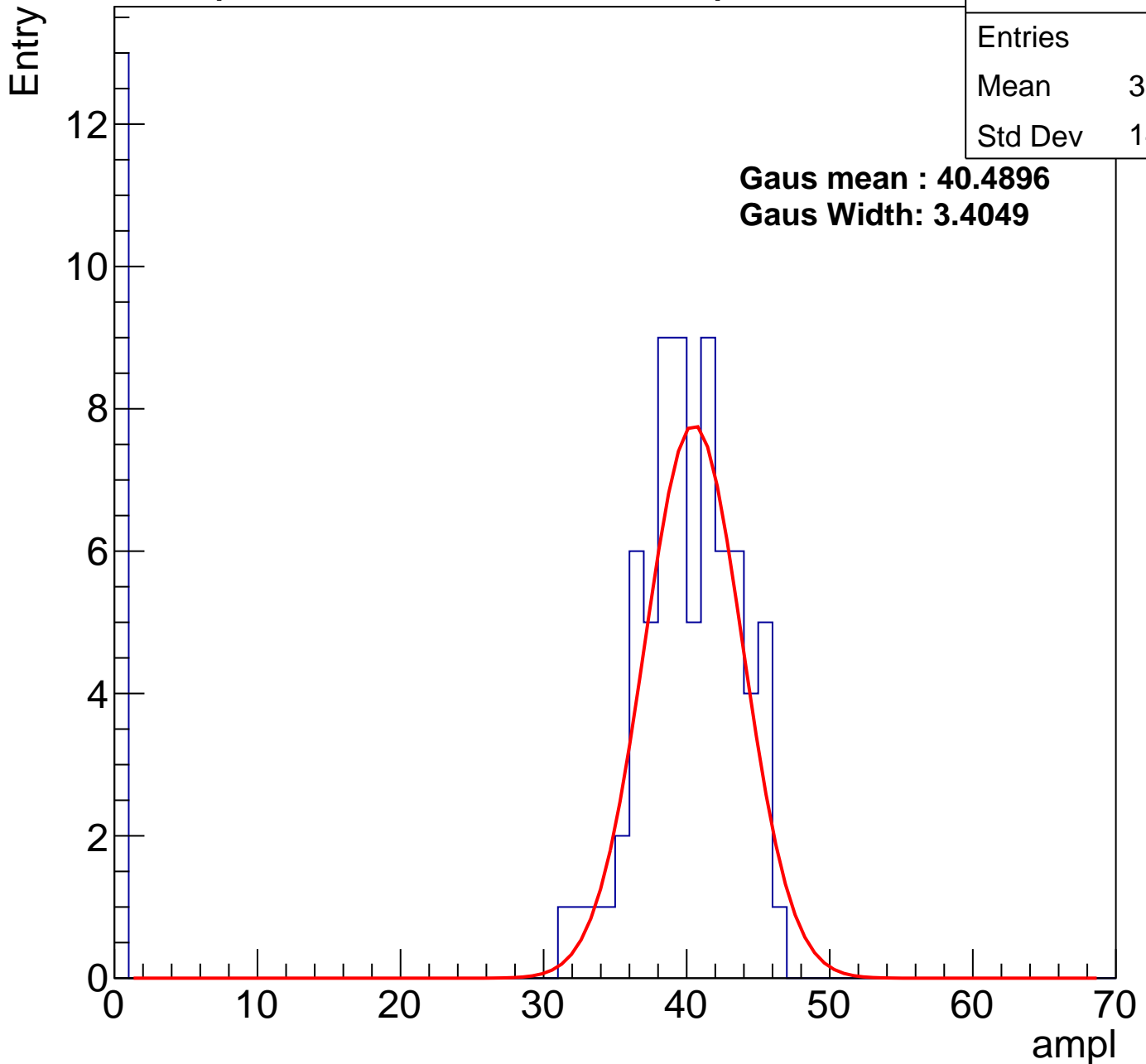
B1L103S, U1-ch5, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	33.57
Std Dev	14.68

Gaus mean : 40.4896

Gaus Width: 3.4049

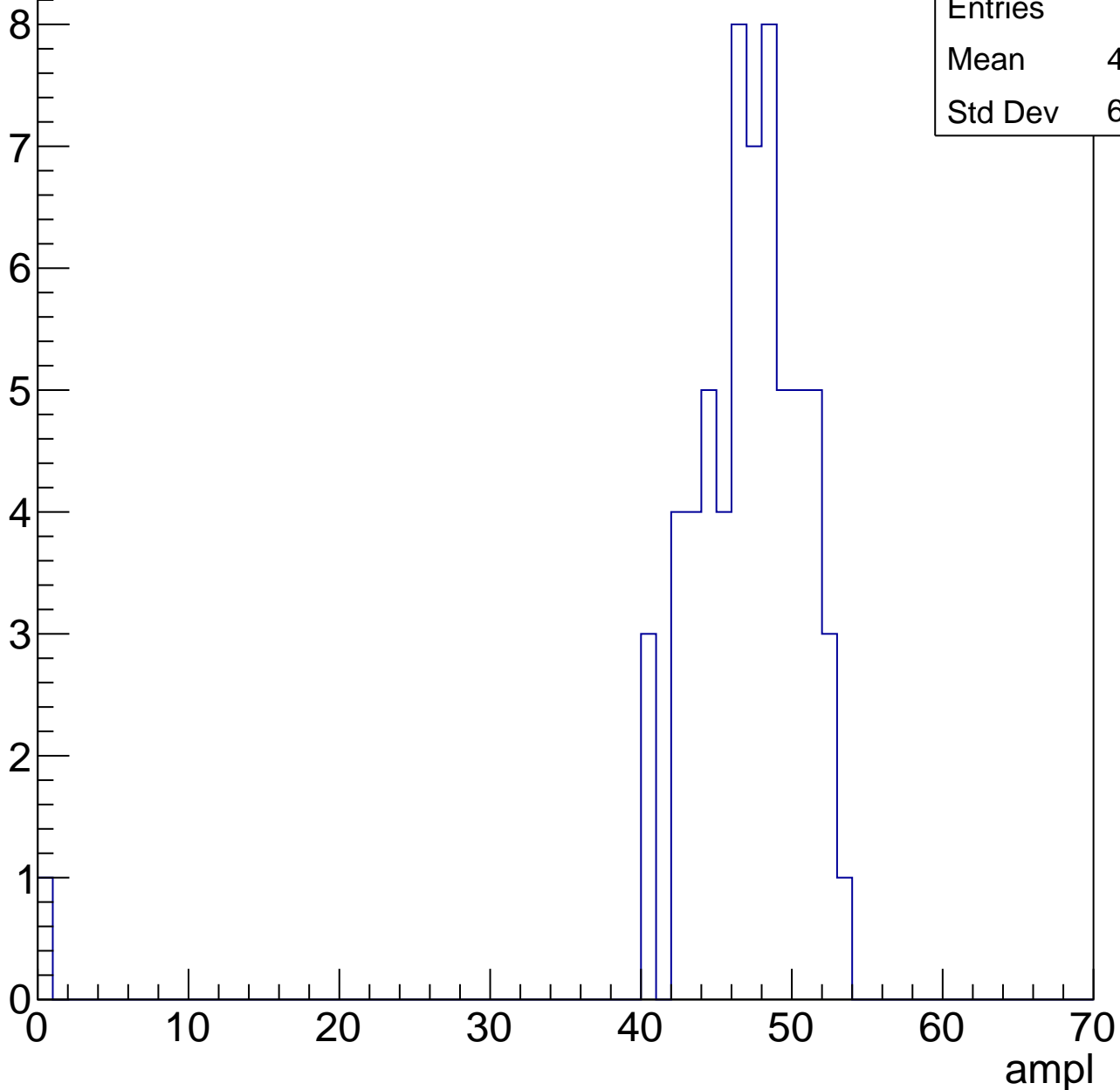


B1L103S, U1-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

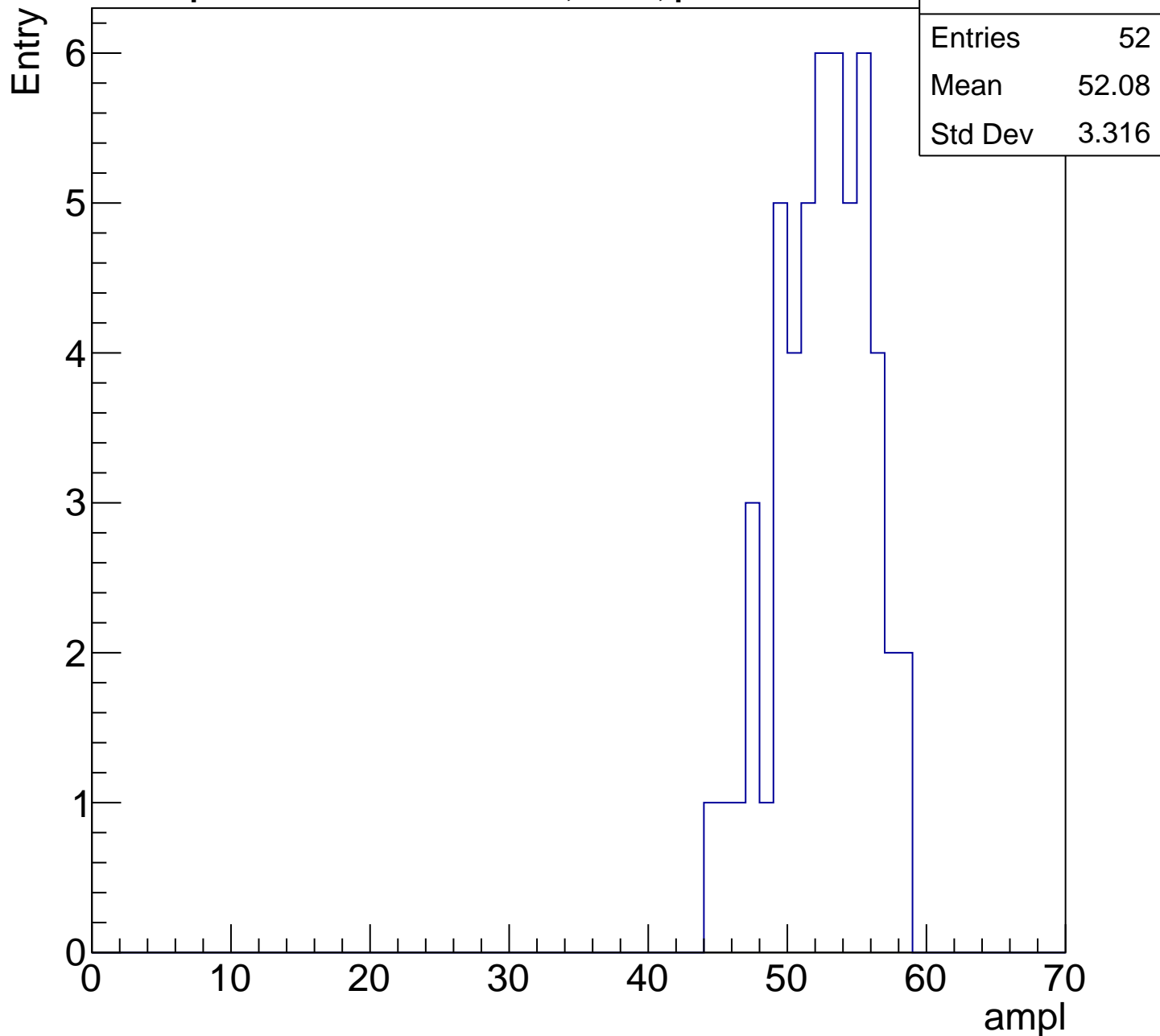
Entry

Entries	63
Mean	46.03
Std Dev	6.659



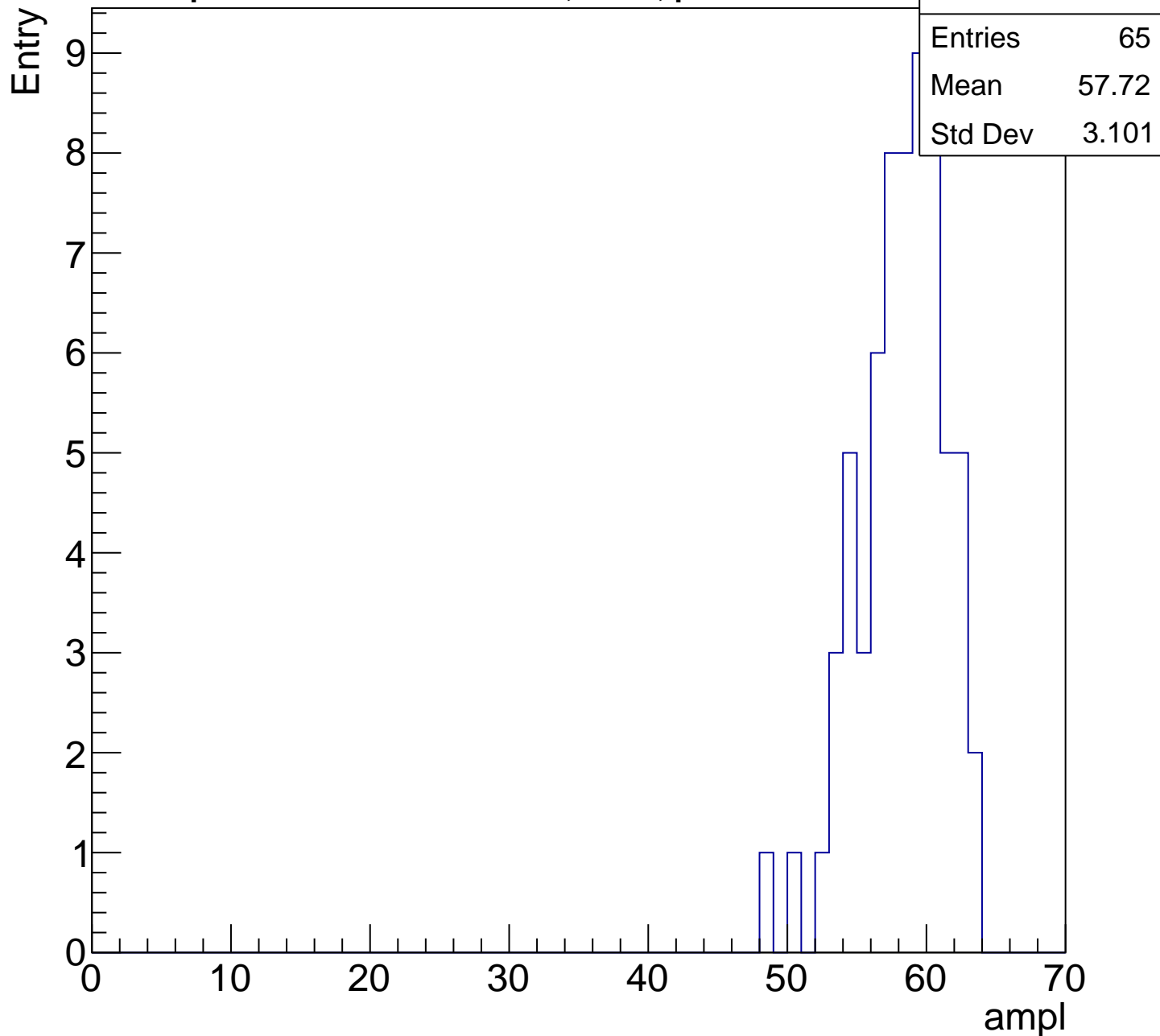
B1L103S, U1-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

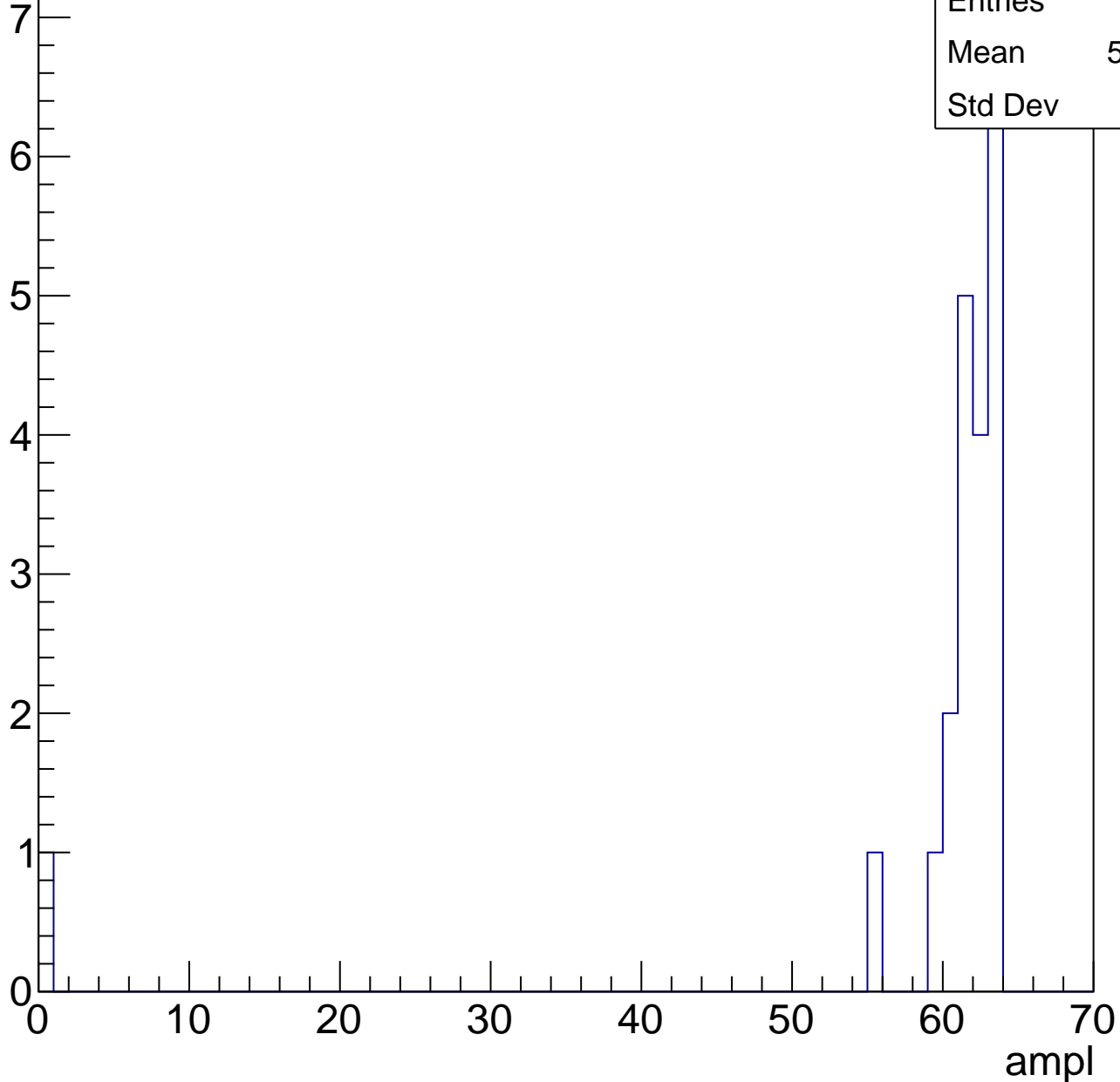


B1L103S, U1-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

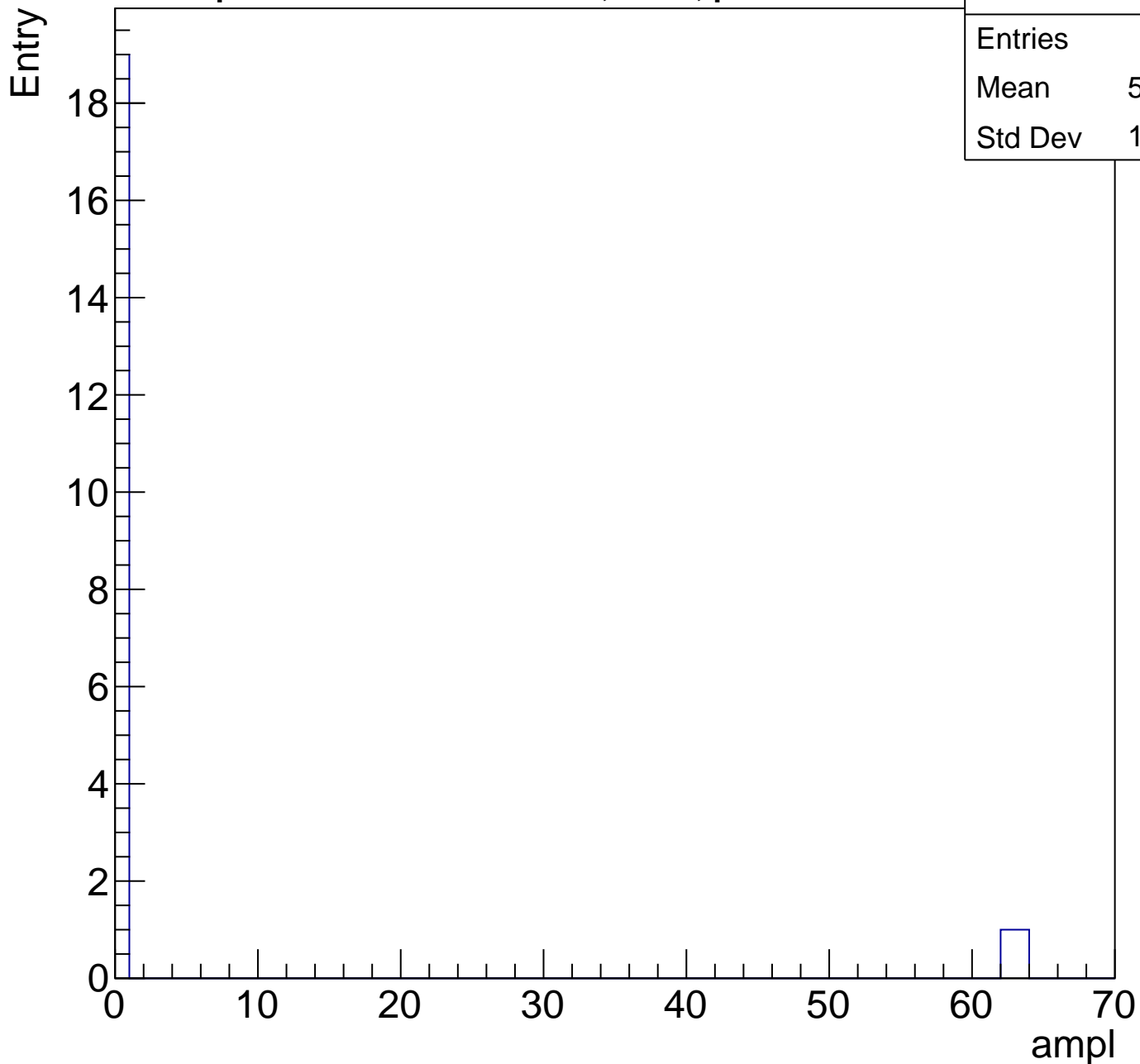
Entries	21
Mean	58.48
Std Dev	13.2



B1L103S, U1-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



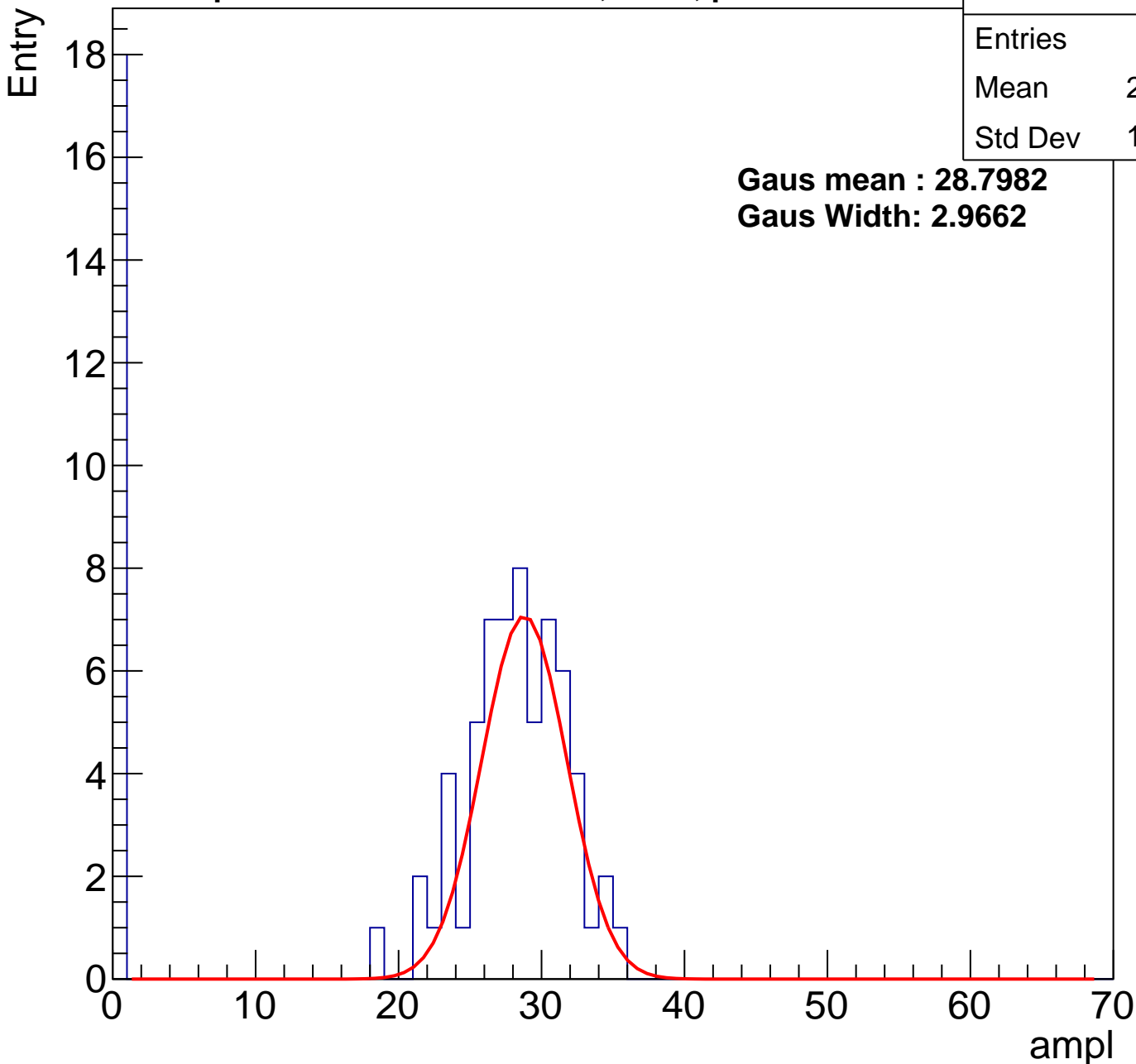
B1L103S, U1-ch6, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21.54
Std Dev	11.99

Gaus mean : 28.7982

Gaus Width: 2.9662



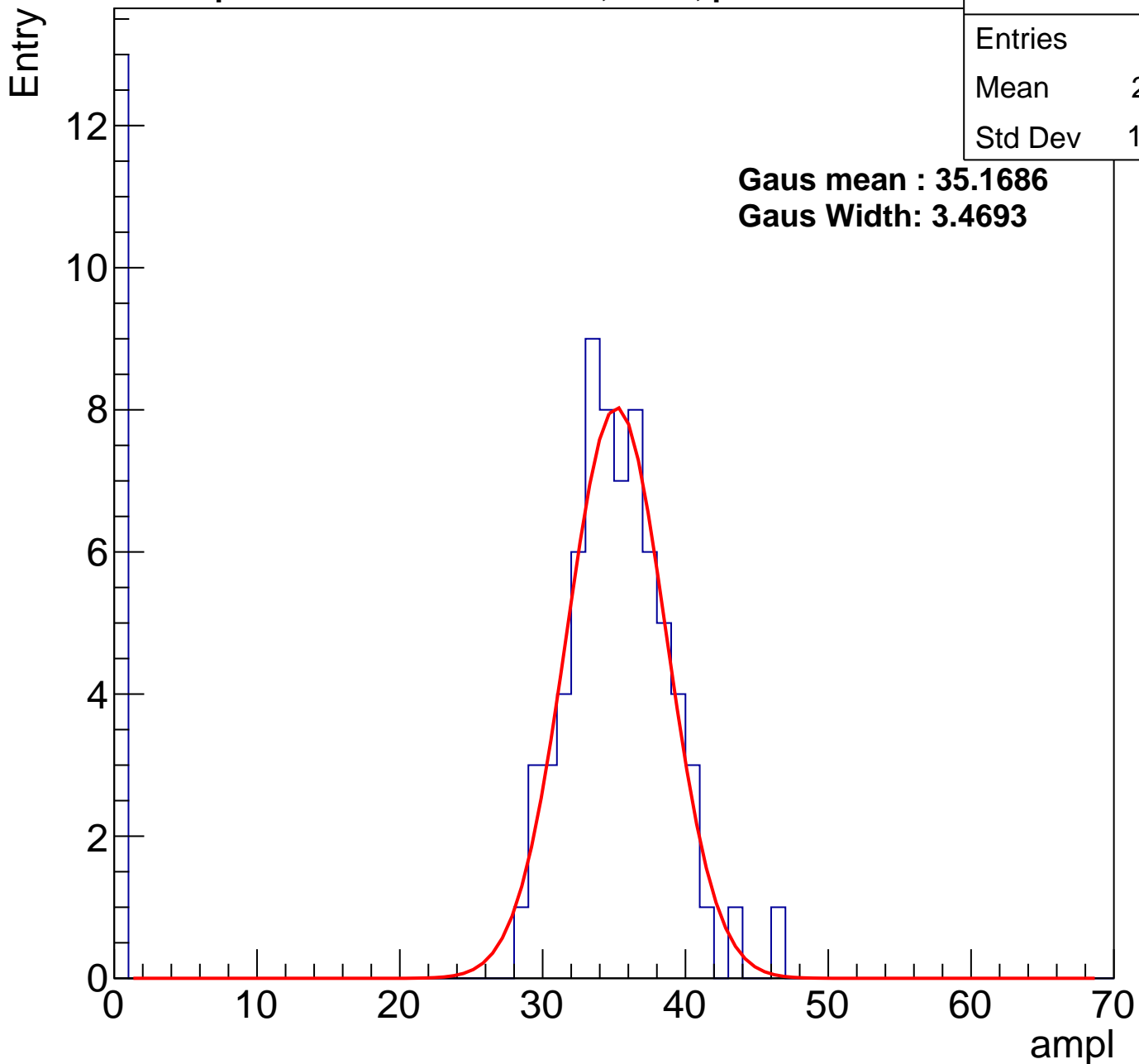
B1L103S, U1-ch6, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	29.41
Std Dev	13.06

Gaus mean : 35.1686

Gaus Width: 3.4693



B1L103S, U1-ch6, adc2

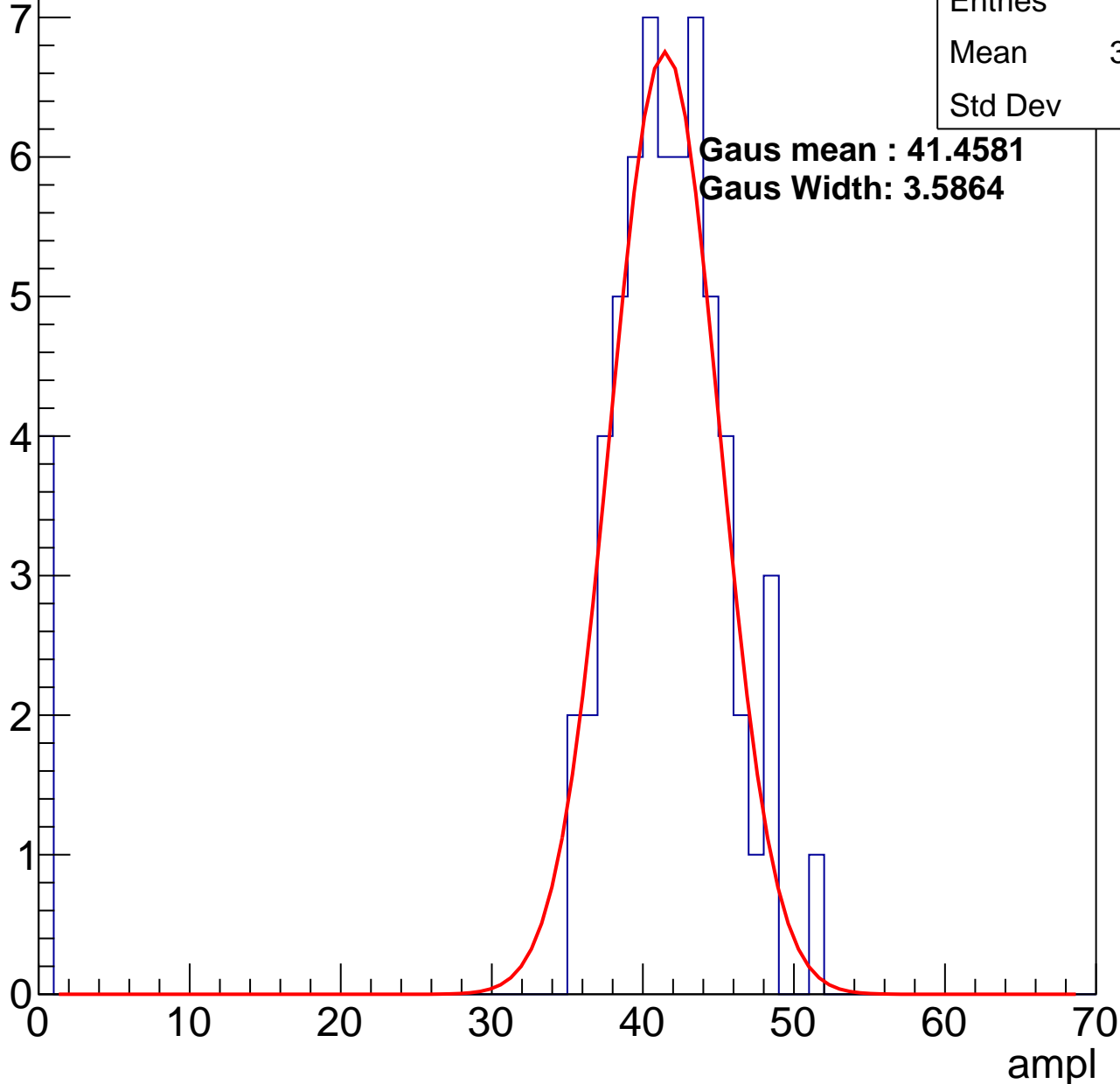
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.88
Std Dev	10.5

Gaus mean : 41.4581

Gaus Width: 3.5864

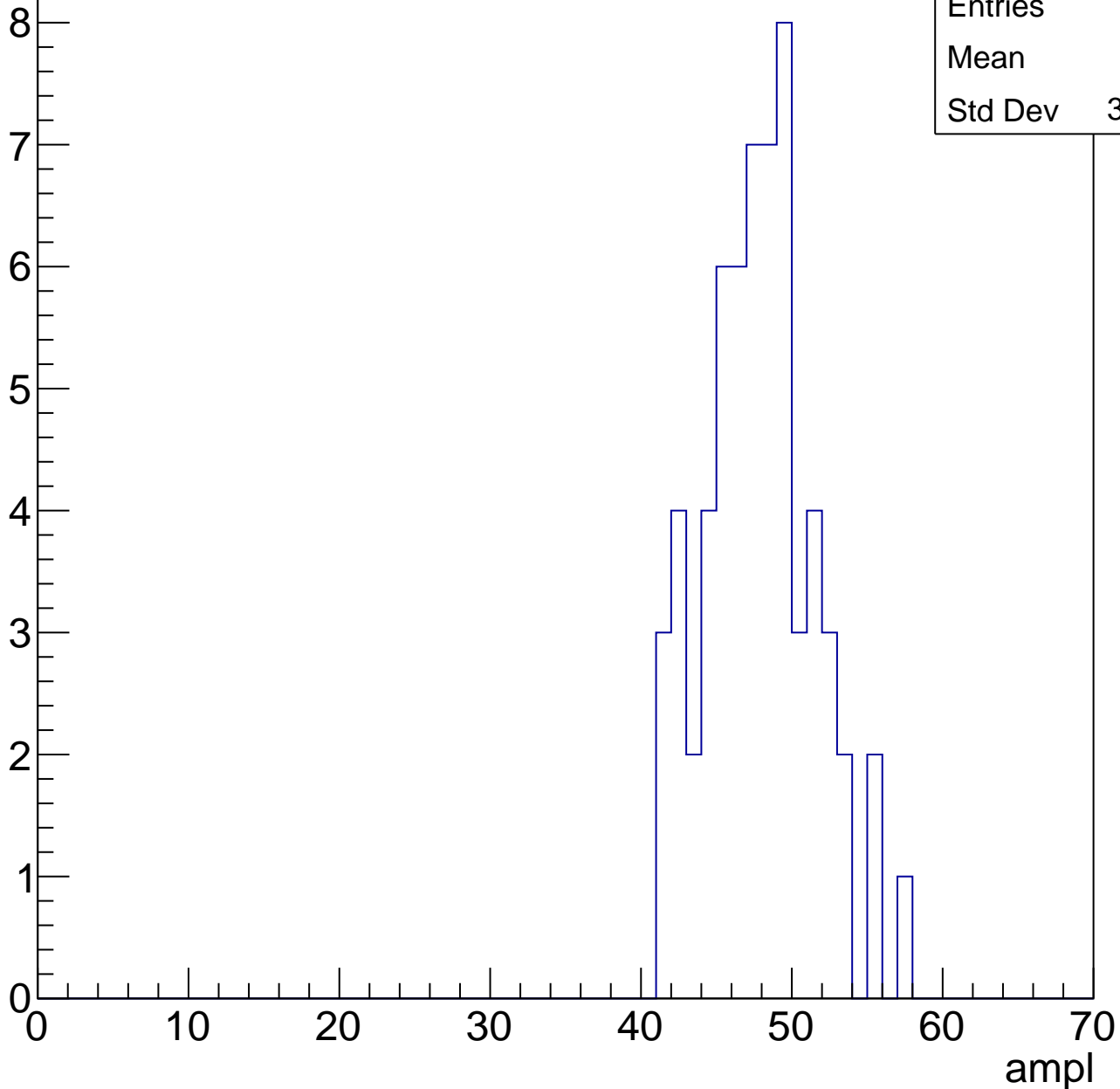


B1L103S, U1-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.4
Std Dev	3.594

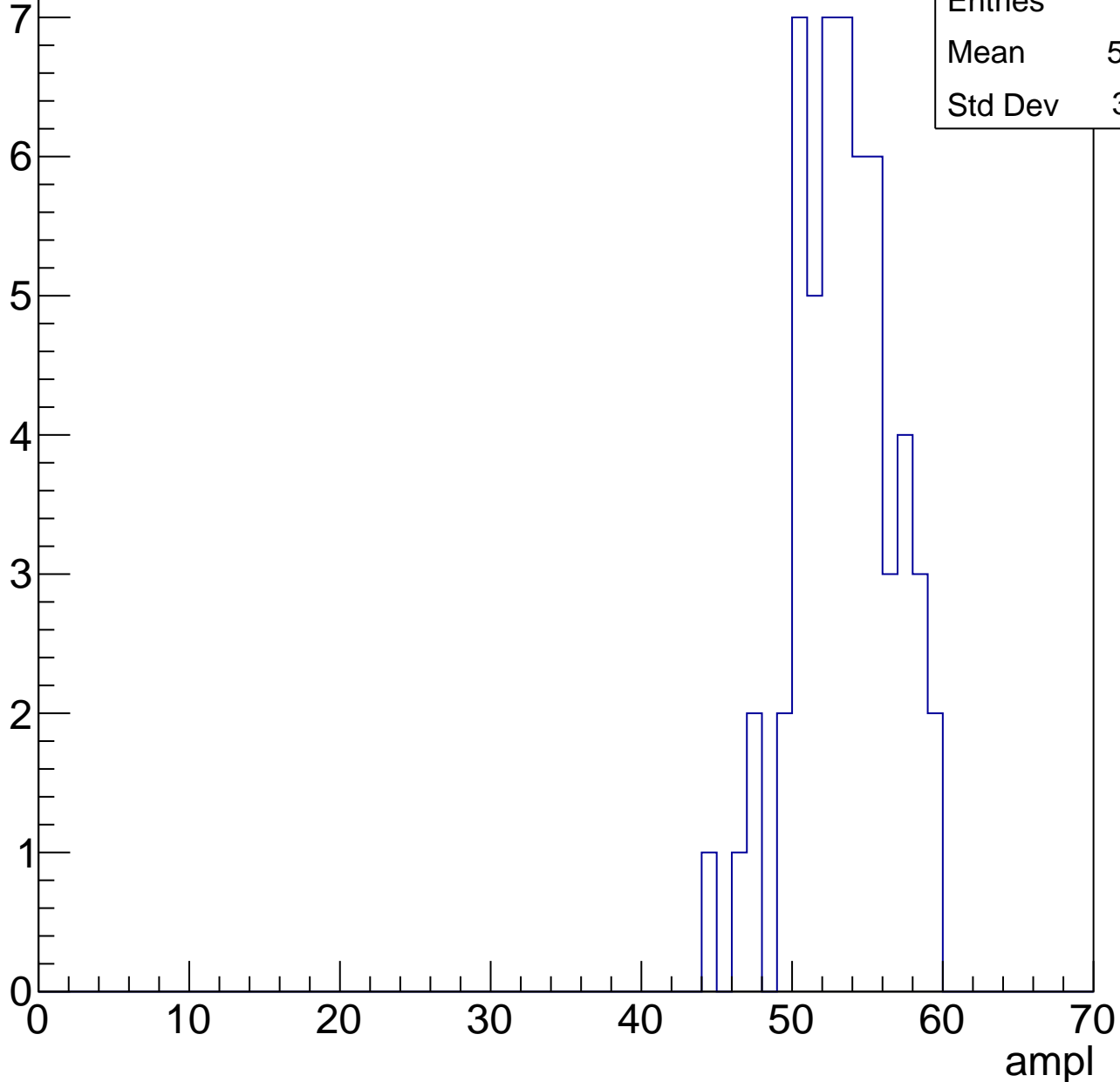


B1L103S, U1-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.93
Std Dev	3.251



B1L103S, U1-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	56
Mean	58.84
Std Dev	2.789

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

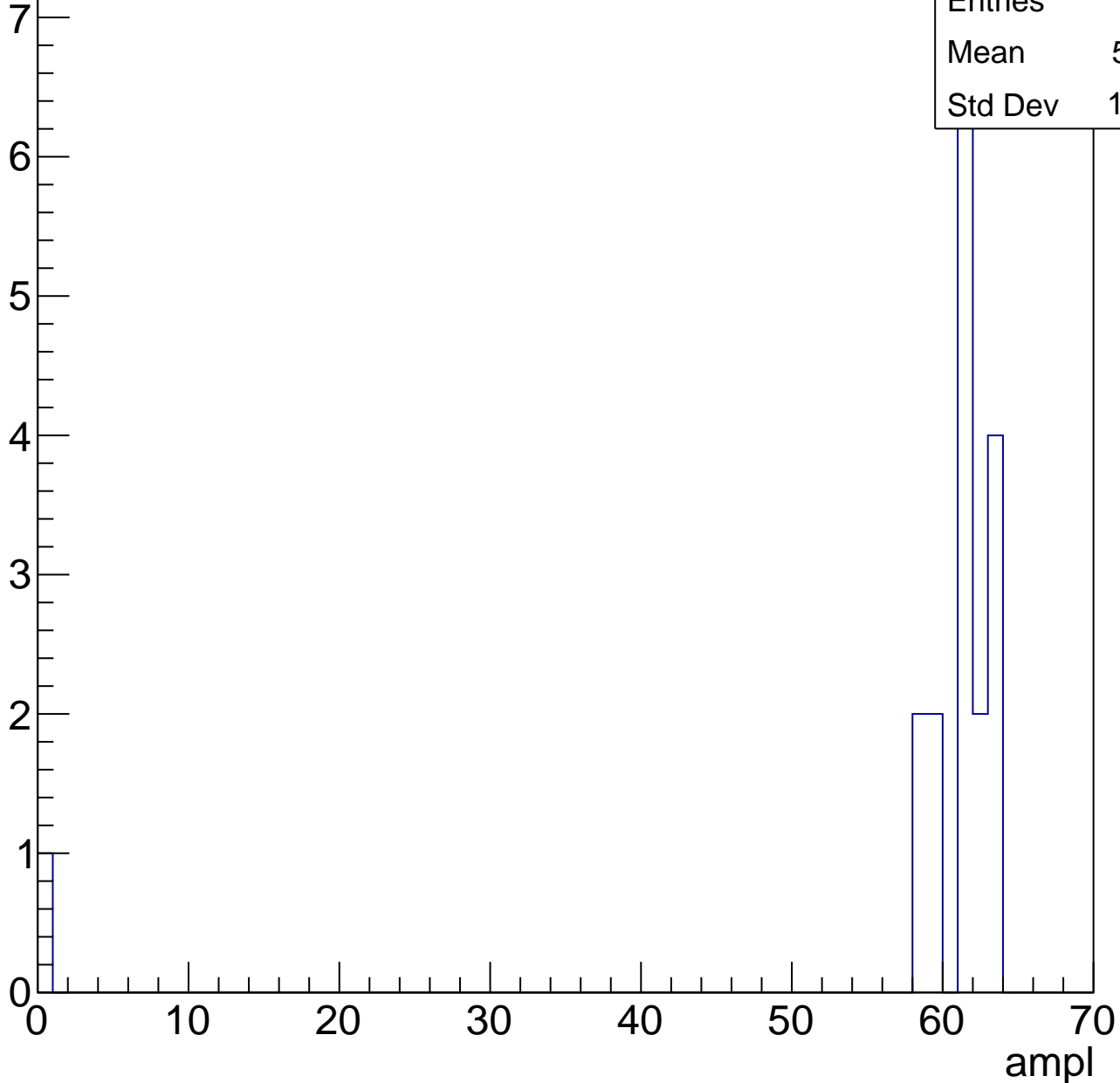
70

B1L103S, U1-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

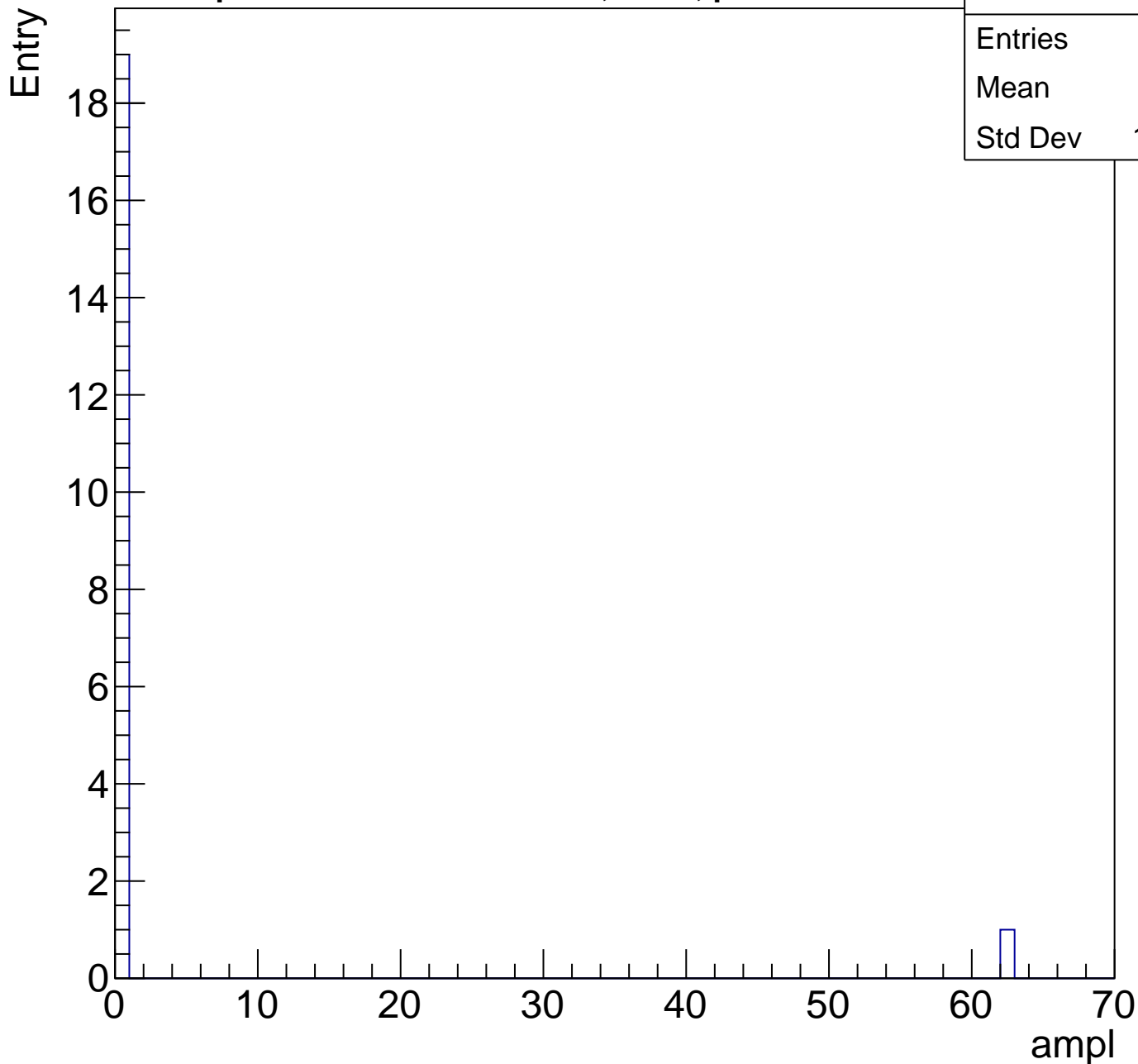
Entries	18
Mean	57.61
Std Dev	14.06



B1L103S, U1-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51



B1L103S, U1-ch7, adc0

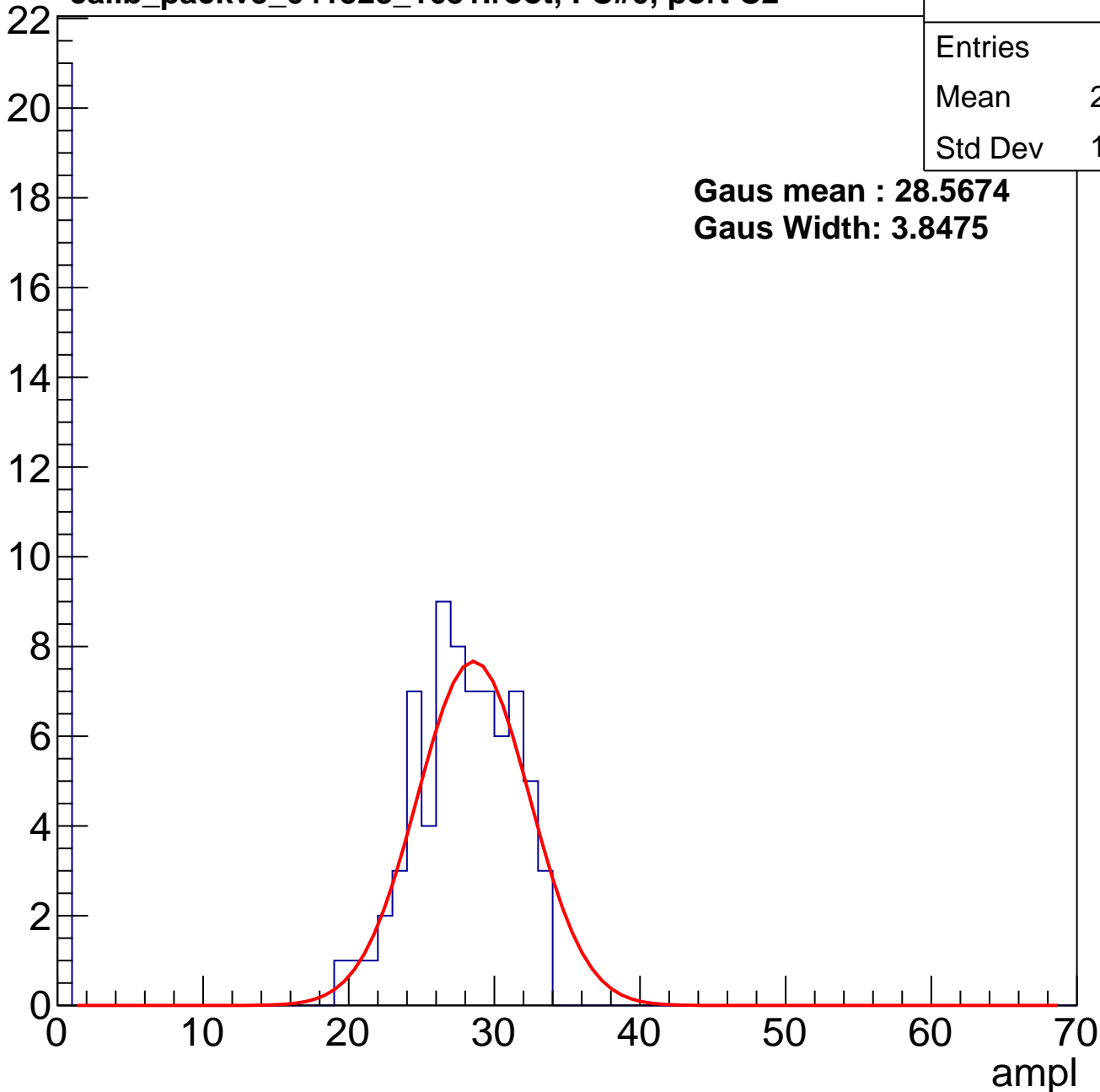
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	21.15
Std Dev	11.86

Gaus mean : 28.5674

Gaus Width: 3.8475

Entry



B1L103S, U1-ch7, adc1

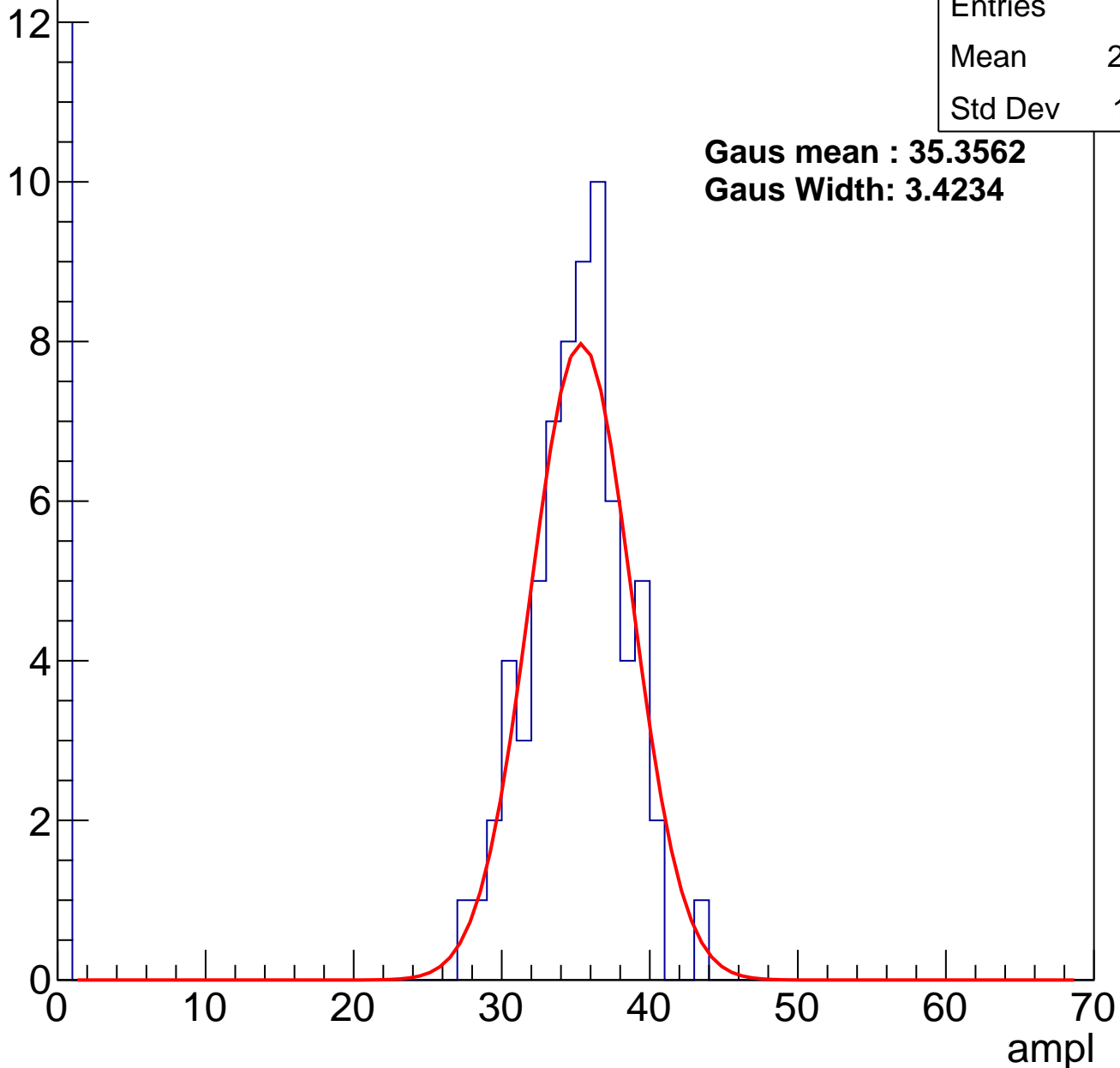
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	29.45
Std Dev	12.71

Gaus mean : 35.3562

Gaus Width: 3.4234

Entry



B1L103S, U1-ch7, adc2

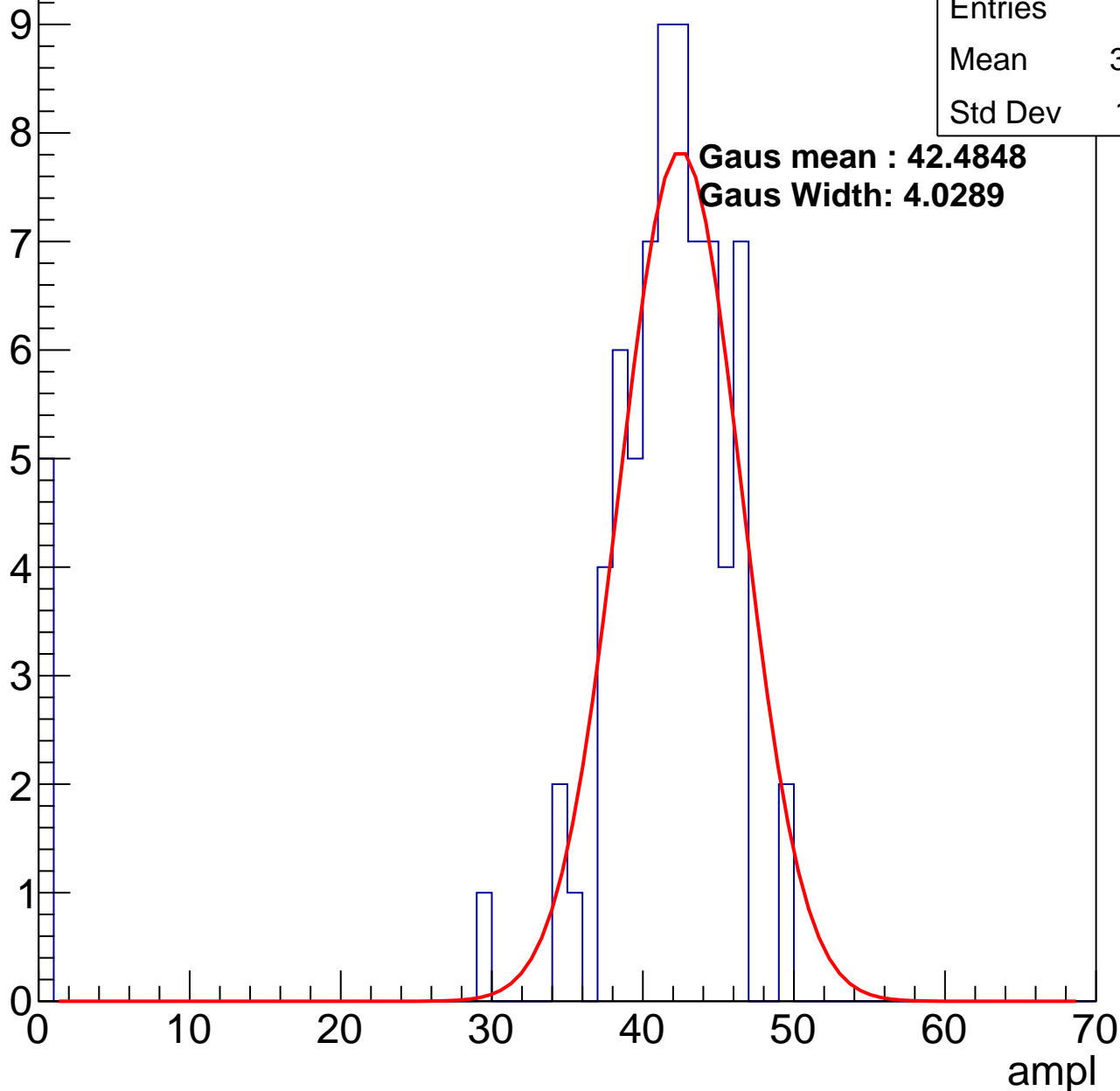
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	38.67
Std Dev	10.81

Gaus mean : 42.4848

Gaus Width: 4.0289

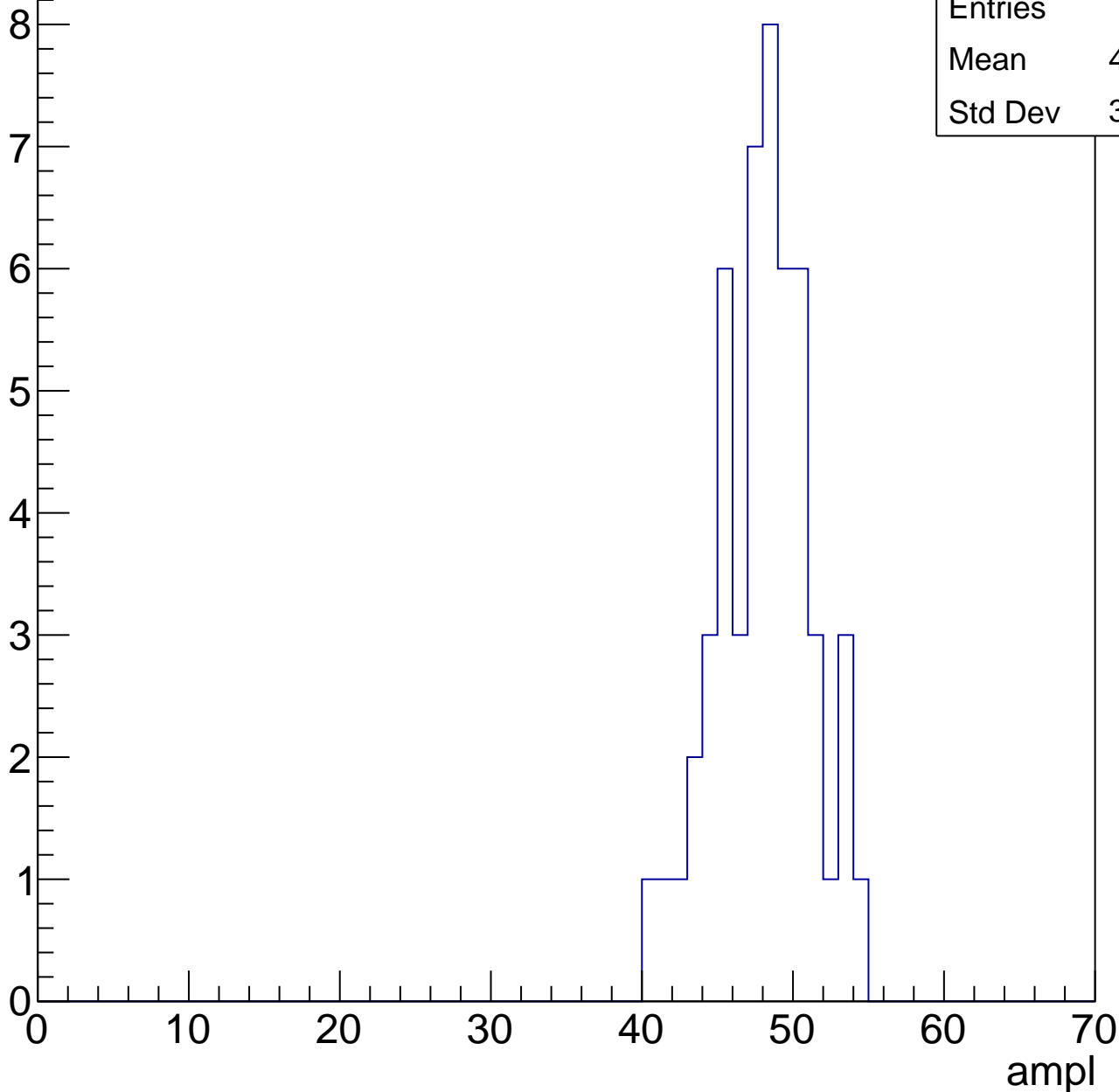


B1L103S, U1-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	47.58
Std Dev	3.084

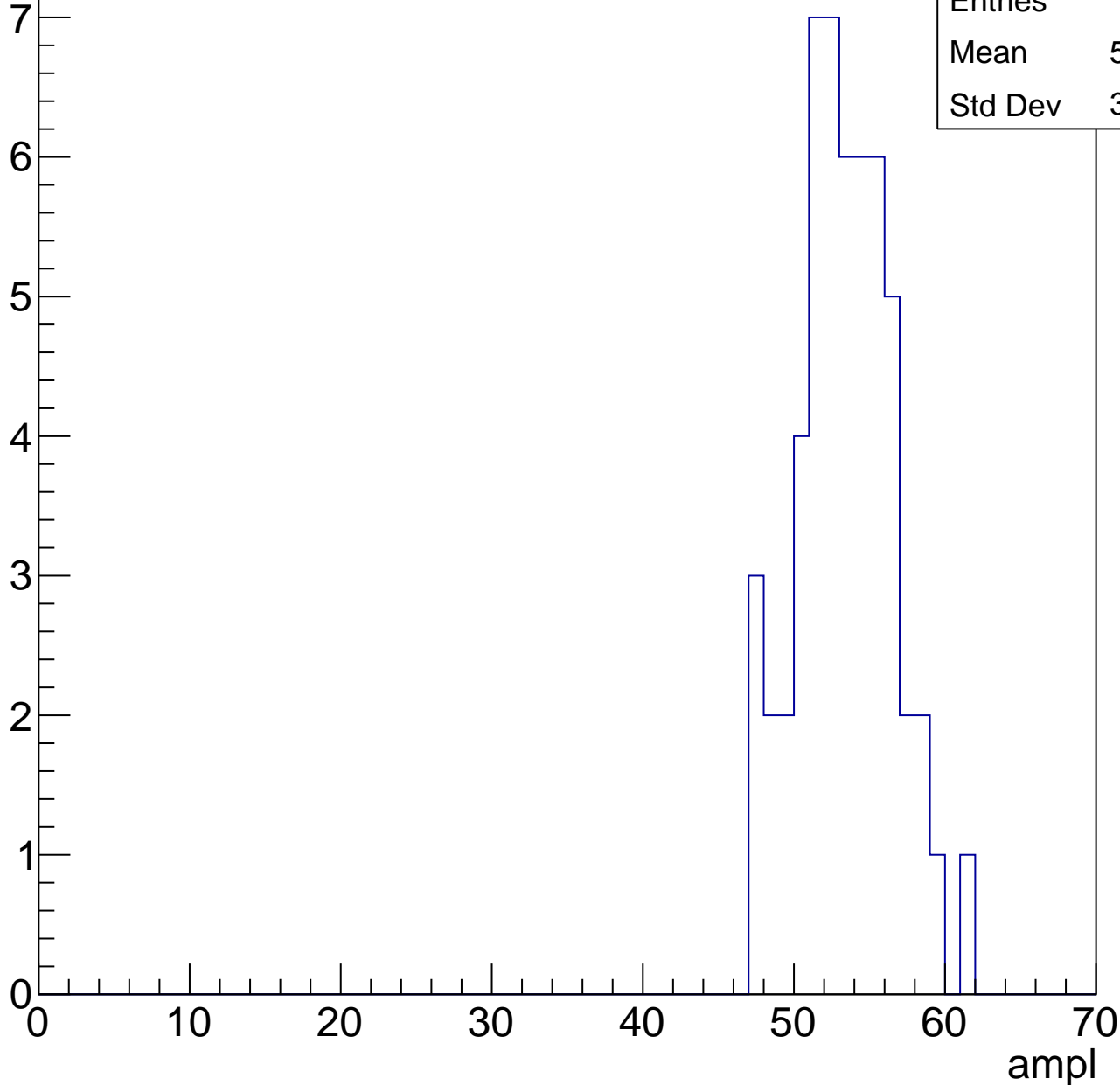


B1L103S, U1-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	52.93
Std Dev	3.096



B1L103S, U1-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	58.61
Std Dev	2.802

Entry

10

8

6

4

2

0

0

10

20

30

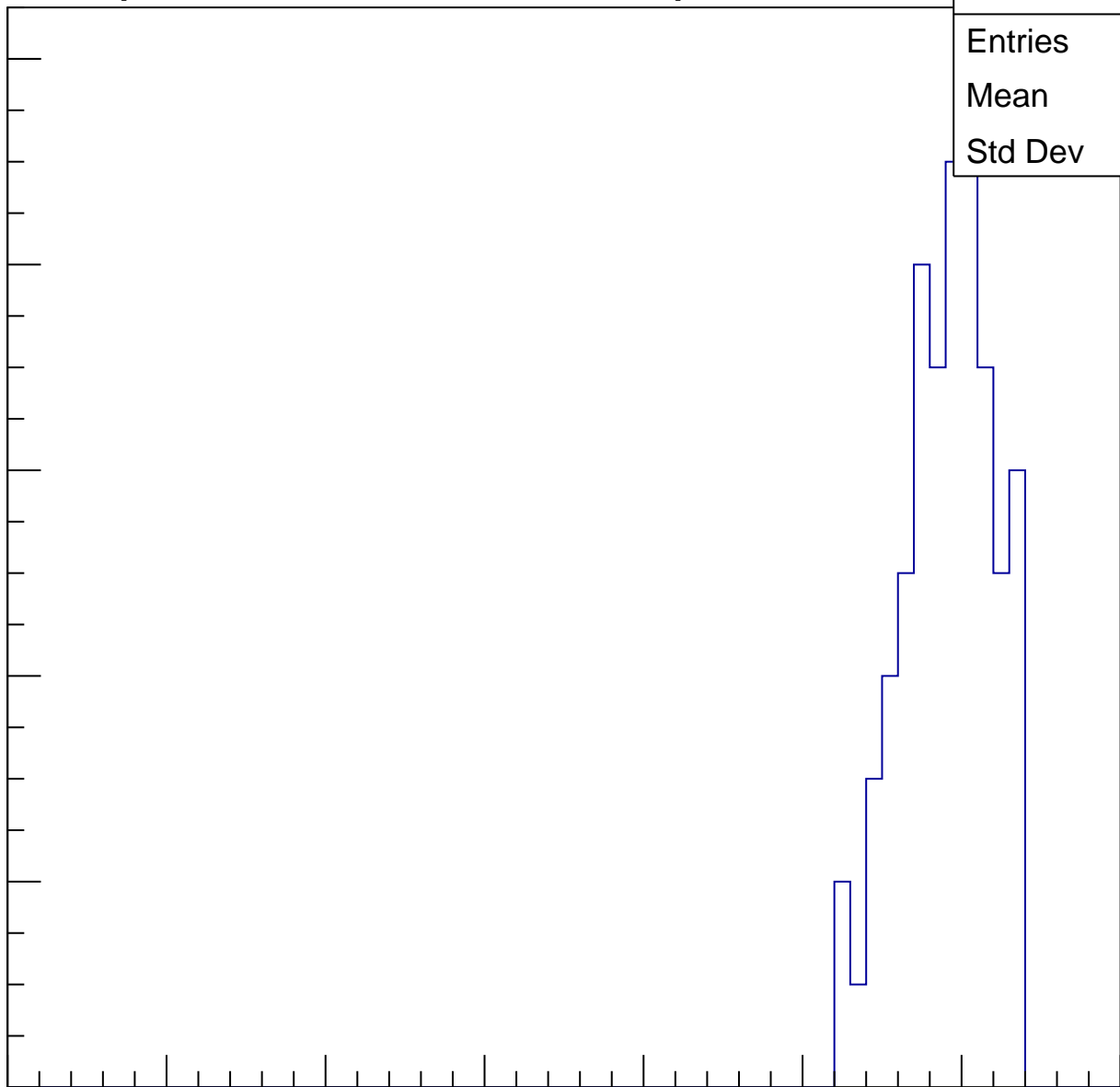
40

50

60

70

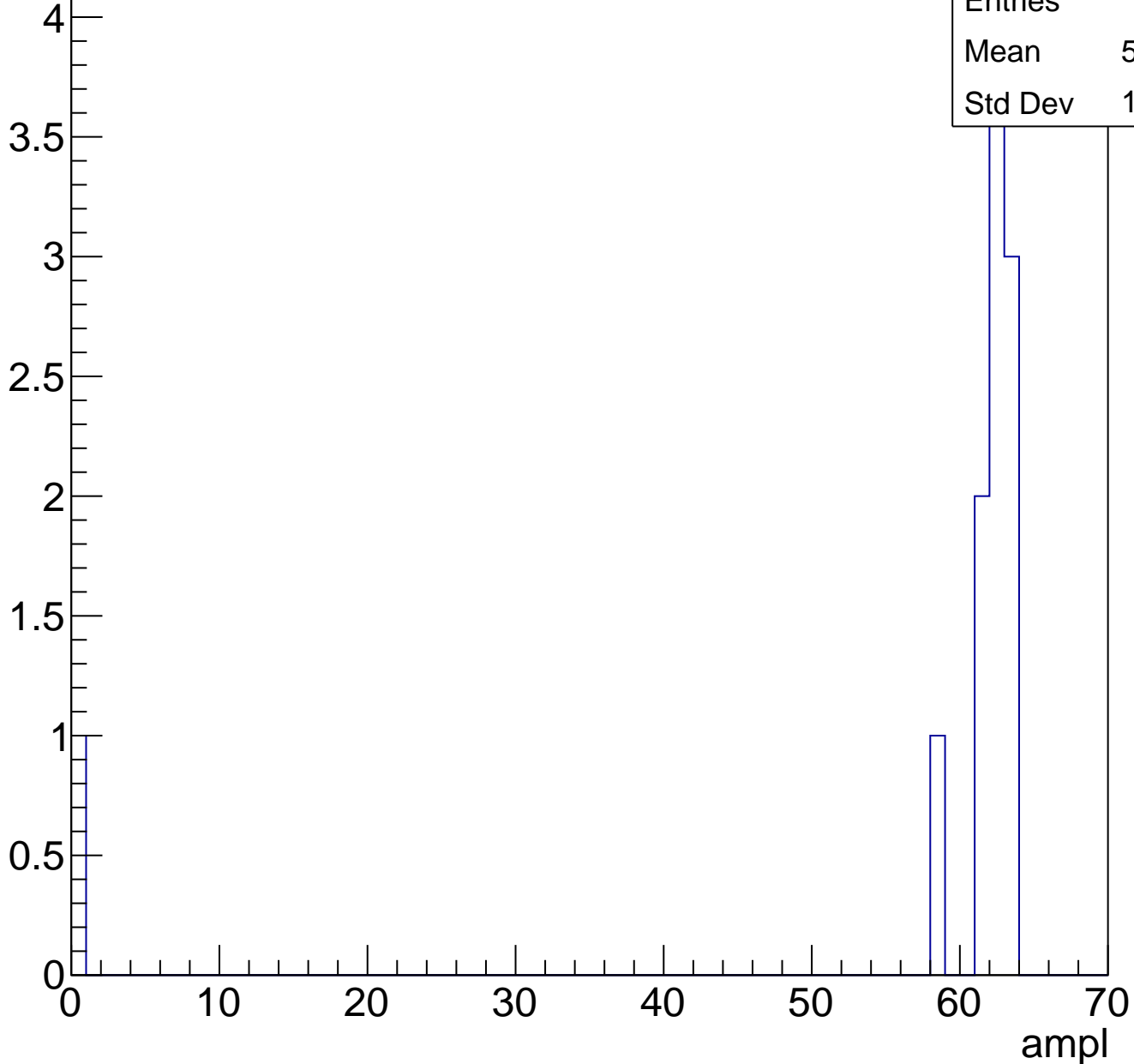
ampl



B1L103S, U1-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

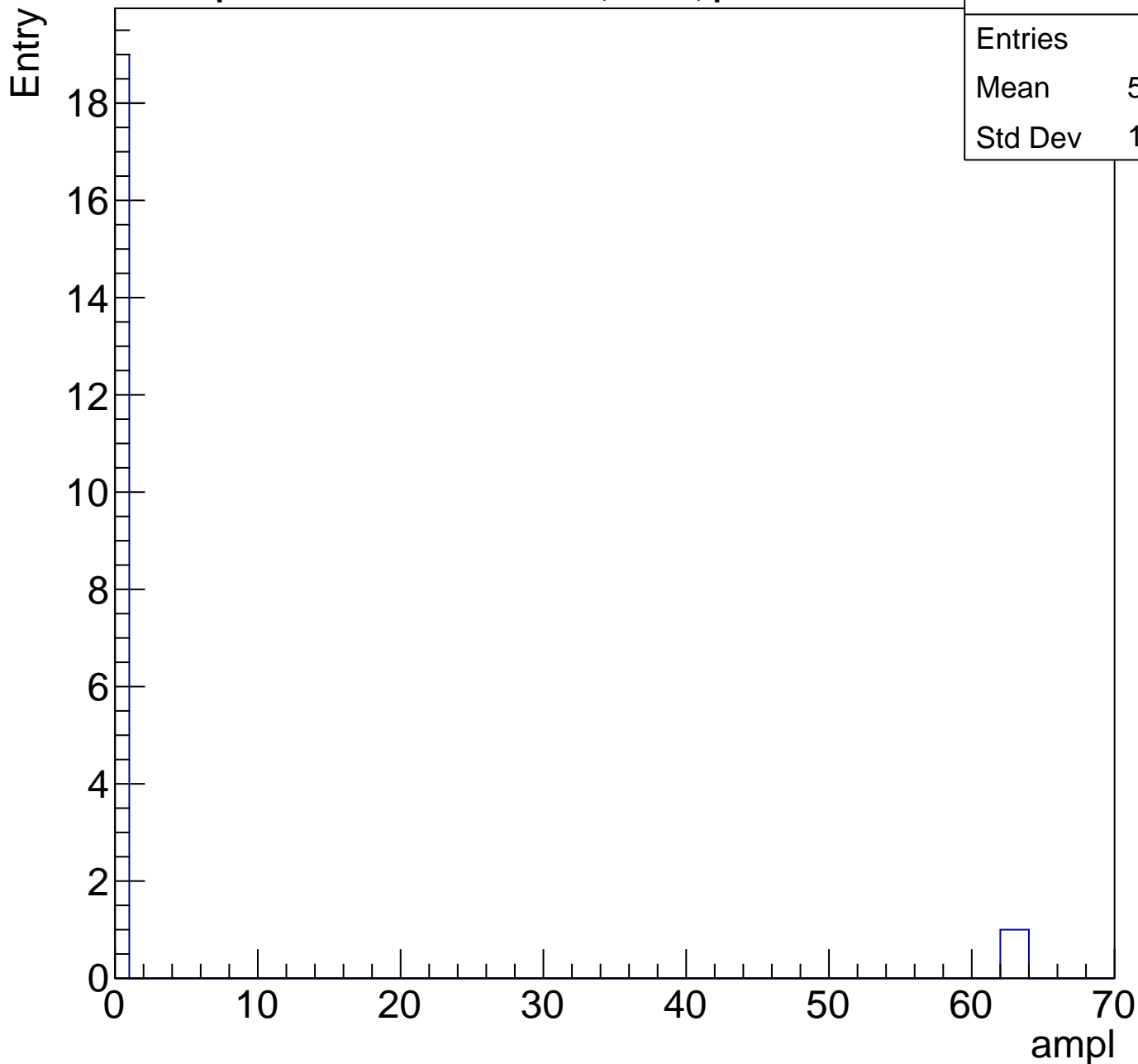
Entry



B1L103S, U1-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



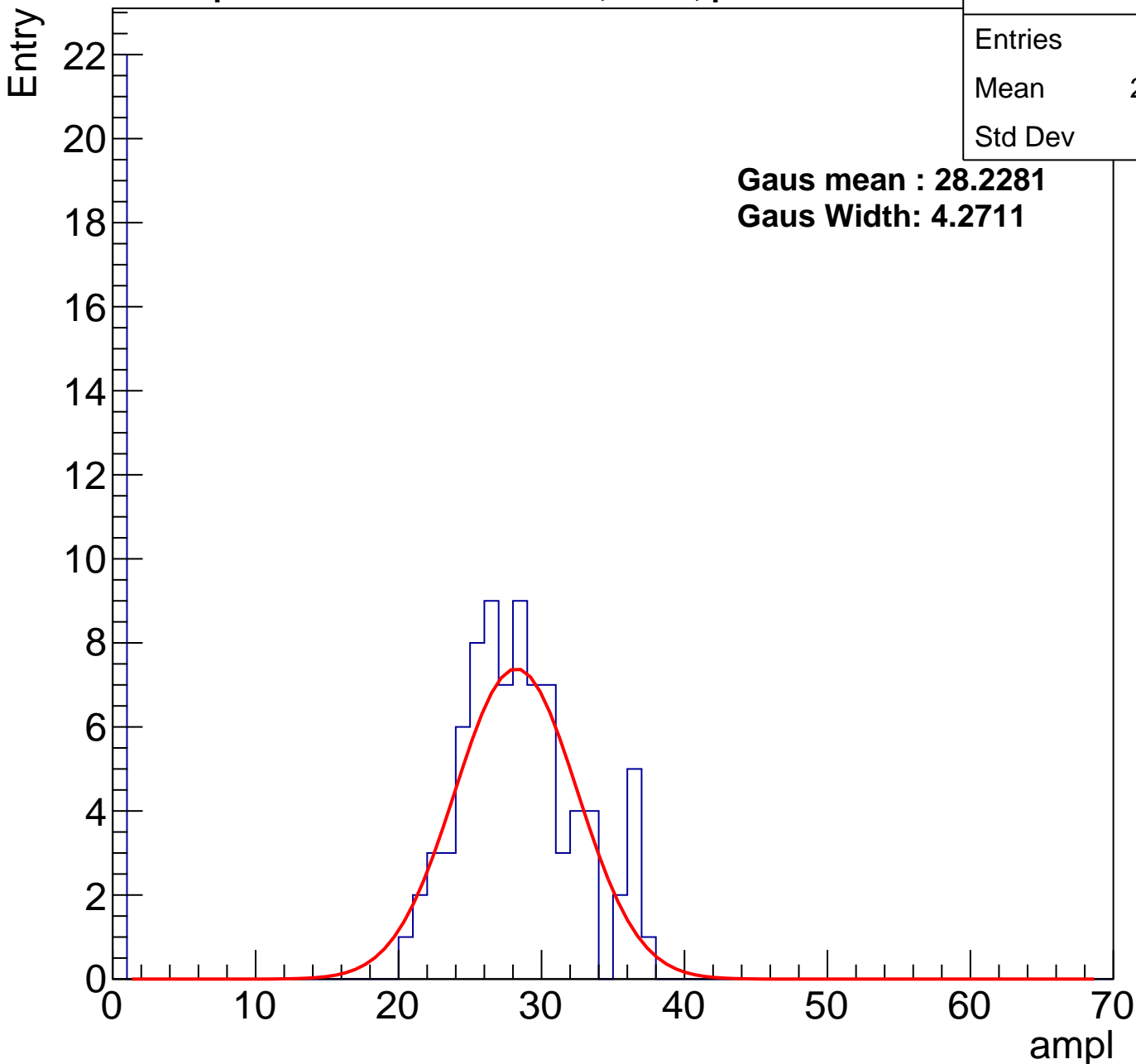
B1L103S, U1-ch8, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	22.03
Std Dev	12.01

Gaus mean : 28.2281

Gaus Width: 4.2711



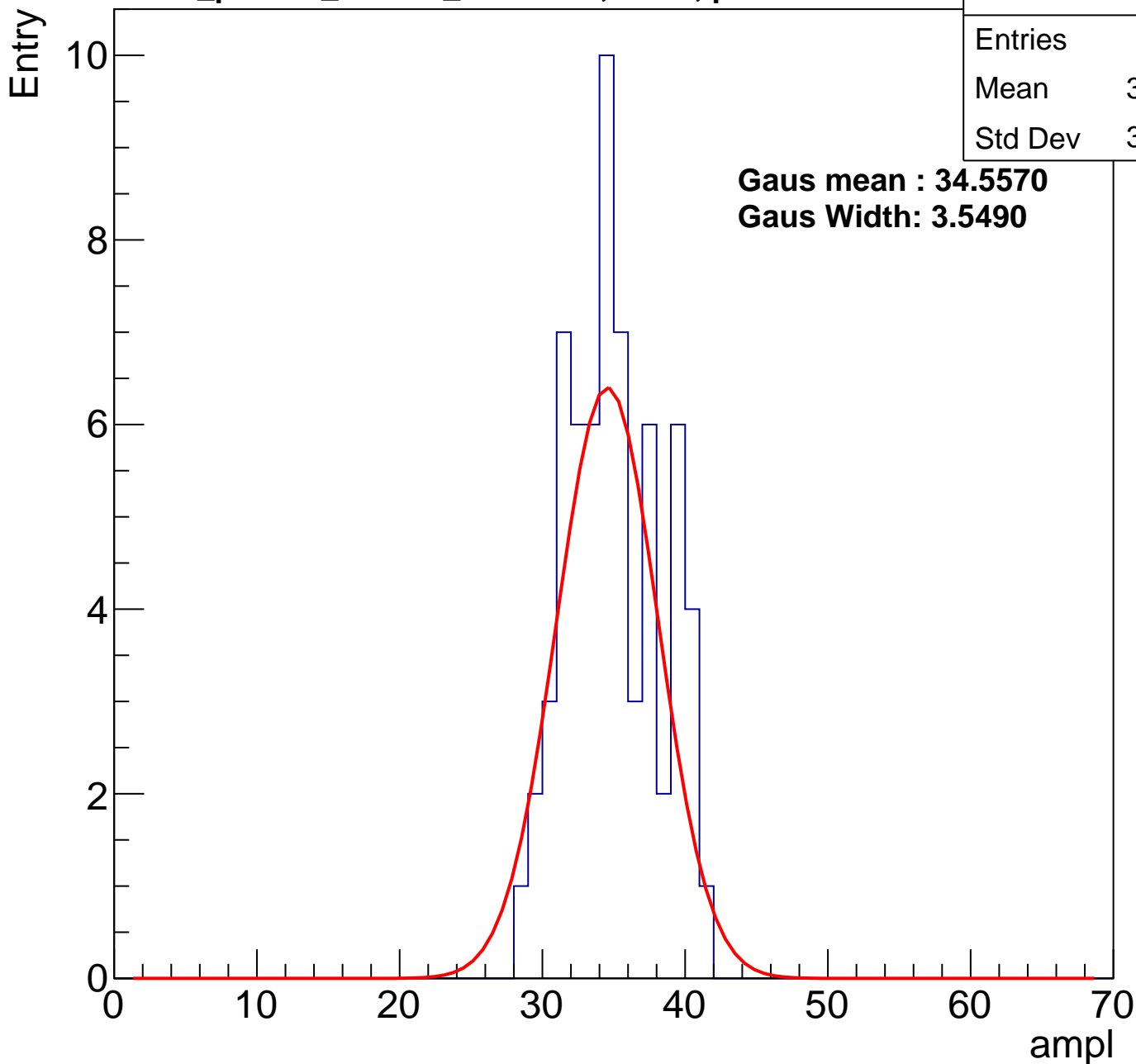
B1L103S, U1-ch8, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	34.52
Std Dev	3.206

Gaus mean : 34.5570

Gaus Width: 3.5490



B1L103S, U1-ch8, adc2

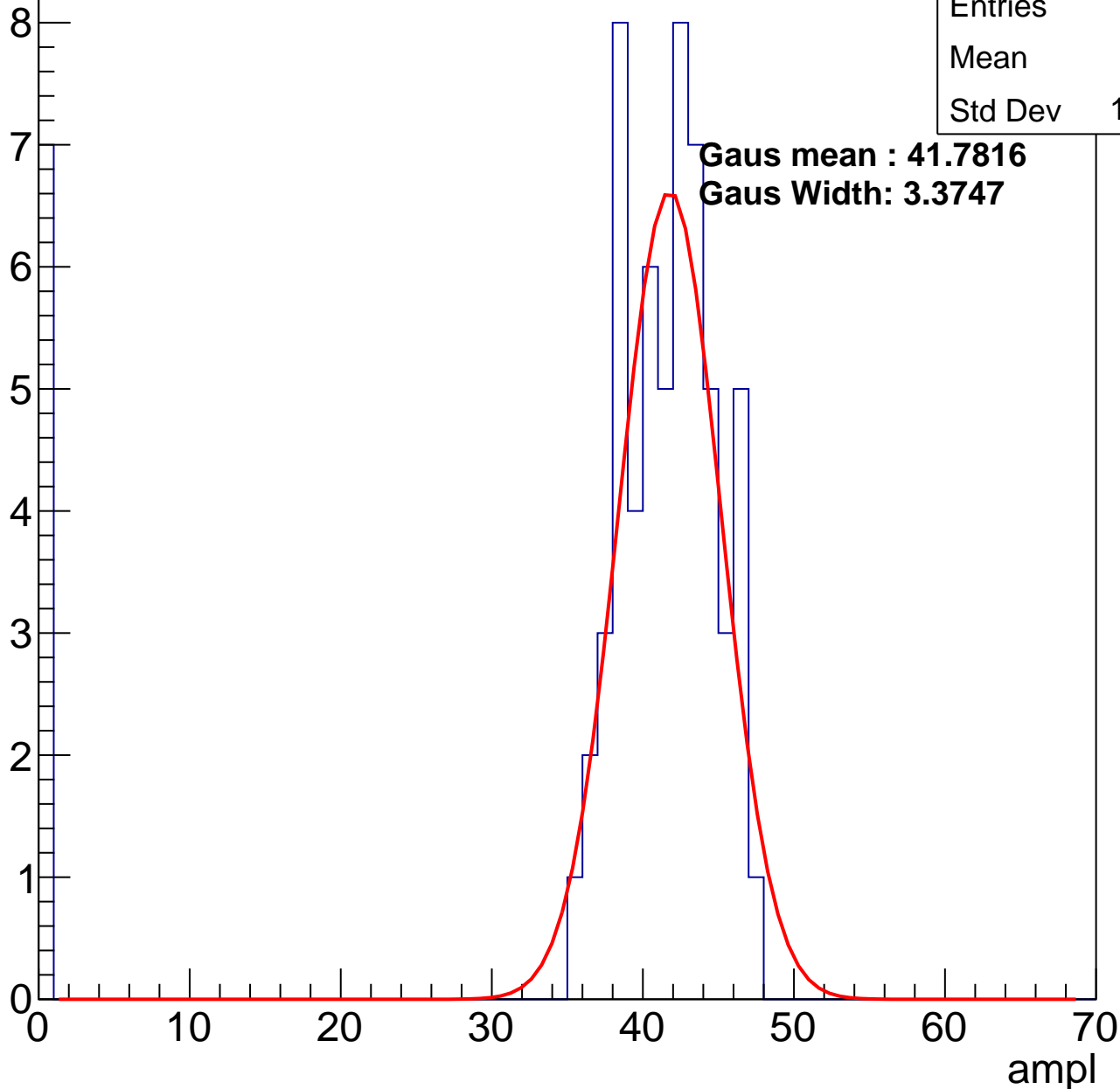
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.8
Std Dev	13.09

Gaus mean : 41.7816

Gaus Width: 3.3747

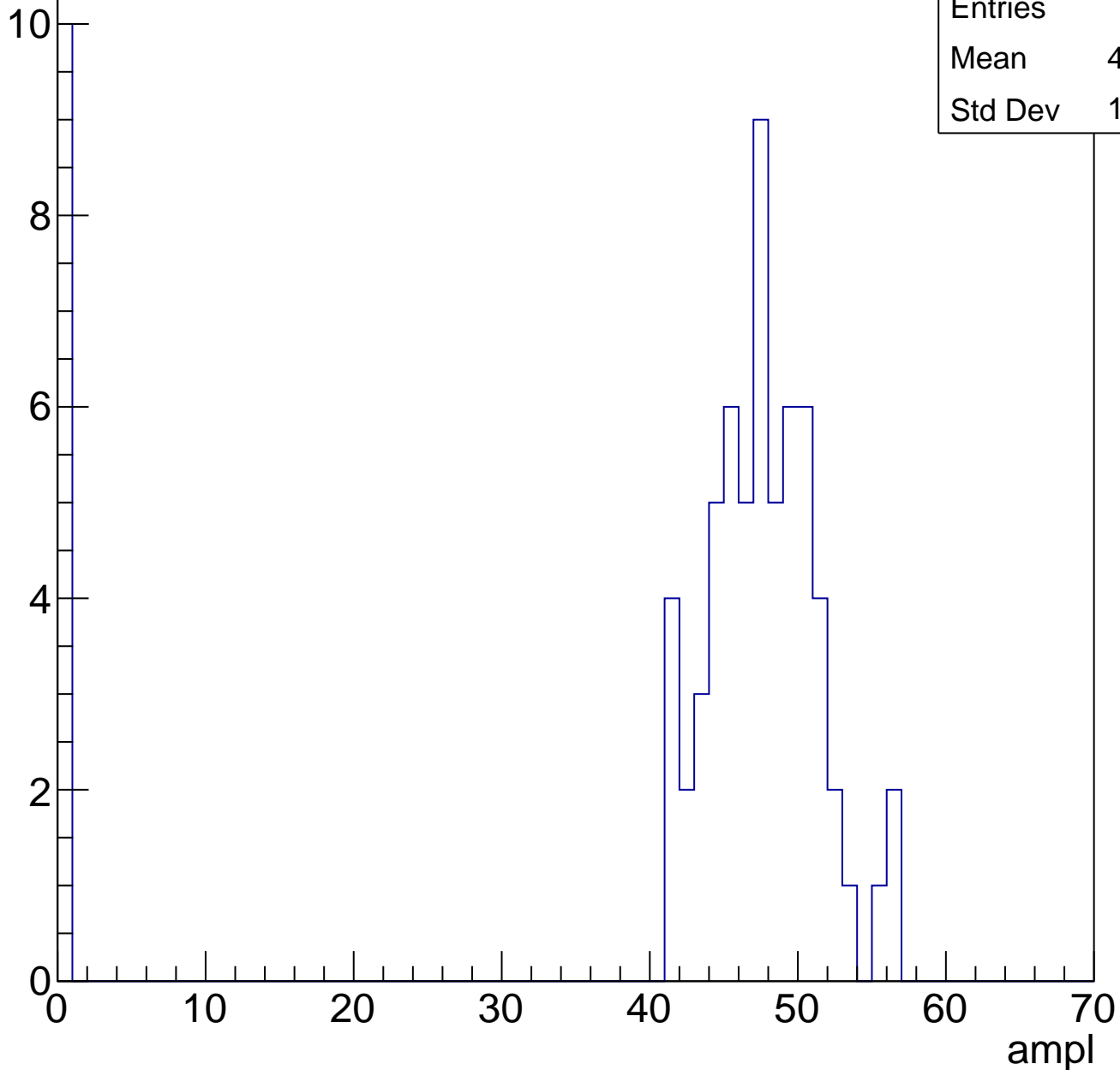


B1L103S, U1-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	40.59
Std Dev	16.76

Entry

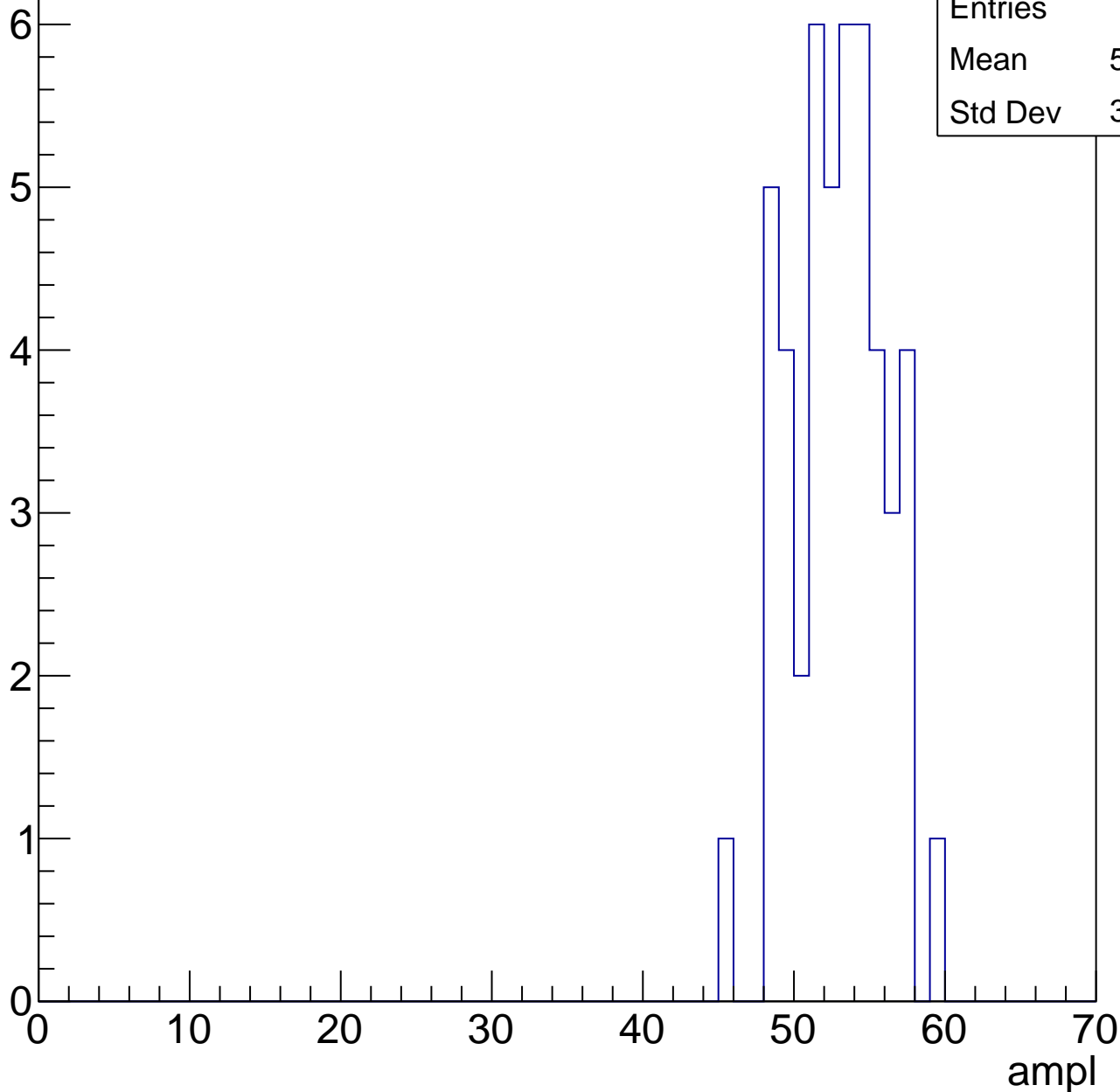


B1L103S, U1-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	52.43
Std Dev	3.037



B1L103S, U1-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	57.61
Std Dev	3.325

Entry

10

8

6

4

2

0

0

10

20

30

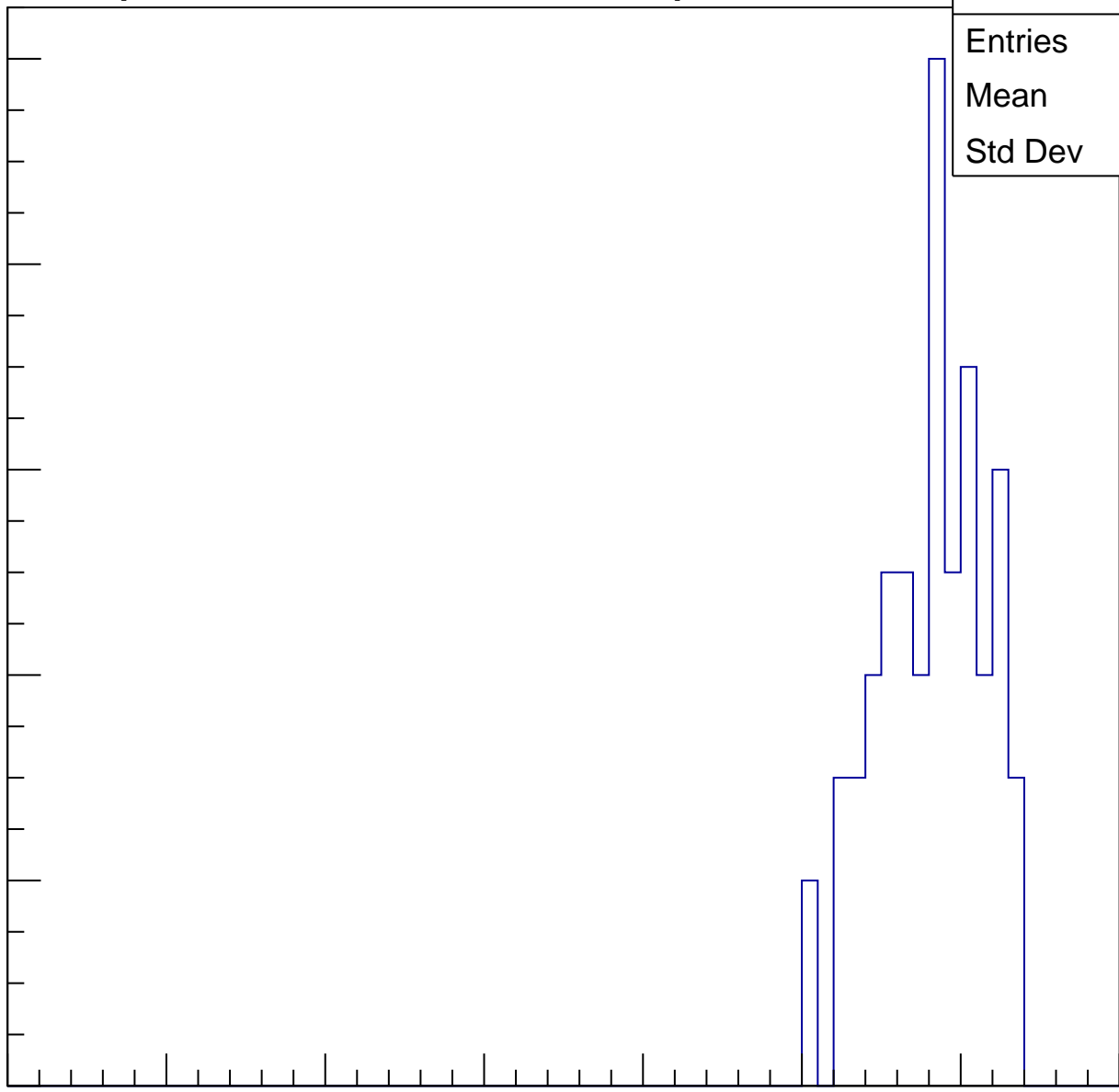
40

50

60

70

ampl

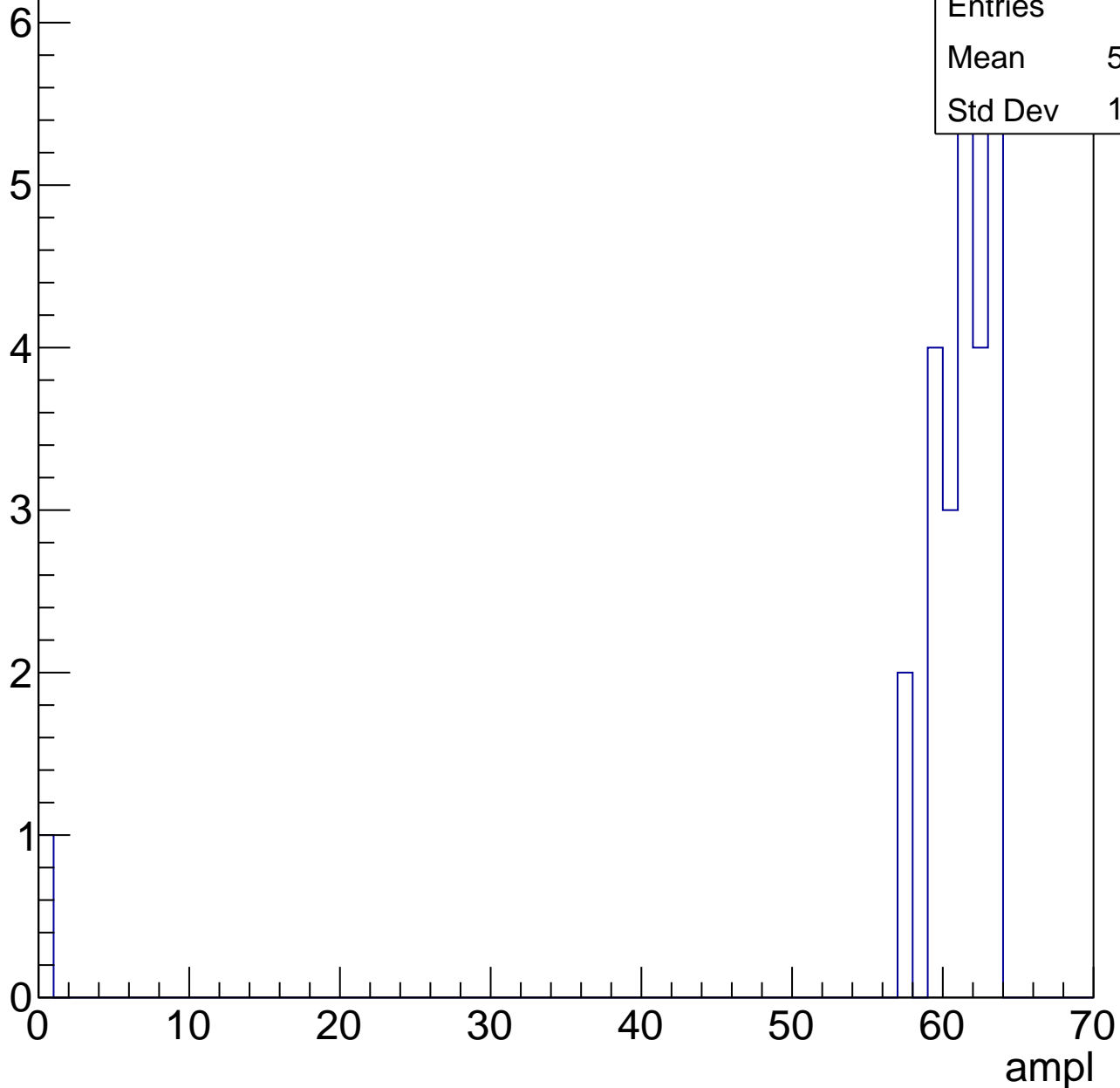


B1L103S, U1-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.54
Std Dev	11.84



B1L103S, U1-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U1-ch9, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	23.75
Std Dev	11.47

Gaus mean : 29.6404

Gaus Width: 3.1902

Entry

12

10

8

6

4

2

0

0

10

20

30

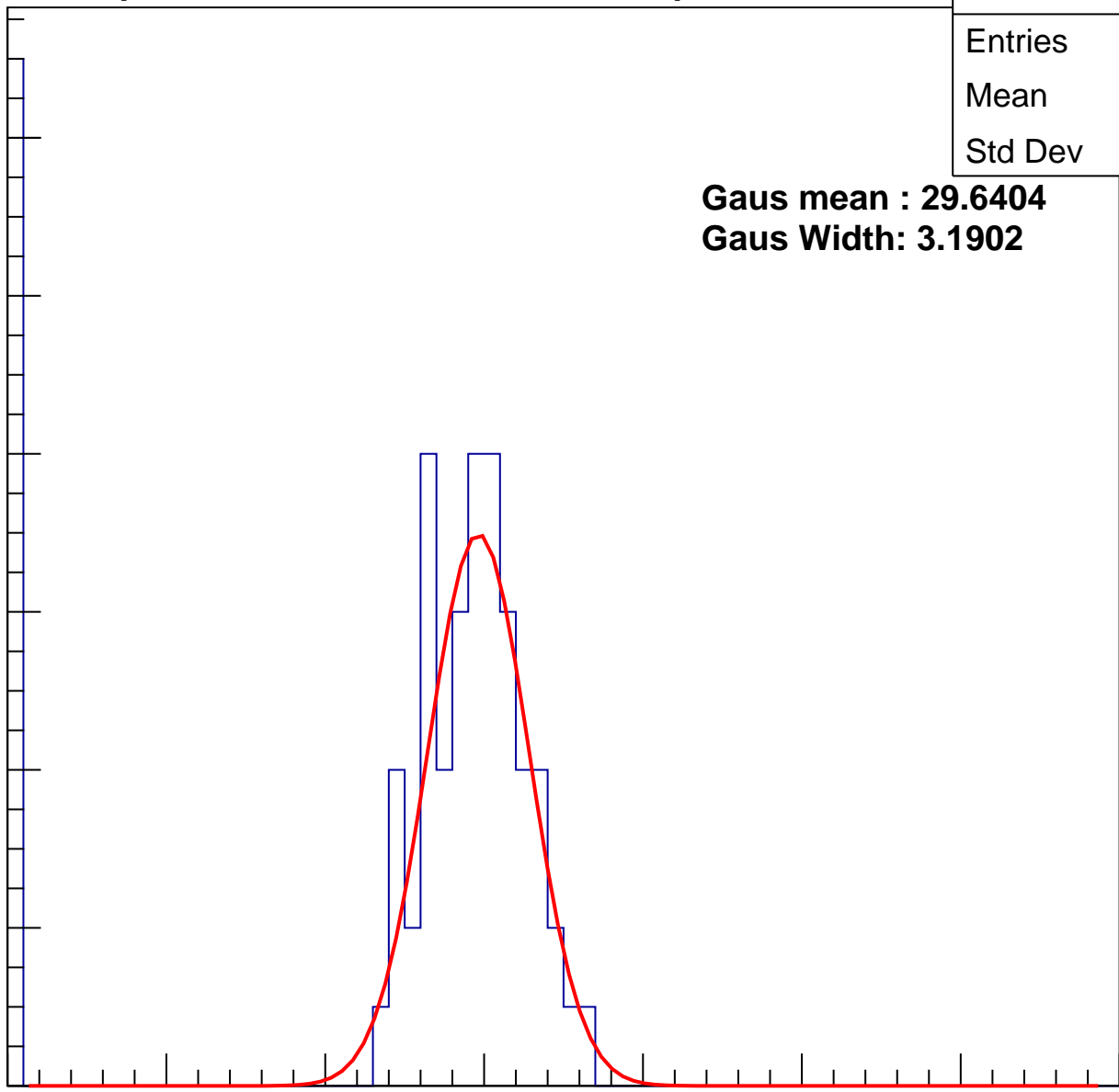
40

50

60

70

ampl



B1L103S, U1-ch9, adc1

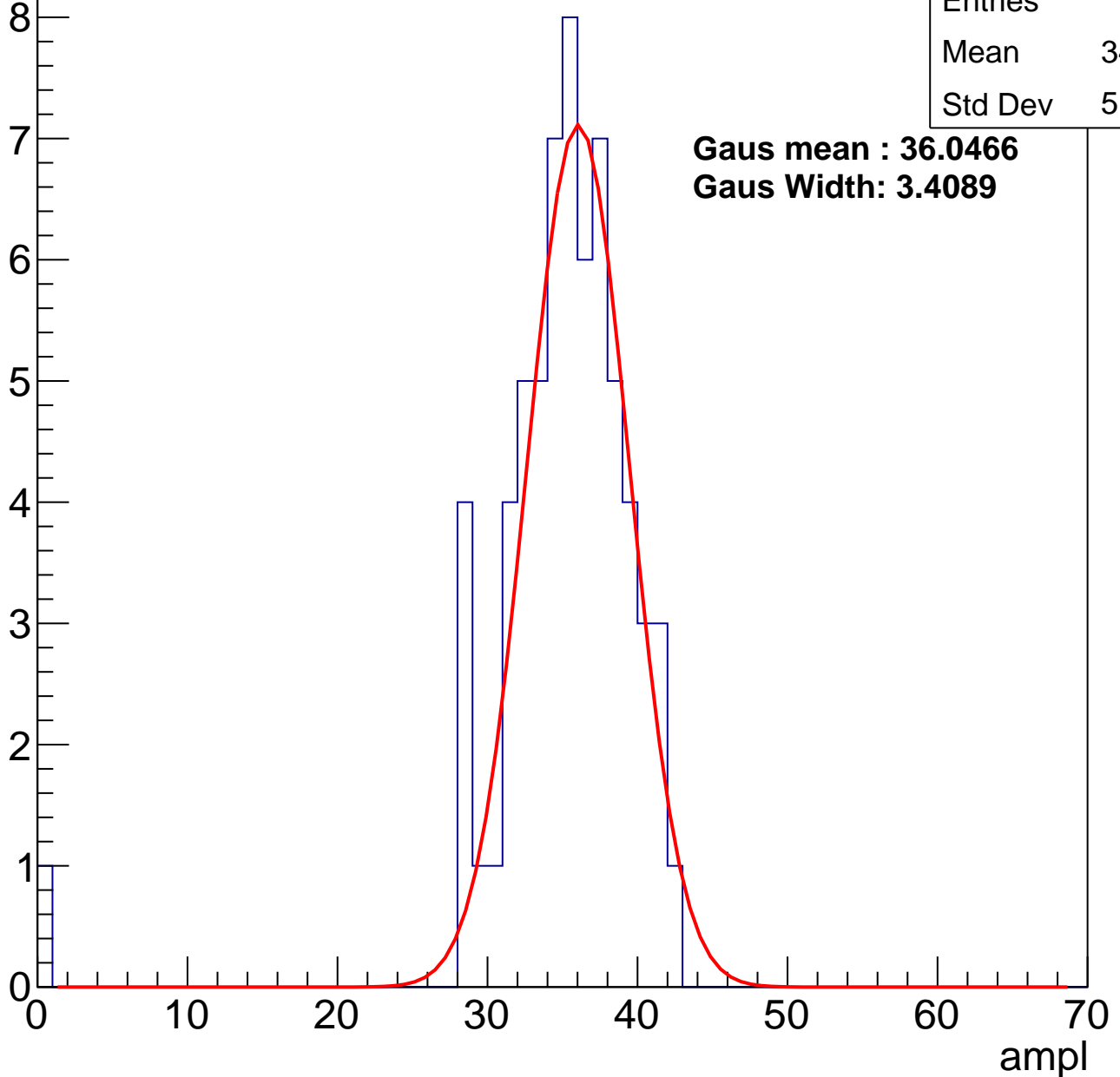
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.52
Std Dev	5.517

Gaus mean : 36.0466

Gaus Width: 3.4089



B1L103S, U1-ch9, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	34.96
Std Dev	15.01

Gaus mean : 41.6165

Gaus Width: 3.7835

Entry

10

8

6

4

2

0

0

10

20

30

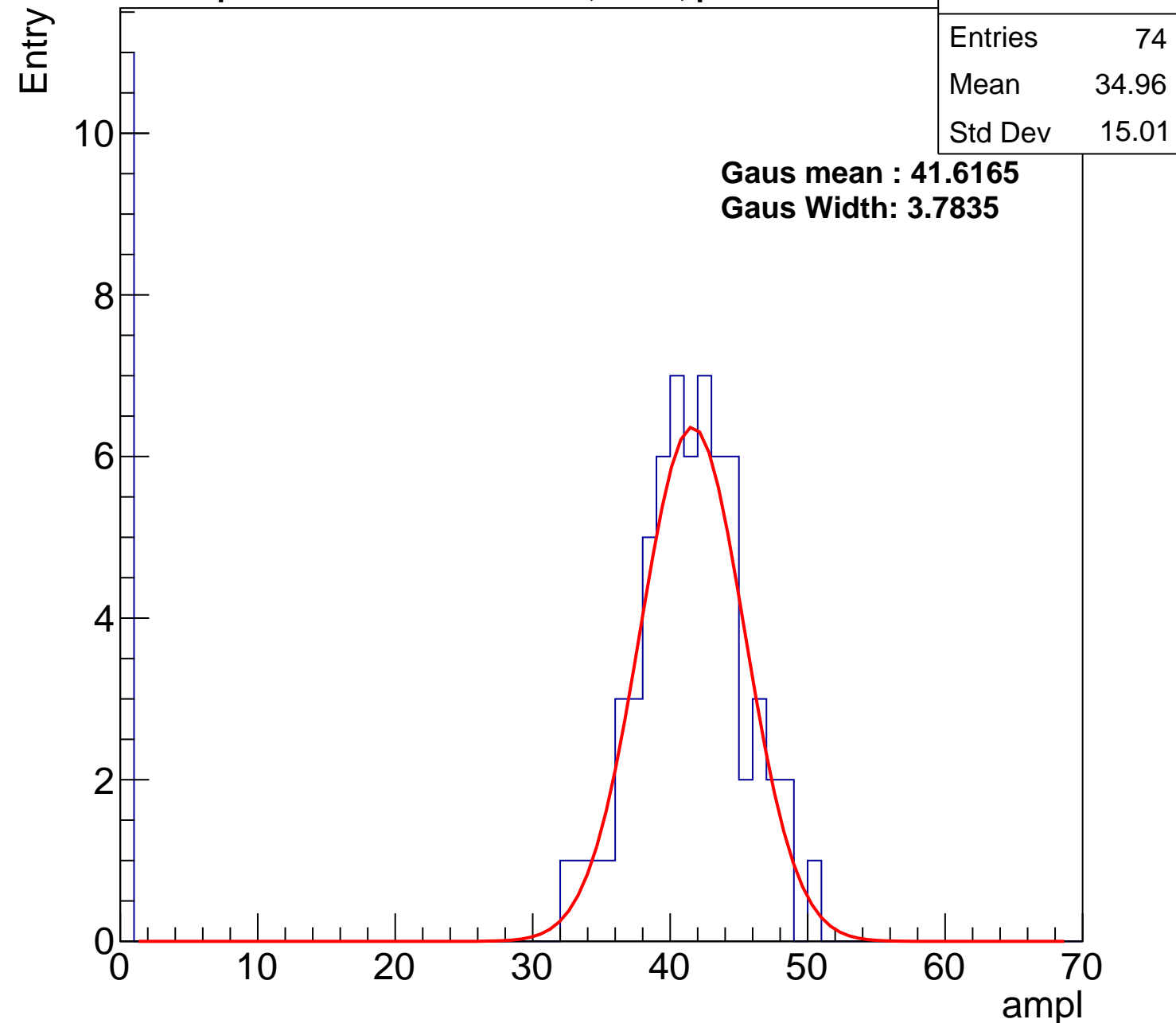
40

50

60

70

ampl

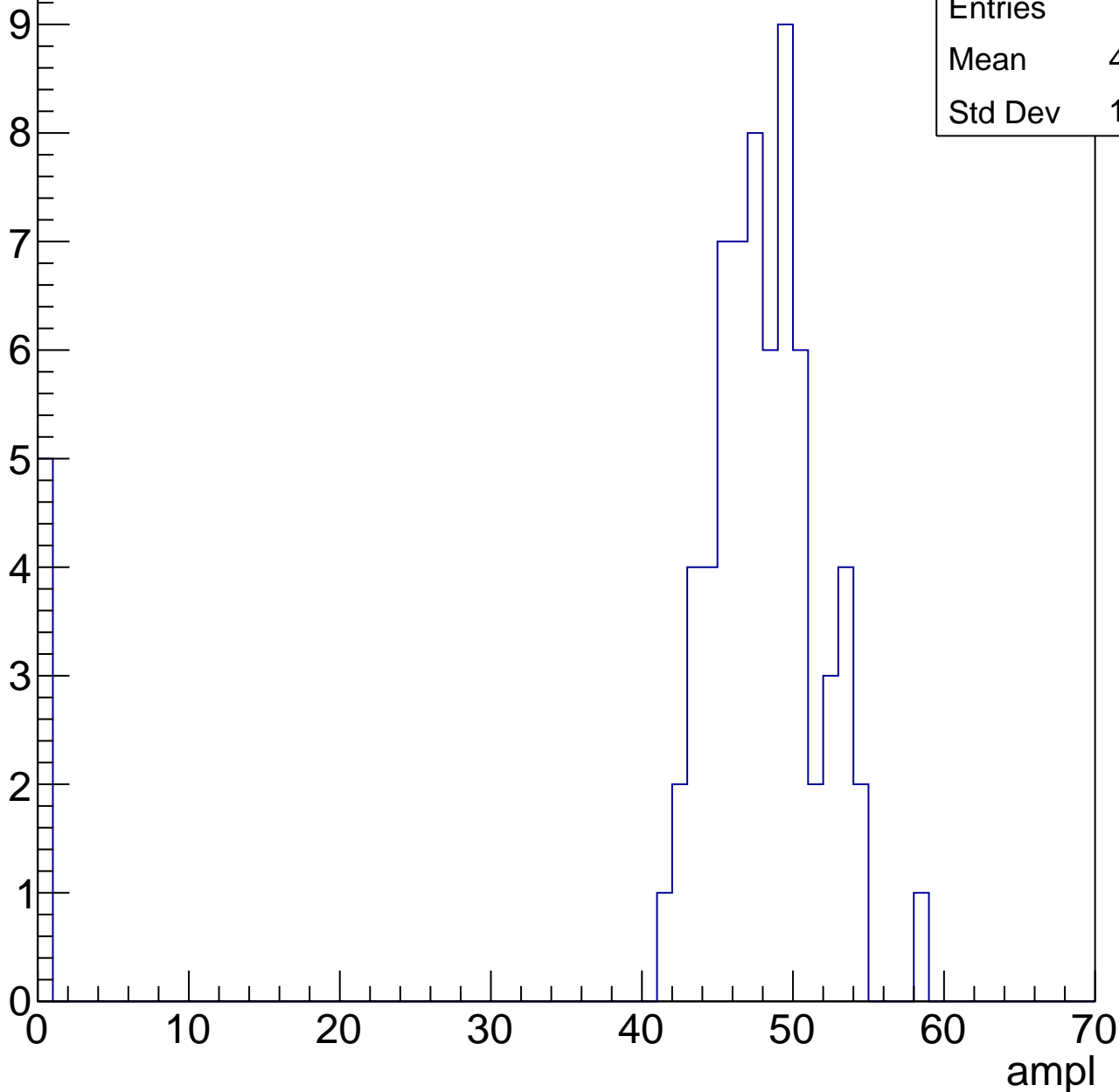


B1L103S, U1-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	44.38
Std Dev	12.64

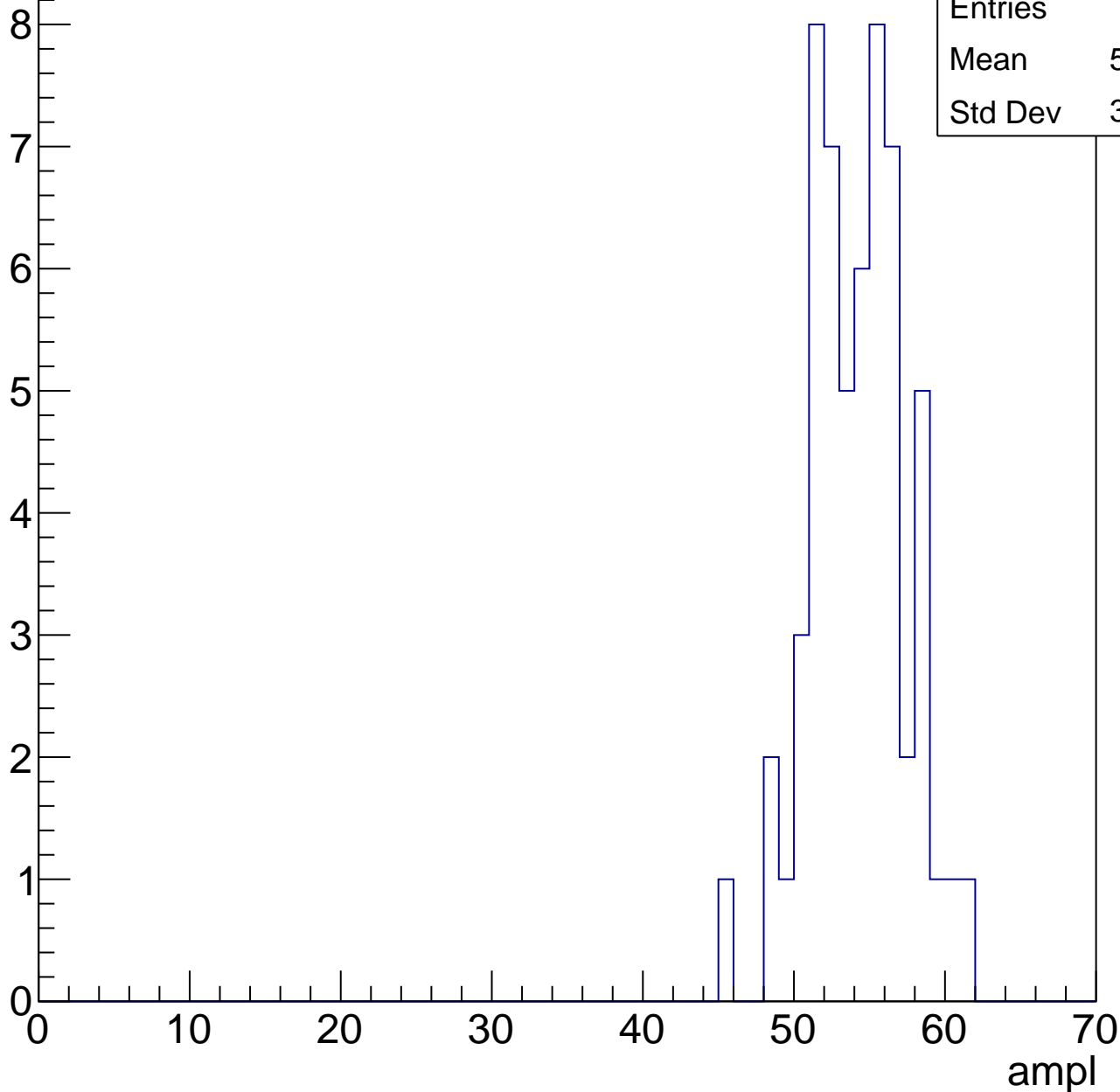


B1L103S, U1-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

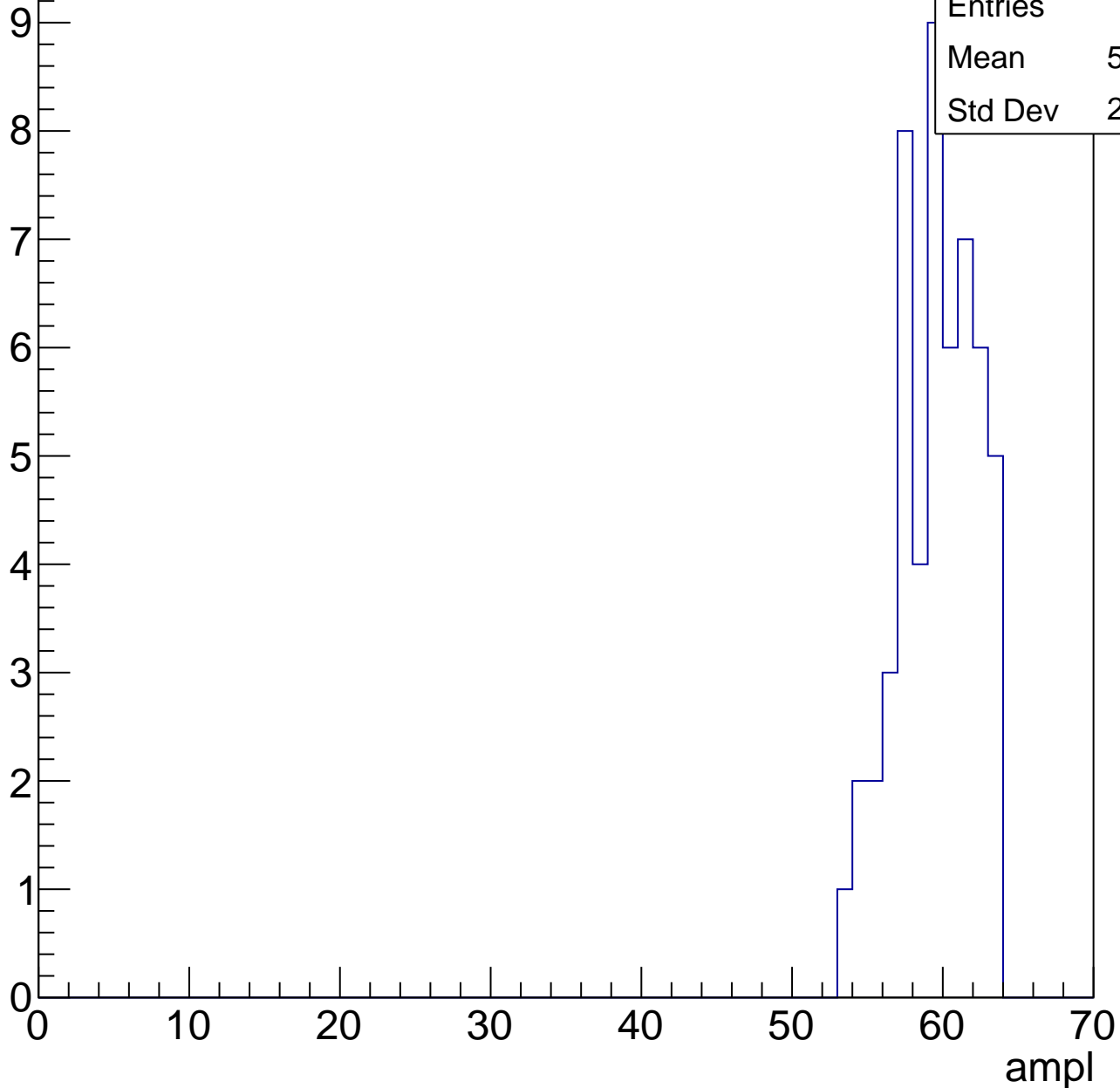
Entries	58
Mean	53.74
Std Dev	3.138



B1L103S, U1-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

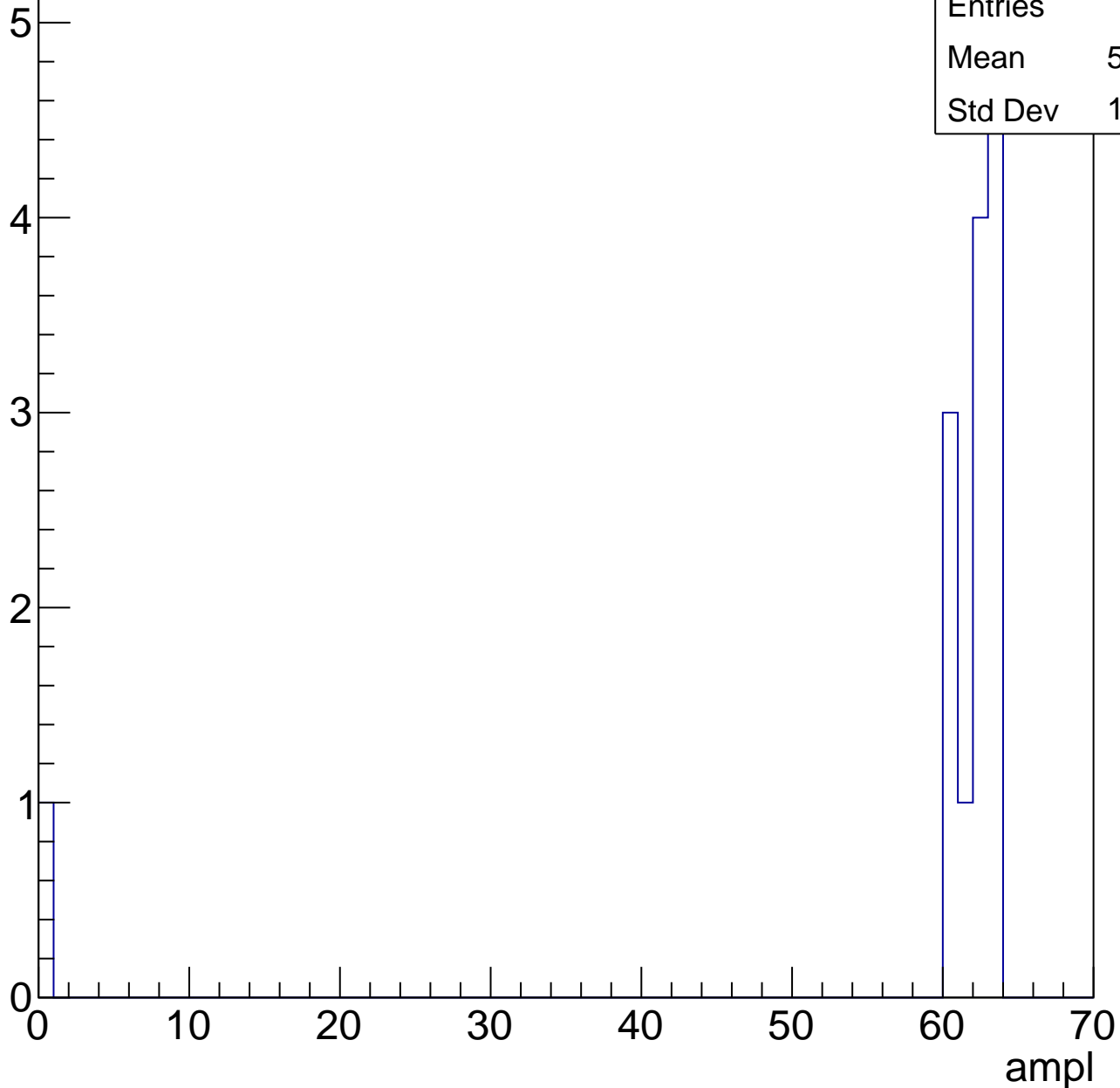


B1L103S, U1-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.43
Std Dev	15.97

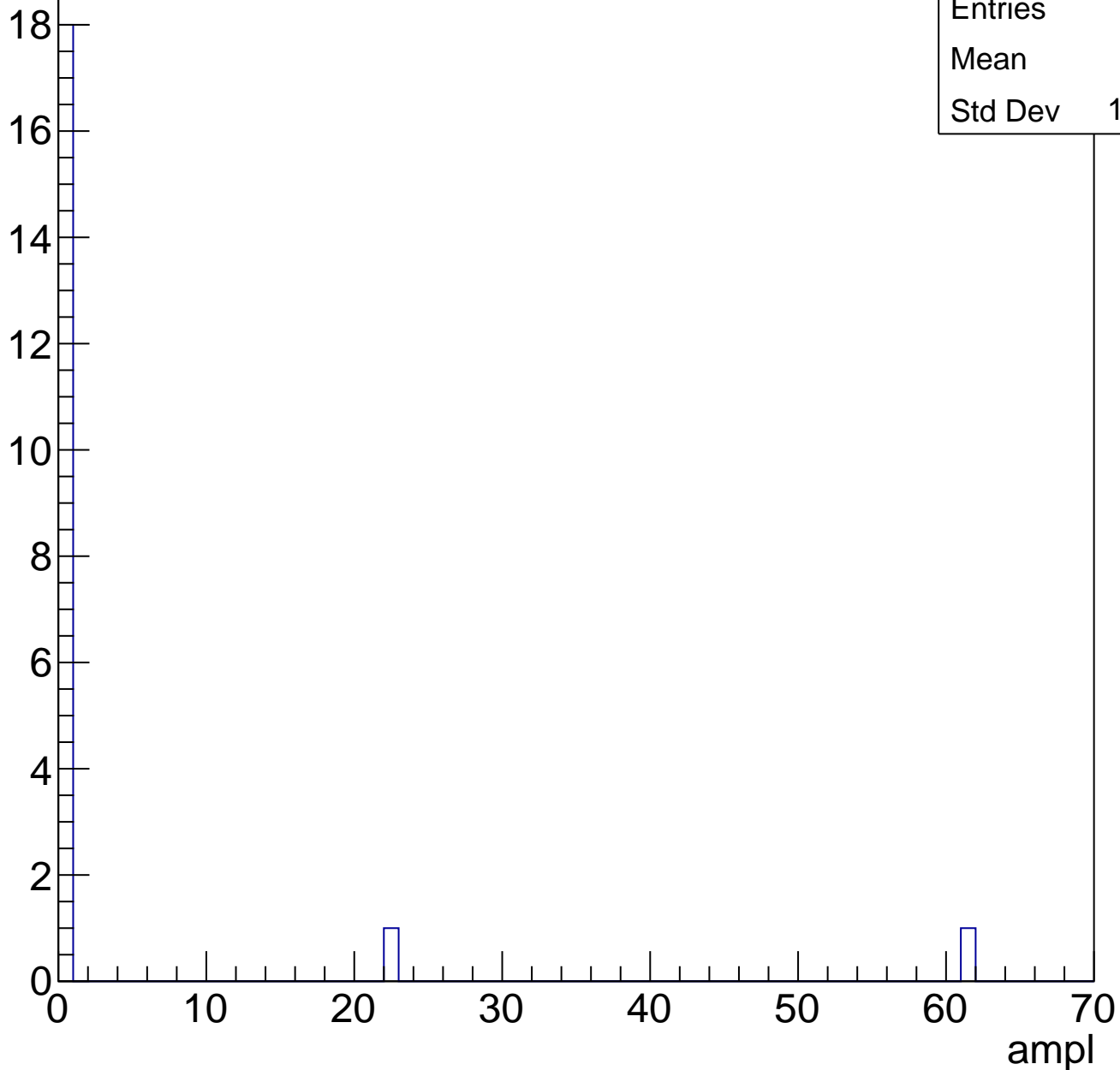


B1L103S, U1-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	13.89

Entry



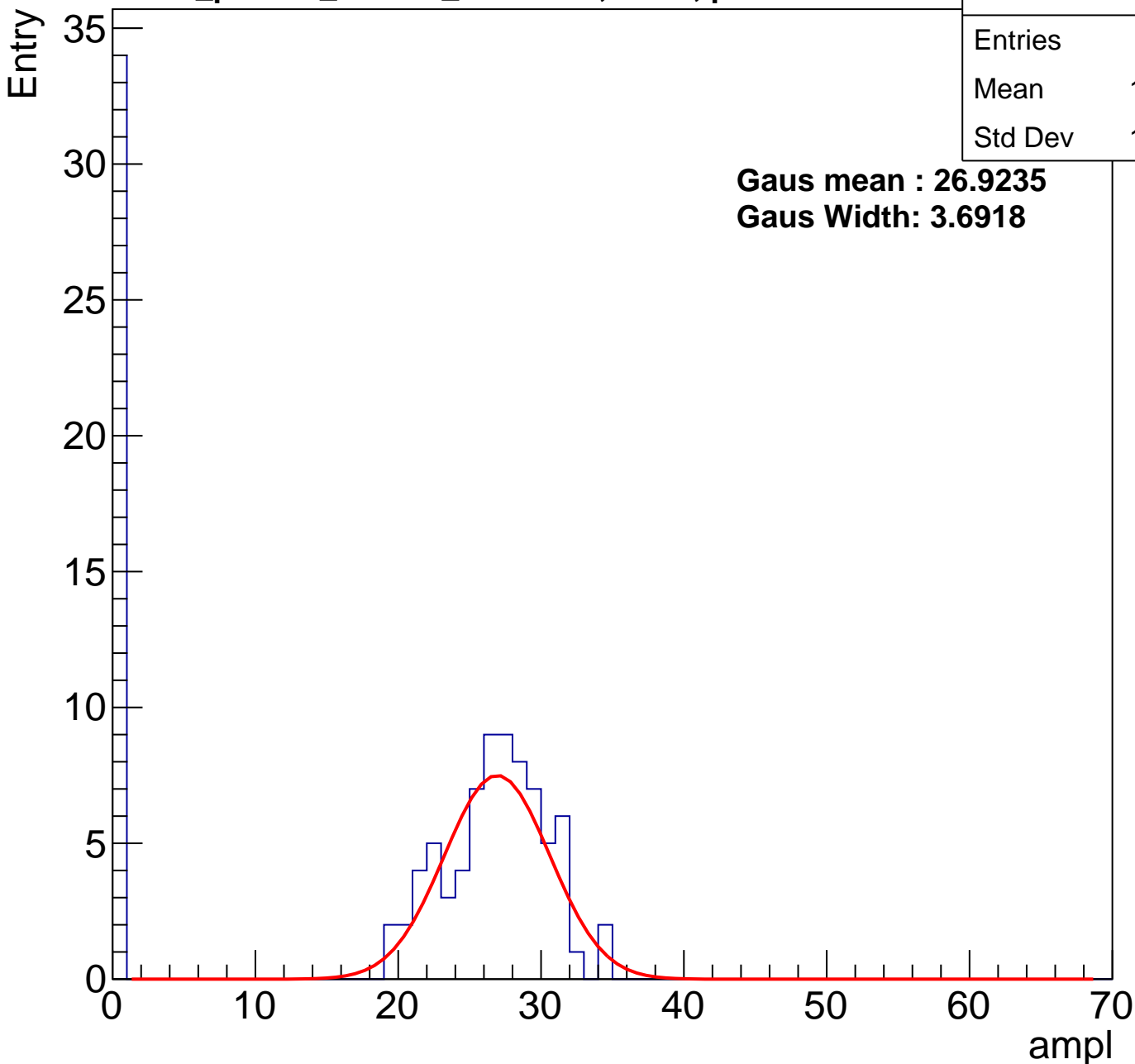
B1L103S, U1-ch10, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	108
Mean	18.07
Std Dev	12.58

Gaus mean : 26.9235

Gaus Width: 3.6918



B1L103S, U1-ch10, adc1

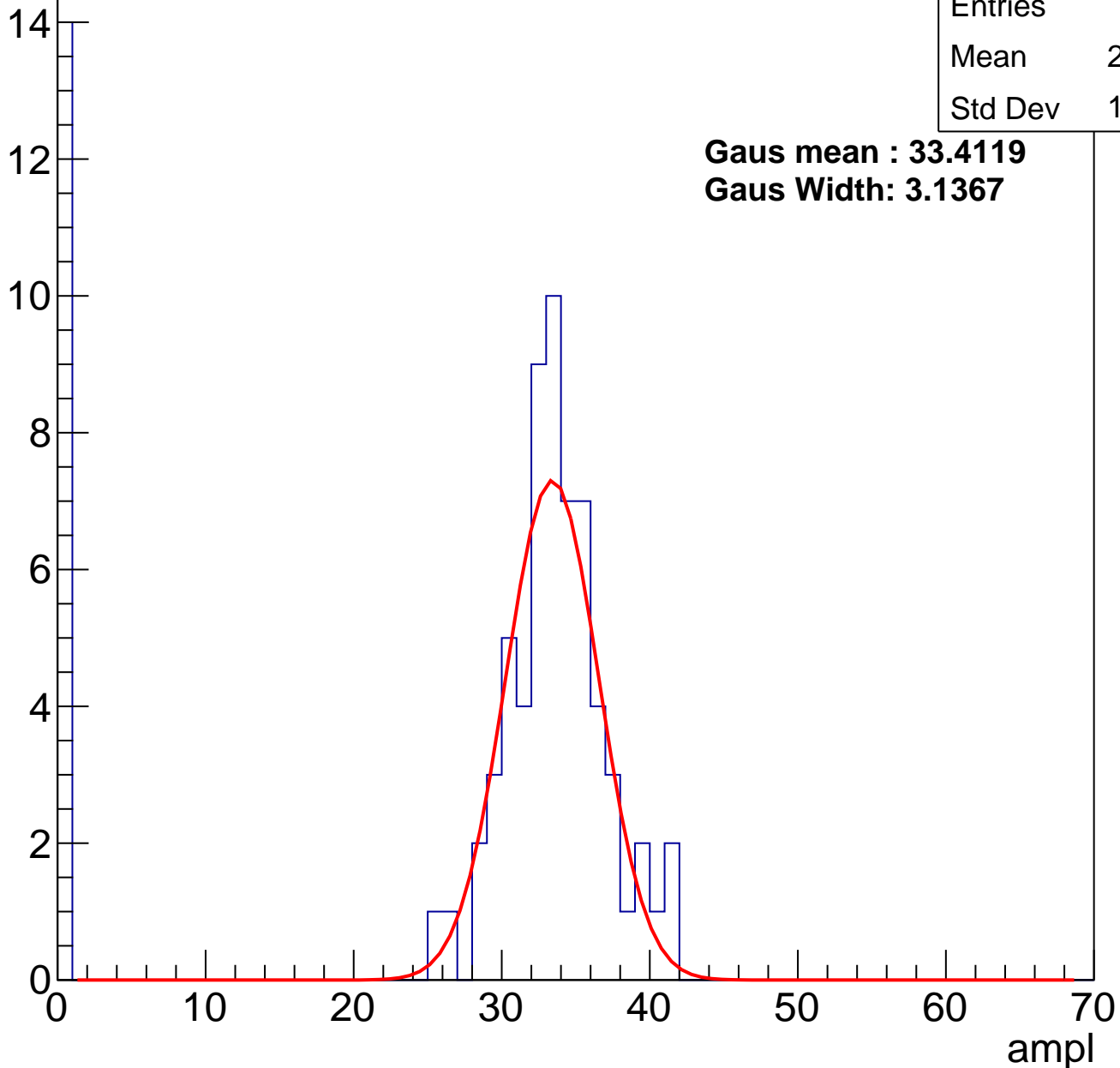
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	27.13
Std Dev	13.23

Gaus mean : 33.4119

Gaus Width: 3.1367

Entry



B1L103S, U1-ch10, adc2

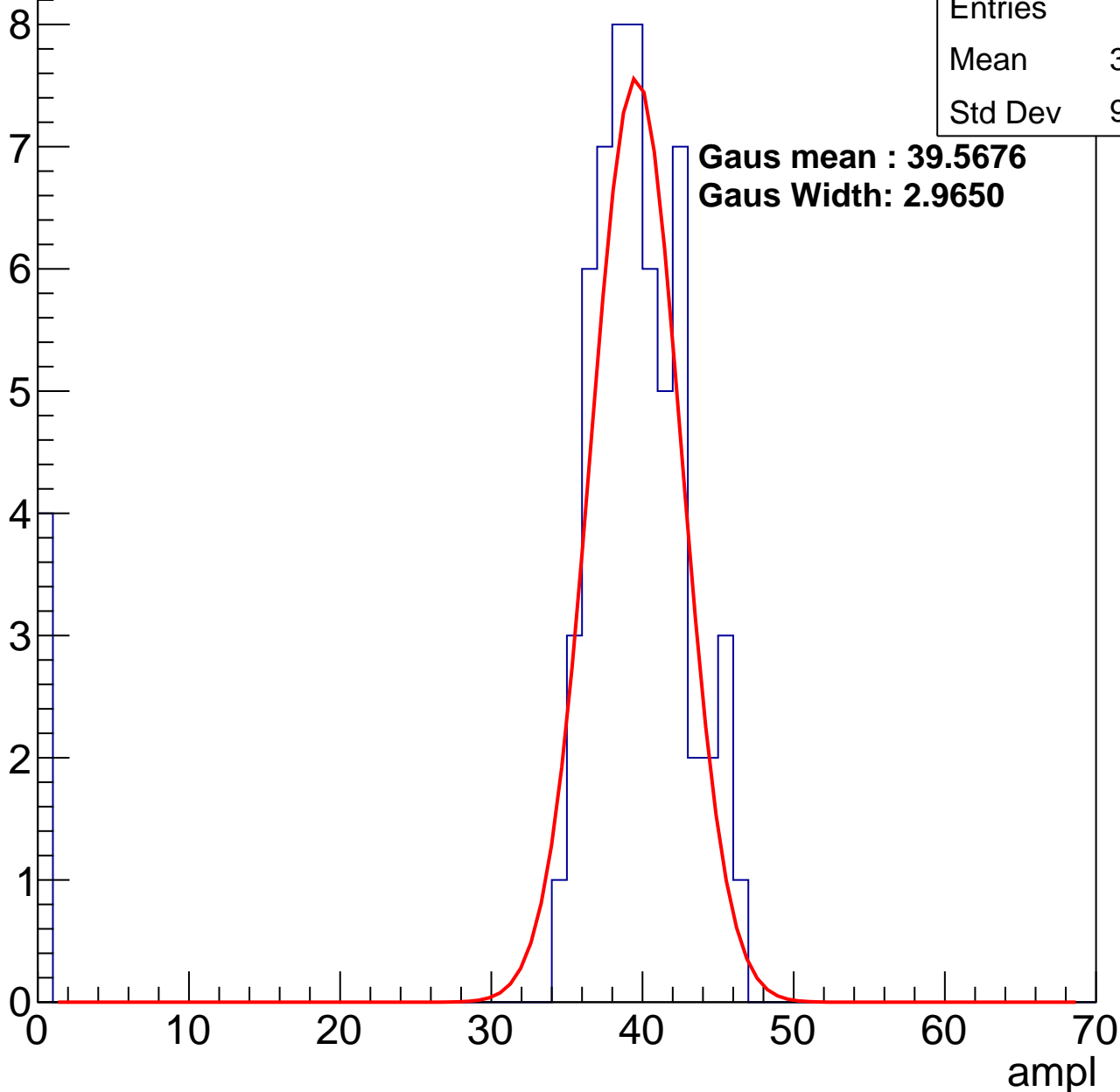
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.89
Std Dev	9.995

Gaus mean : 39.5676

Gaus Width: 2.9650

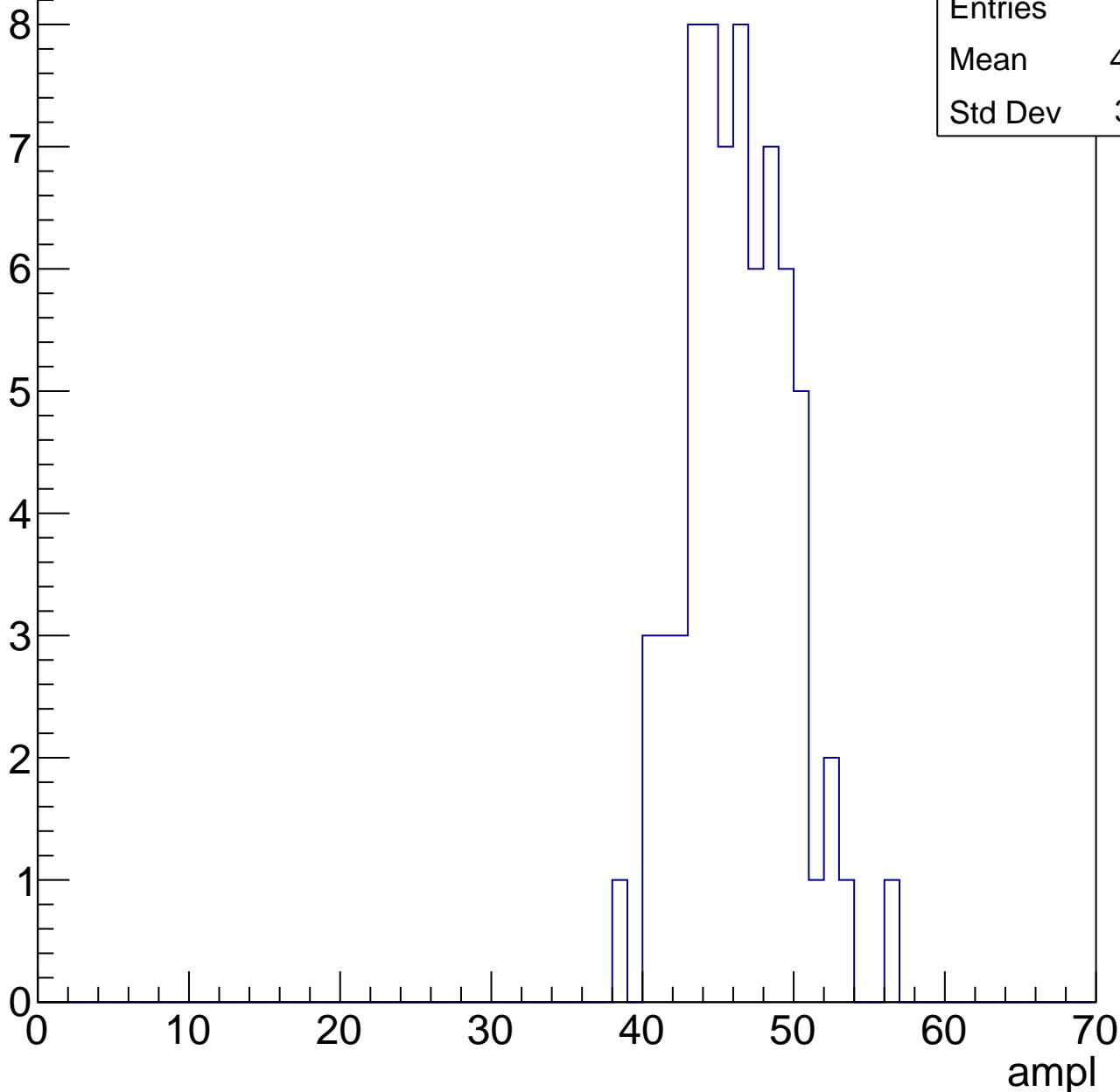


B1L103S, U1-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.89
Std Dev	3.421

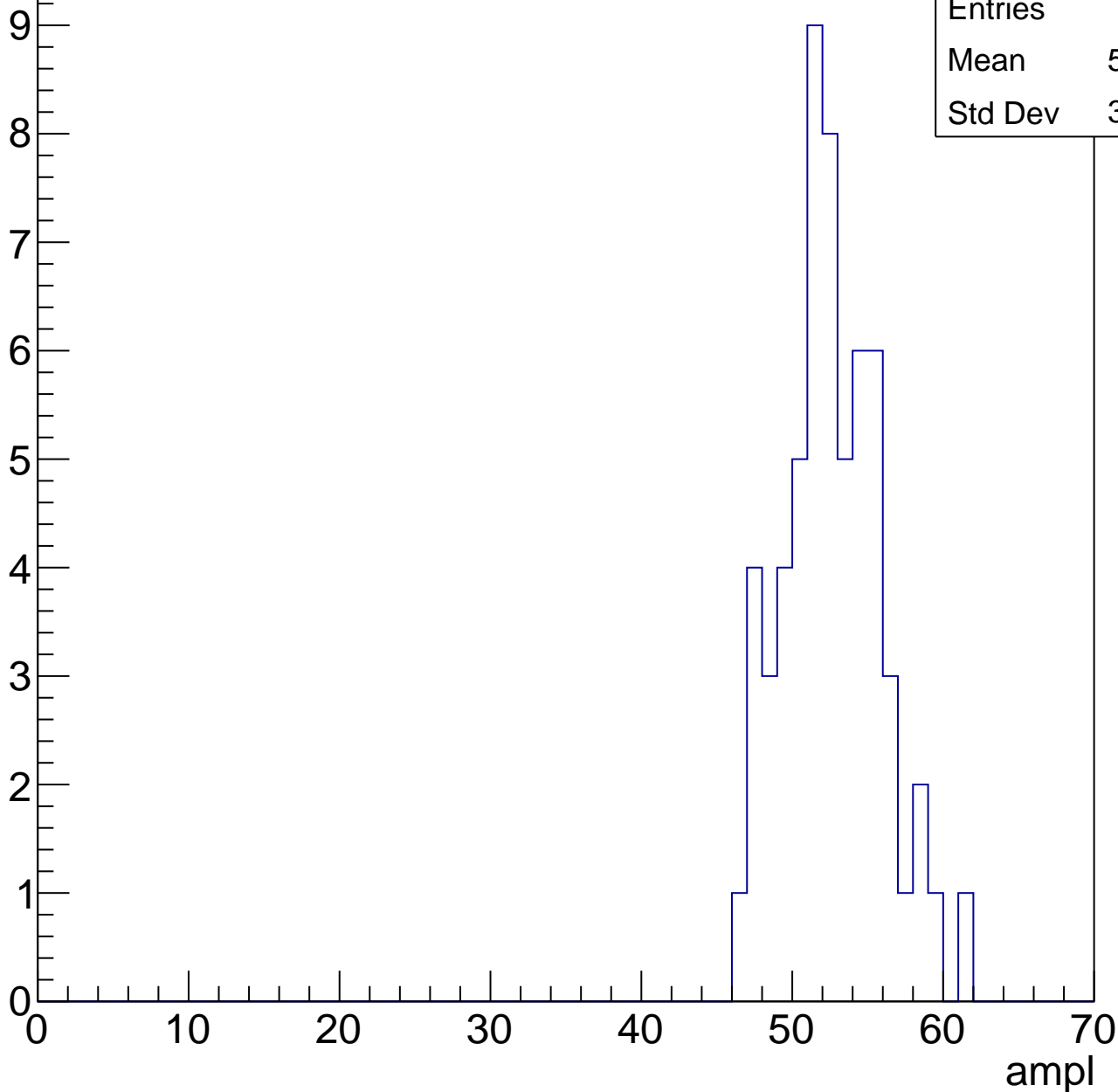


B1L103S, U1-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

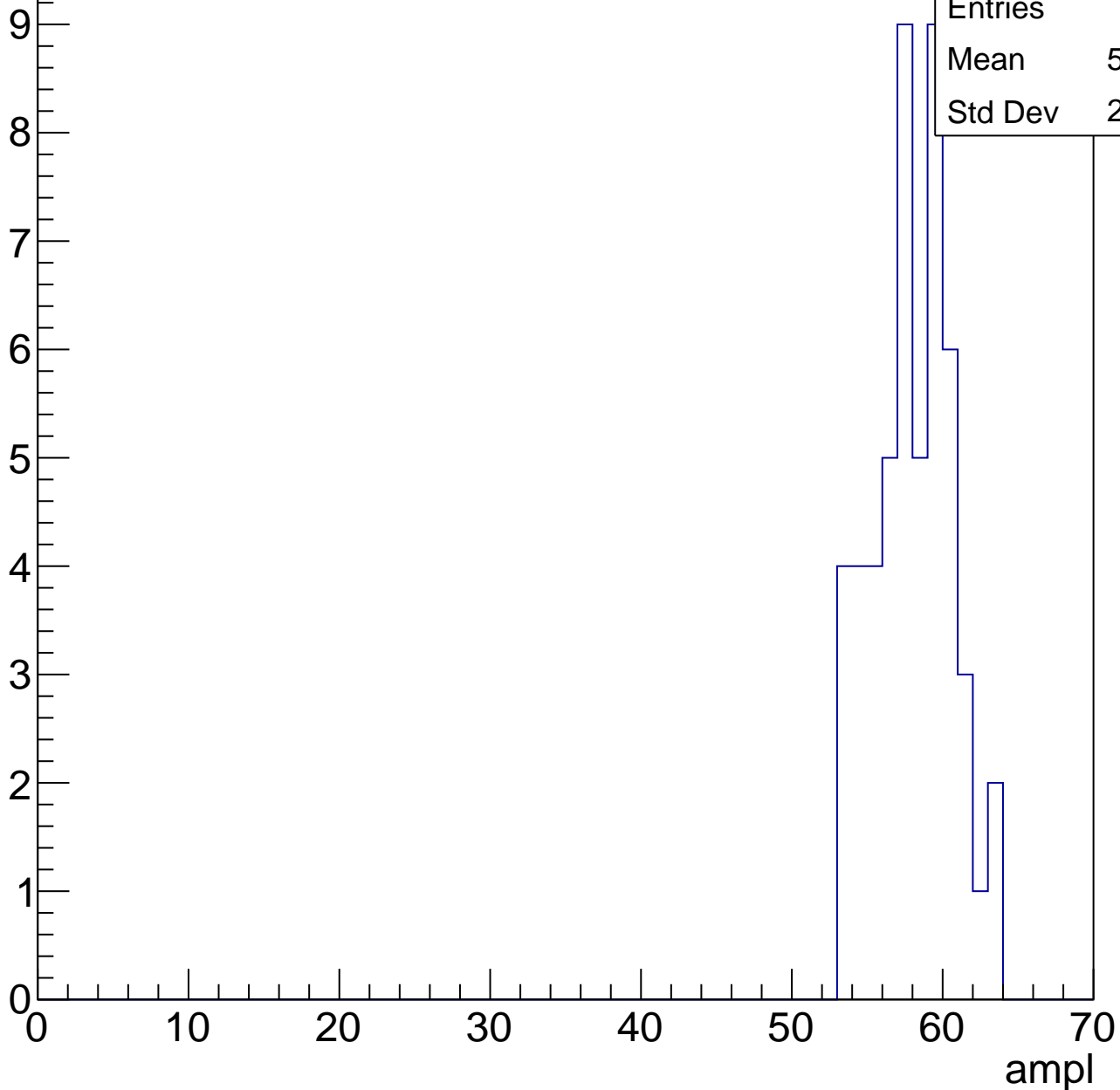
Entries	59
Mean	52.19
Std Dev	3.202



B1L103S, U1-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

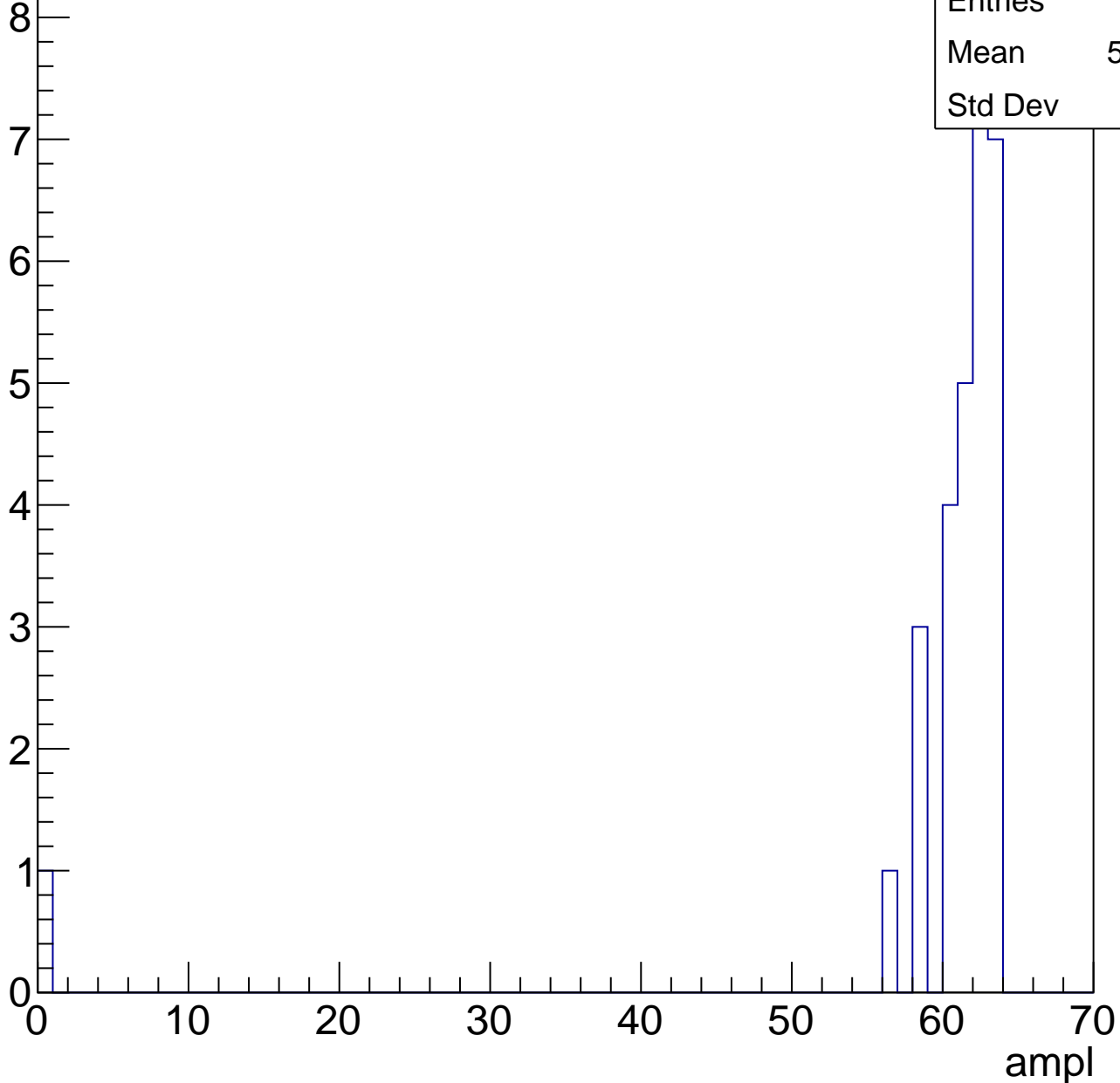


B1L103S, U1-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

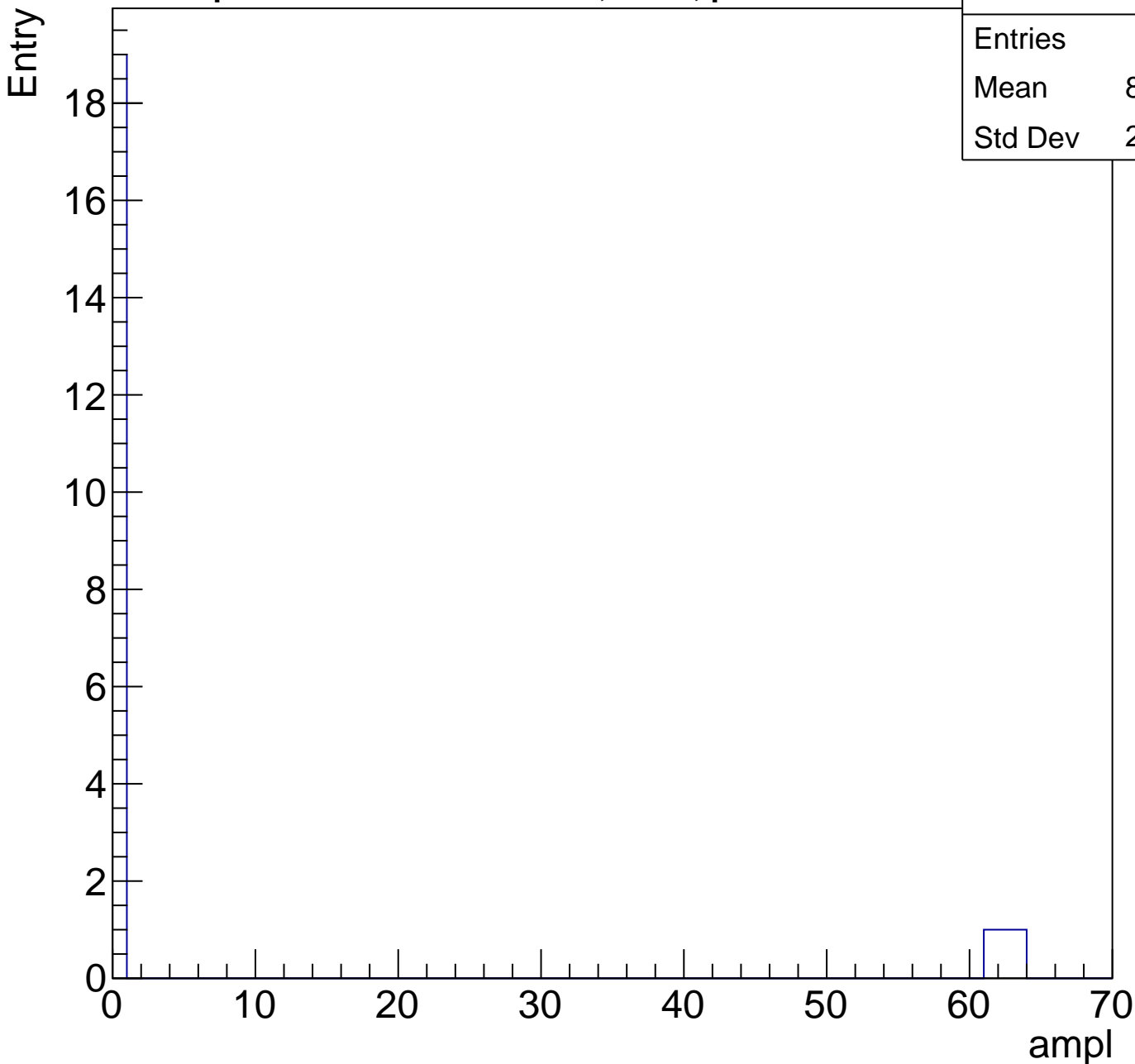
Entries	29
Mean	59.03
Std Dev	11.3



B1L103S, U1-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28



B1L103S, U1-ch11, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	18.72
Std Dev	13.45

Gaus mean : 28.1650

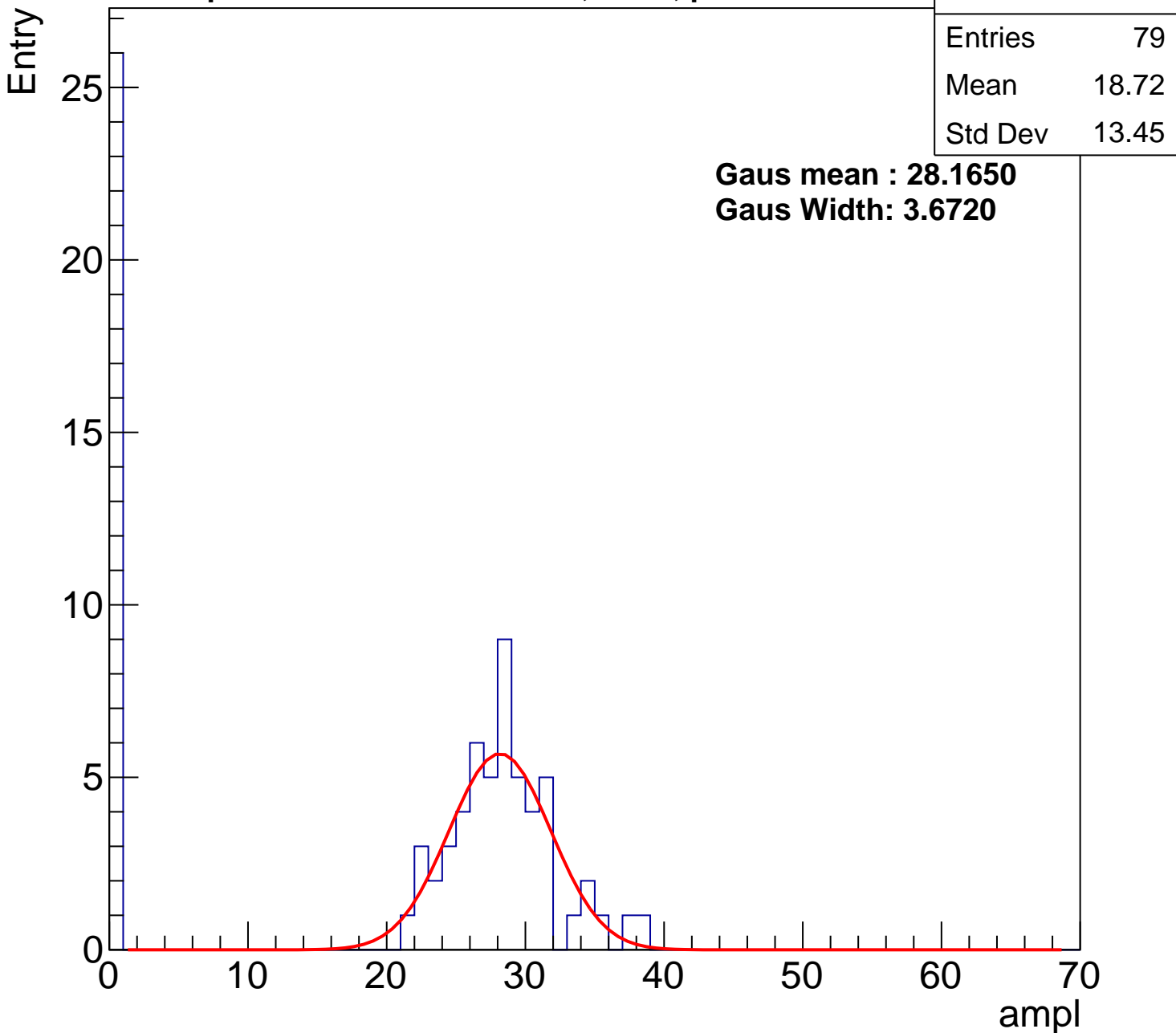
Gaus Width: 3.6720

Entry

25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



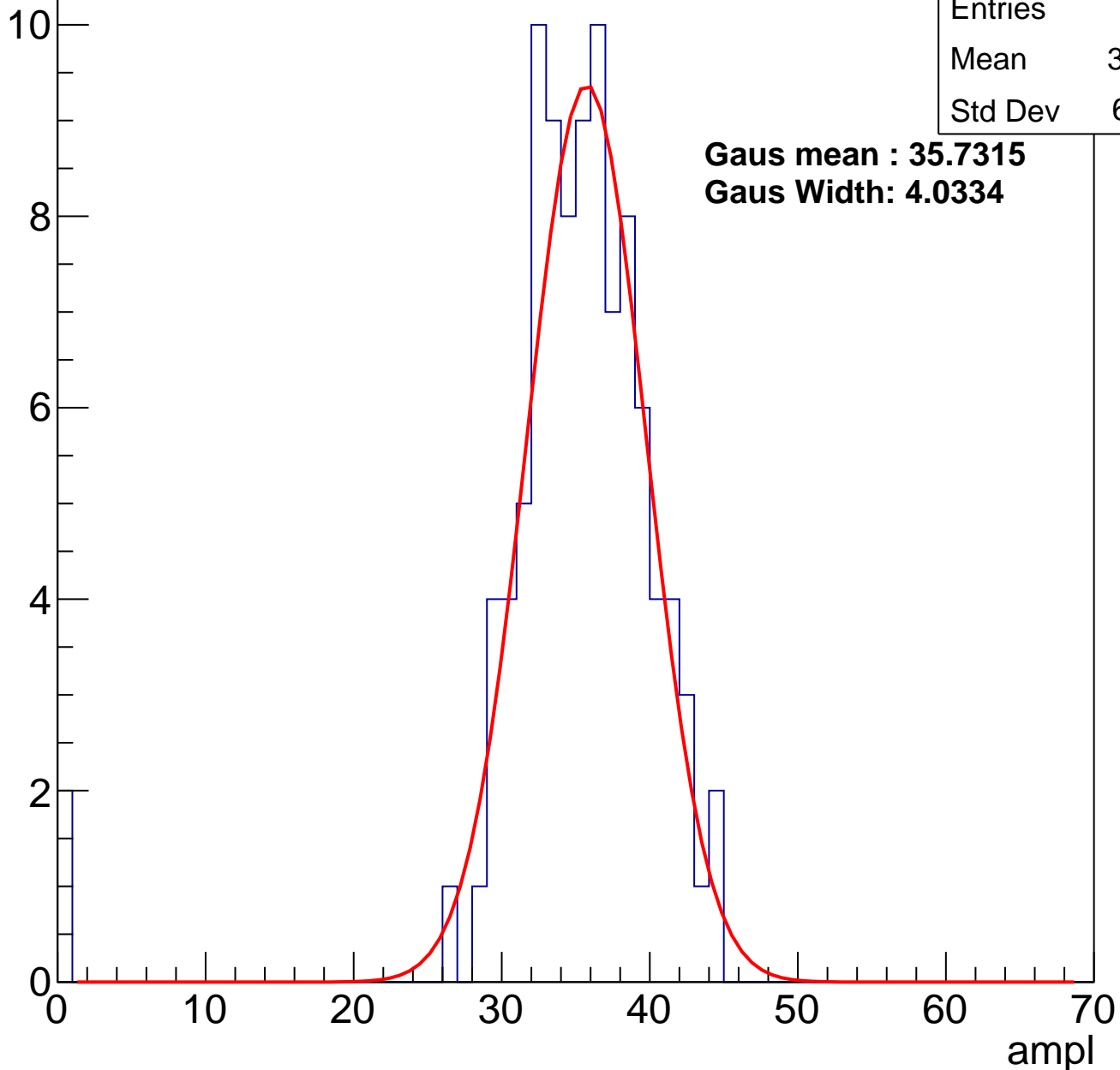
B1L103S, U1-ch11, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	34.56
Std Dev	6.261

Gaus mean : 35.7315
Gaus Width: 4.0334

Entry



B1L103S, U1-ch11, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	34.62
Std Dev	16.95

Gaus mean : 43.2014

Gaus Width: 3.1918

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

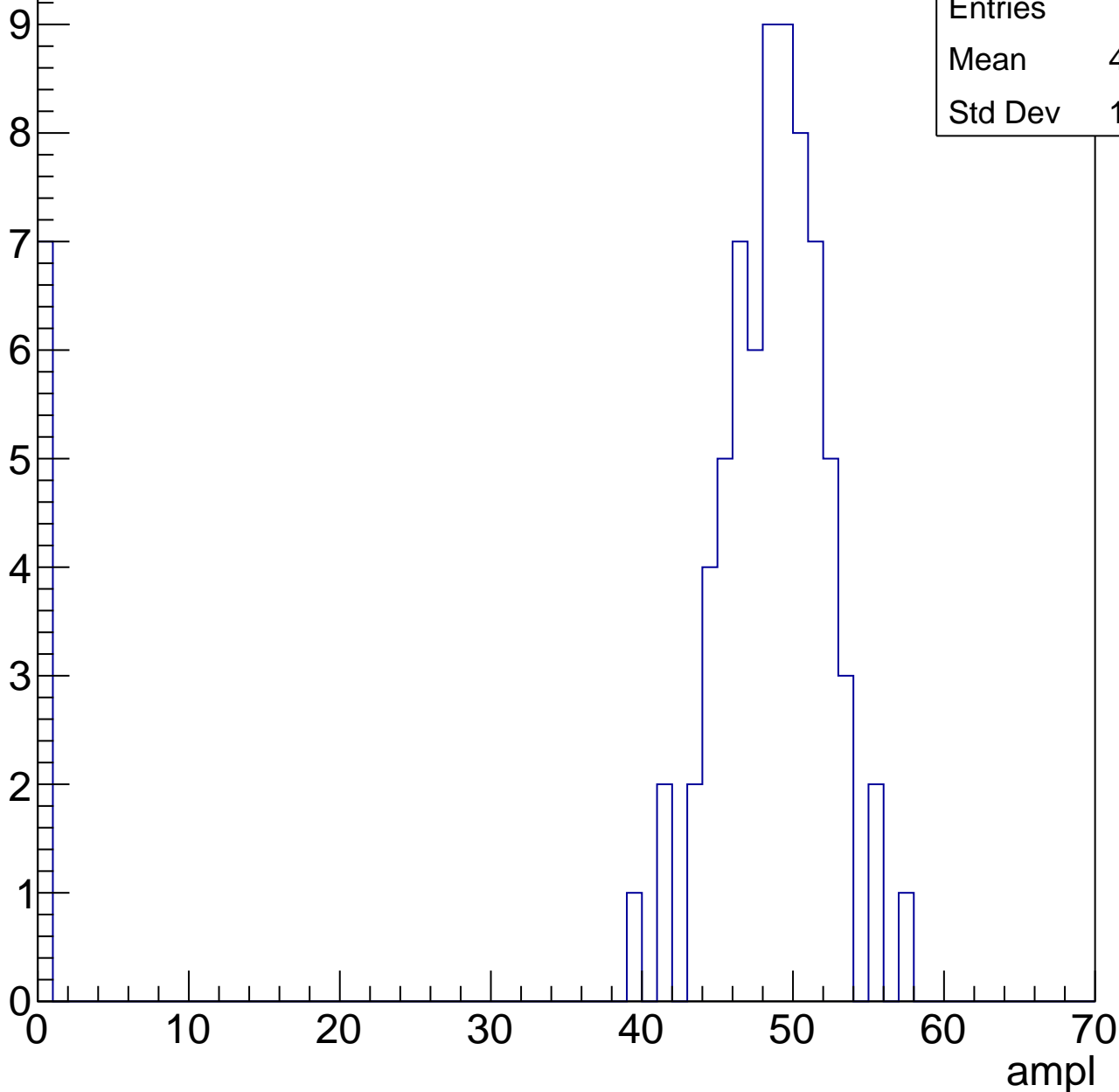
10

B1L103S, U1-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.95
Std Dev	14.17

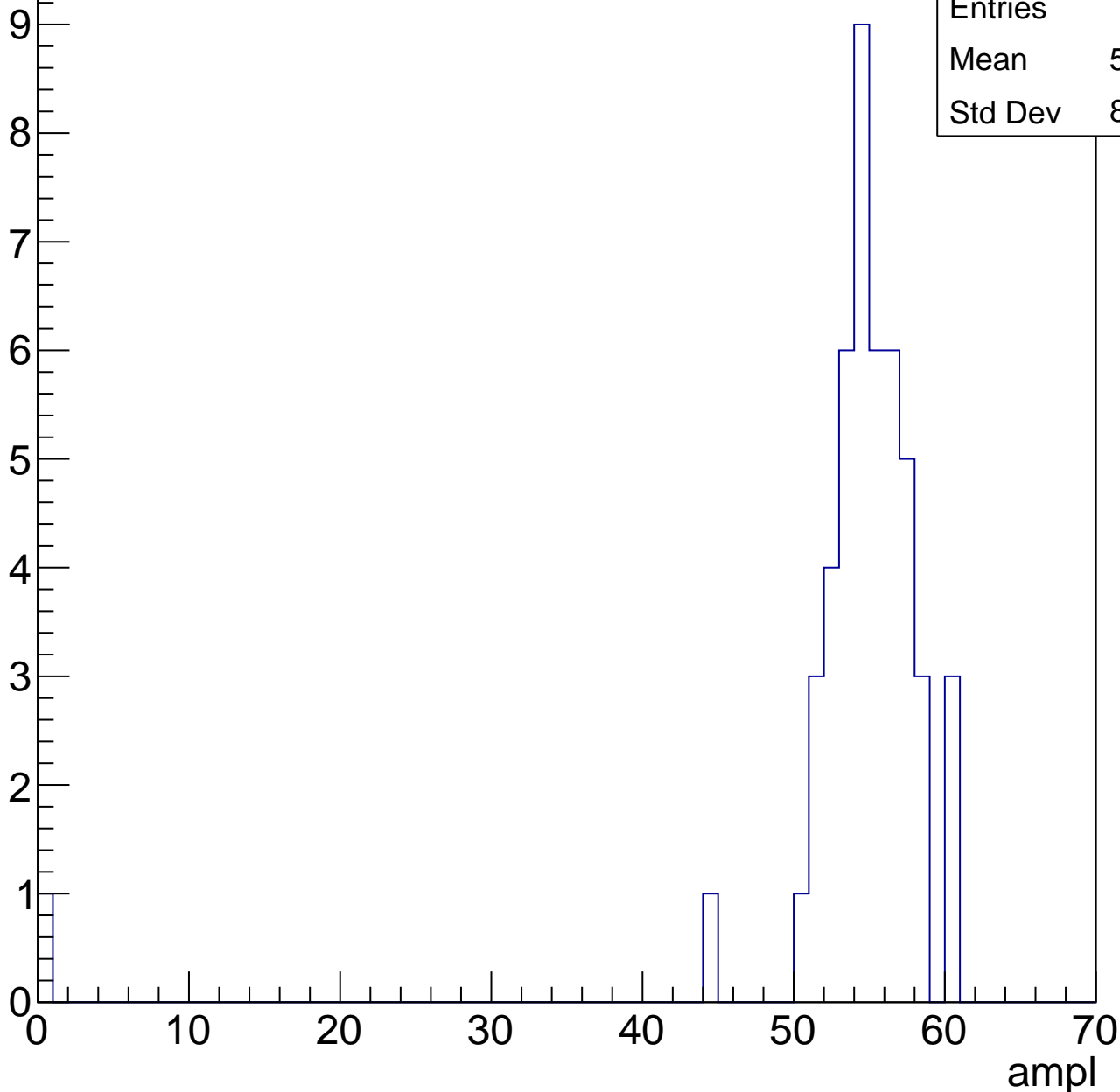


B1L103S, U1-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.42
Std Dev	8.286



B1L103S, U1-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

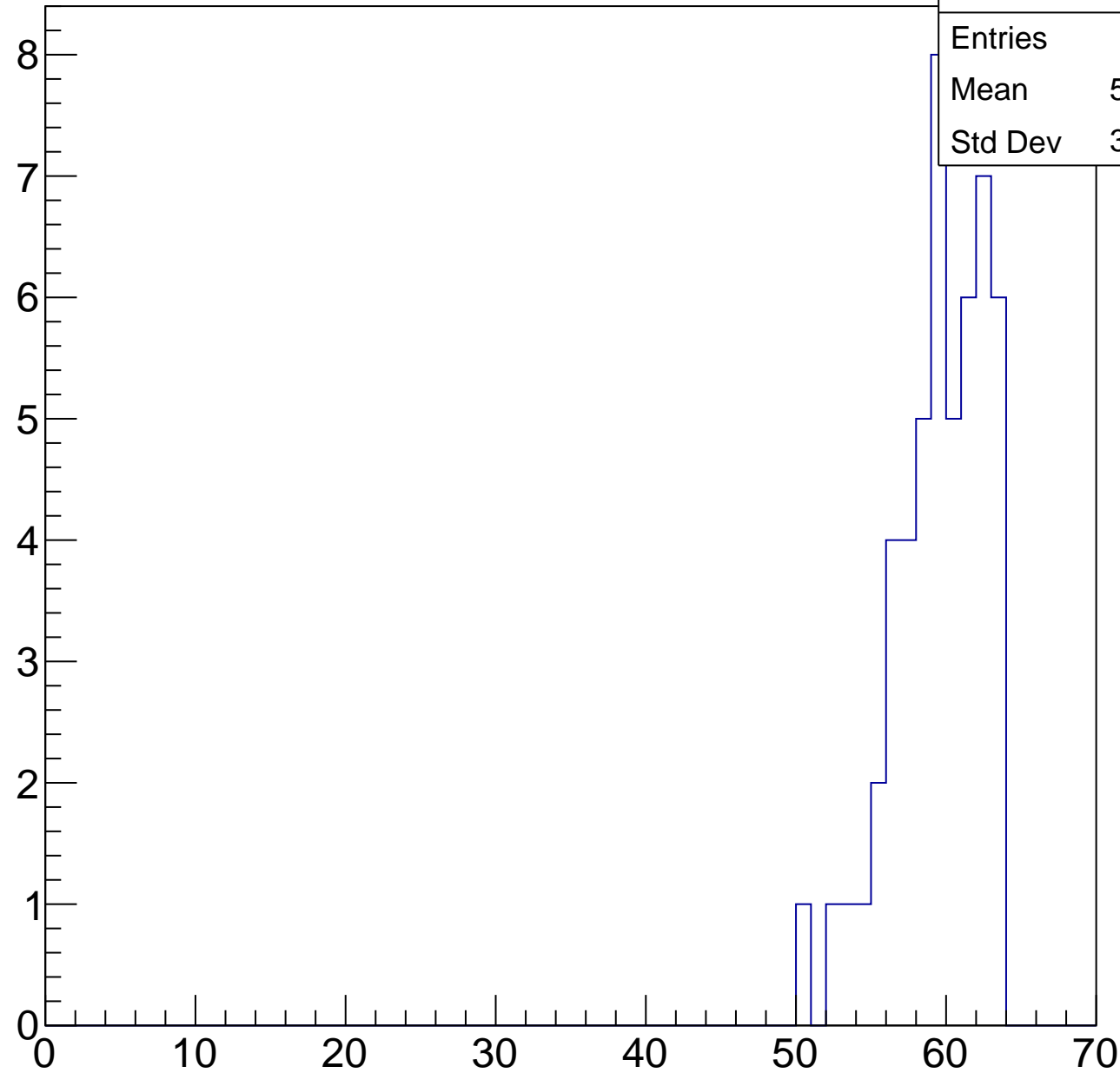
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.04
Std Dev	3.029

ampl

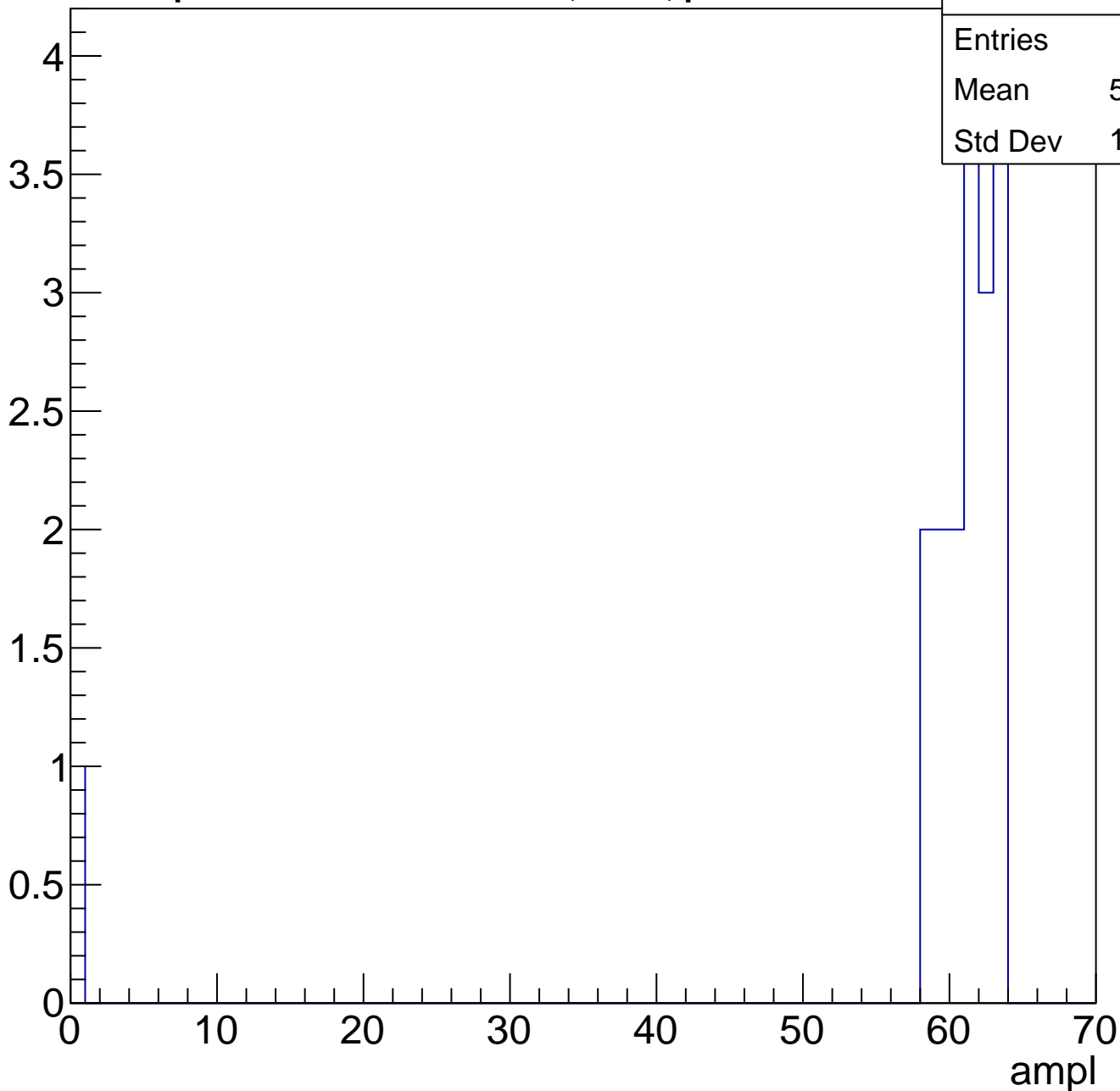
0 10 20 30 40 50 60 70



B1L103S, U1-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	57.56
Std Dev	14.05

B1L103S, U1-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U1-ch12, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	22.33
Std Dev	10.16

Gaus mean : 27.3414

Gaus Width: 3.3423

Entry

12

10

8

6

4

2

0

0

10

20

30

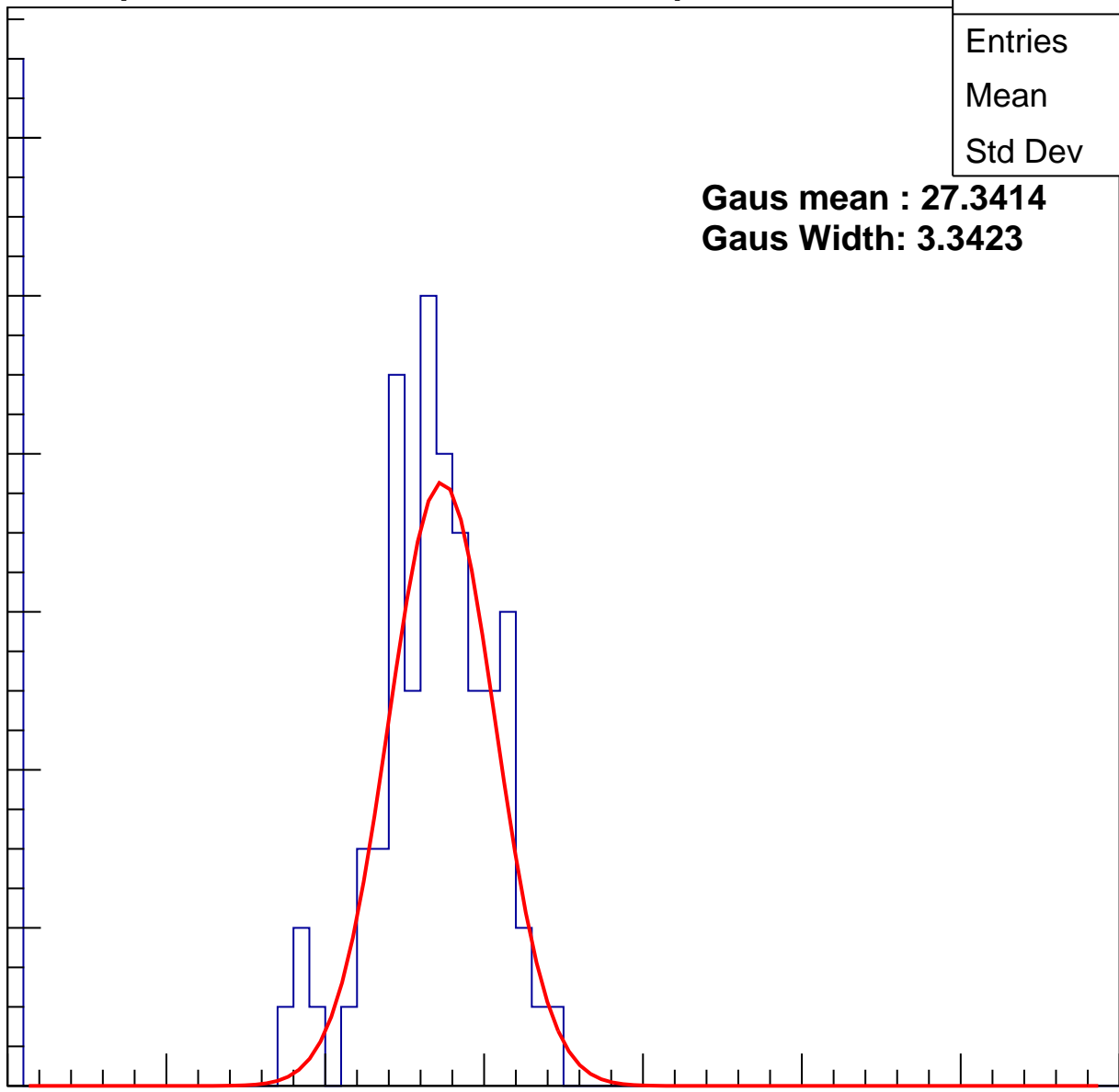
40

50

60

70

ampl



B1L103S, U1-ch12, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	33.33
Std Dev	3.15

Gaus mean : 34.2622

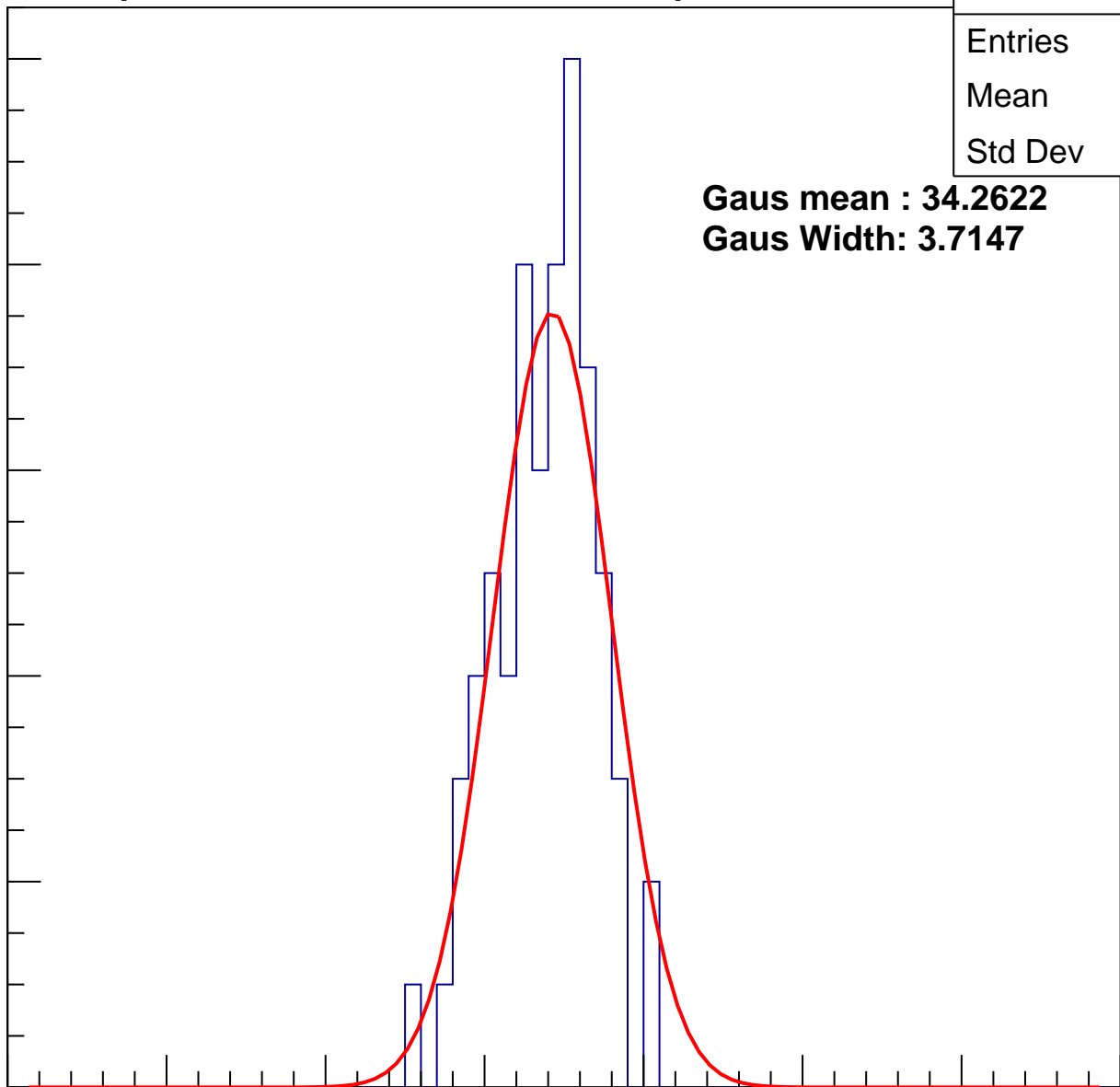
Gaus Width: 3.7147

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch12, adc2

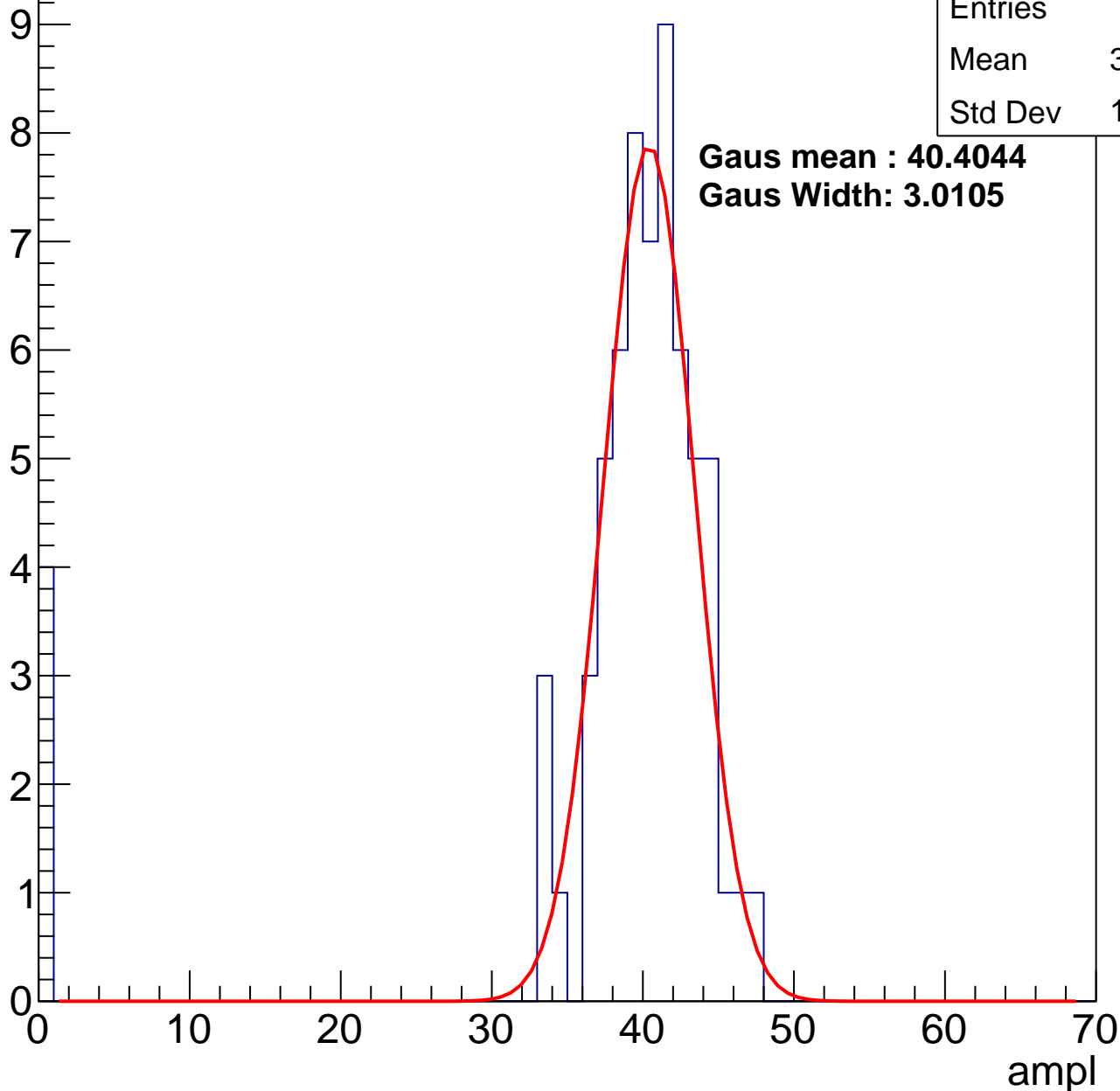
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	37.54
Std Dev	10.06

Gaus mean : 40.4044

Gaus Width: 3.0105

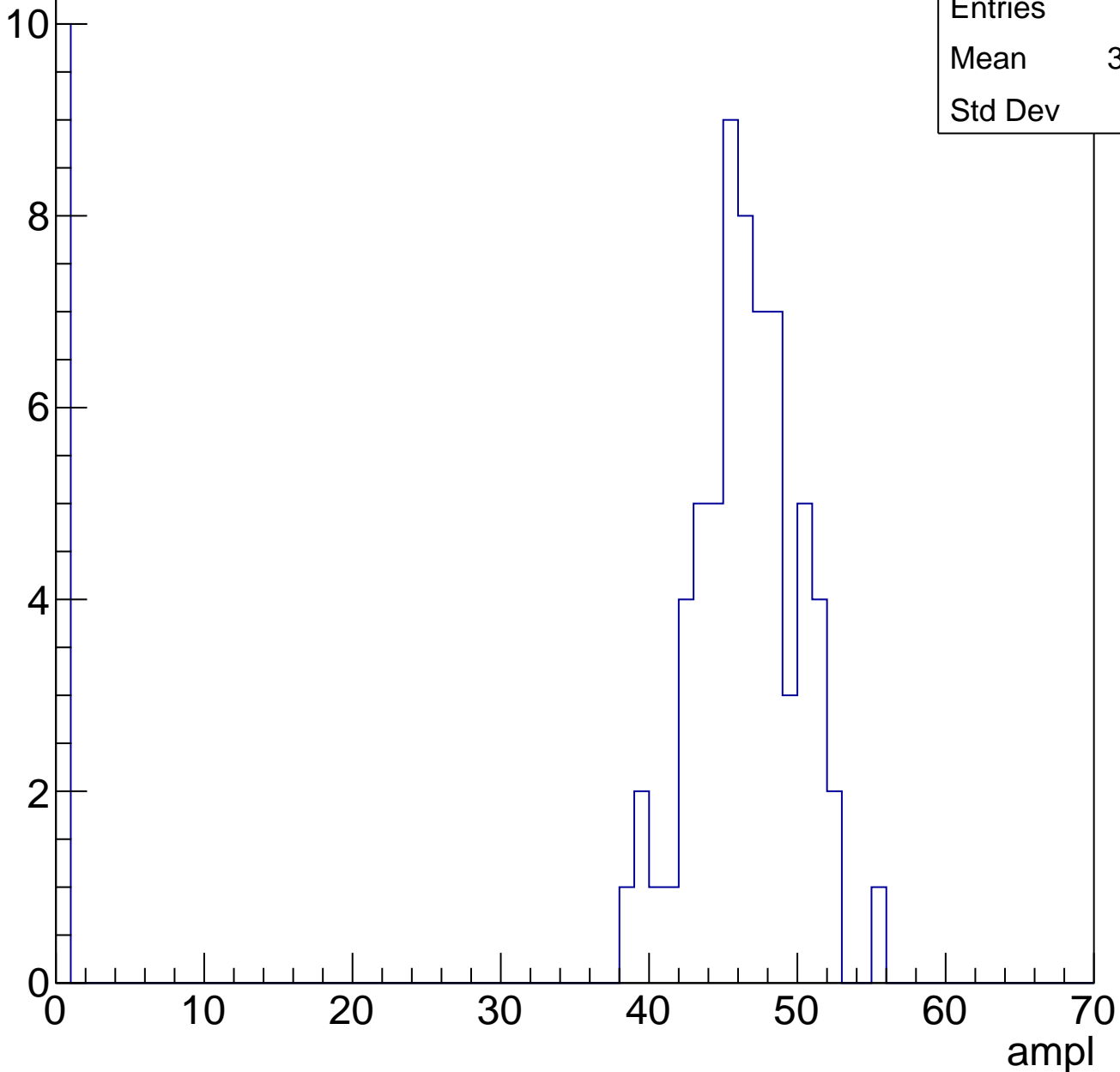


B1L103S, U1-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	39.97
Std Dev	16

Entry

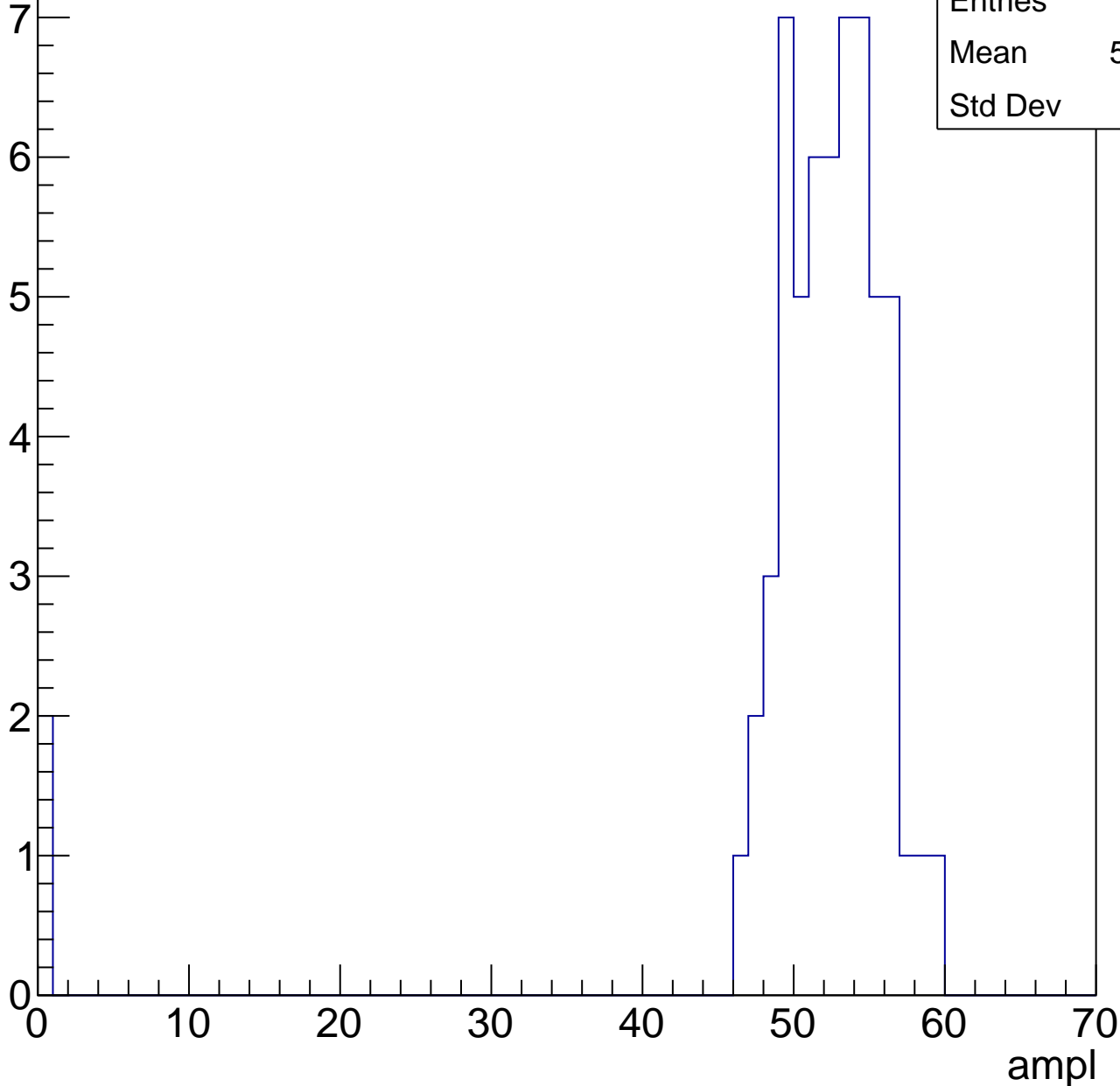


B1L103S, U1-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

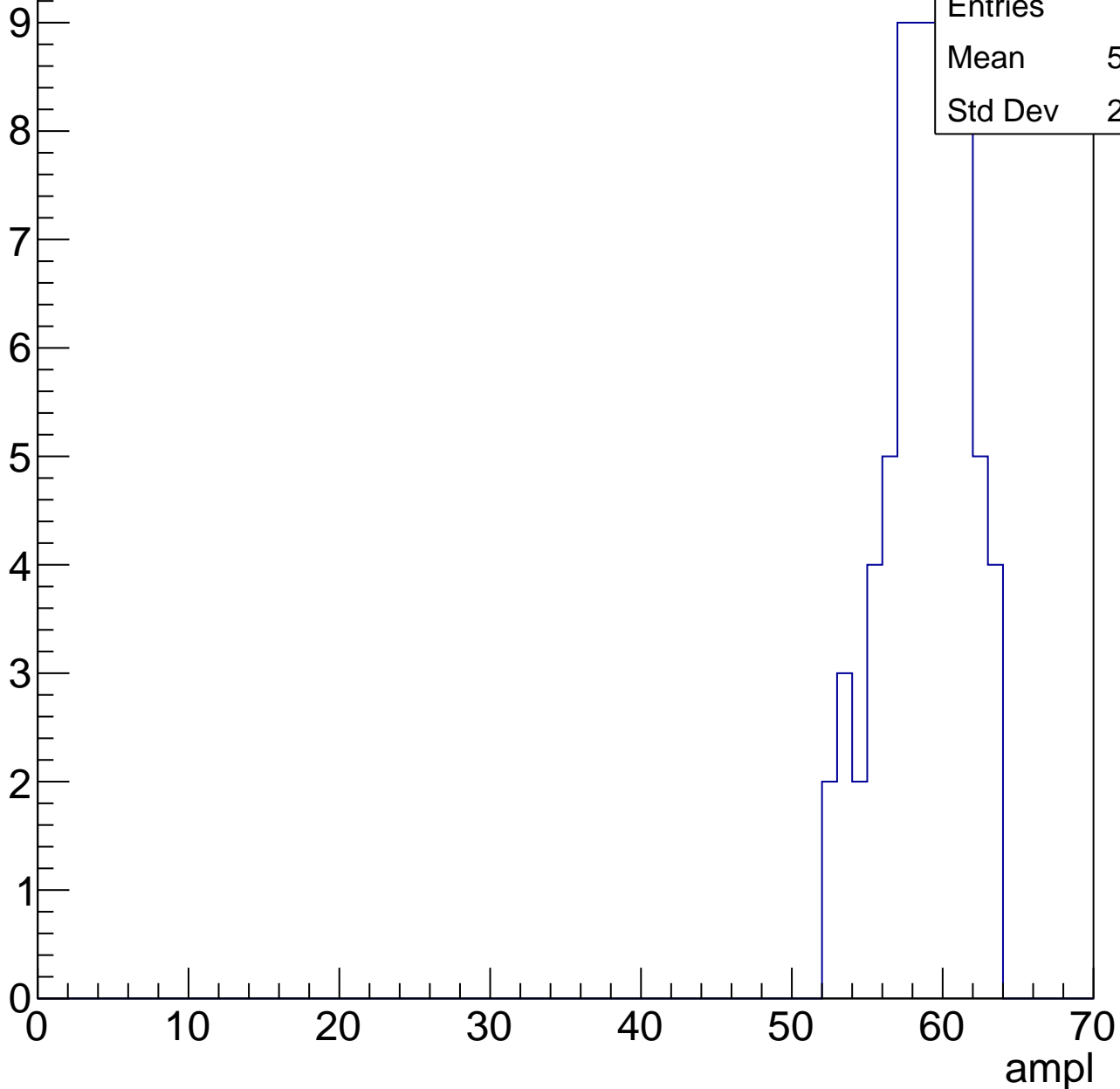
Entries	59
Mean	50.39
Std Dev	9.87



B1L103S, U1-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



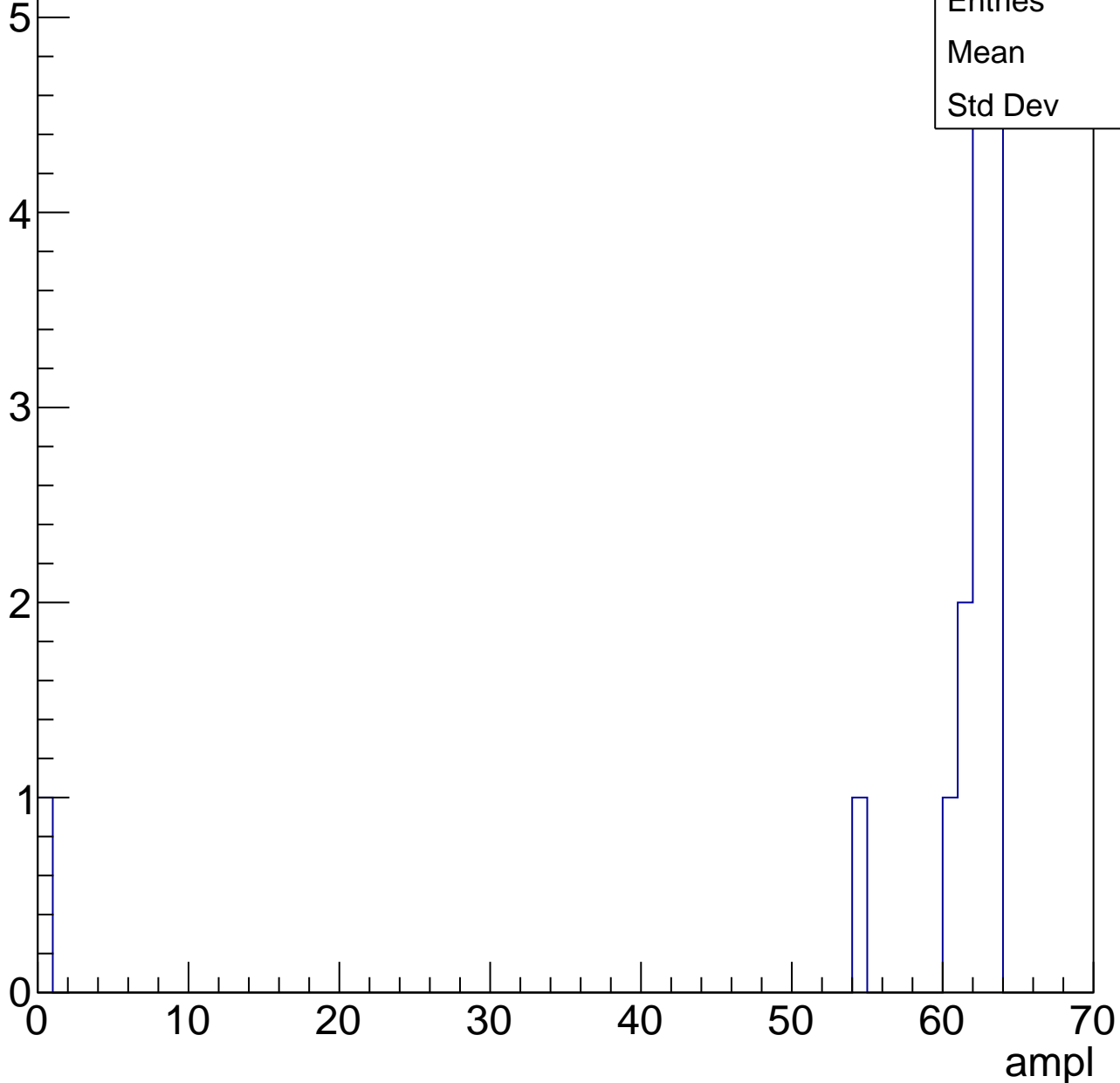
Entries	69
Mean	58.36
Std Dev	2.777

B1L103S, U1-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.4
Std Dev	15.5



B1L103S, U1-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.15
Std Dev	13.73

B1L103S, U1-ch13, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	24.23
Std Dev	11.24

Gaus mean : 29.8363

Gaus Width: 4.2311

Entry

12

10

8

6

4

2

0

0

10

20

30

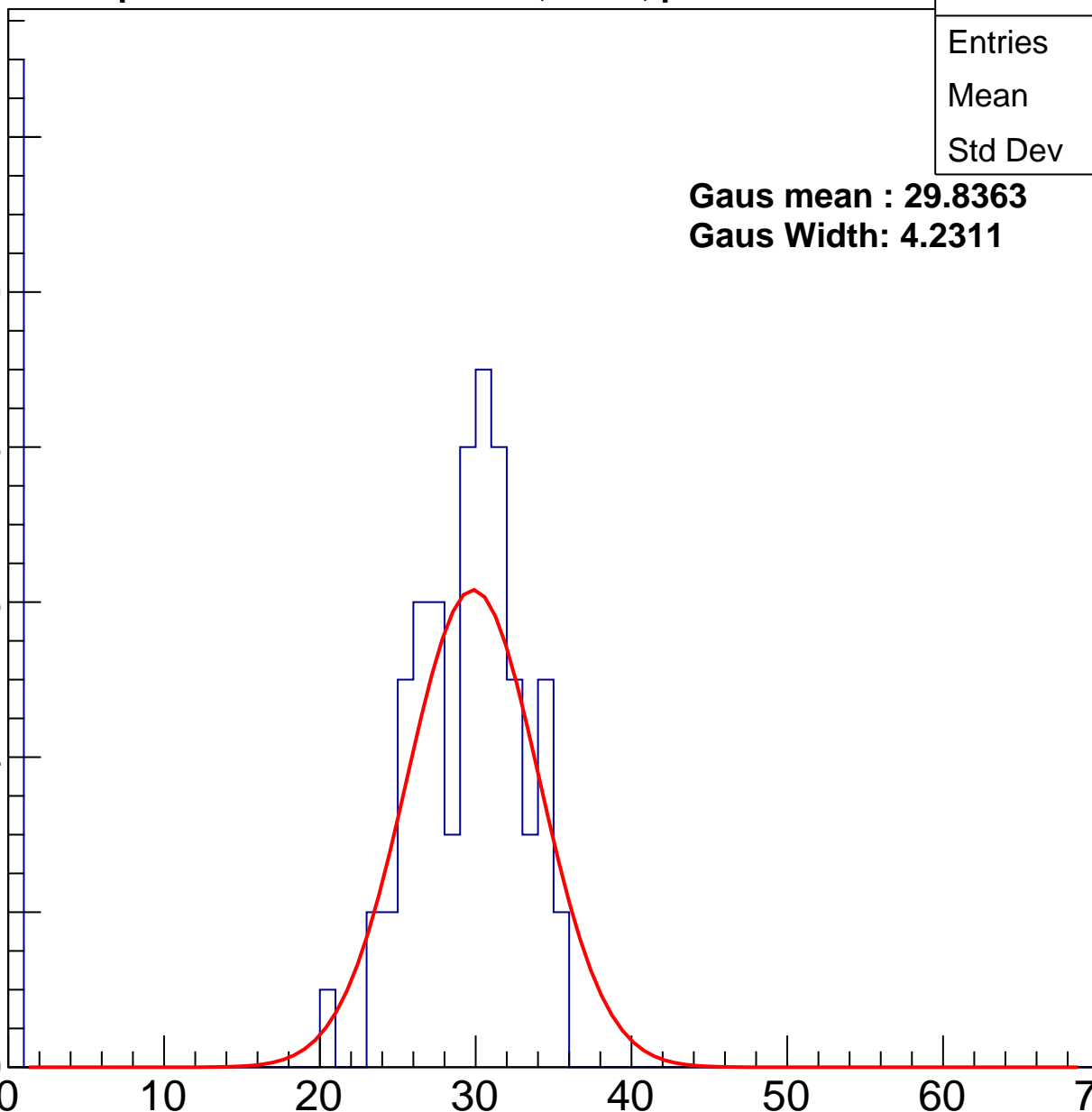
40

50

60

70

ampl



B1L103S, U1-ch13, adc1

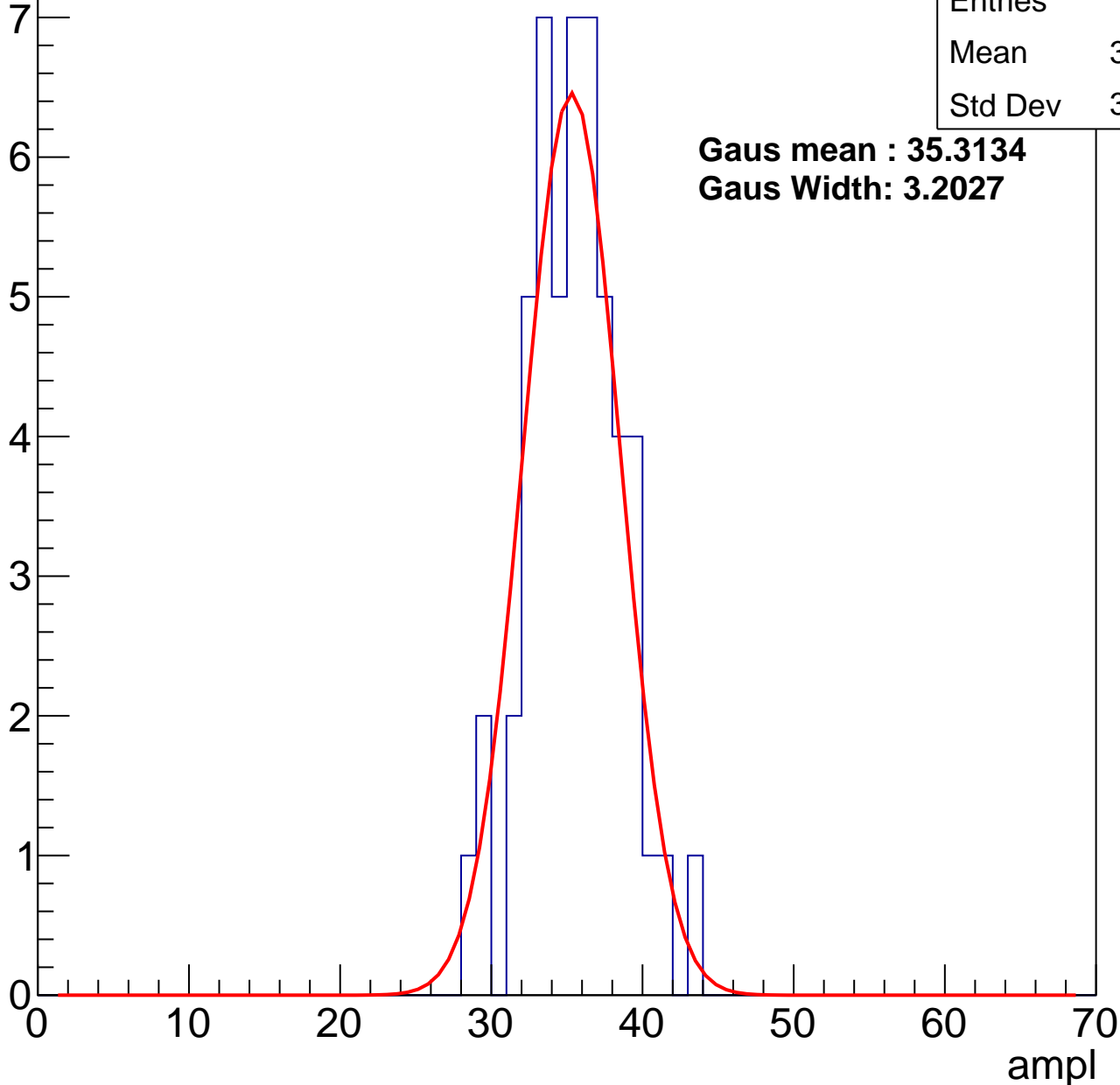
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	35.06
Std Dev	3.047

Gaus mean : 35.3134

Gaus Width: 3.2027



B1L103S, U1-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	35.53
Std Dev	15.35

Gaus mean : 41.9104

Gaus Width: 3.7854

Entry

12

10

8

6

4

2

0

0

10

20

30

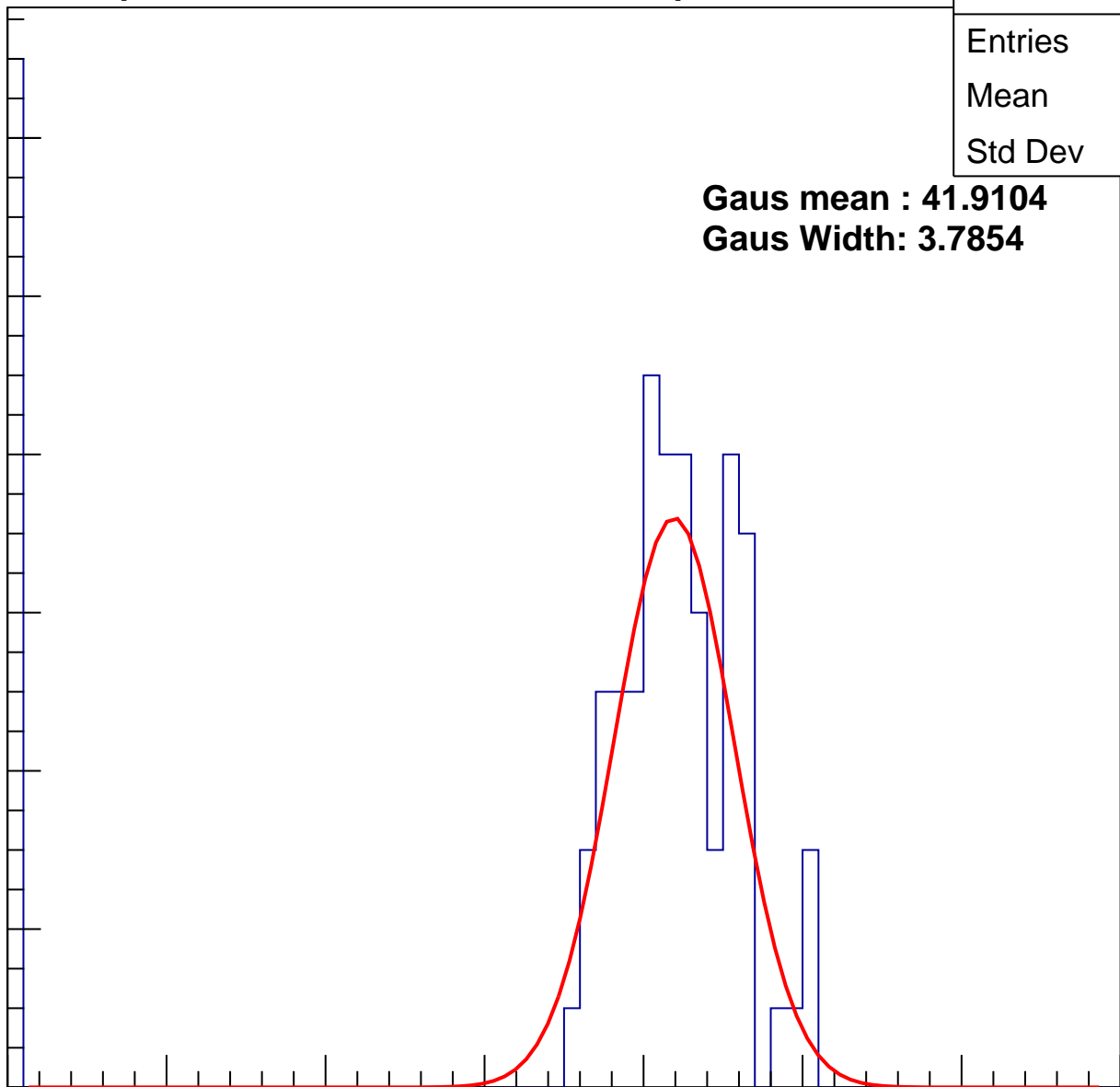
40

50

60

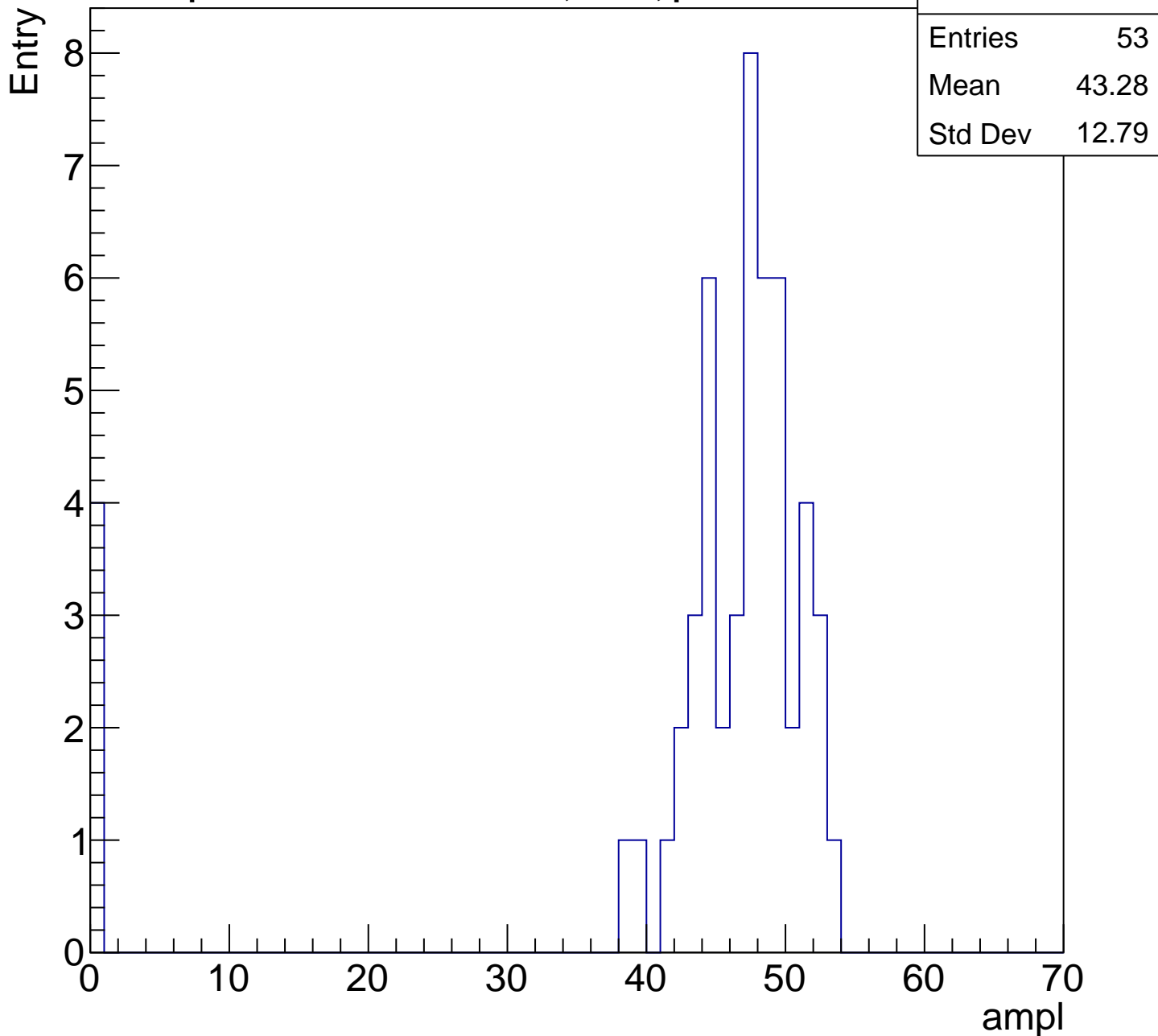
70

ampl



B1L103S, U1-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

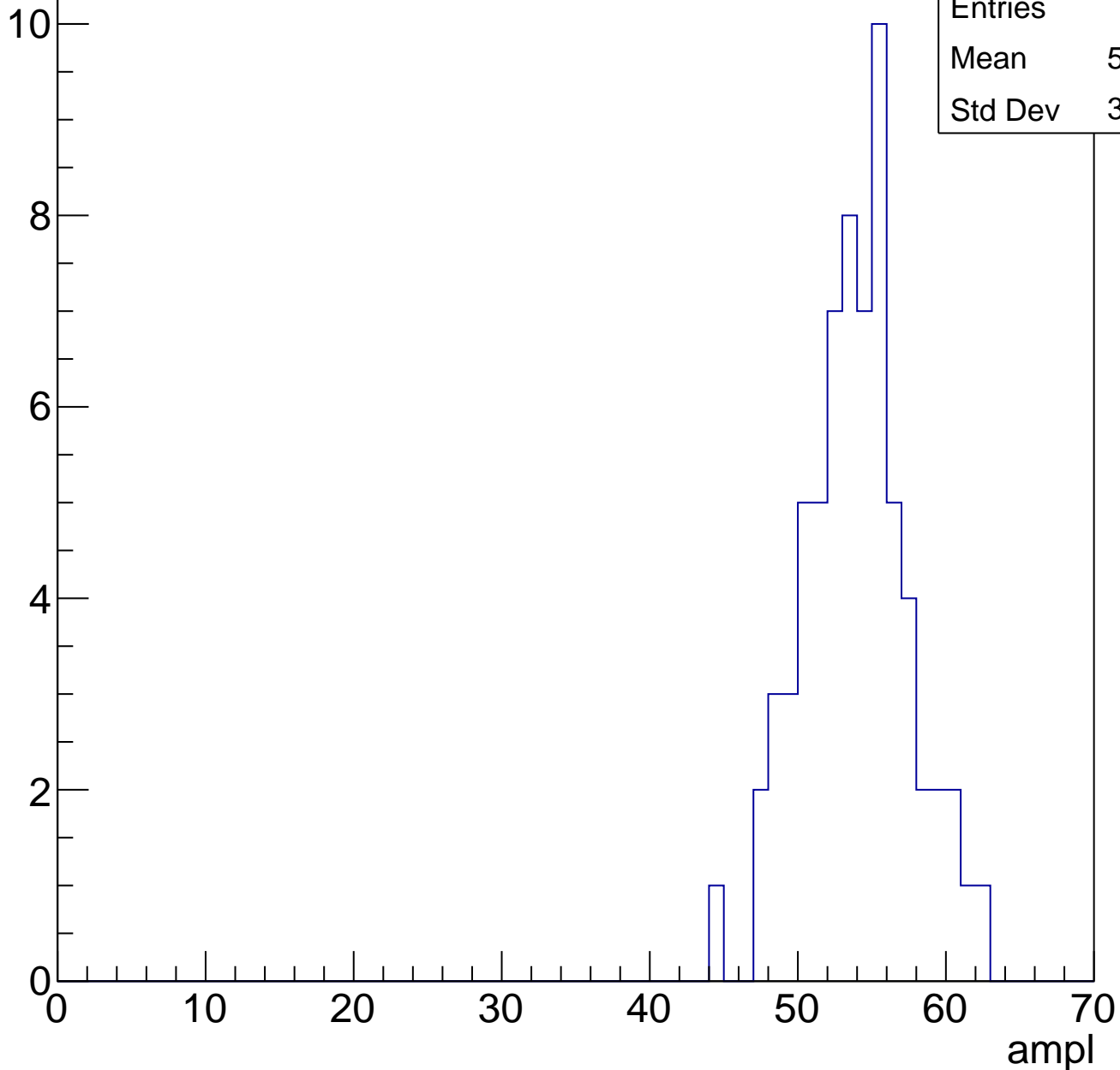


B1L103S, U1-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	53.46
Std Dev	3.546

Entry

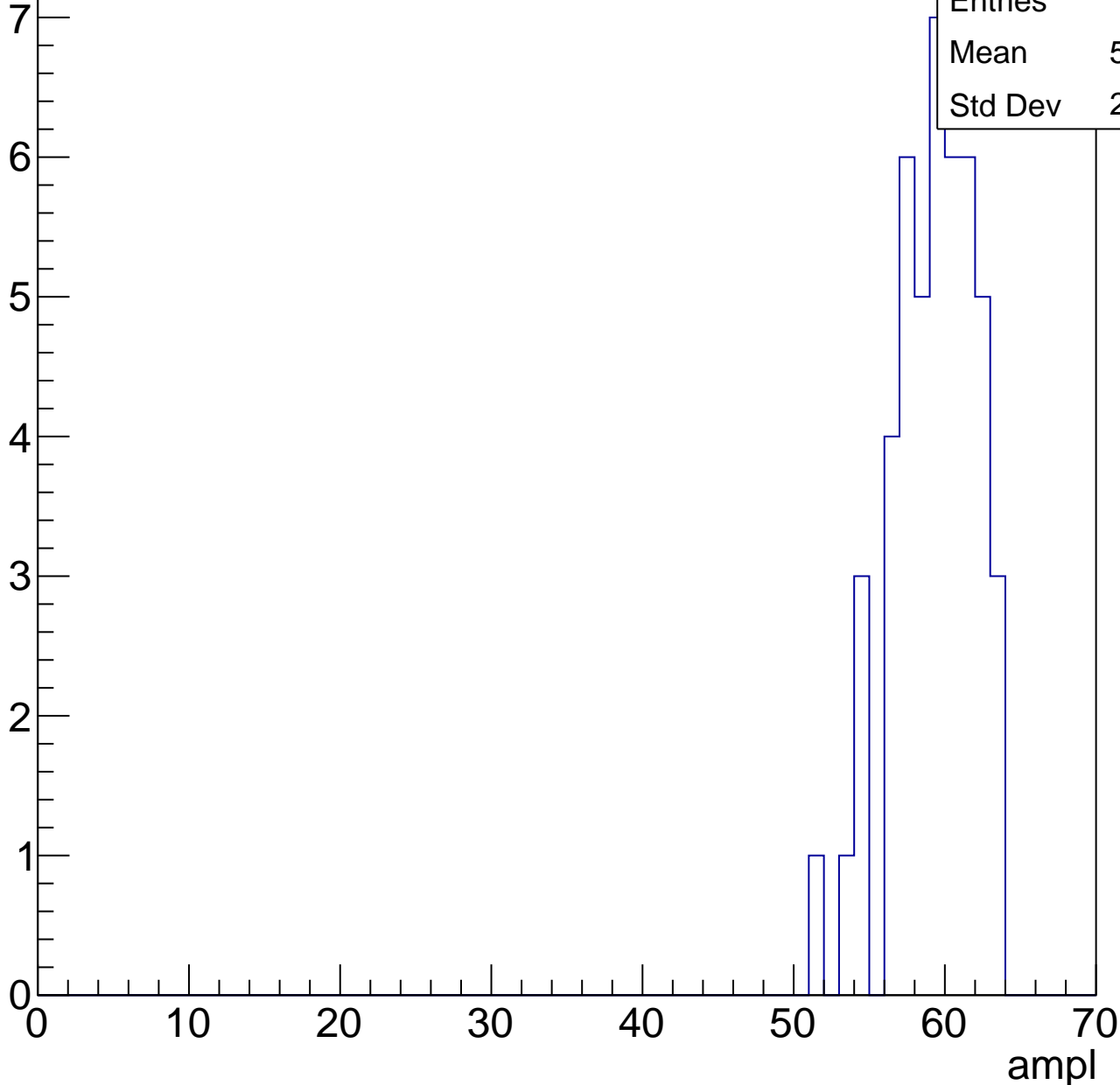


B1L103S, U1-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.72
Std Dev	2.765

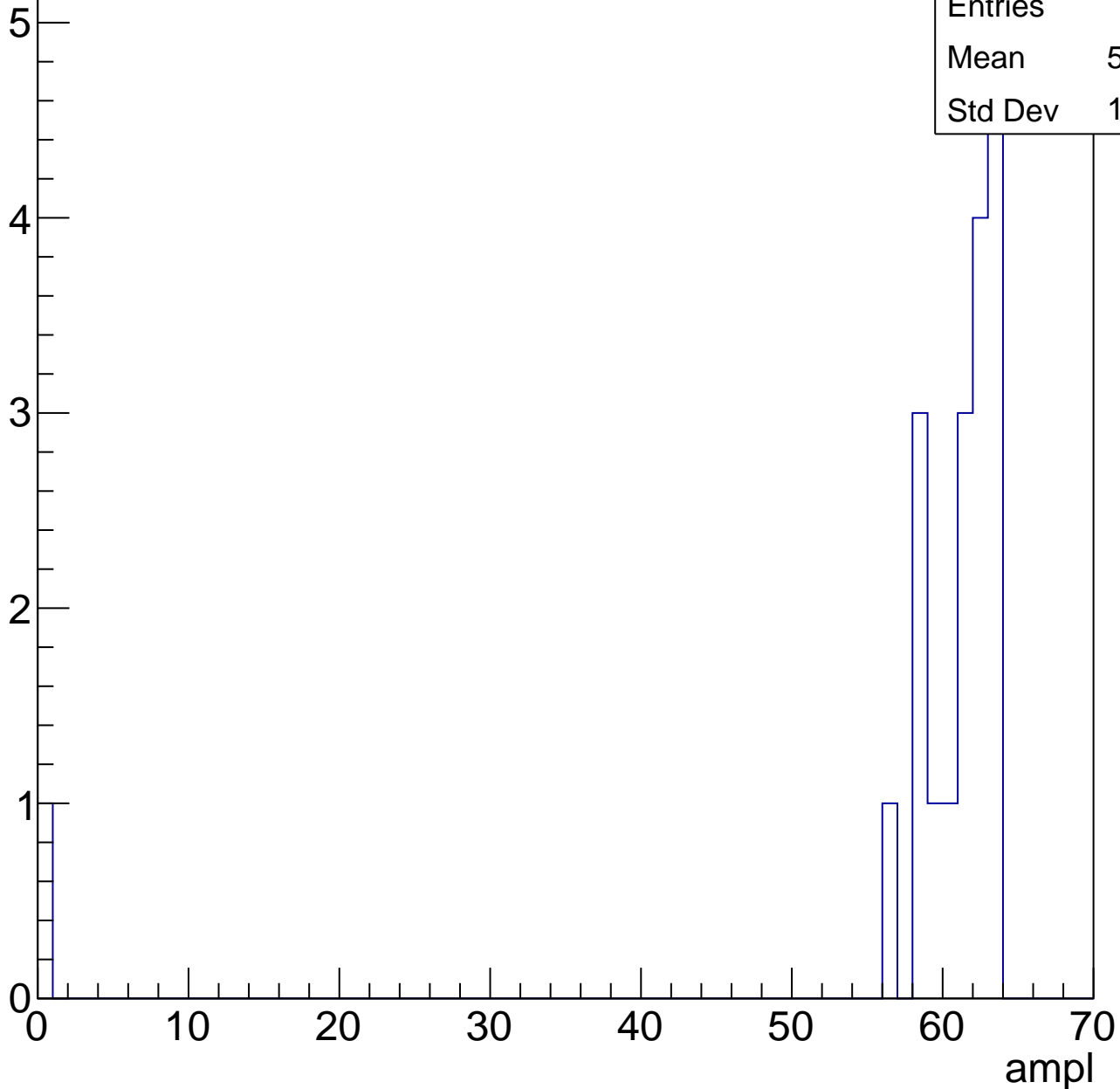


B1L103S, U1-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	57.63
Std Dev	13.74

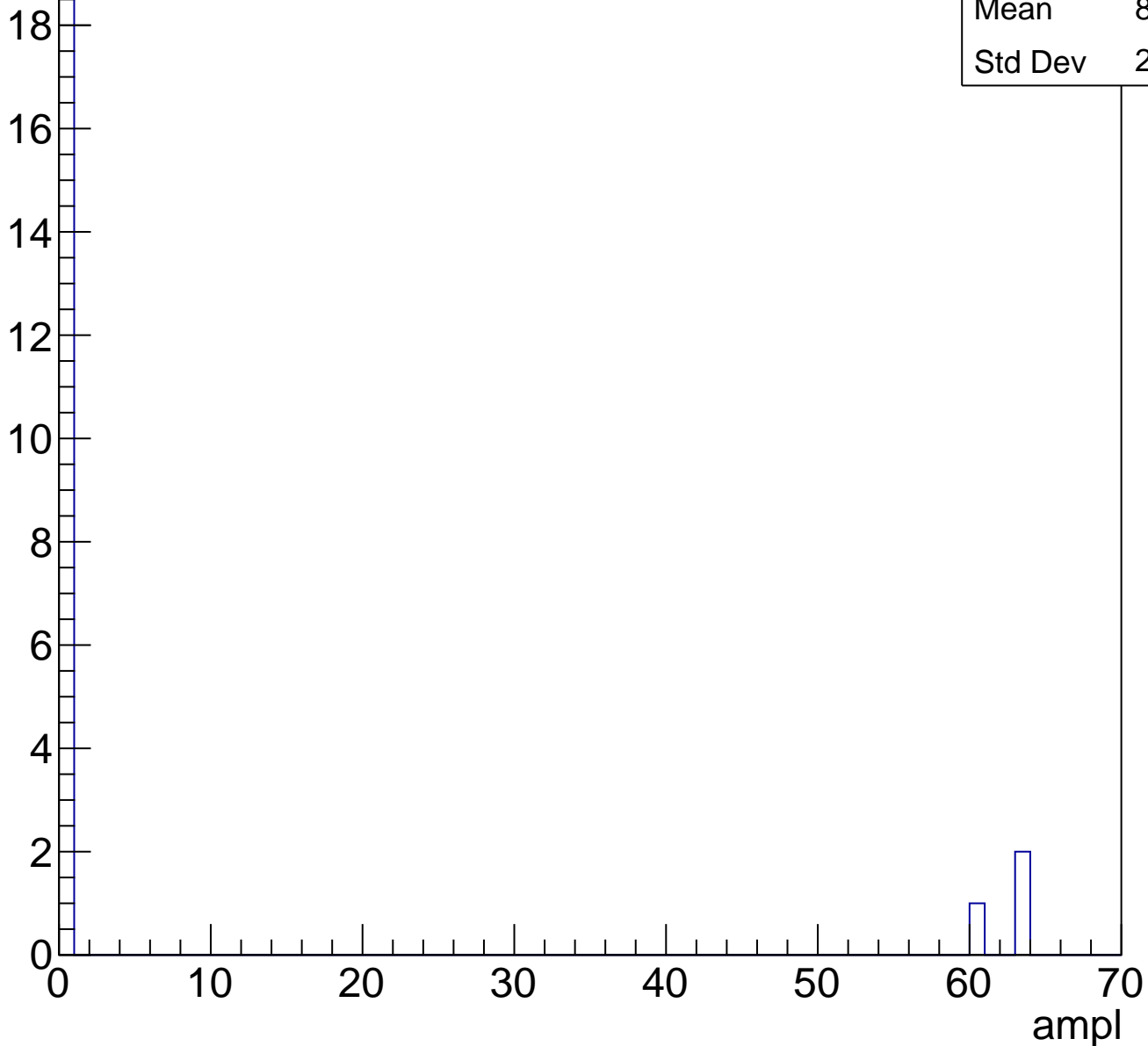


B1L103S, U1-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U1-ch14, adc0

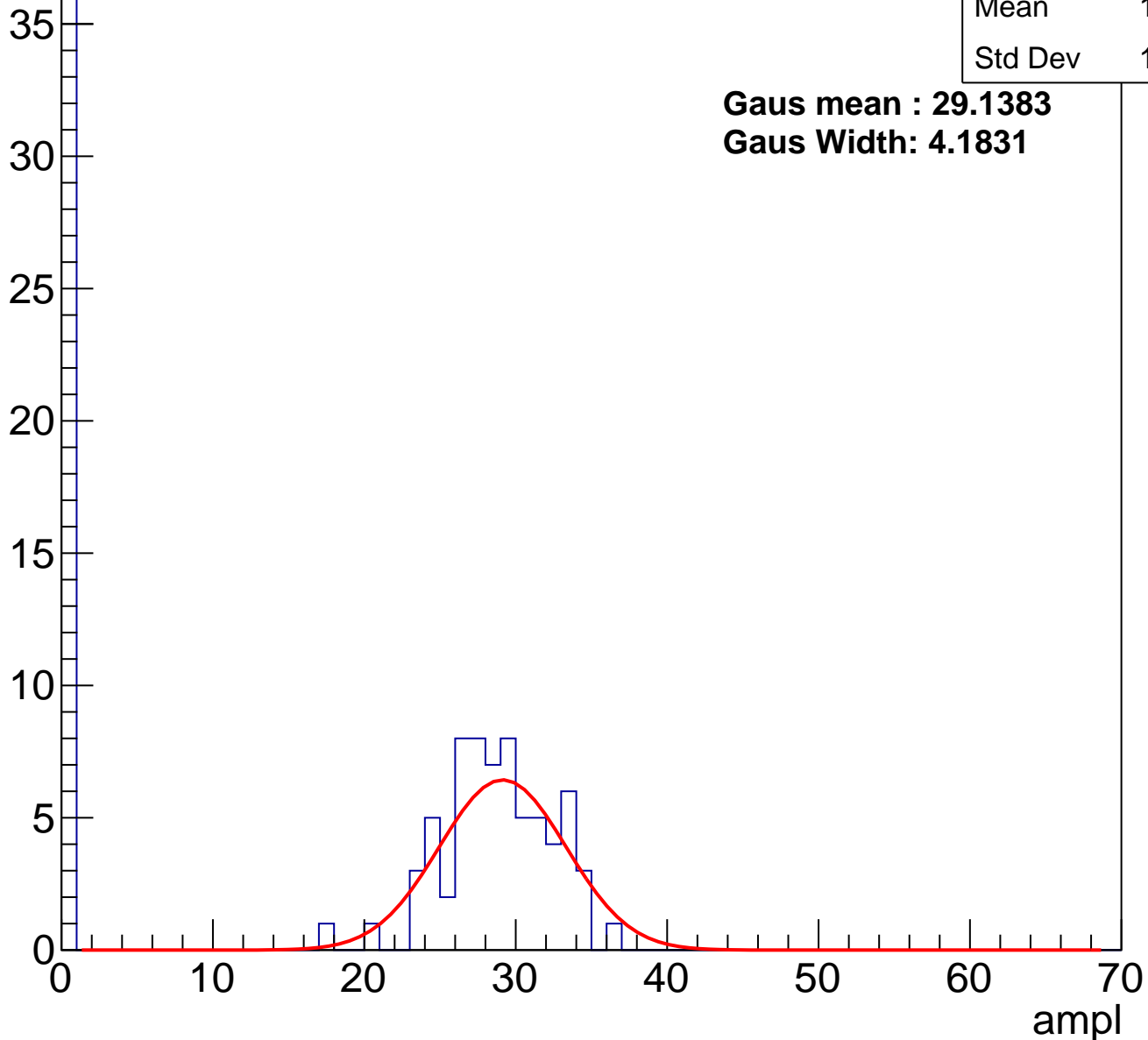
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	18.24
Std Dev	13.85

Gaus mean : 29.1383

Gaus Width: 4.1831

Entry



B1L103S, U1-ch14, adc1

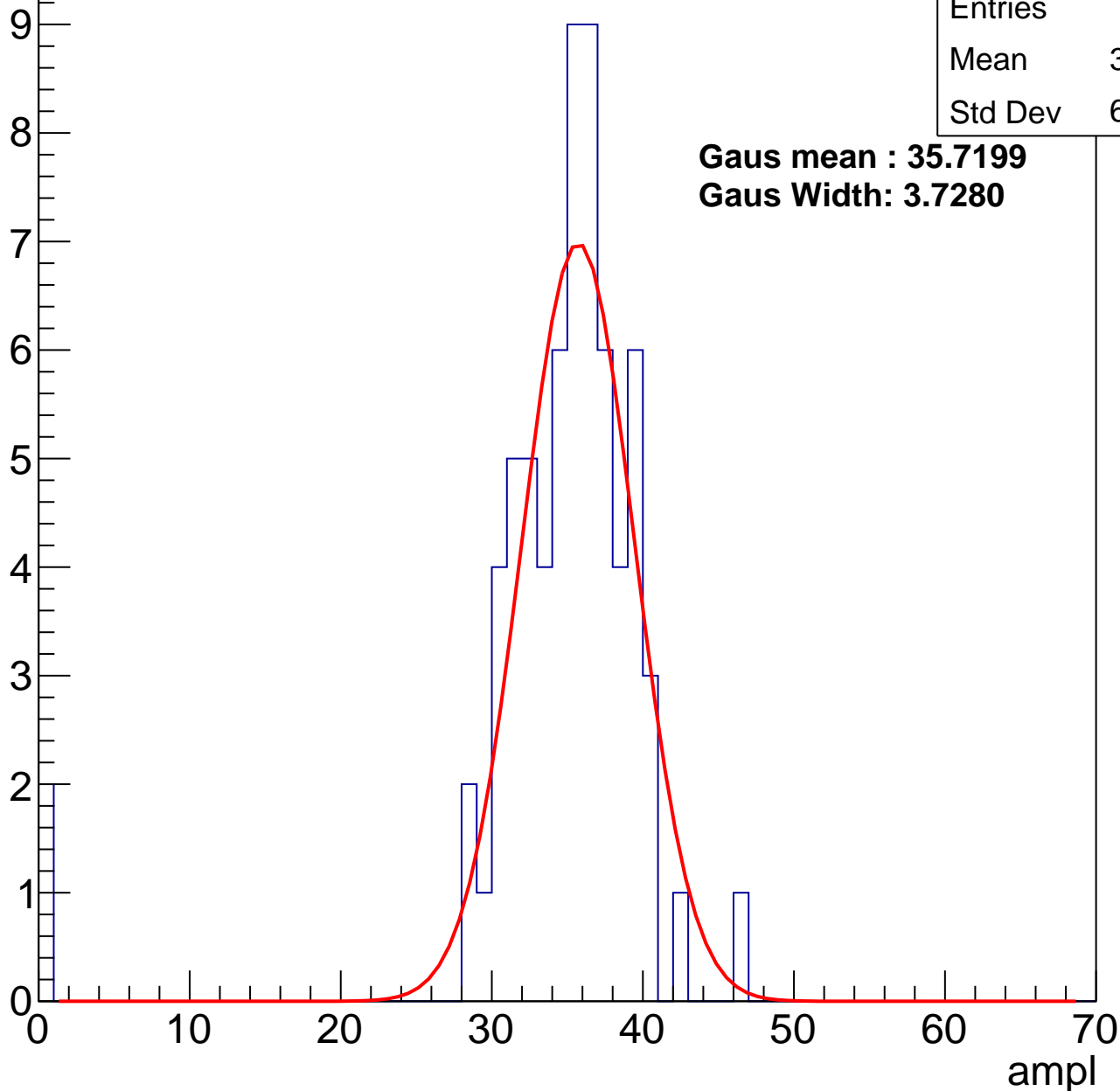
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.99
Std Dev	6.829

Gaus mean : 35.7199

Gaus Width: 3.7280



B1L103S, U1-ch14, adc2

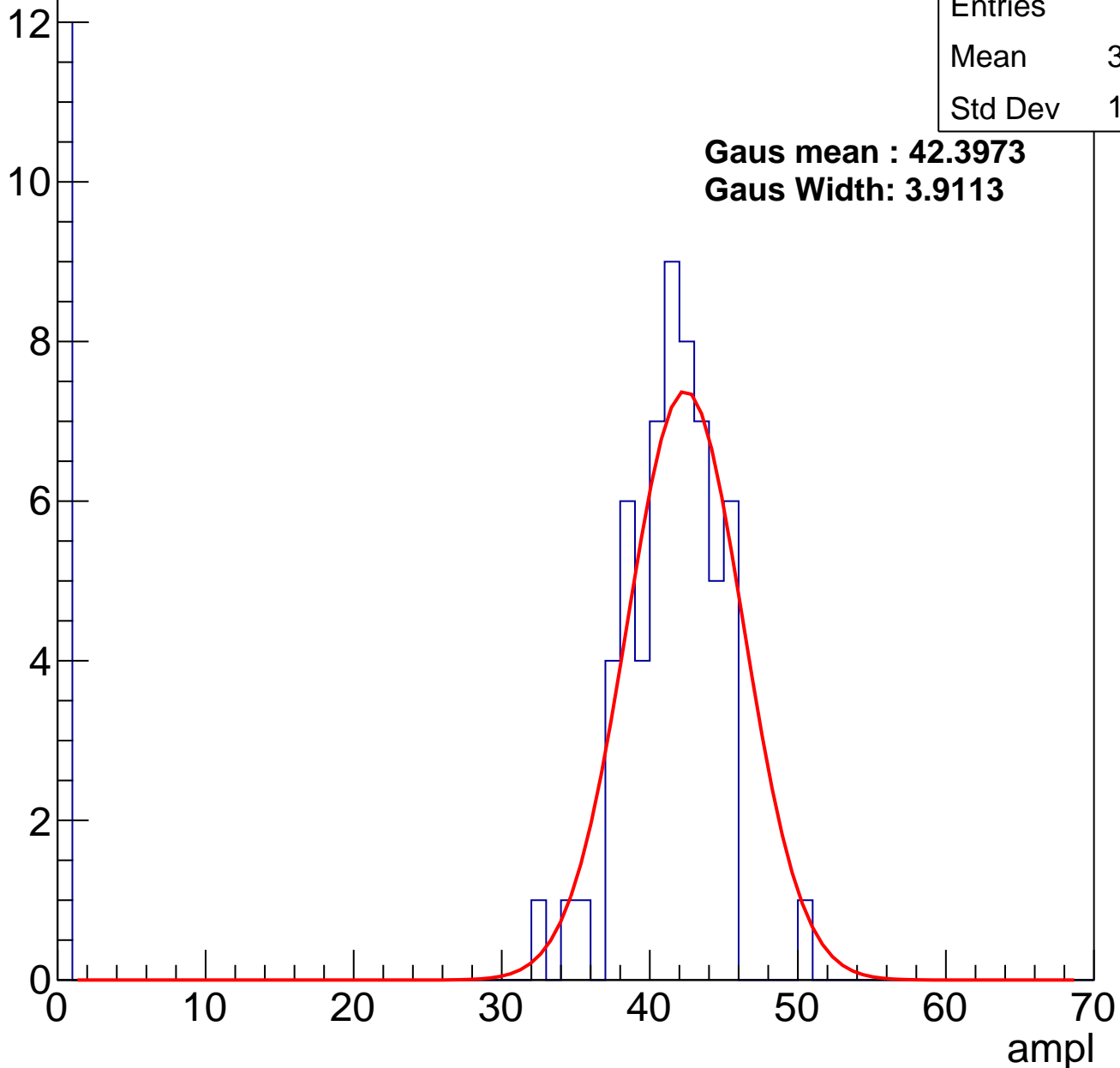
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.15
Std Dev	15.53

Gaus mean : 42.3973

Gaus Width: 3.9113

Entry

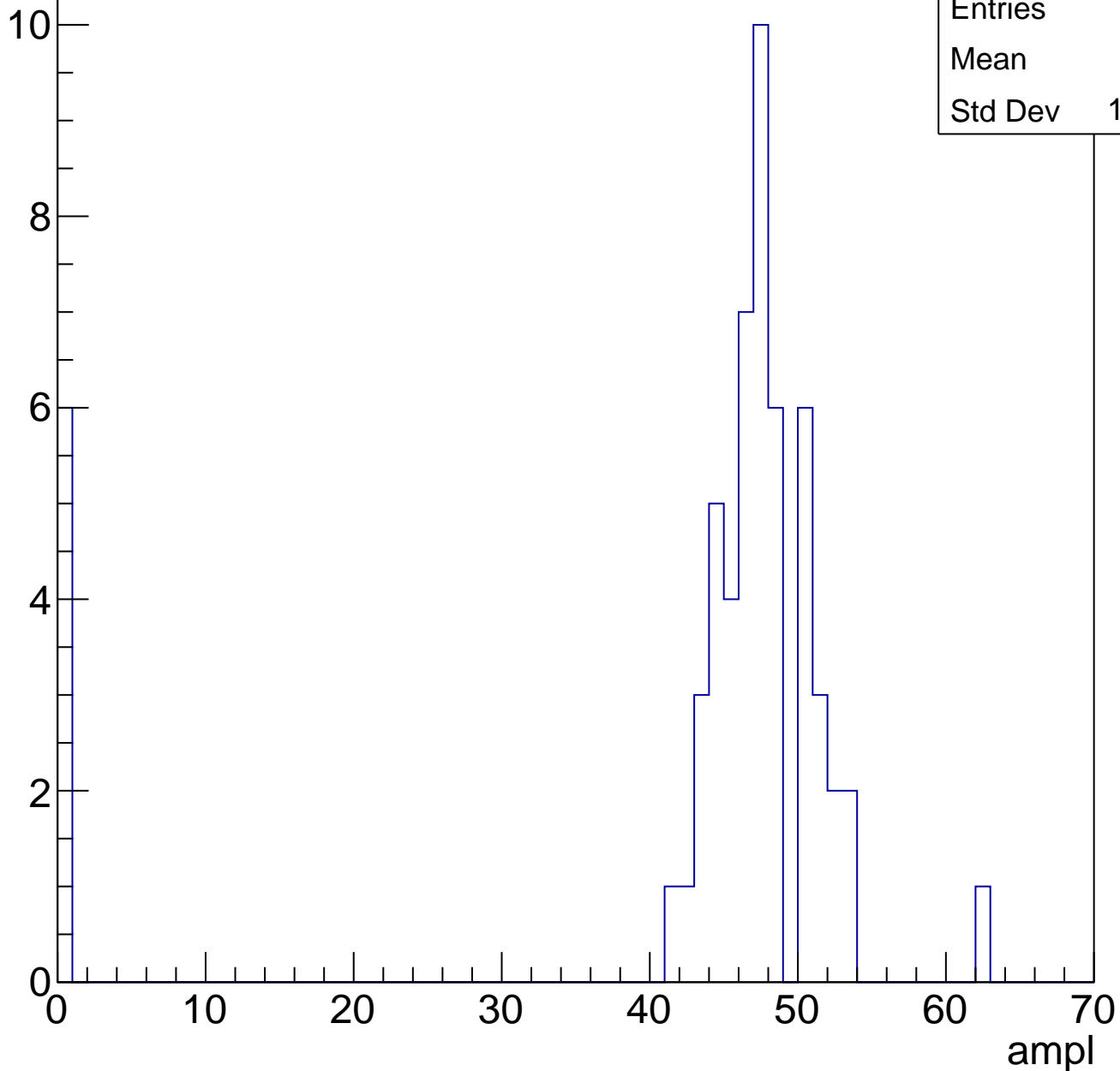


B1L103S, U1-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	57
Mean	42.4
Std Dev	14.92

Entry



B1L103S, U1-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	53.28
Std Dev	3.42

Entry

10

8

6

4

2

0

0

10

20

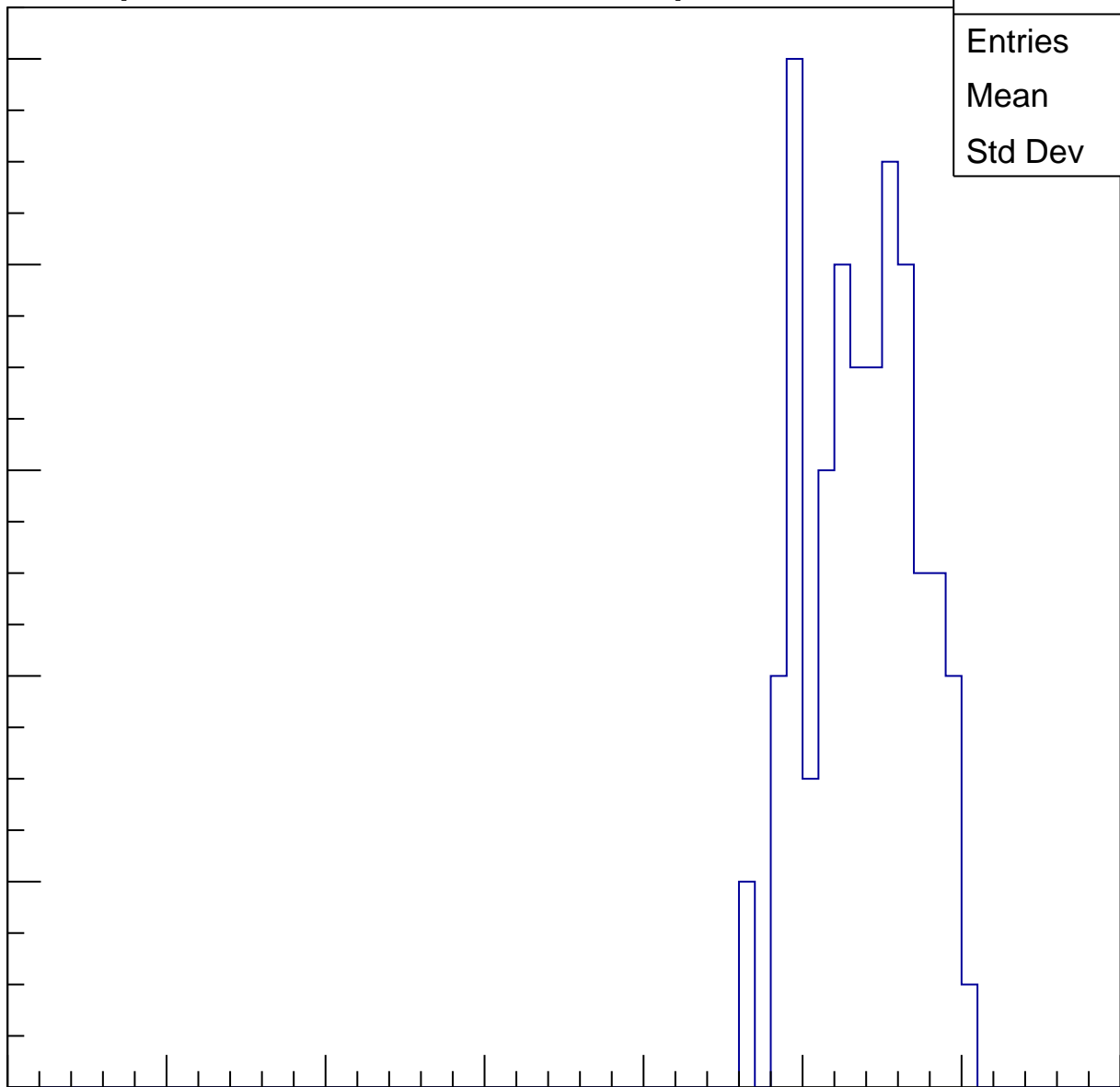
30

40

50

60

ampl

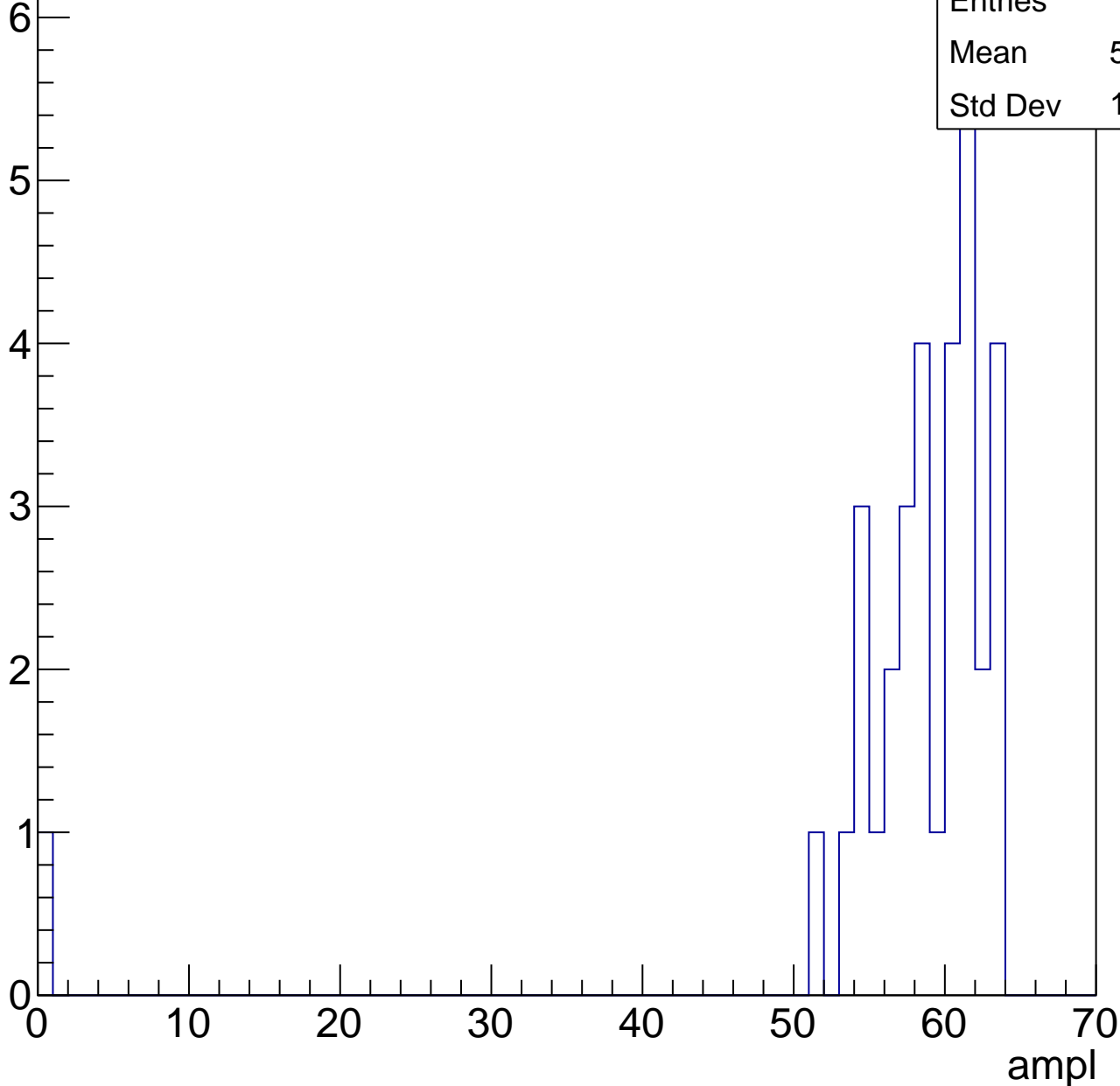


B1L103S, U1-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	56.88
Std Dev	10.54

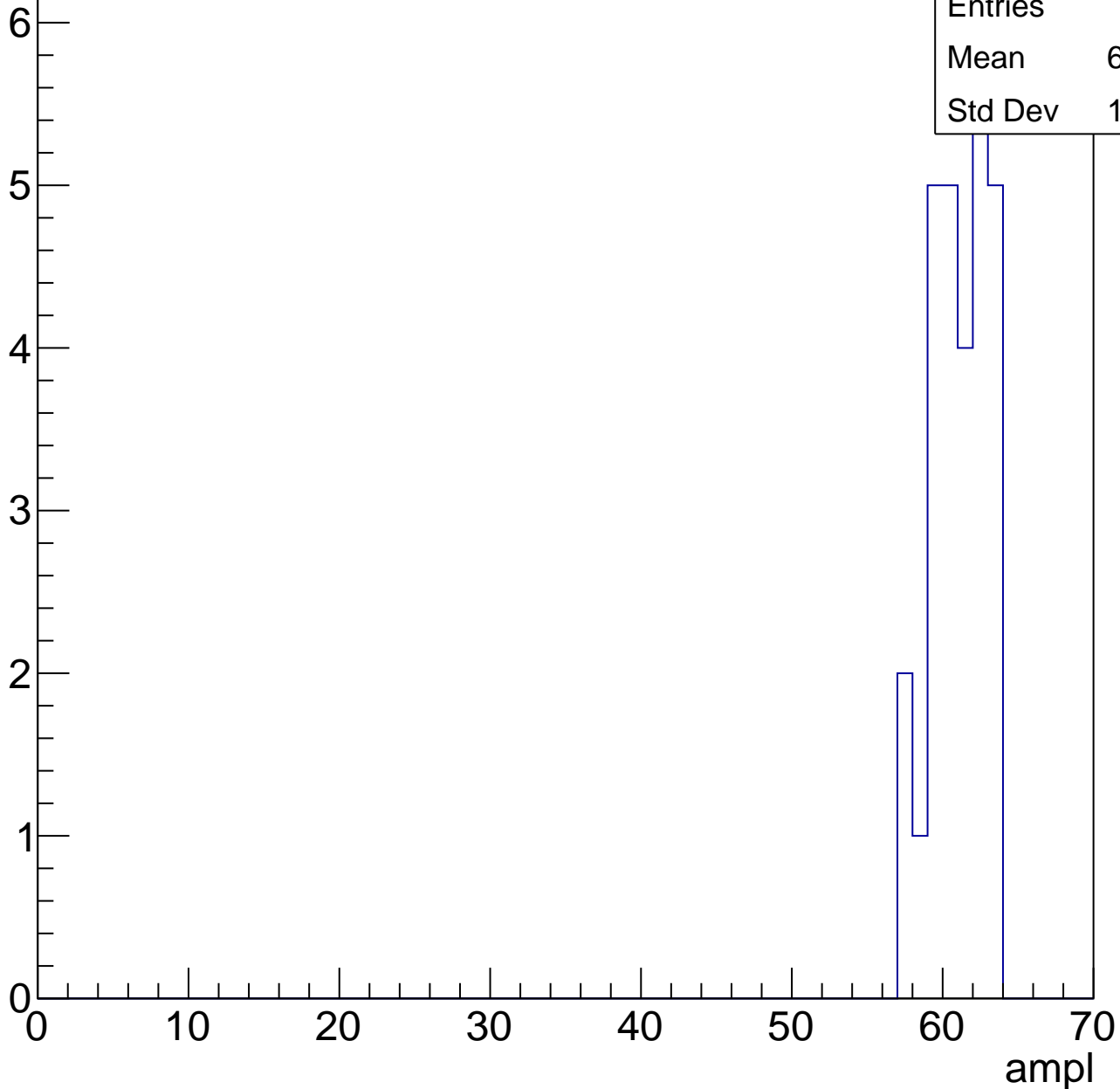


B1L103S, U1-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

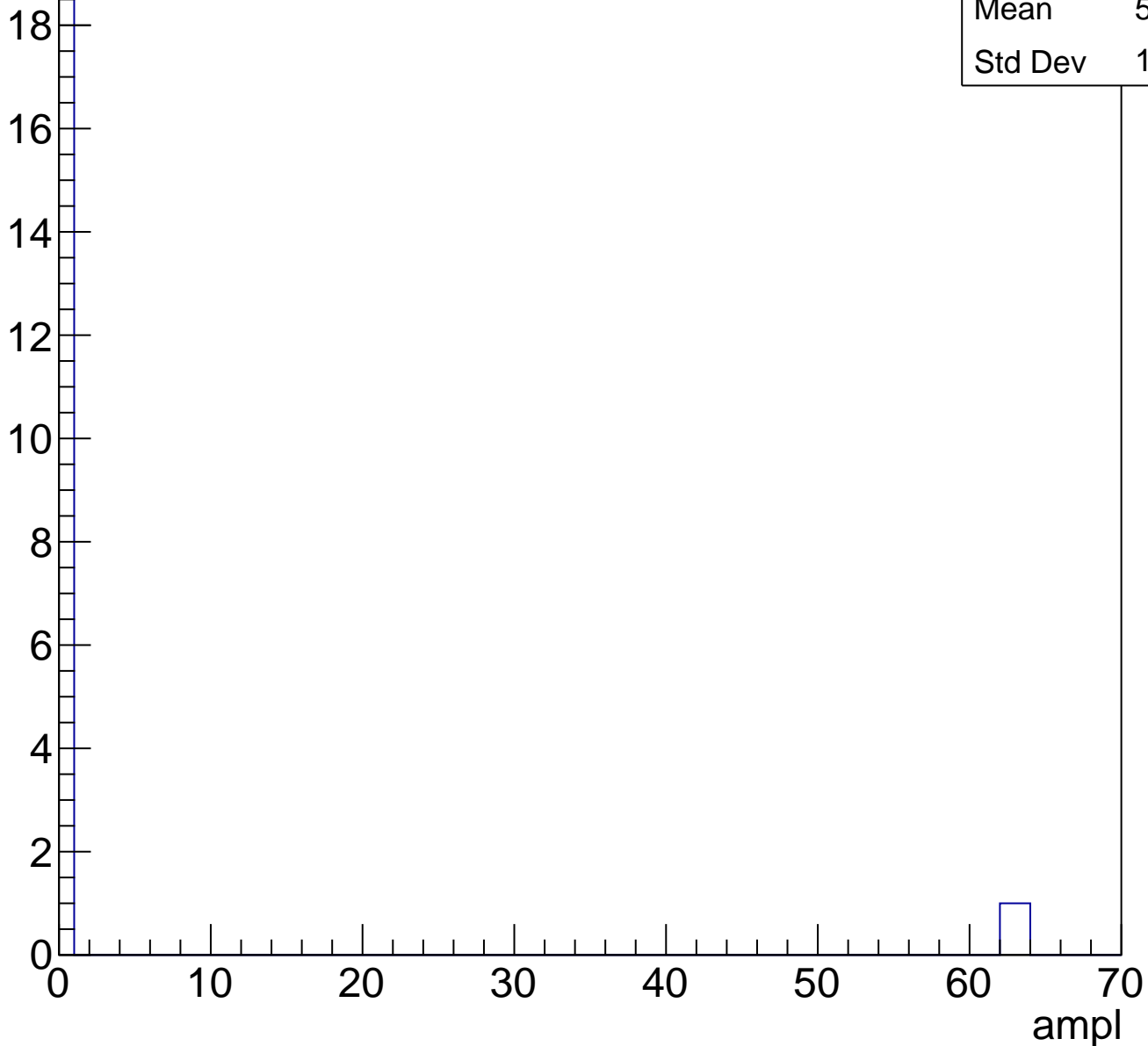
Entries	28
Mean	60.64
Std Dev	1.777



B1L103S, U1-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



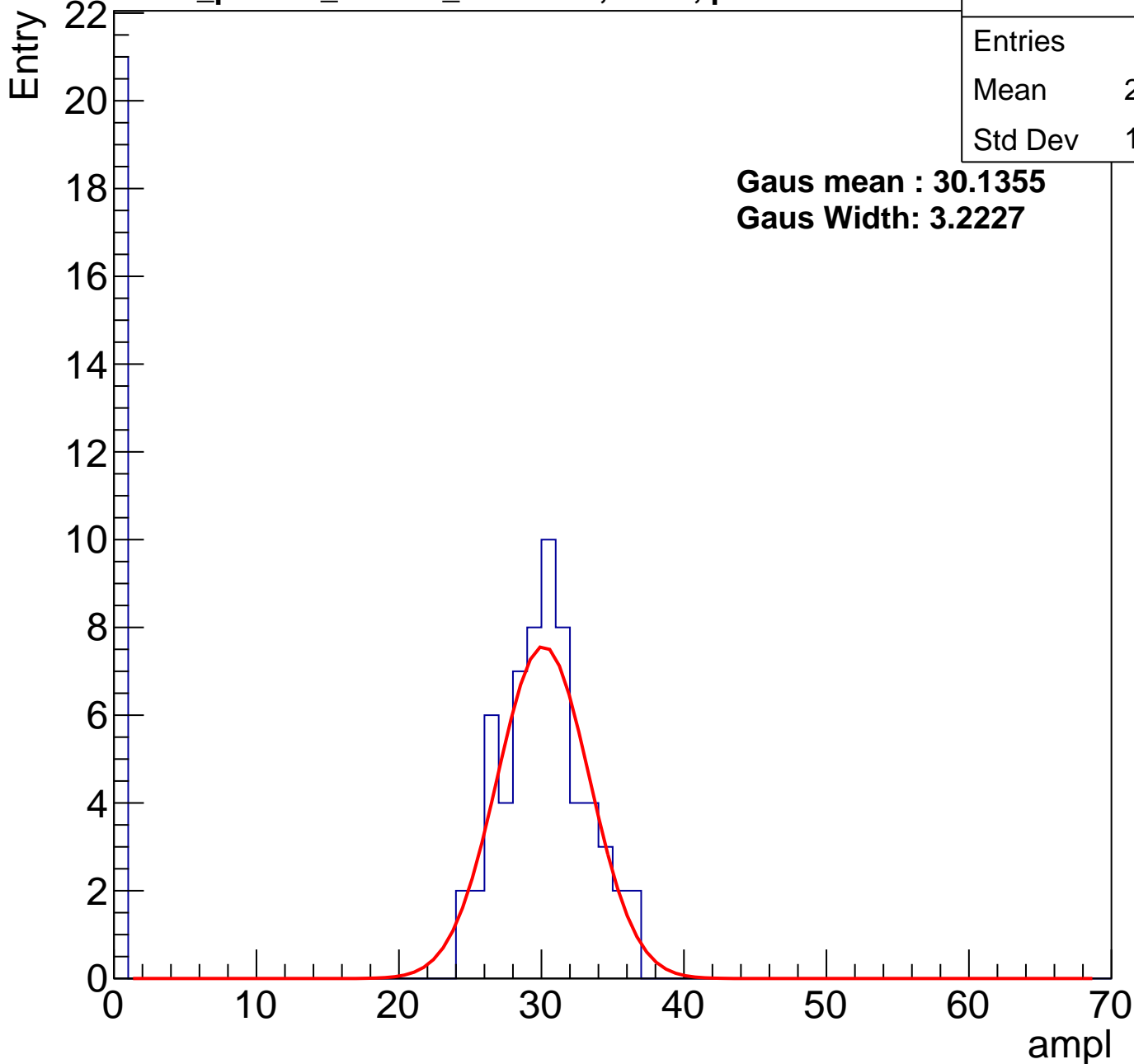
B1L103S, U1-ch15, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	22.19
Std Dev	13.15

Gaus mean : 30.1355

Gaus Width: 3.2227



B1L103S, U1-ch15, adc1

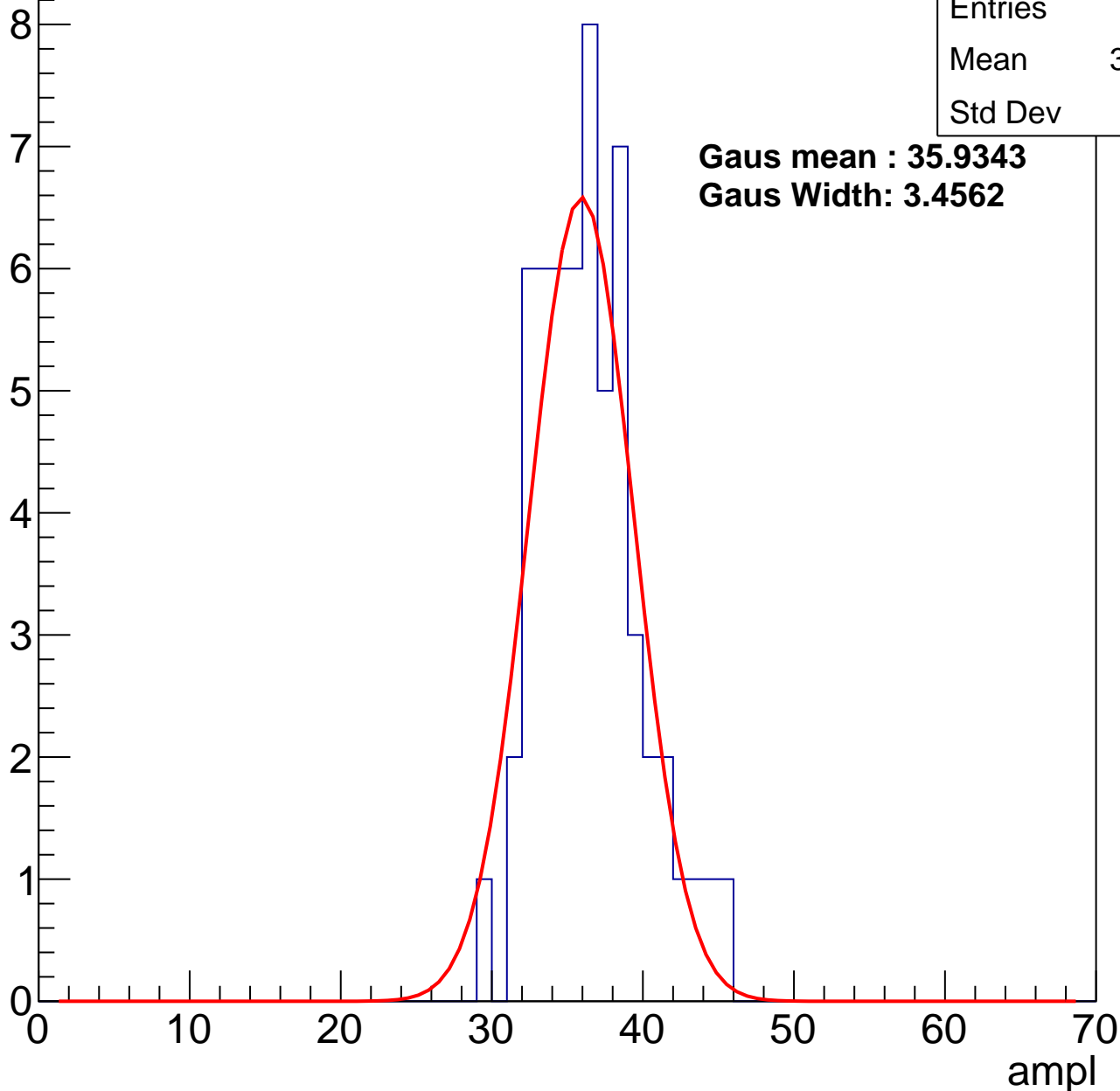
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	35.98
Std Dev	3.35

Gaus mean : 35.9343

Gaus Width: 3.4562



B1L103S, U1-ch15, adc2

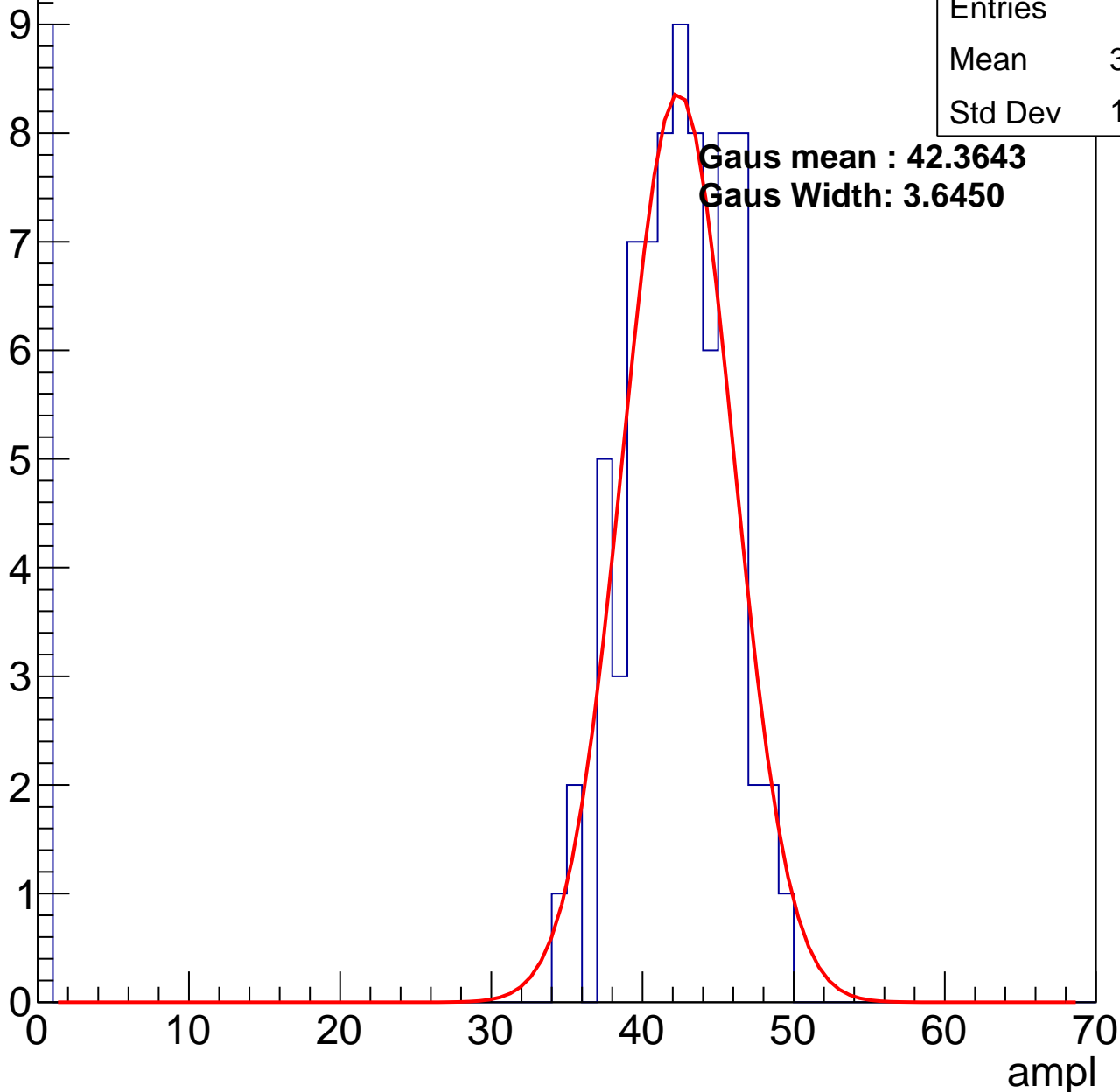
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	37.64
Std Dev	13.24

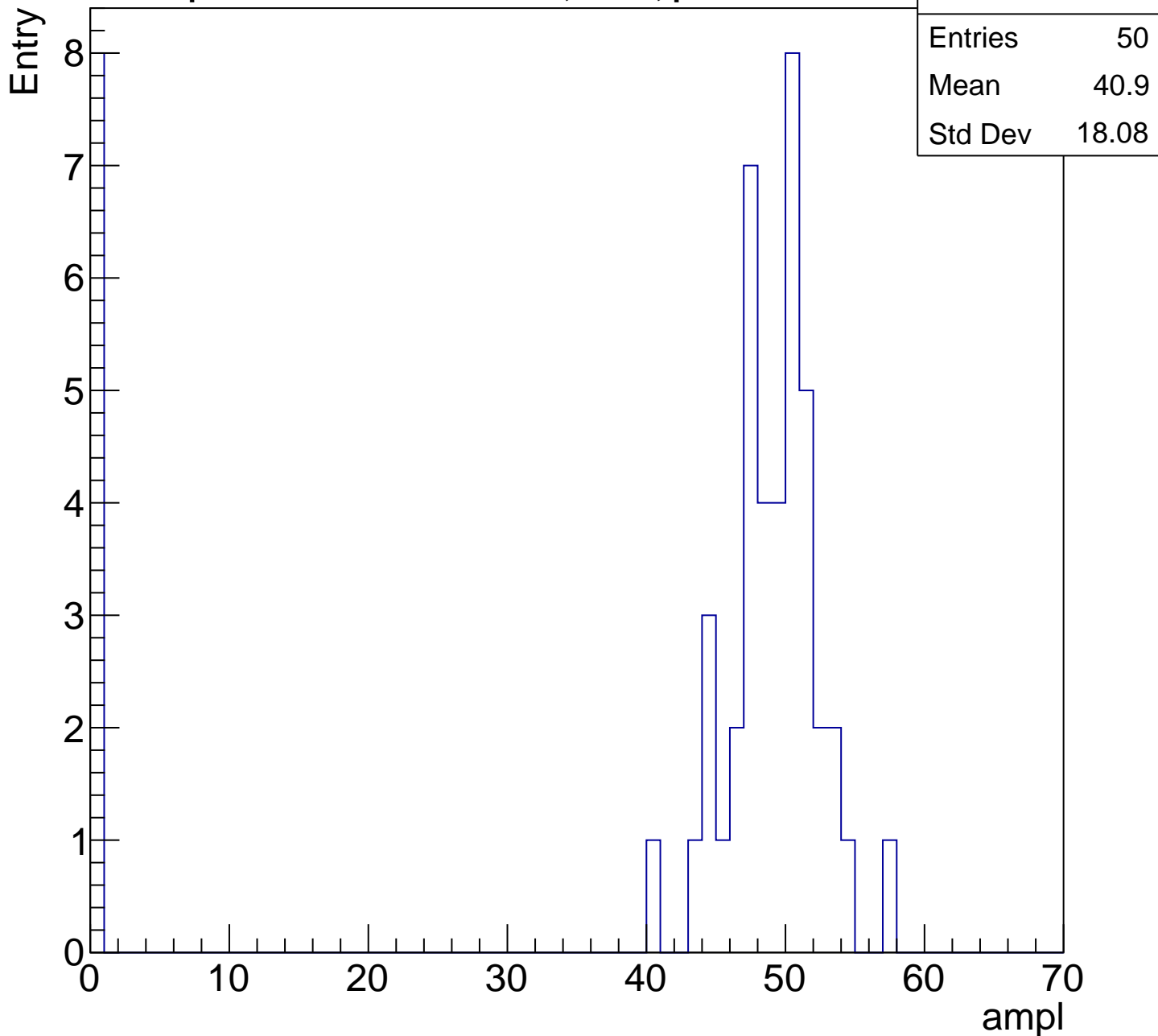
Gaus mean : 42.3643

Gaus Width: 3.6450



B1L103S, U1-ch15, adc3

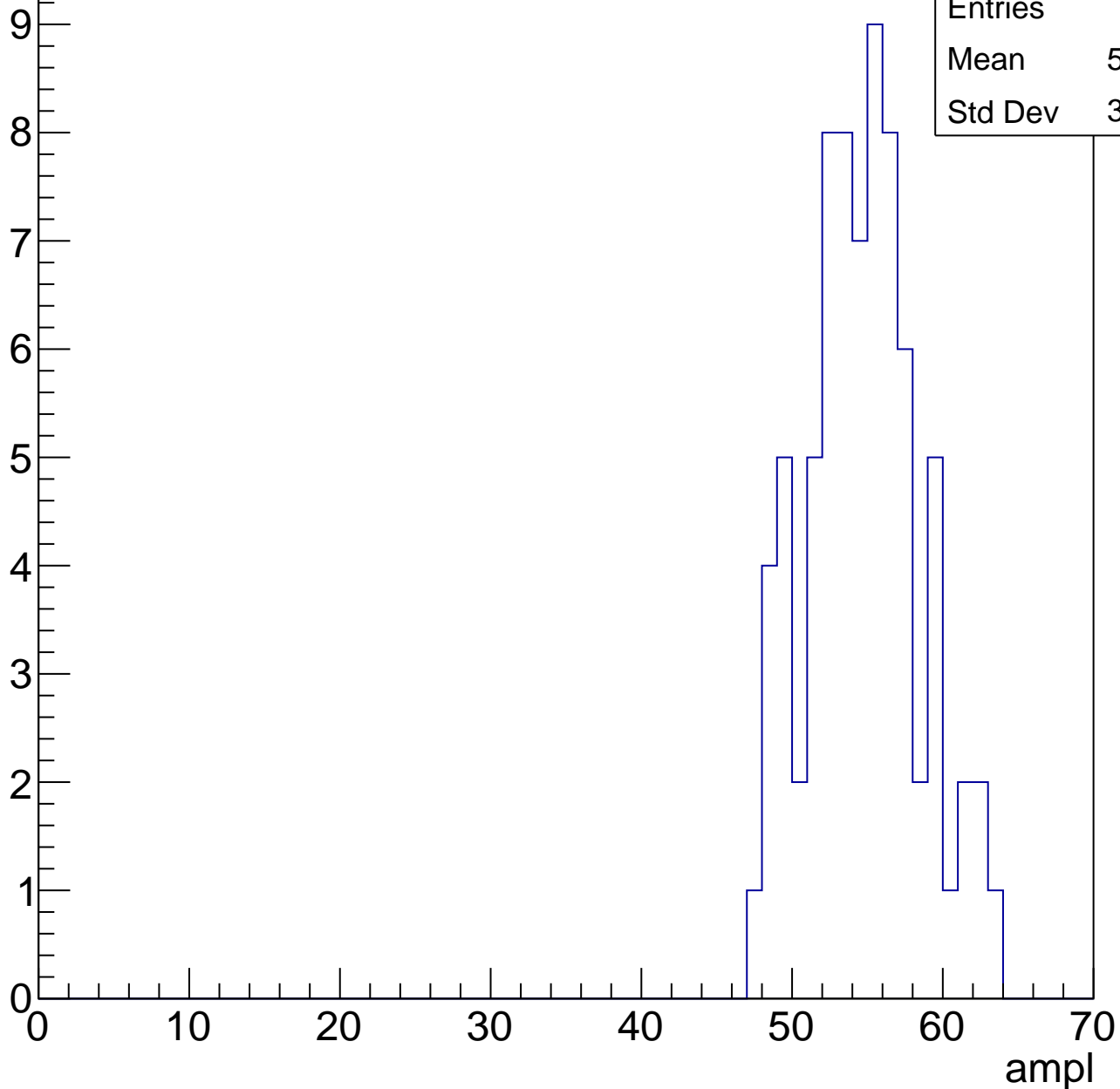
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

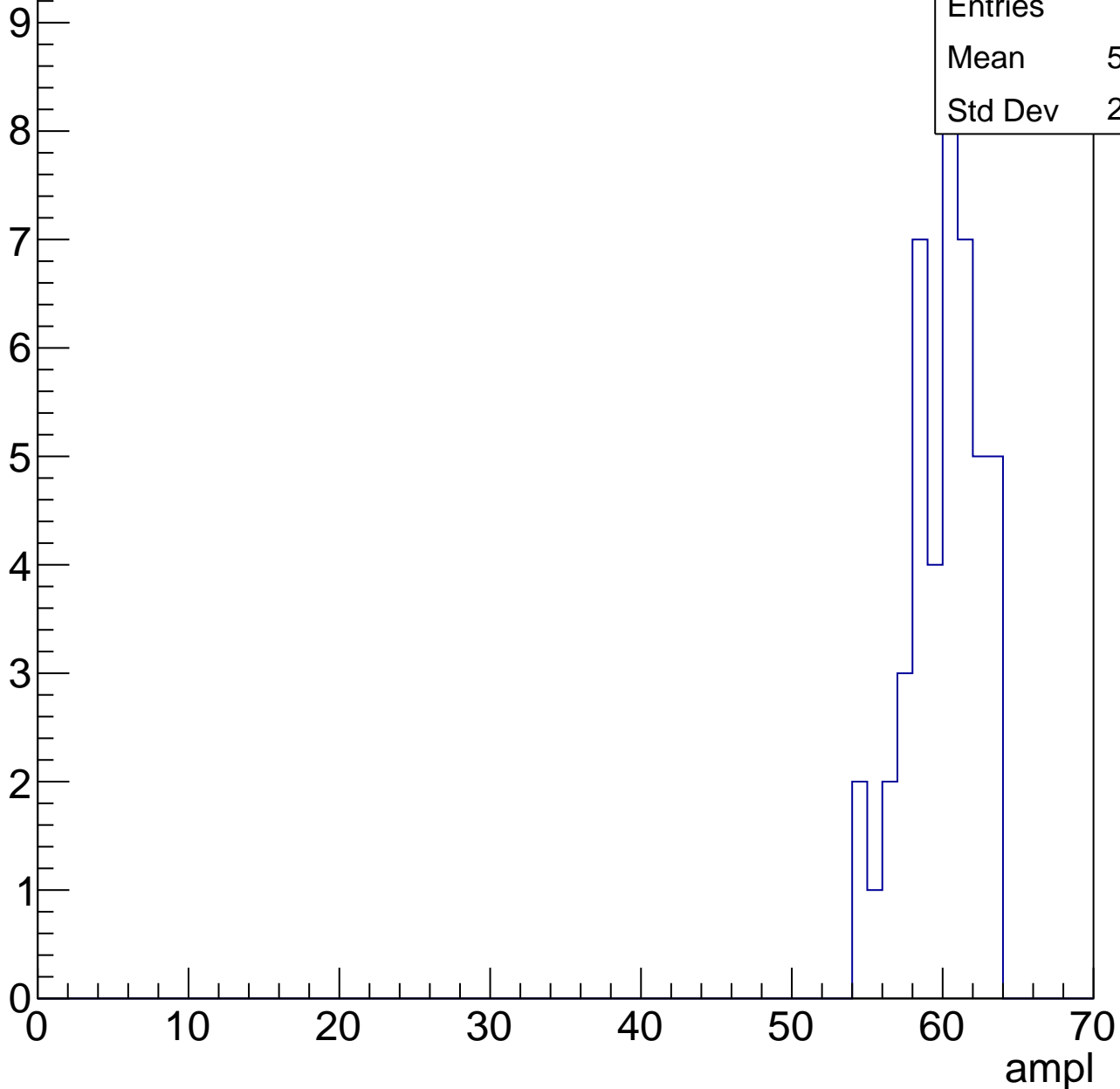


Entries	76
Mean	54.24
Std Dev	3.674

B1L103S, U1-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

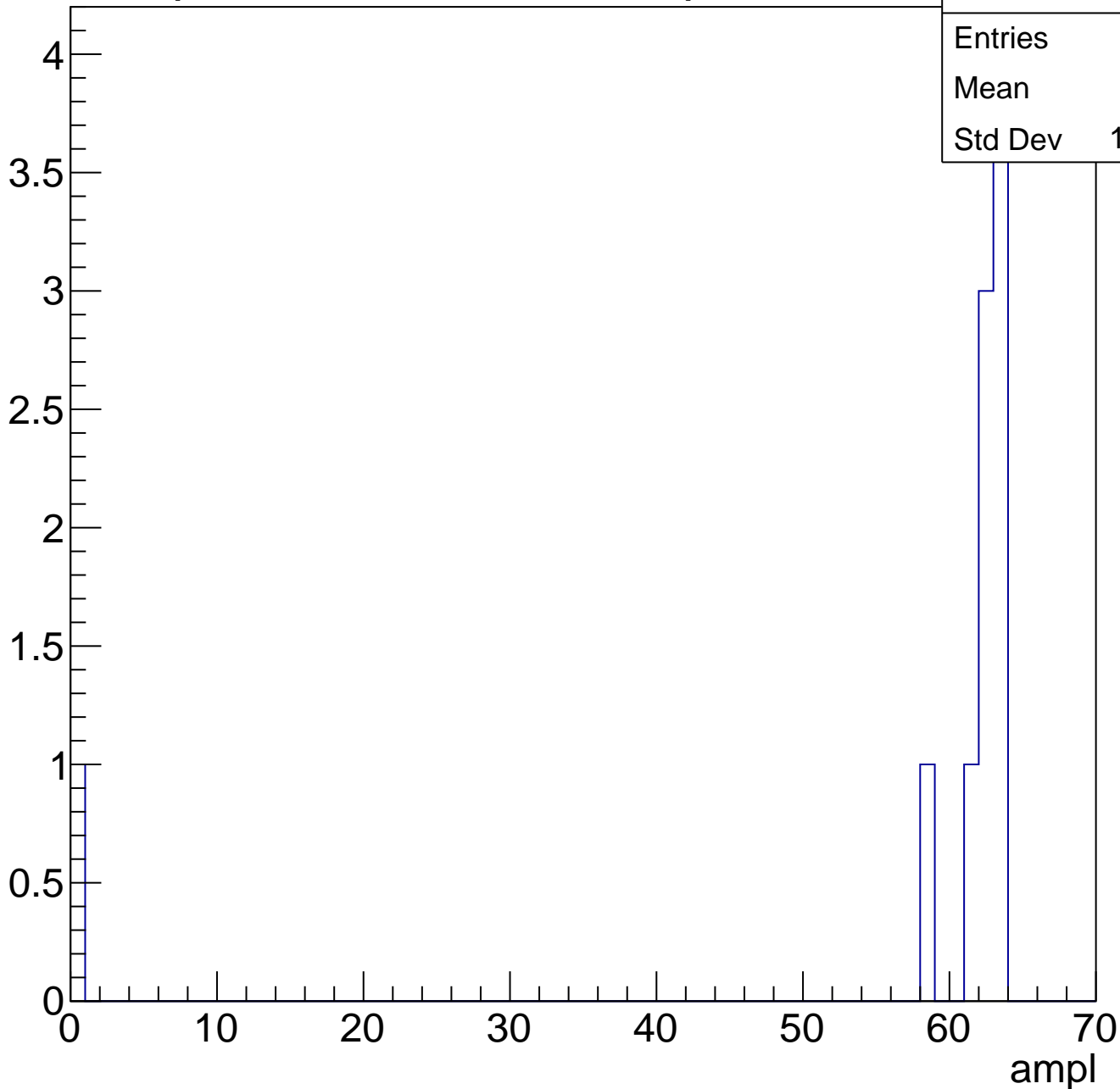
Entry



B1L103S, U1-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

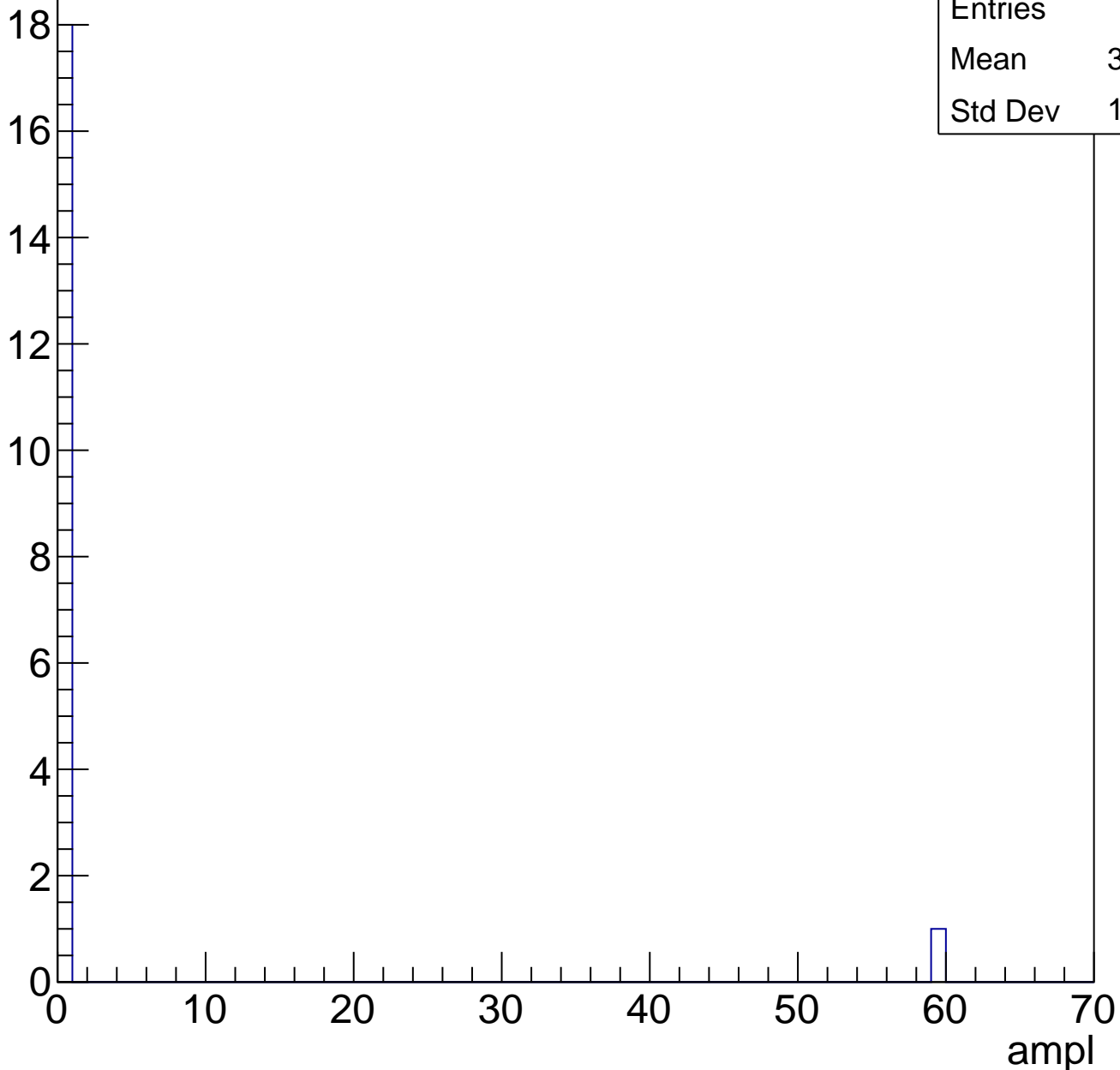
Entry



B1L103S, U1-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	3.105
Std Dev	13.17

B1L103S, U1-ch16, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	20.56
Std Dev	13.13

Gaus mean : 28.5677

Gaus Width: 3.8482

Entry

25

20

15

10

5

0

0

10

20

30

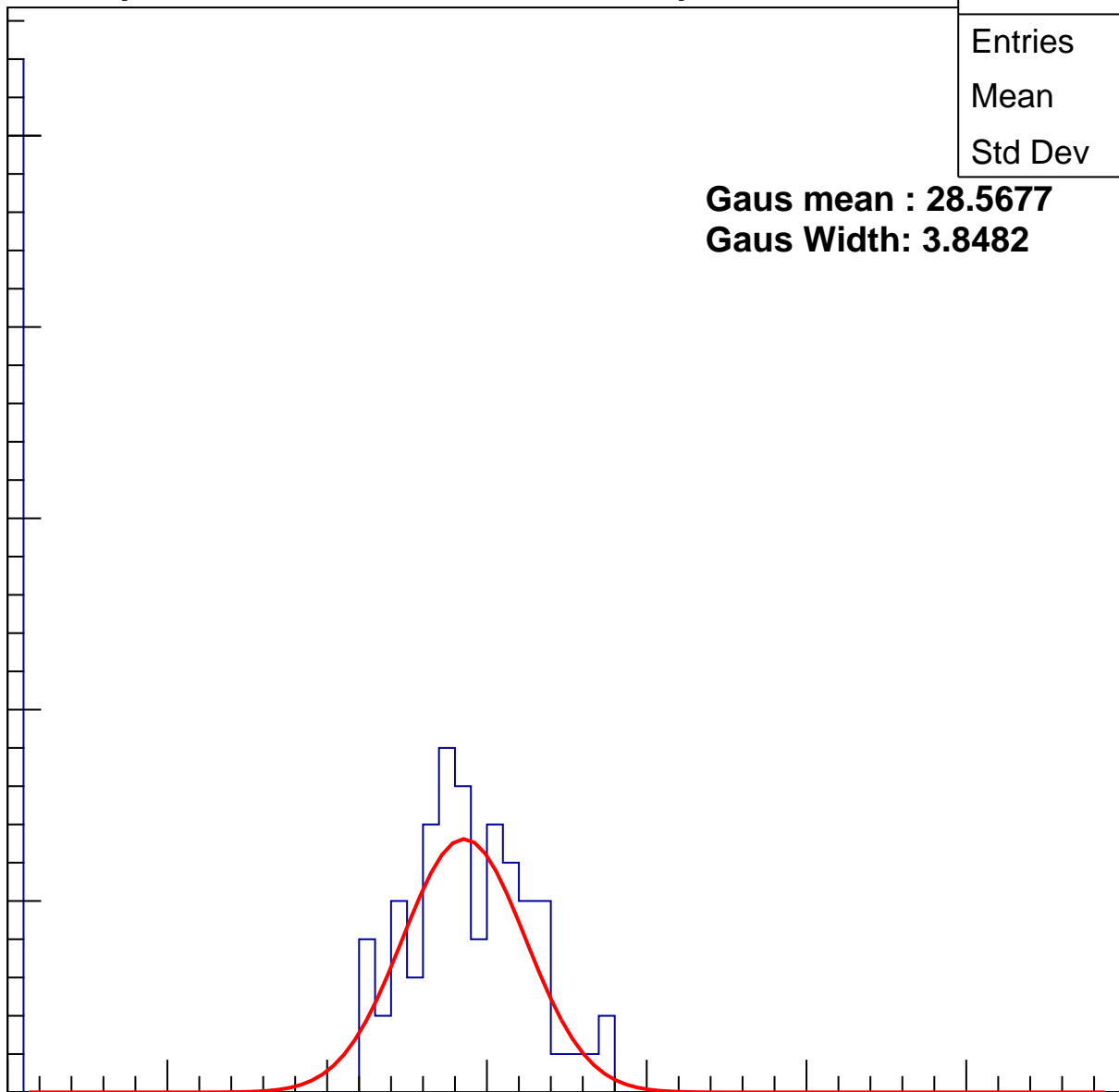
40

50

60

70

ampl



B1L103S, U1-ch16, adc1

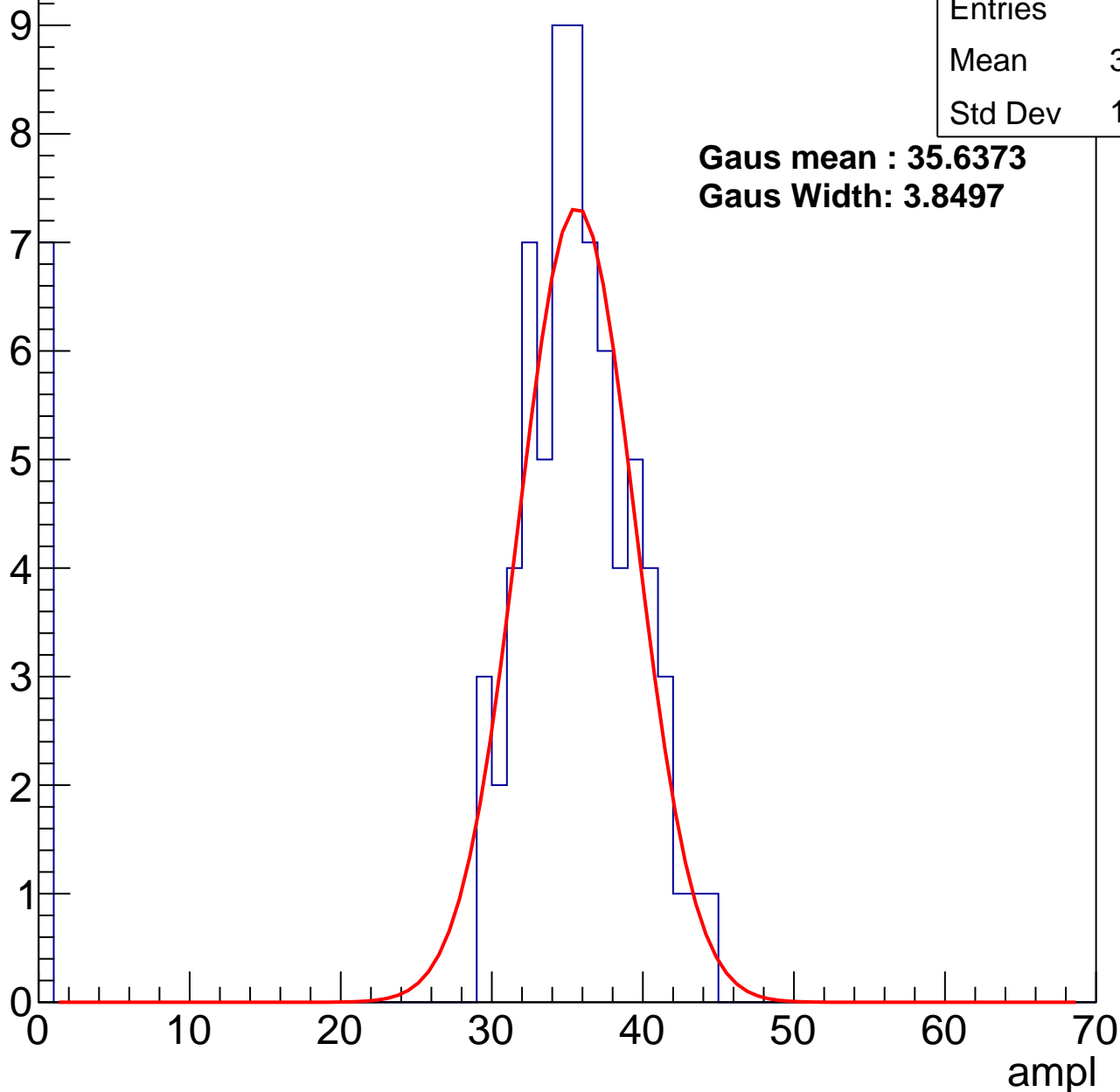
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.23
Std Dev	10.65

Gaus mean : 35.6373

Gaus Width: 3.8497



B1L103S, U1-ch16, adc2

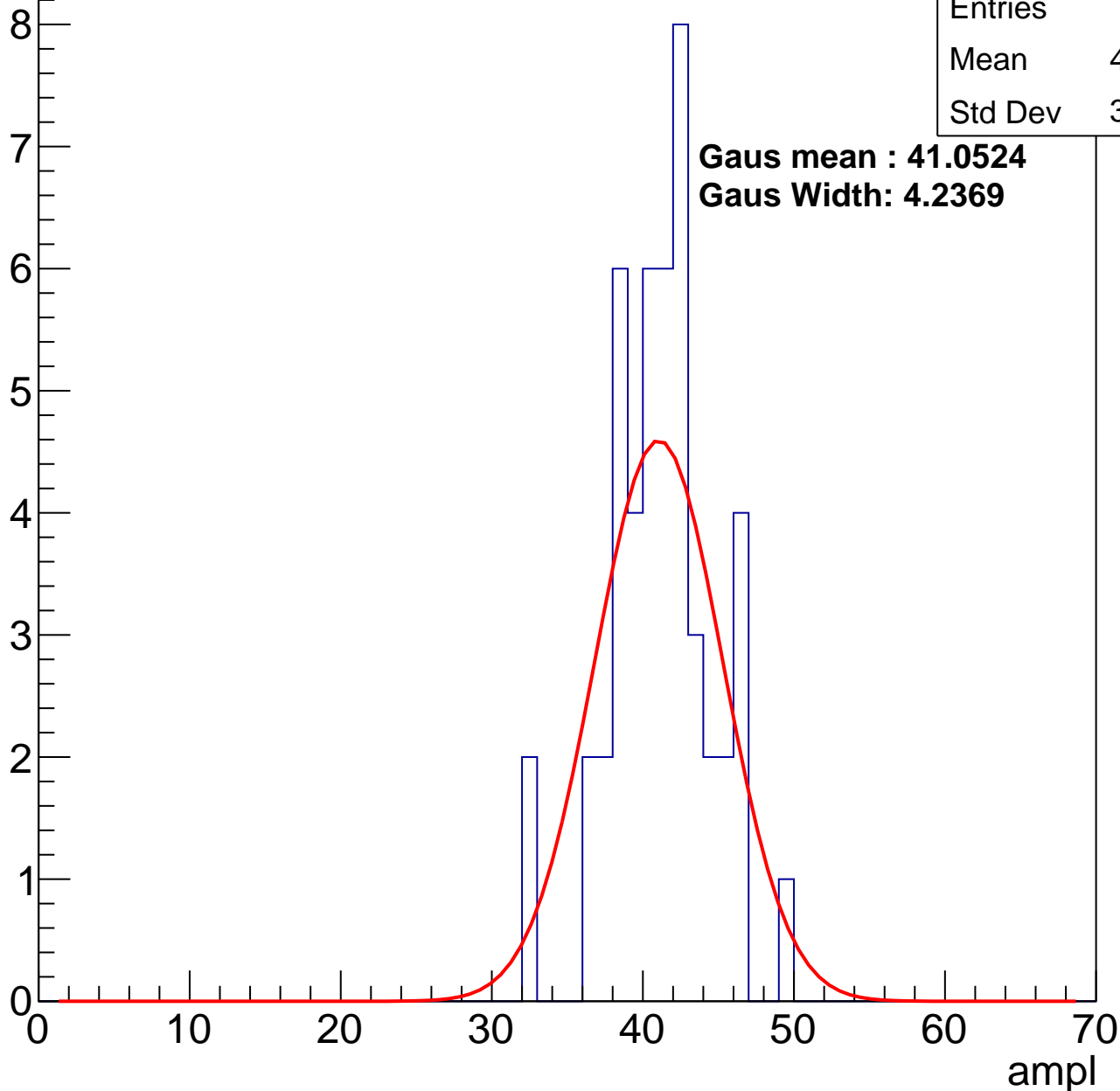
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	40.75
Std Dev	3.388

Gaus mean : 41.0524

Gaus Width: 4.2369

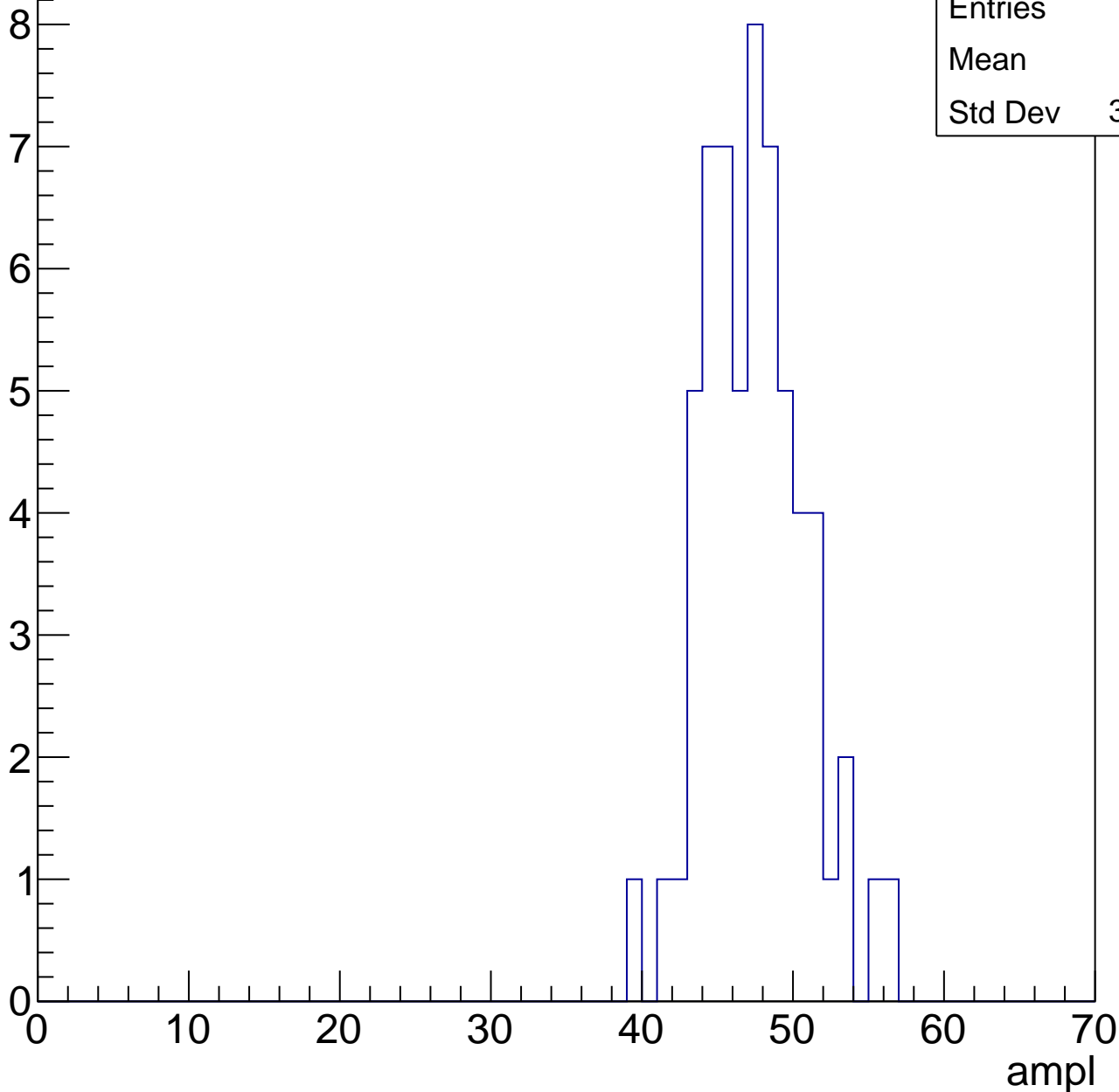


B1L103S, U1-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

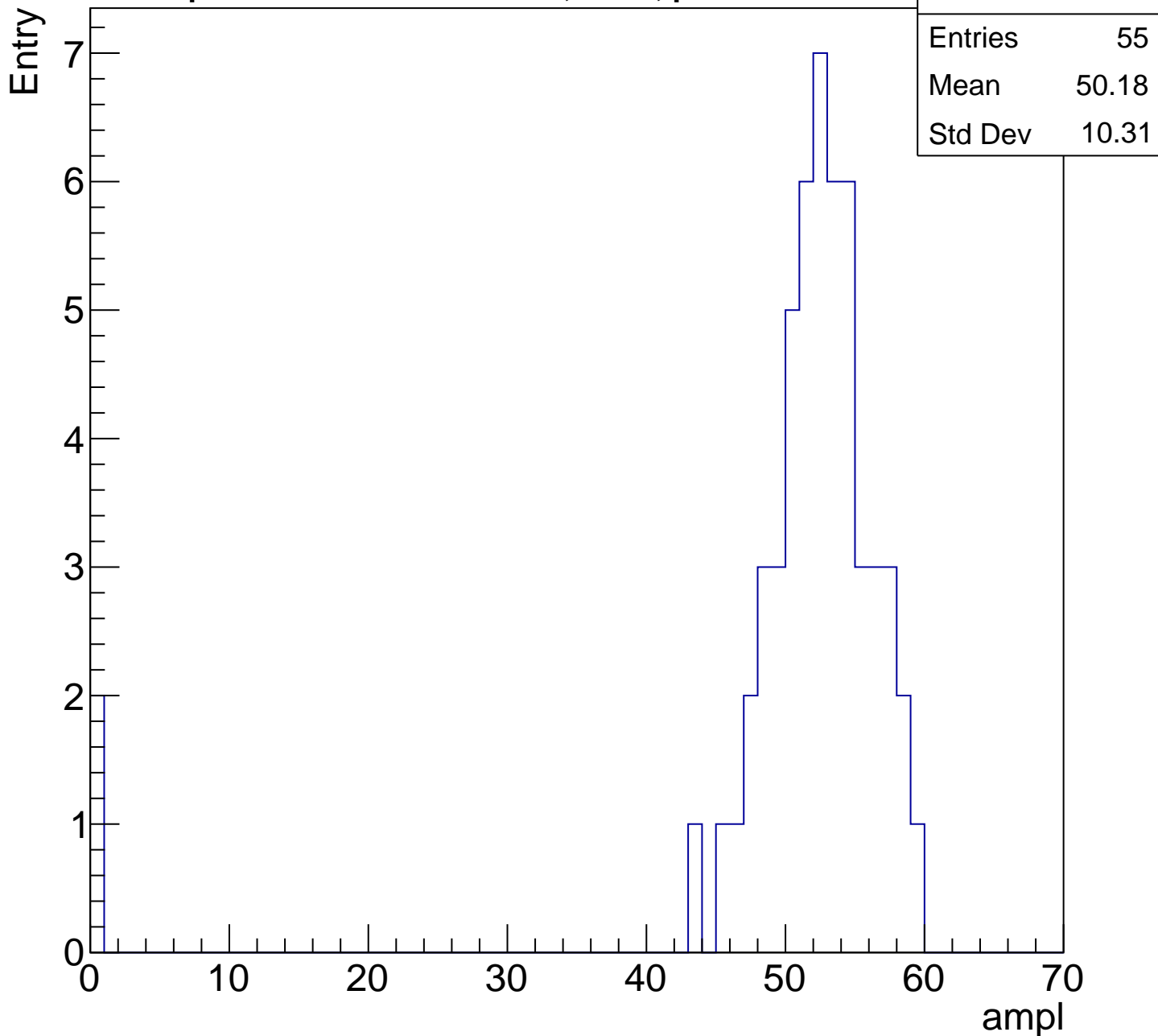
Entry

Entries	60
Mean	47
Std Dev	3.342



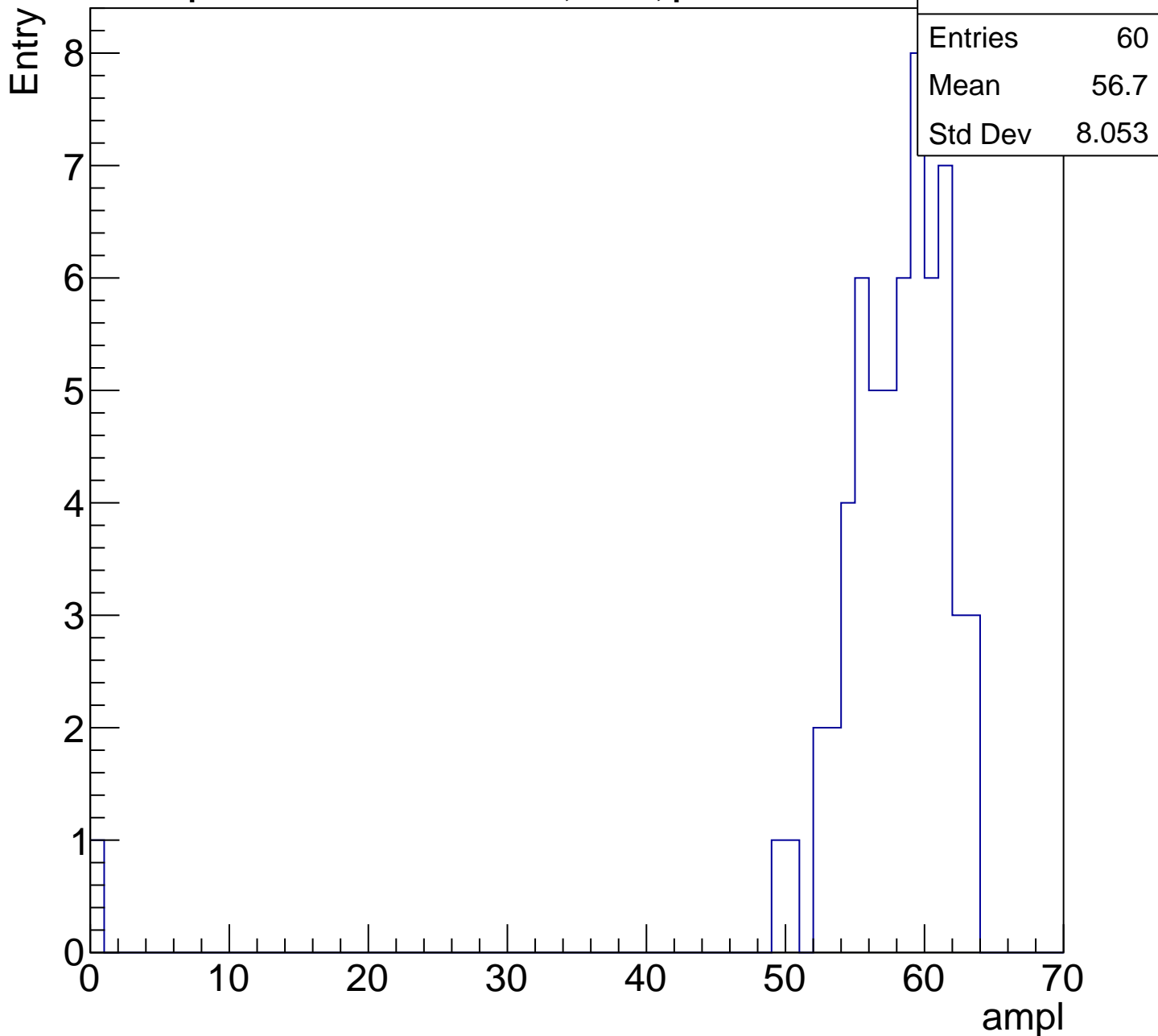
B1L103S, U1-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

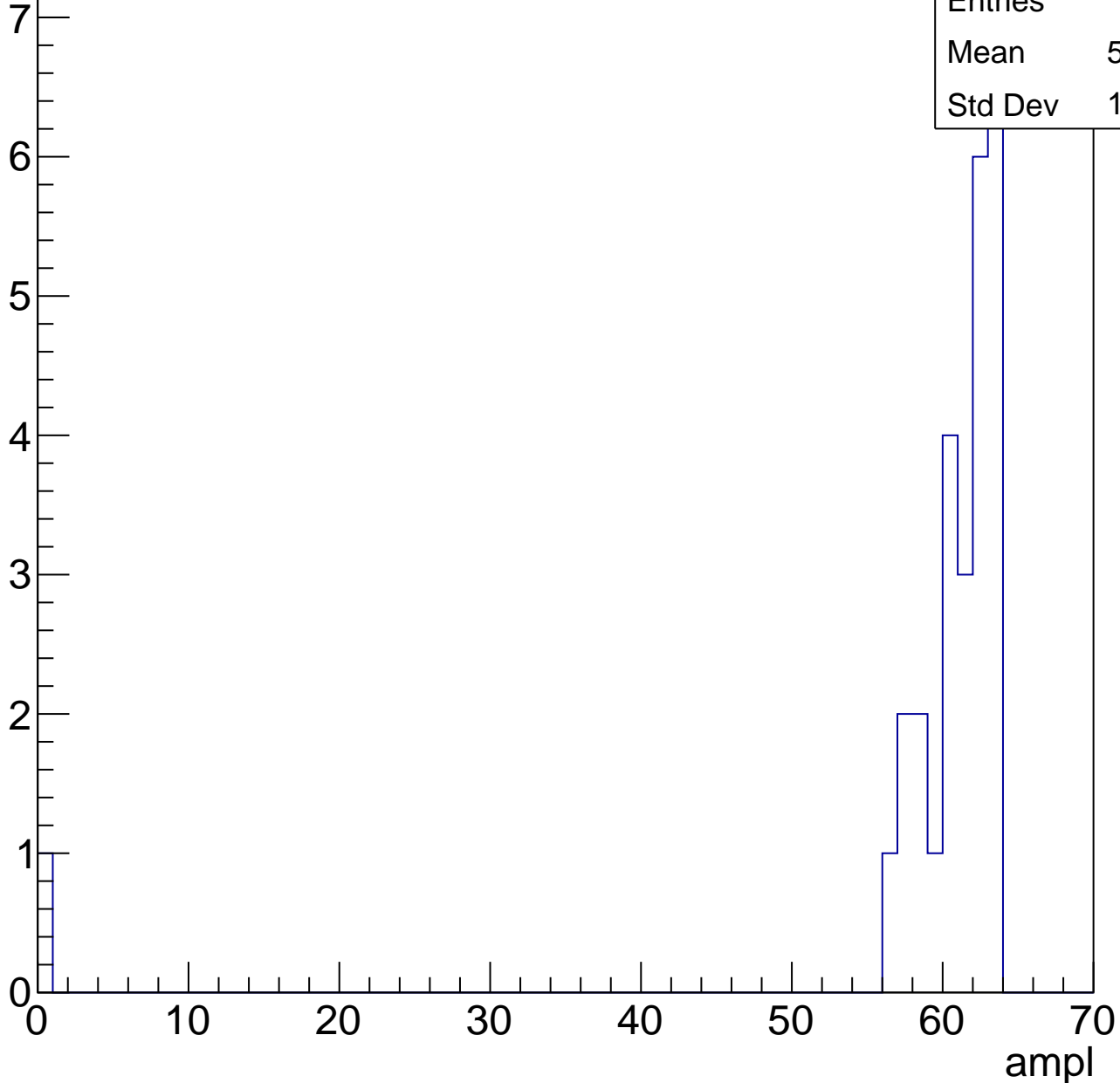


B1L103S, U1-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

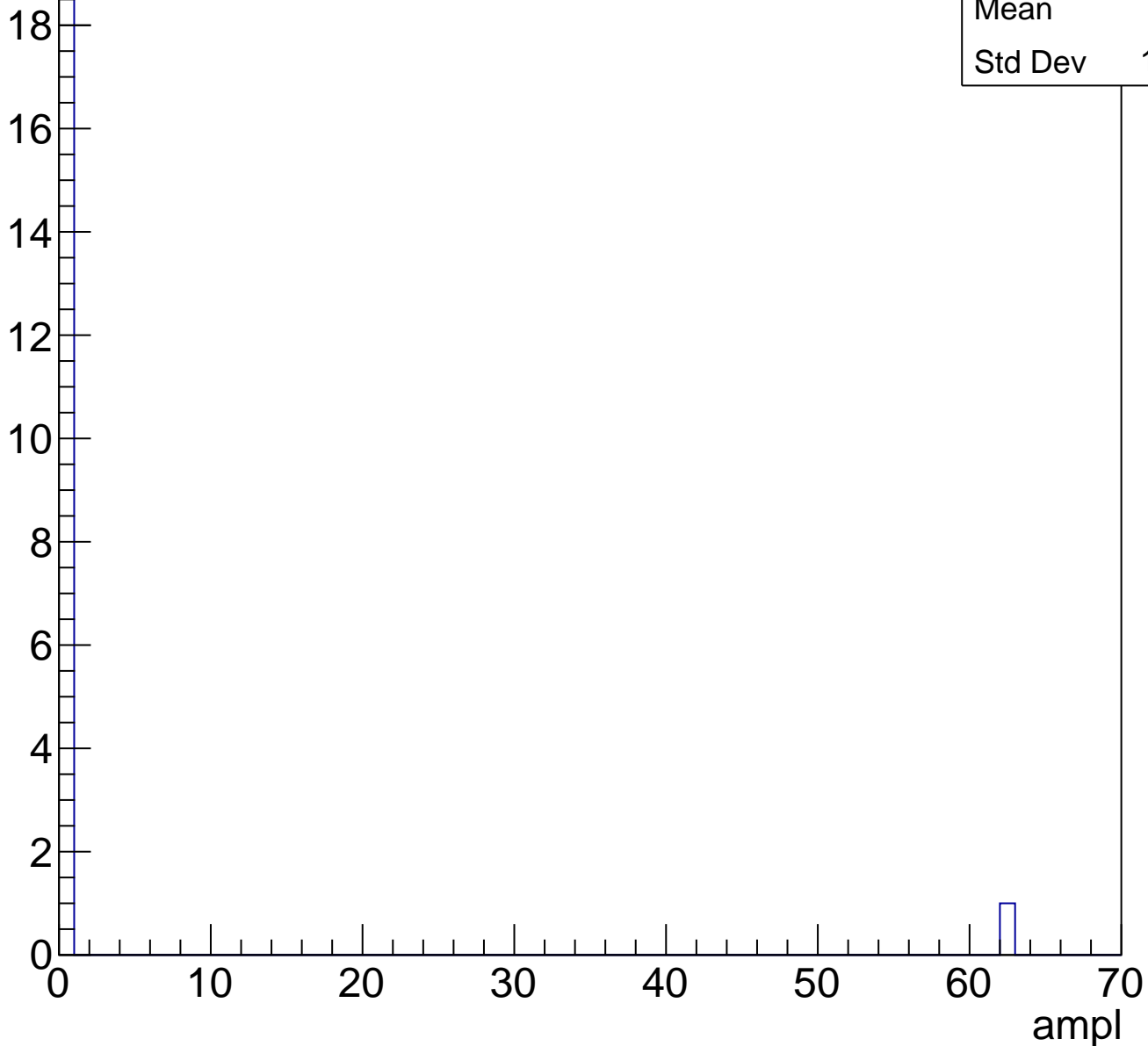
Entries	27
Mean	58.56
Std Dev	11.67



B1L103S, U1-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.1
Std Dev	13.51

B1L103S, U1-ch17, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	21.08
Std Dev	12.17

Gaus mean : 28.0844

Gaus Width: 3.0067

Entry

25

20

15

10

5

0

0

10

20

30

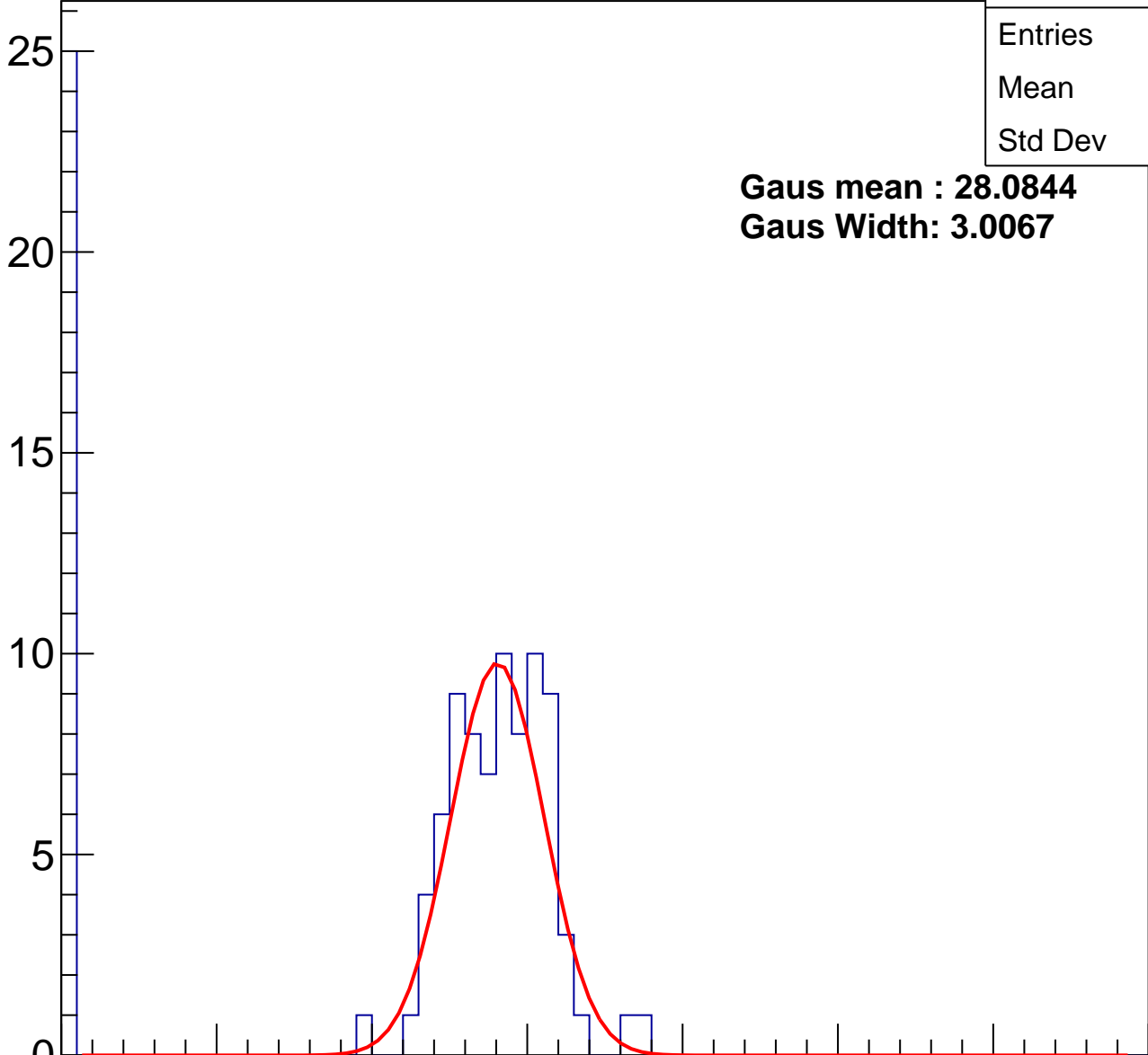
40

50

60

70

ampl



B1L103S, U1-ch17, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	82
Mean	29.82
Std Dev	13.26

Gaus mean : 36.1336

Gaus Width: 2.9603

0

2

4

6

8

10

12

Entry

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	29.82
Std Dev	13.26

Gaus mean : 36.1336

Gaus Width: 2.9603

B1L103S, U1-ch17, adc2

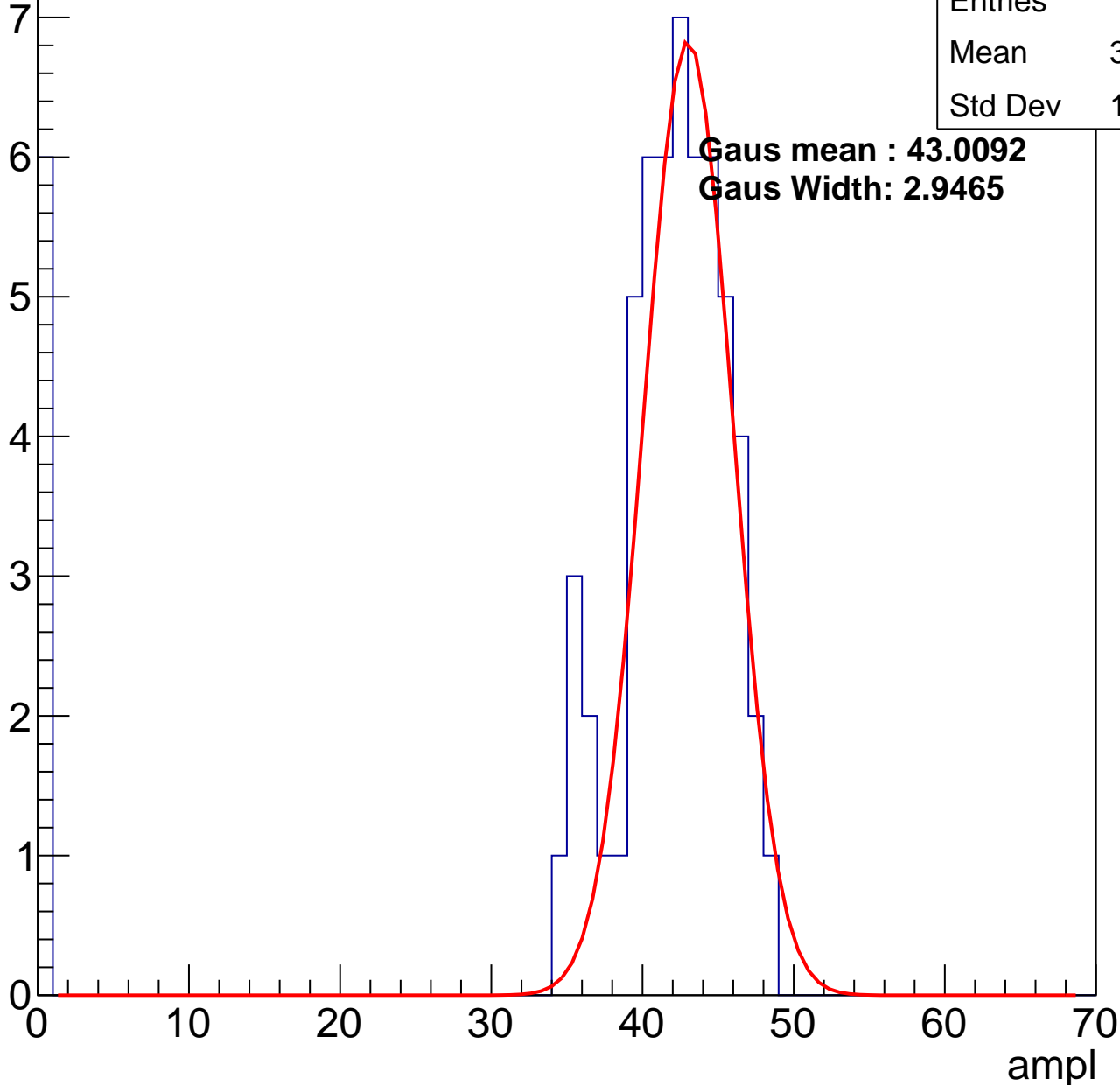
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	37.65
Std Dev	12.72

Gaus mean : 43.0092

Gaus Width: 2.9465

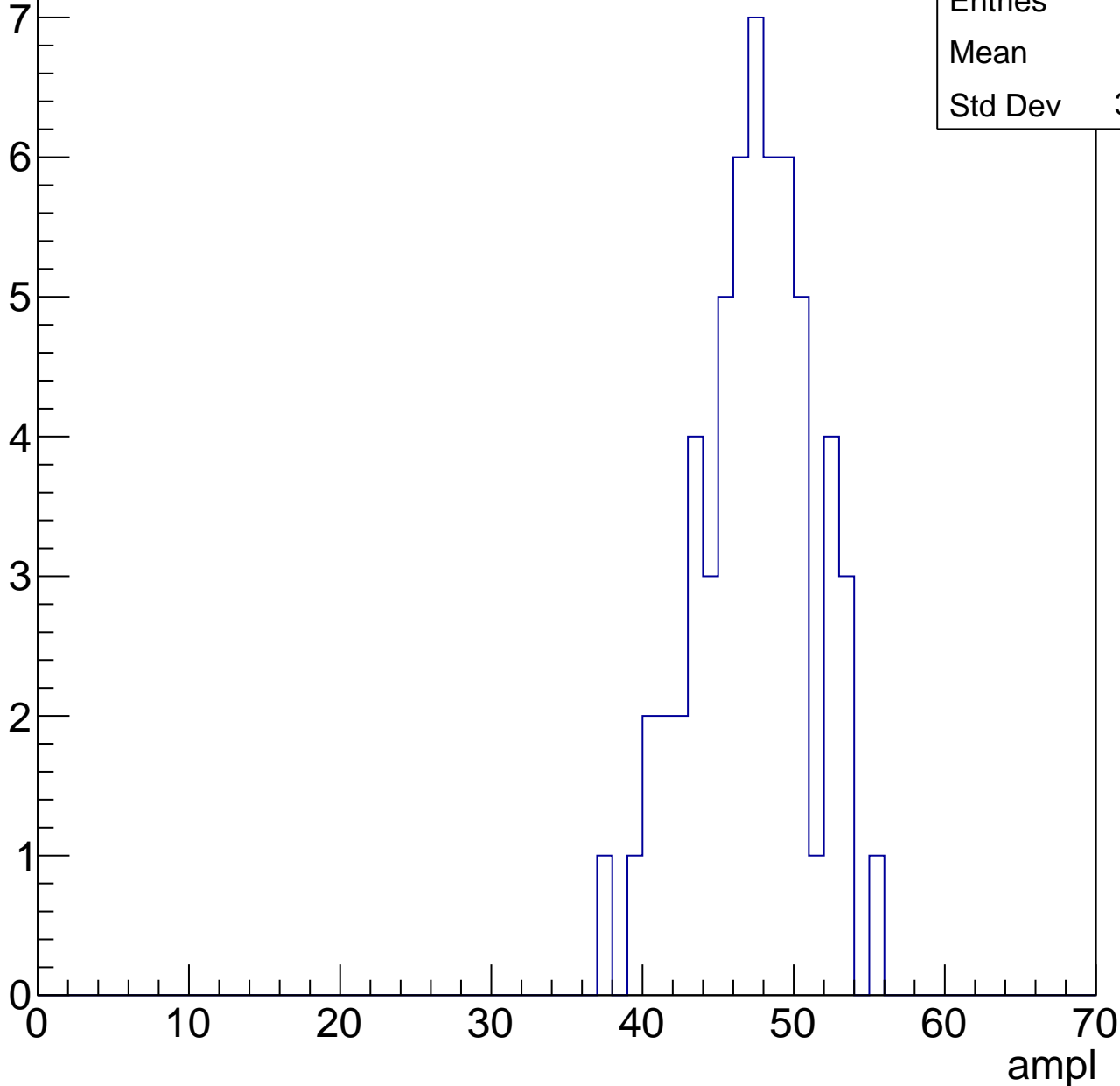


B1L103S, U1-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	46.8
Std Dev	3.821

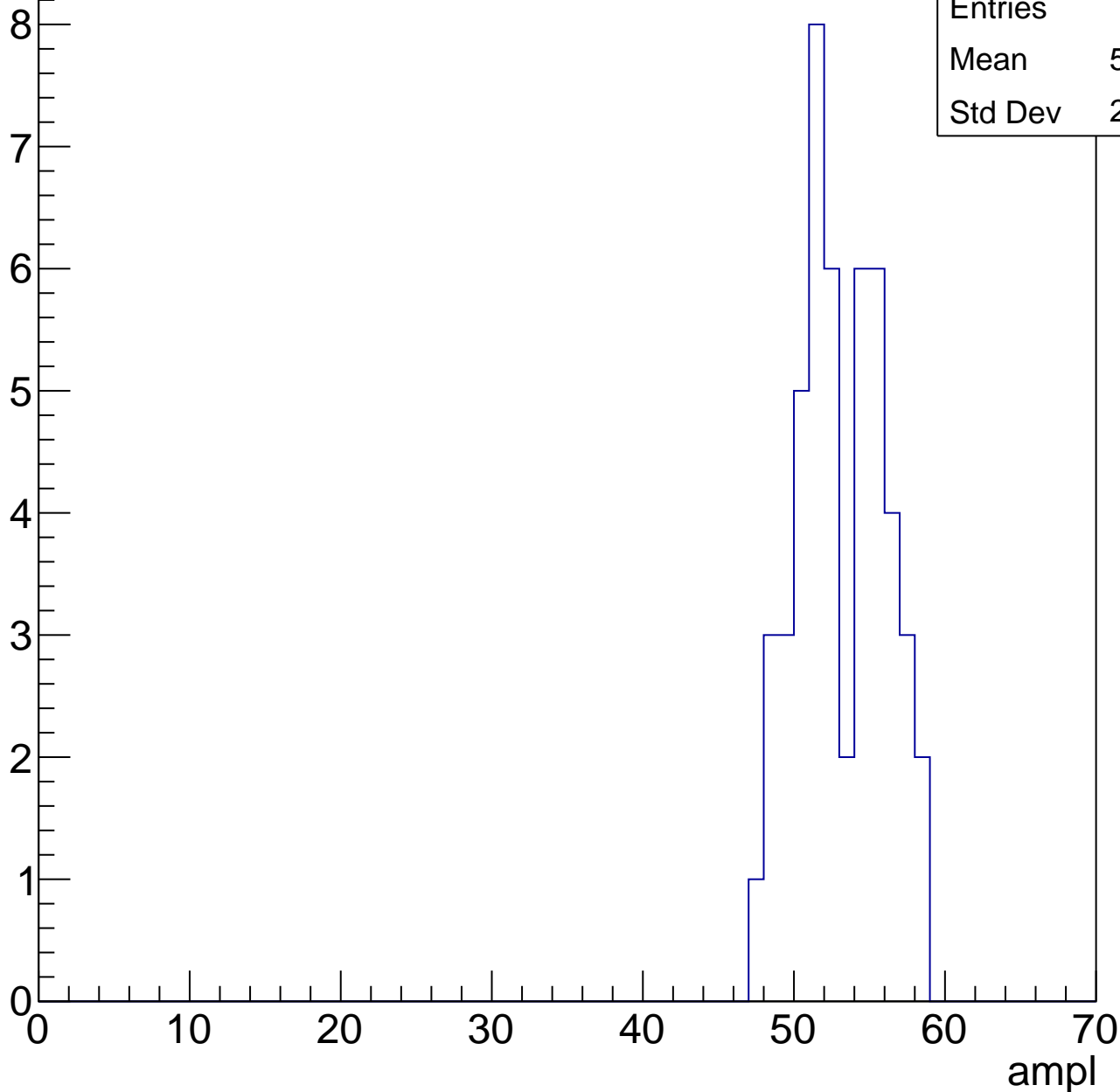


B1L103S, U1-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.63
Std Dev	2.855

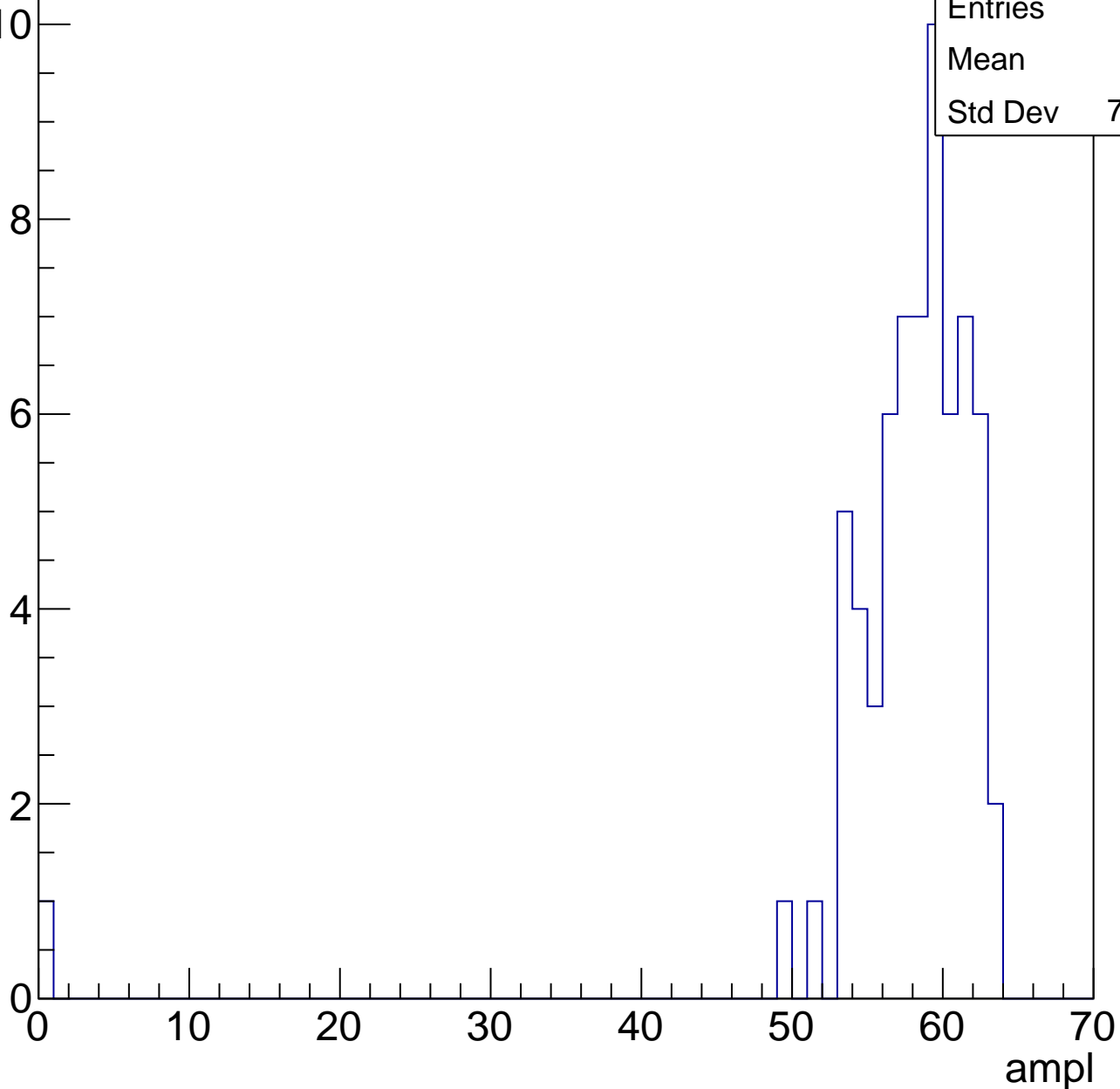


B1L103S, U1-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	57
Std Dev	7.705

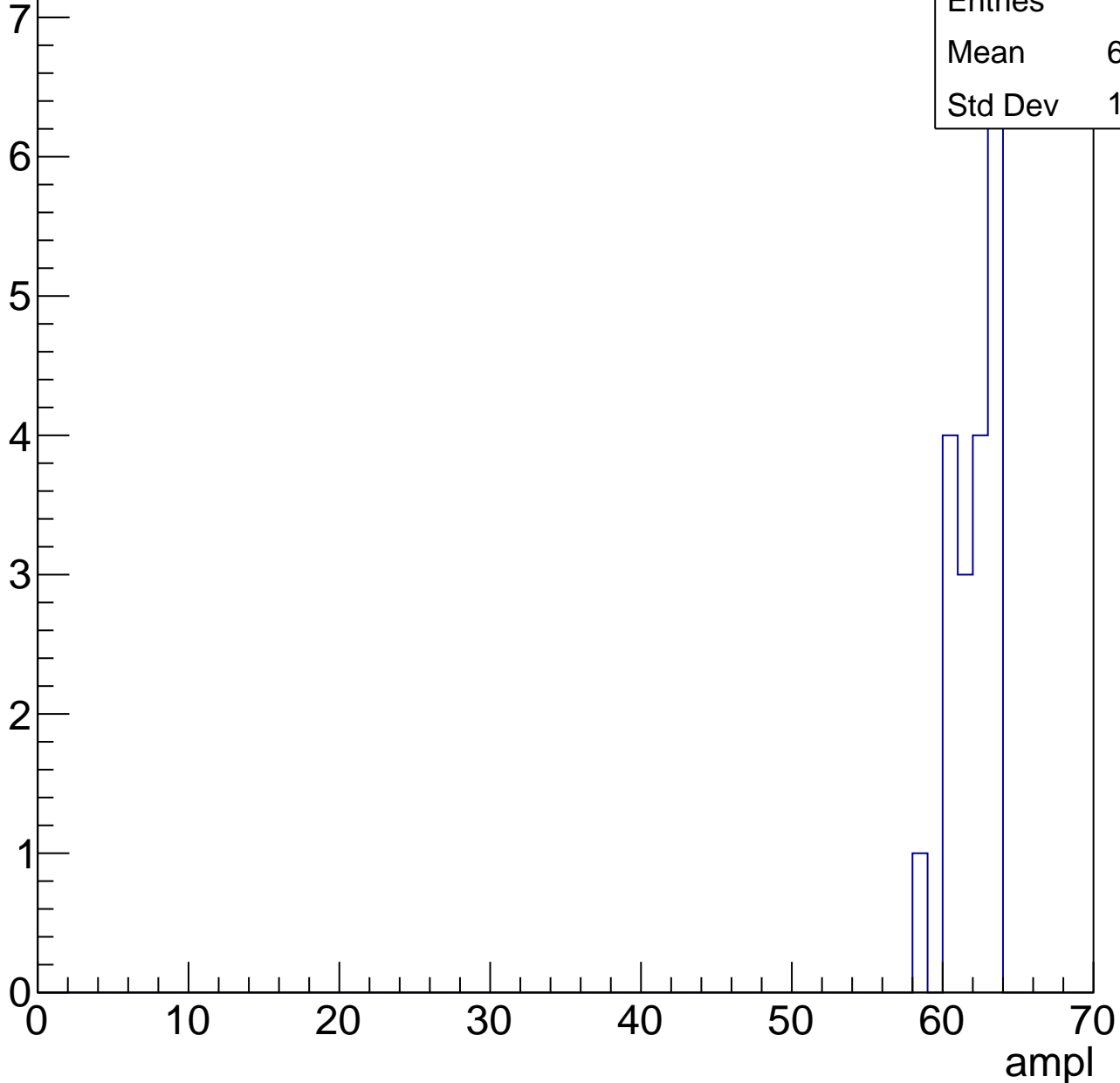


B1L103S, U1-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.58
Std Dev	1.426



B1L103S, U1-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	3.15
Std Dev	13.73

ampl

0 10 20 30 40 50 60 70

B1L103S, U1-ch18, adc0

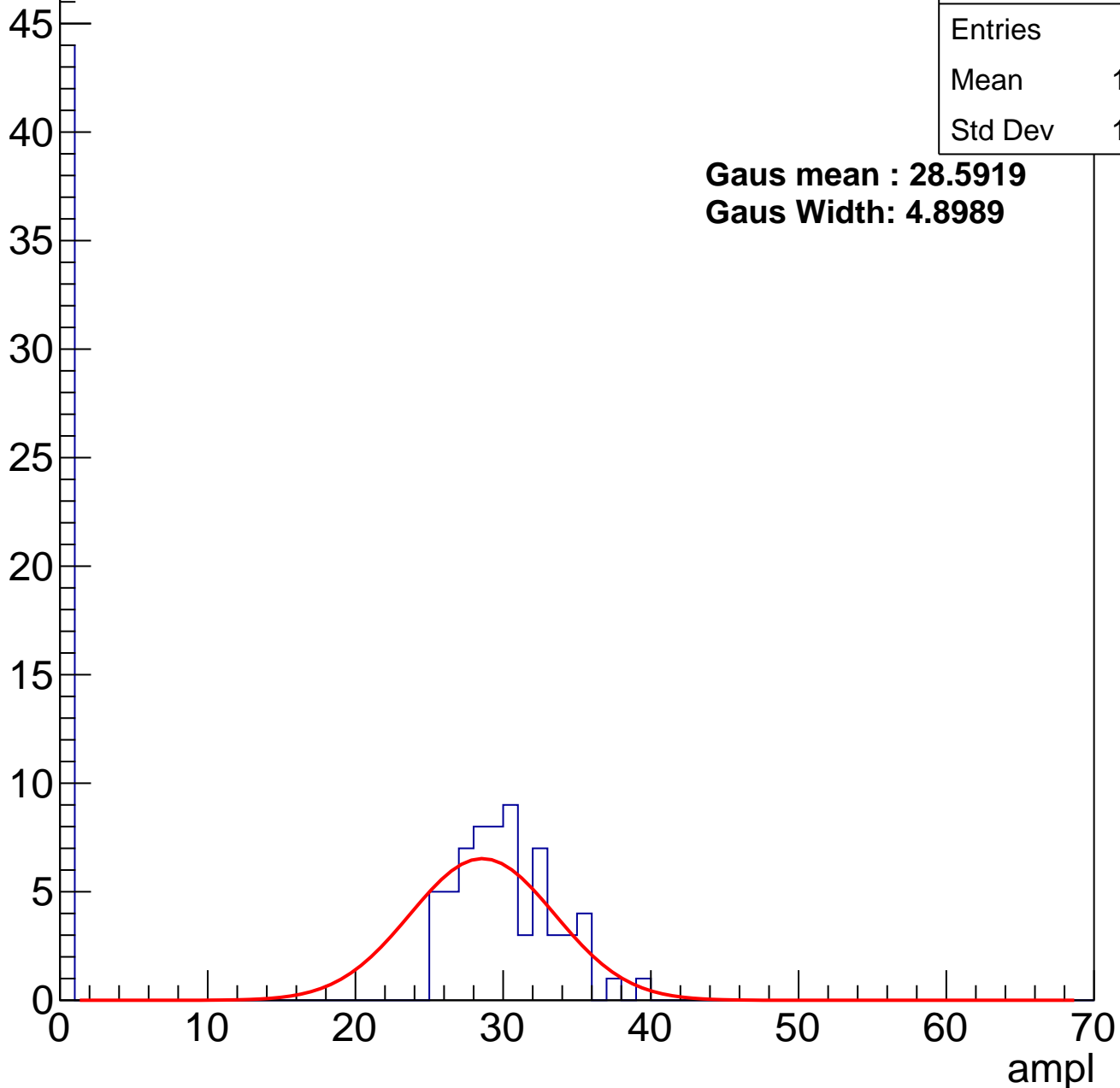
calib_packv5_041523_1651.root, FC#0, port C2

Entries	108
Mean	17.63
Std Dev	14.82

Gaus mean : 28.5919

Gaus Width: 4.8989

Entry



B1L103S, U1-ch18, adc1

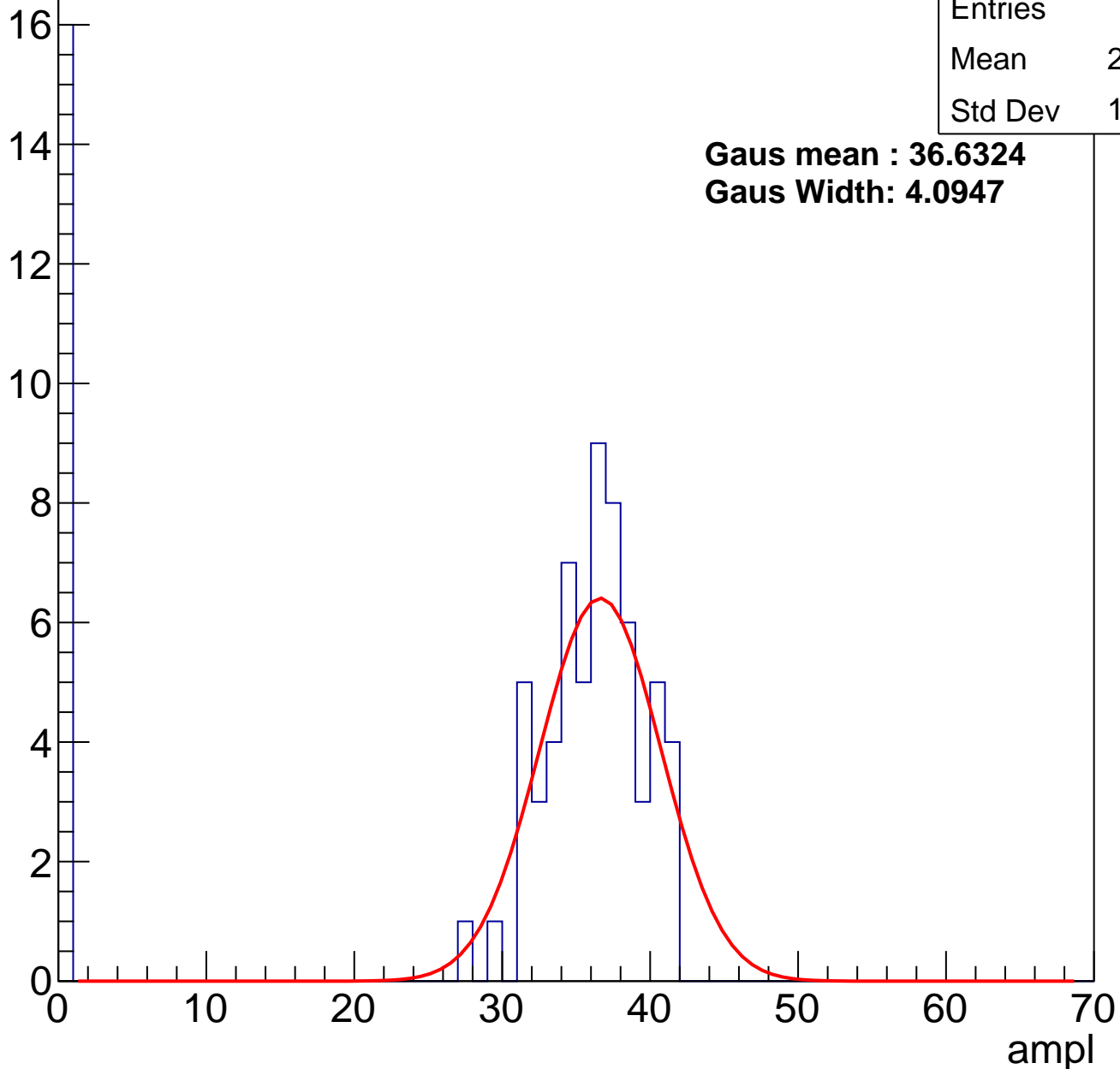
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	28.32
Std Dev	14.78

Gaus mean : 36.6324

Gaus Width: 4.0947

Entry



B1L103S, U1-ch18, adc2

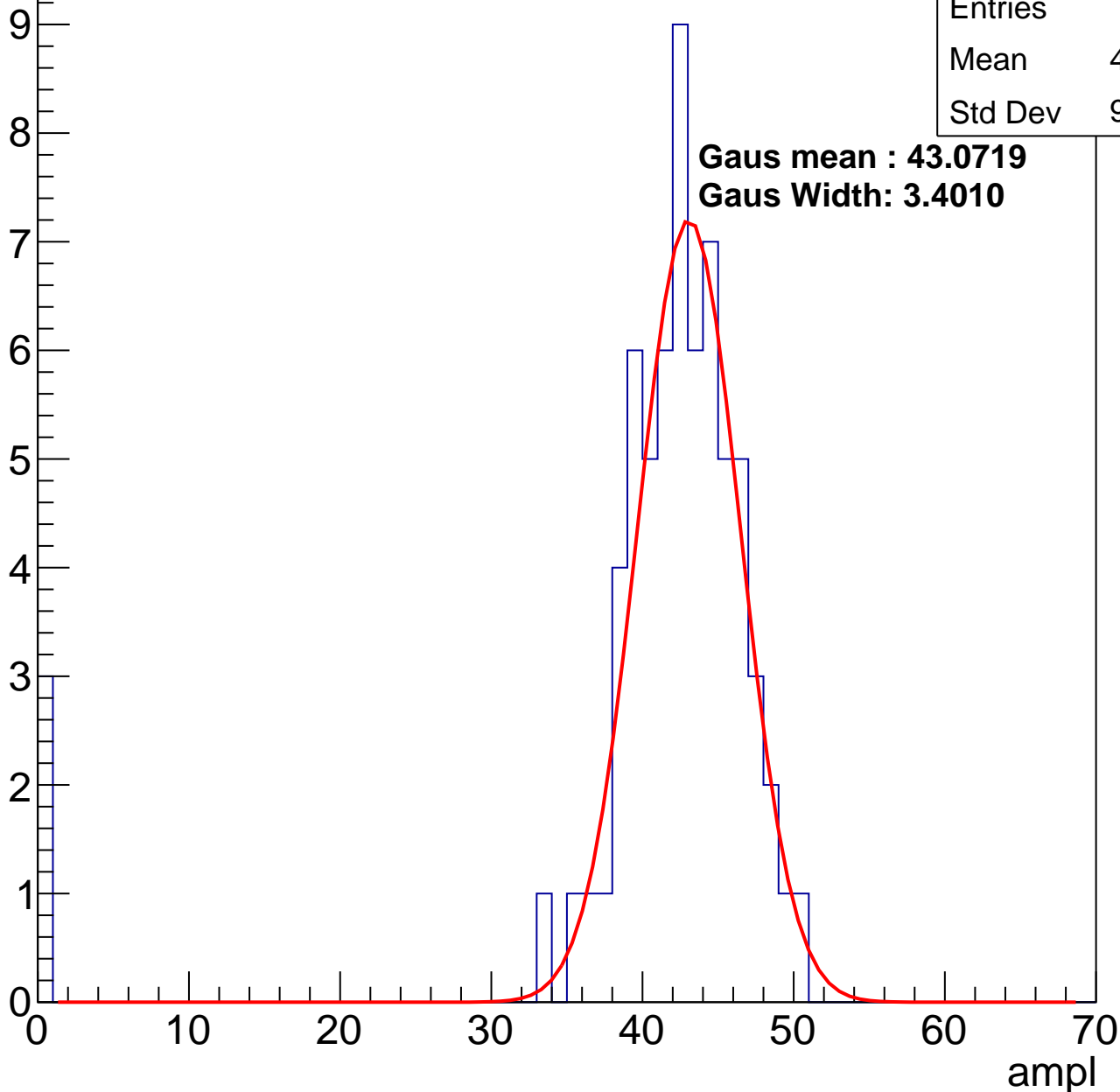
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	40.42
Std Dev	9.367

Gaus mean : 43.0719

Gaus Width: 3.4010

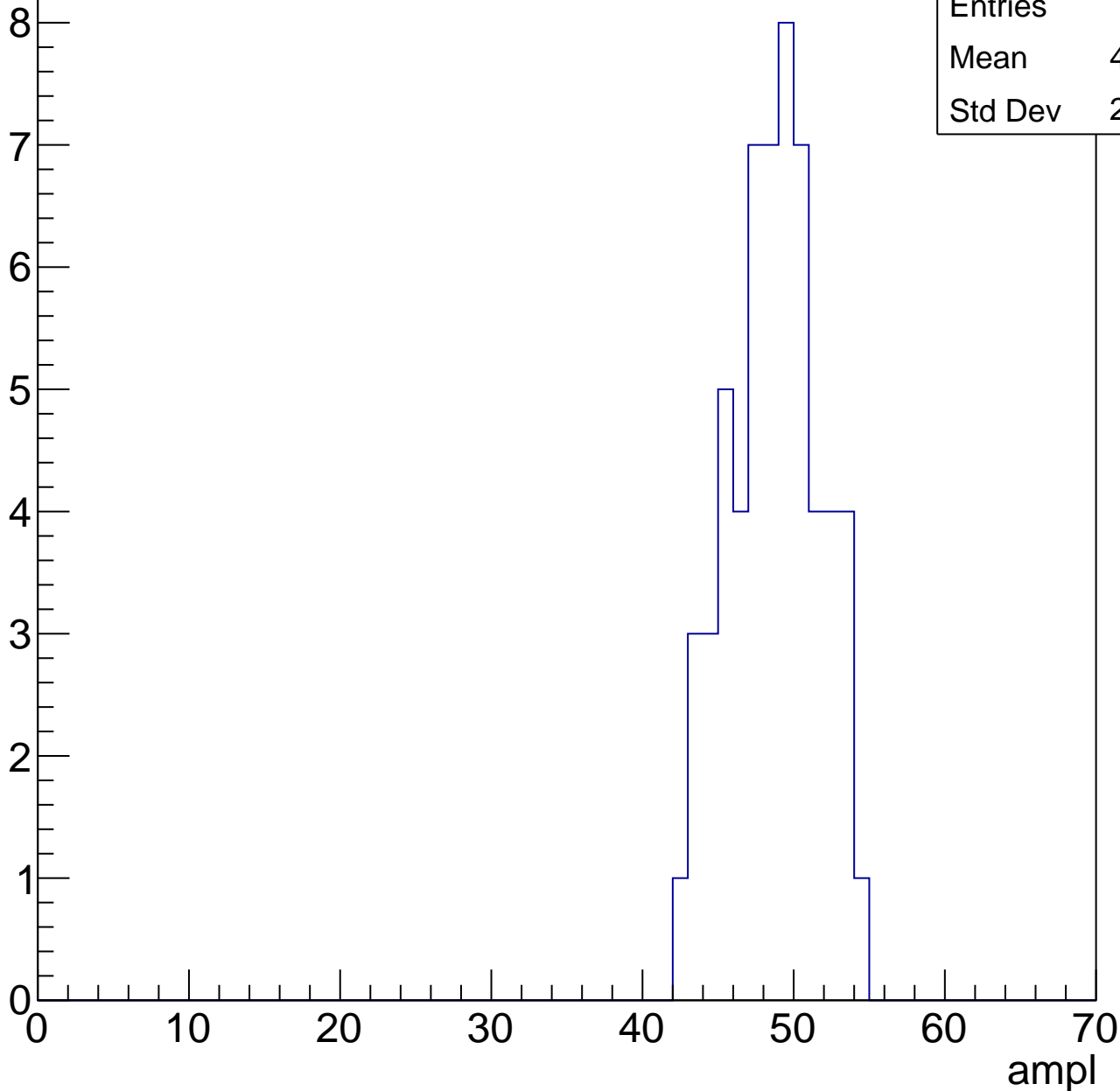


B1L103S, U1-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

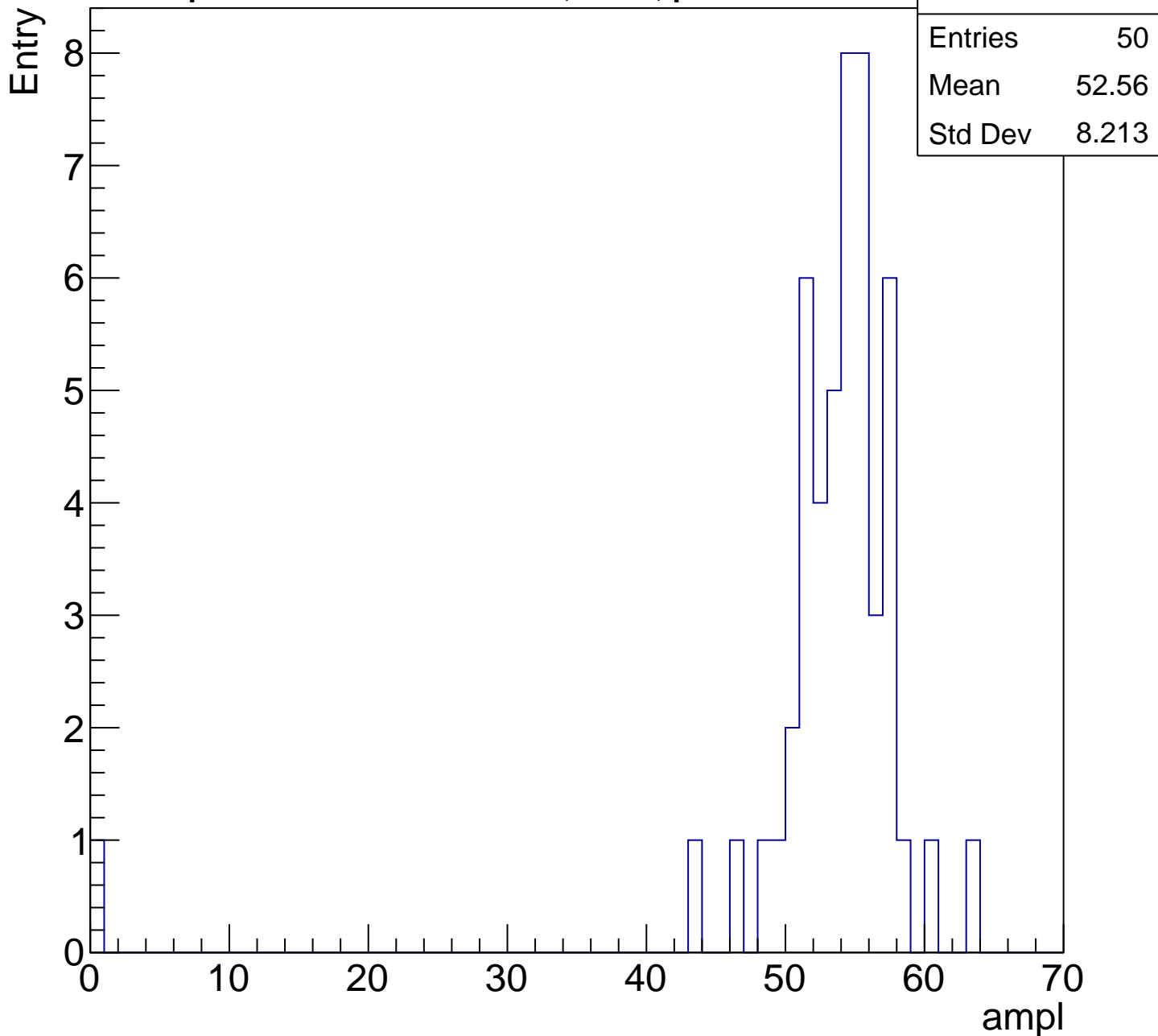
Entry

Entries	58
Mean	48.22
Std Dev	2.925



B1L103S, U1-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

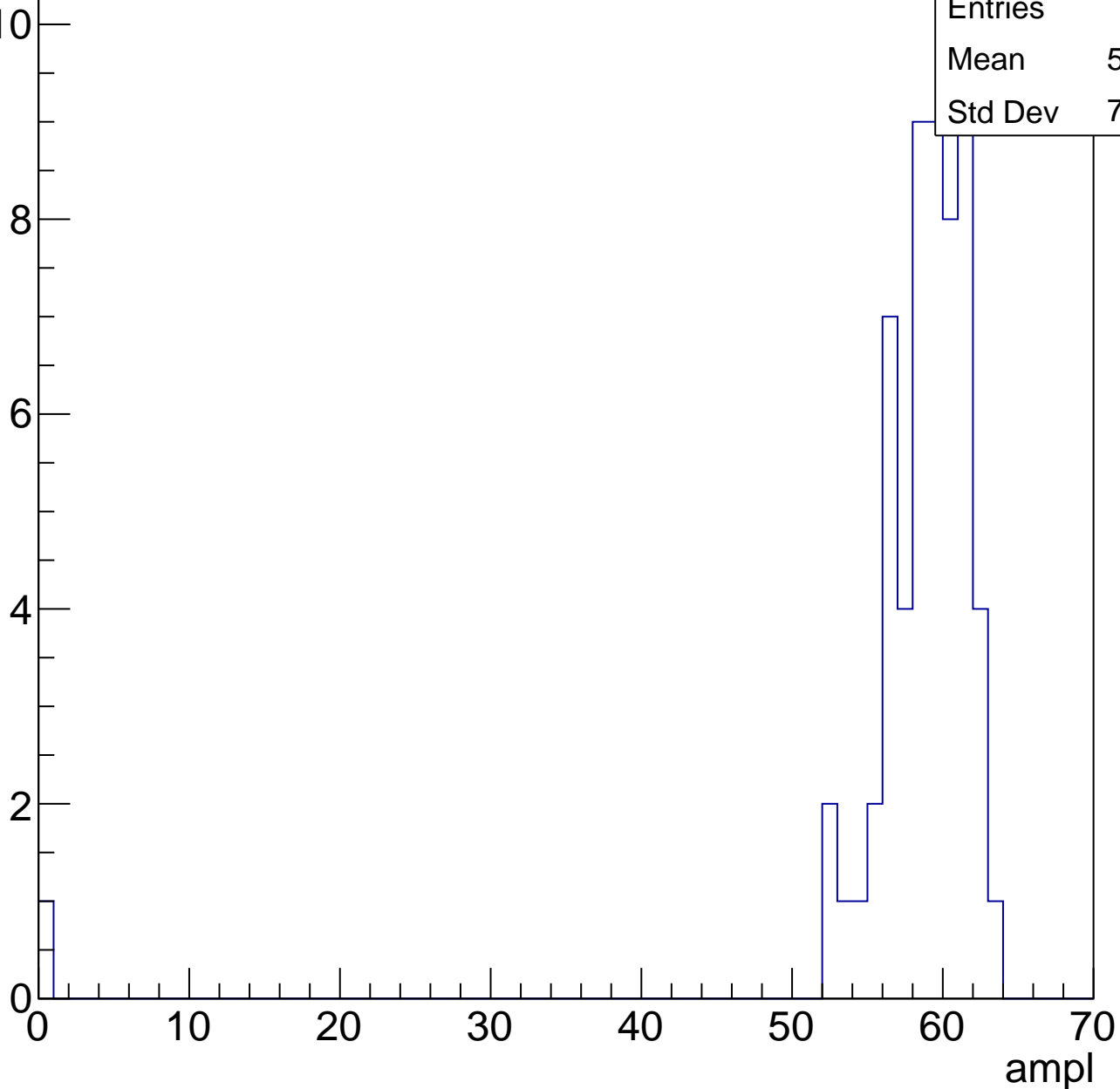


B1L103S, U1-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.54
Std Dev	7.956

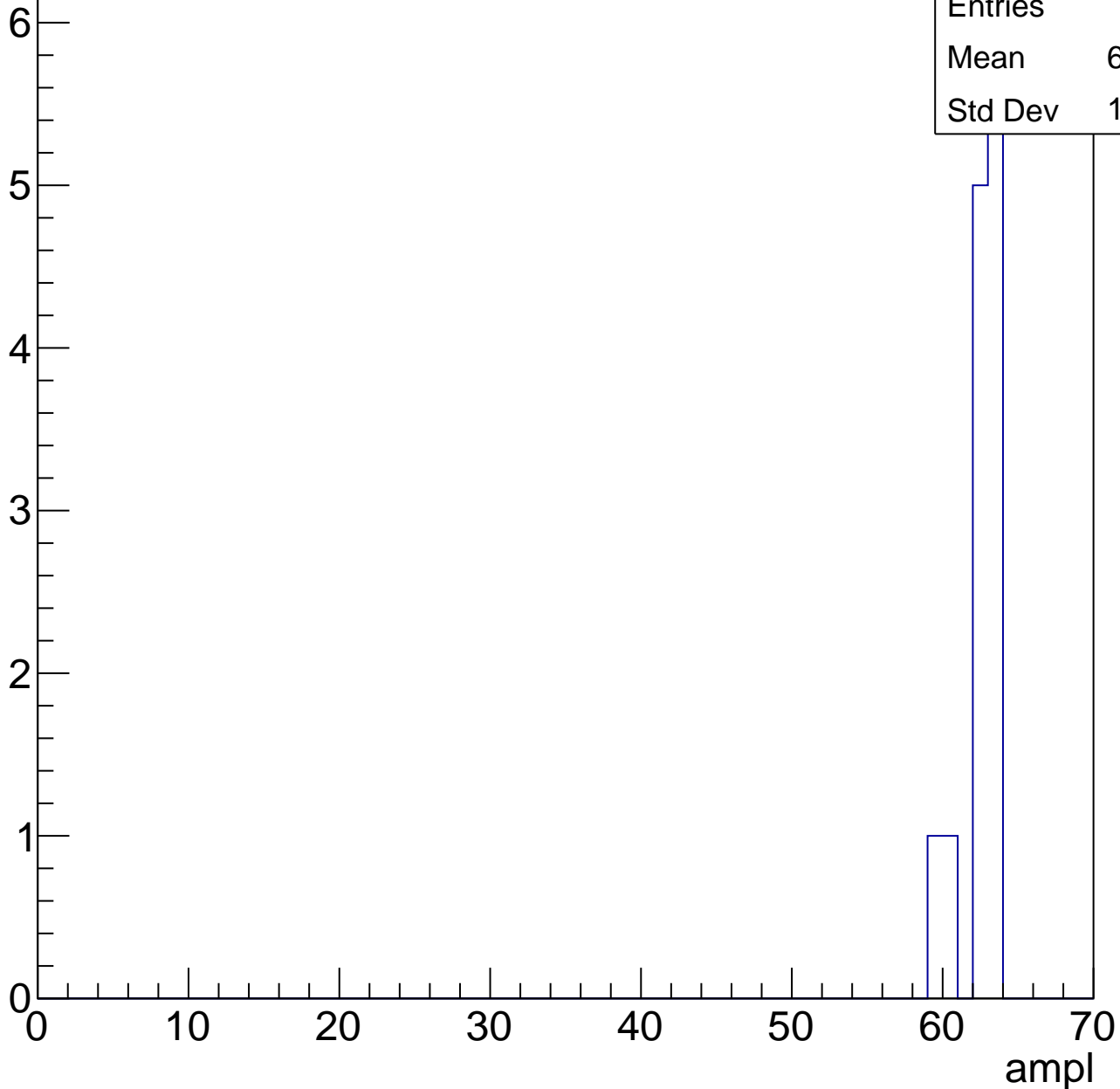


B1L103S, U1-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	62.08
Std Dev	1.206



B1L103S, U1-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



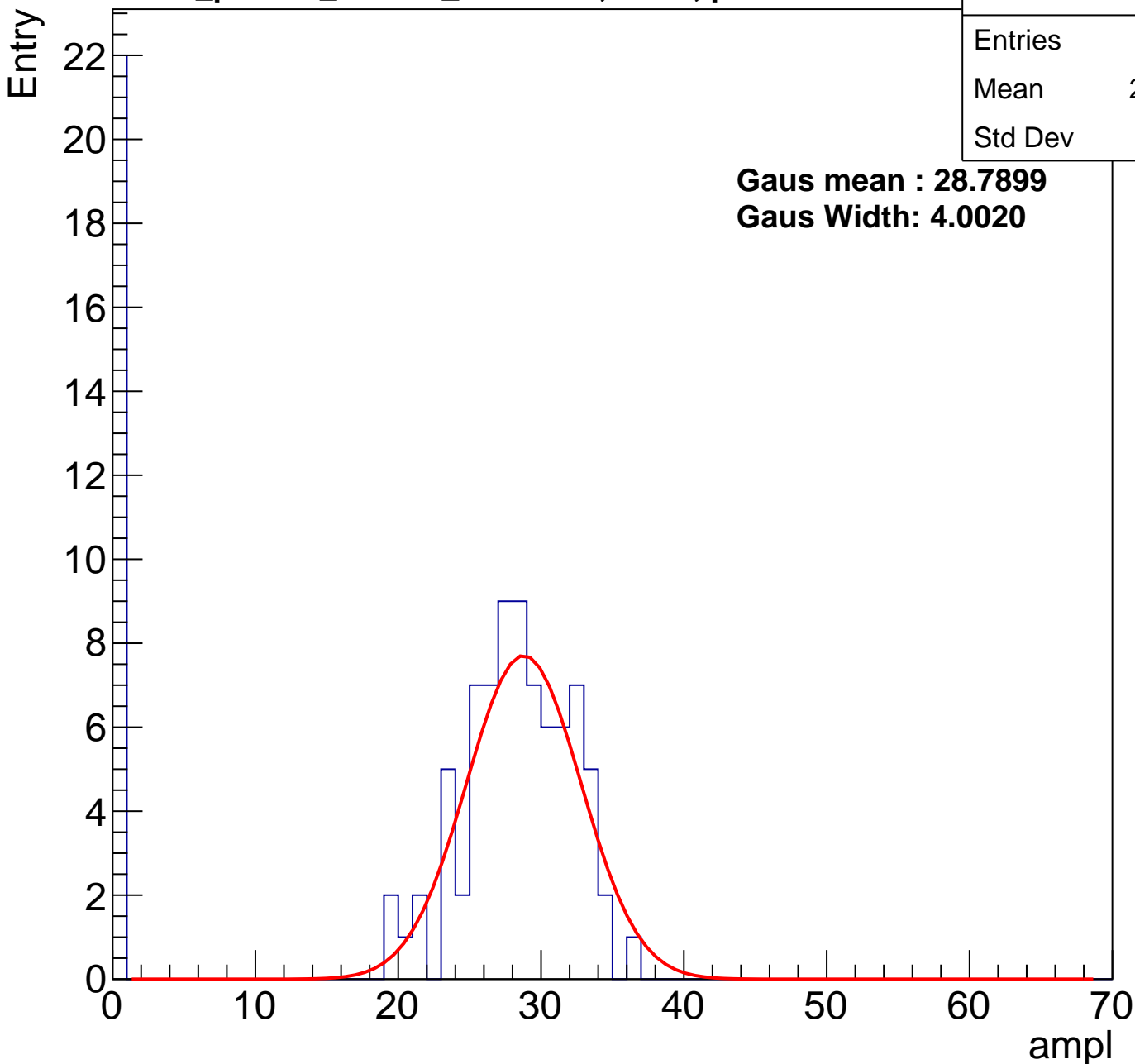
B1L103S, U1-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	21.77
Std Dev	12

Gaus mean : 28.7899

Gaus Width: 4.0020



B1L103S, U1-ch19, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	35.03
Std Dev	3.142

Gaus mean : 35.2631

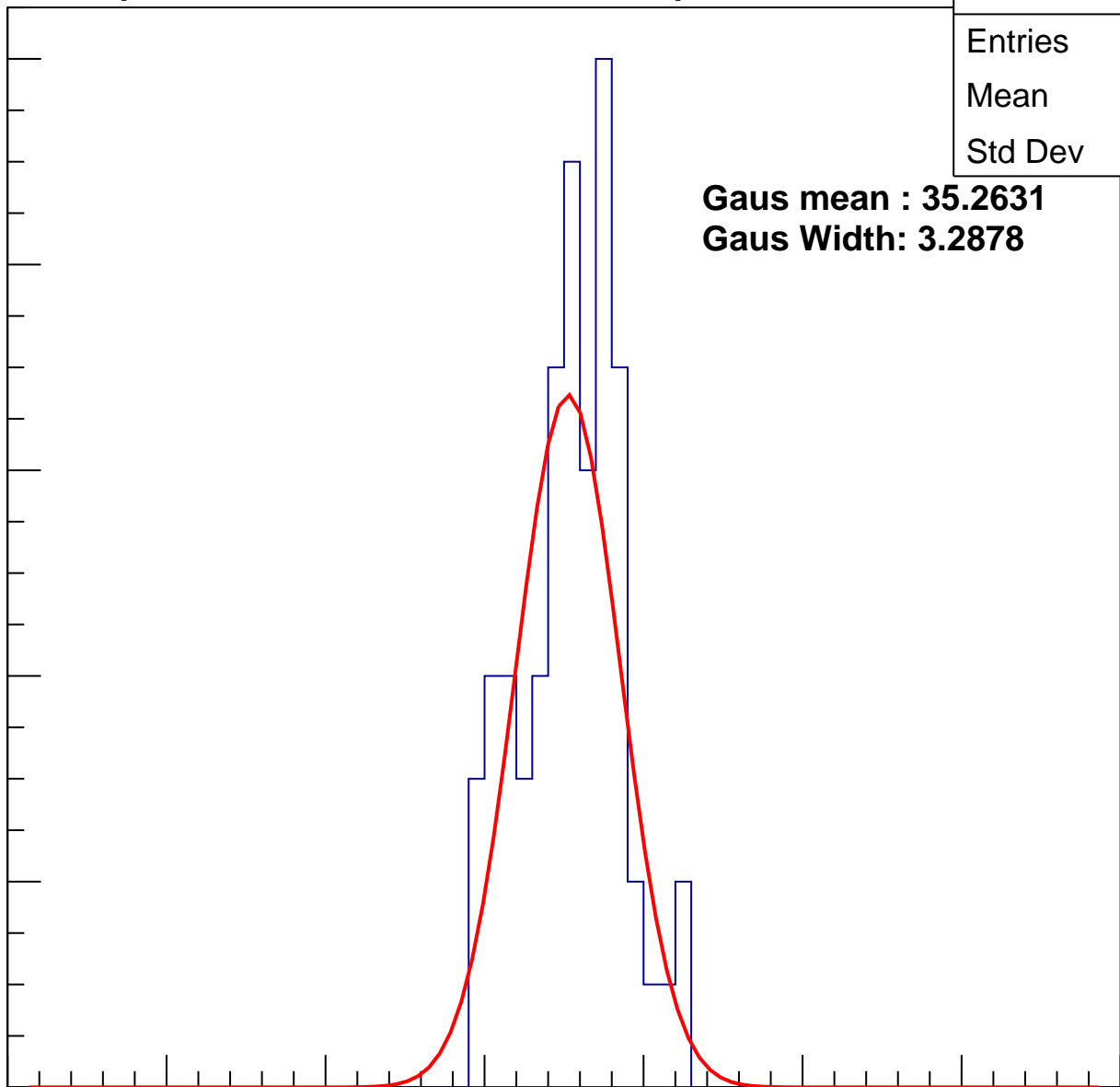
Gaus Width: 3.2878

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch19, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

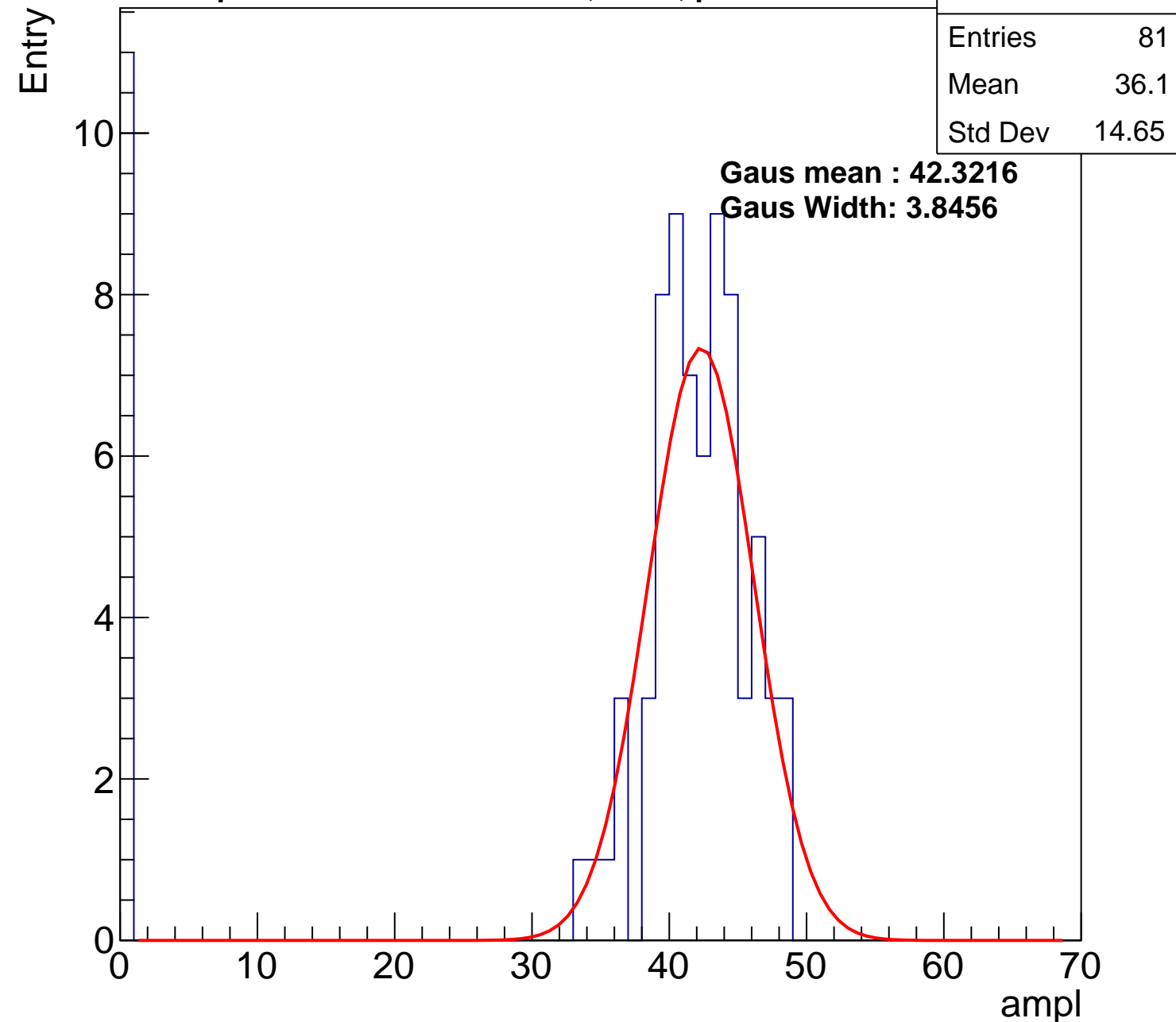
70

ampl

Entries	81
Mean	36.1
Std Dev	14.65

Gaus mean : 42.3216

Gaus Width: 3.8456

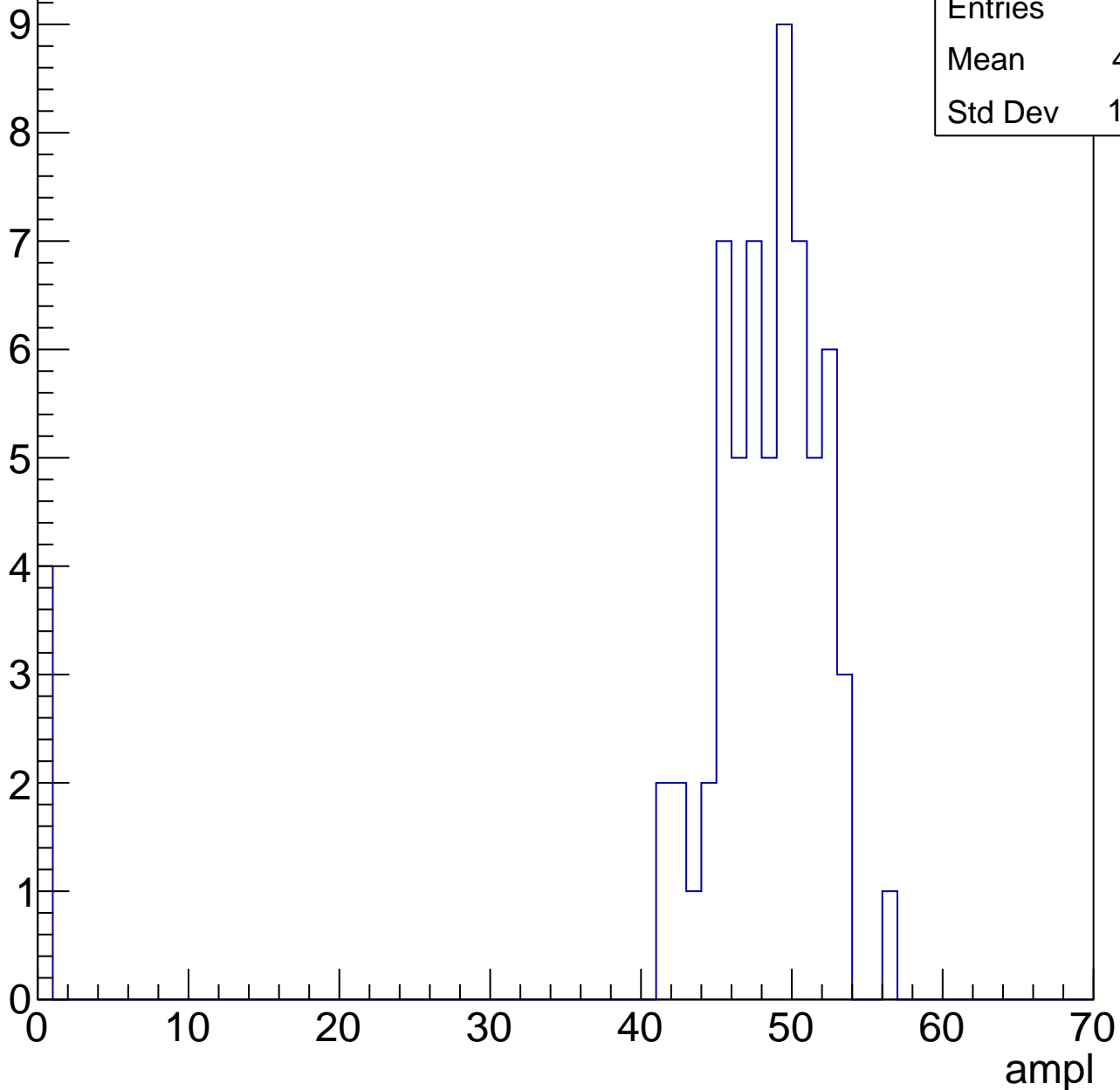


B1L103S, U1-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	45.21
Std Dev	11.89

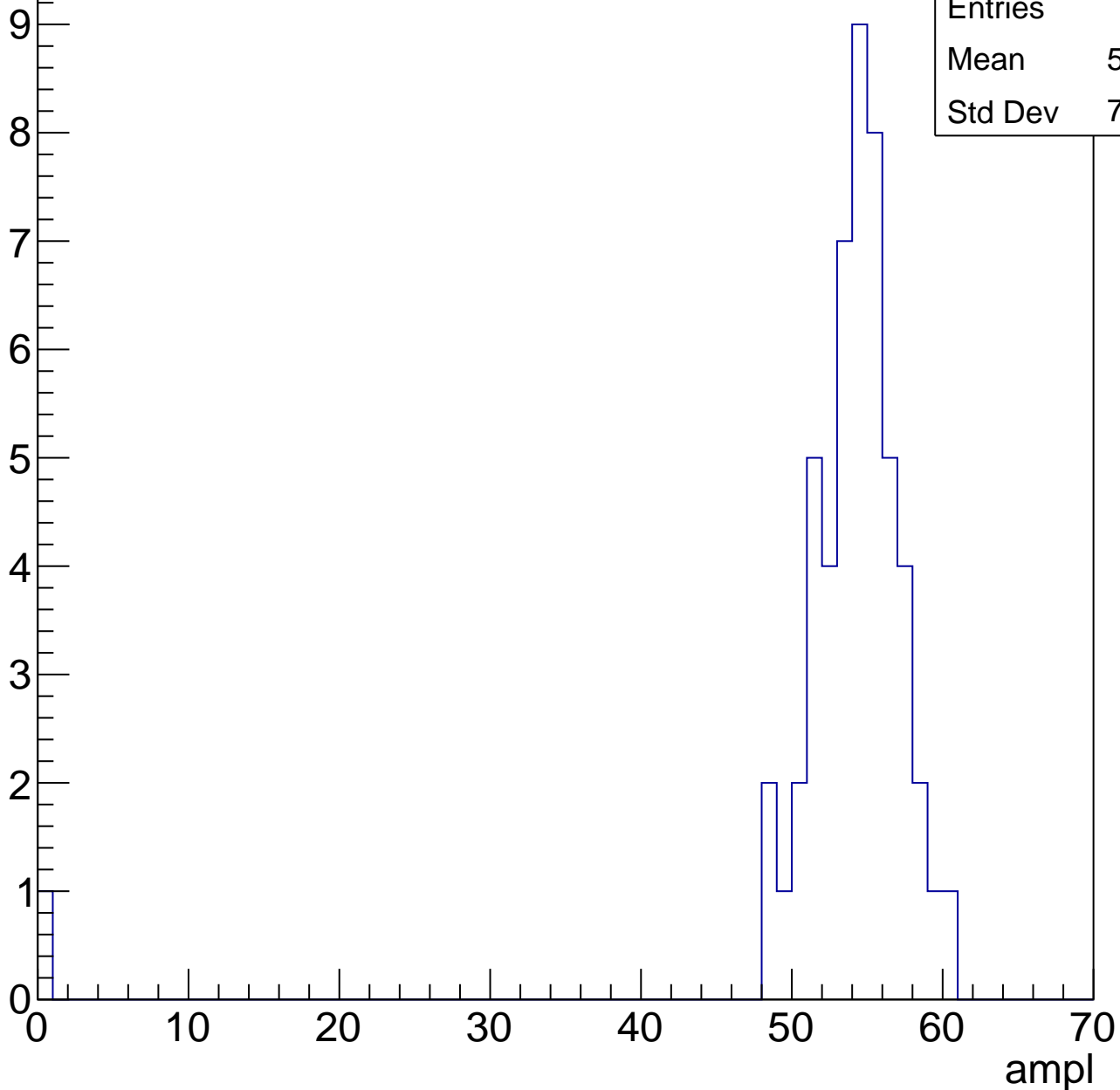


B1L103S, U1-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	52.85
Std Dev	7.846

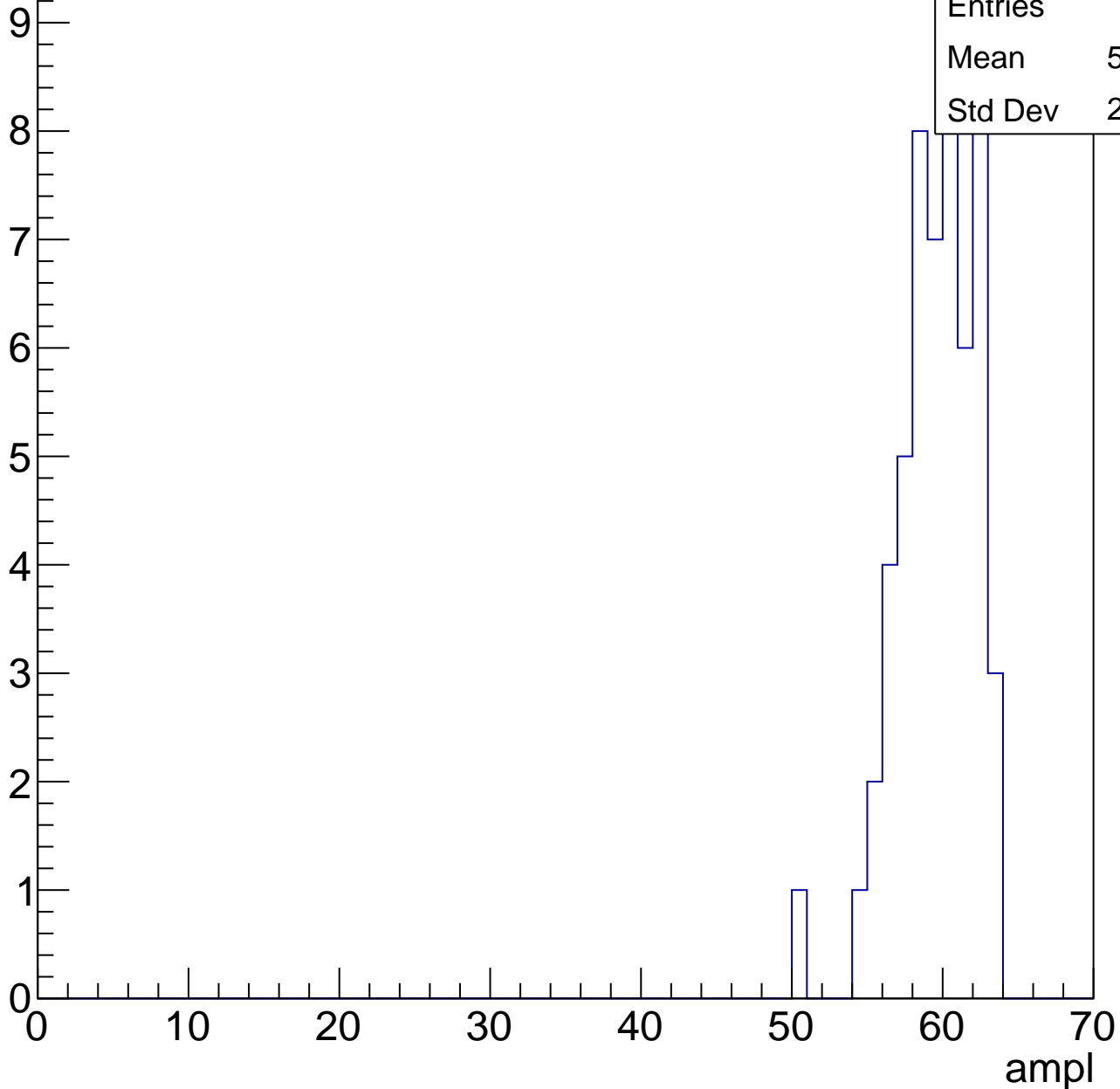


B1L103S, U1-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	59.15
Std Dev	2.568

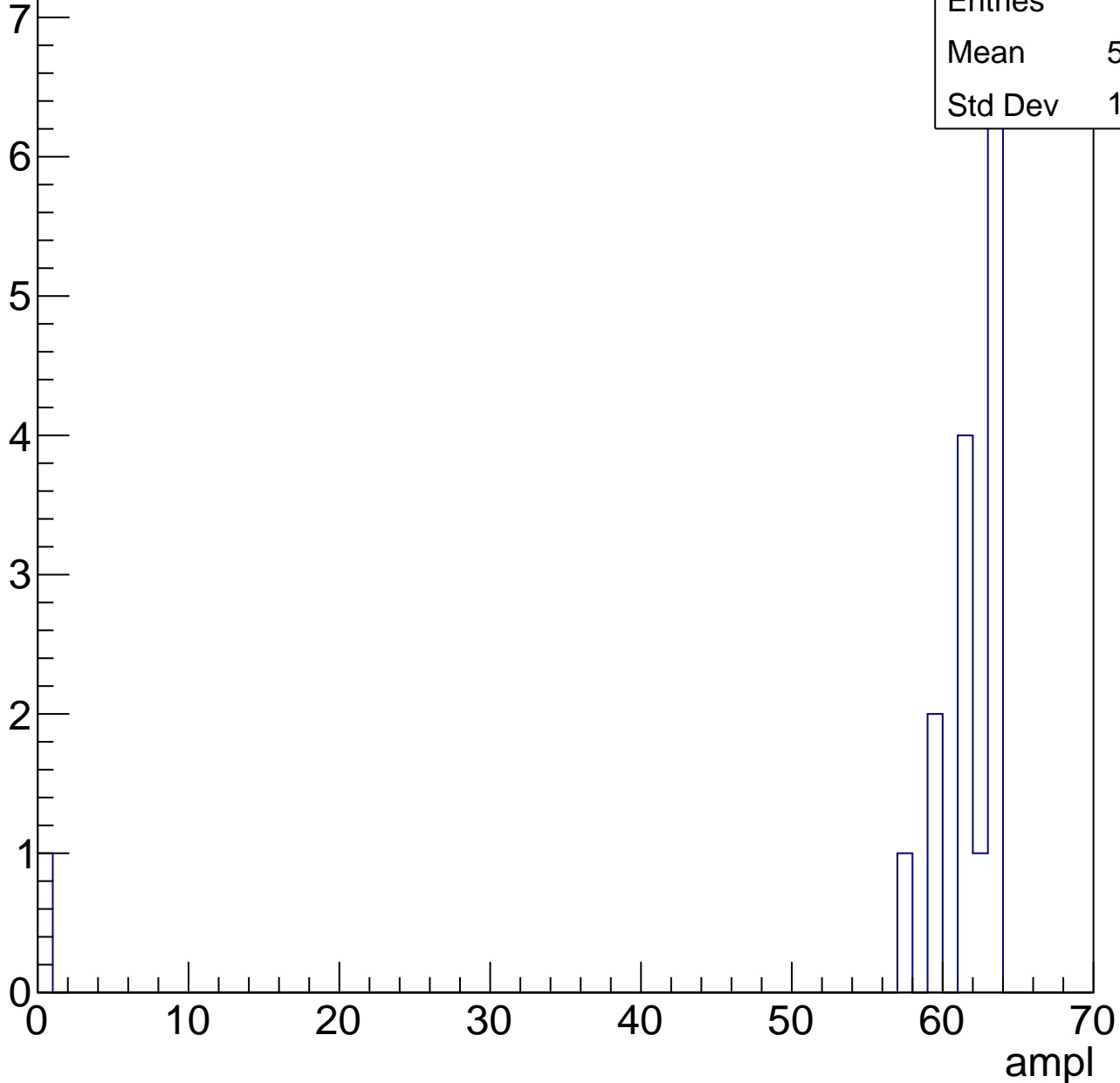


B1L103S, U1-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.62
Std Dev	14.98



B1L103S, U1-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

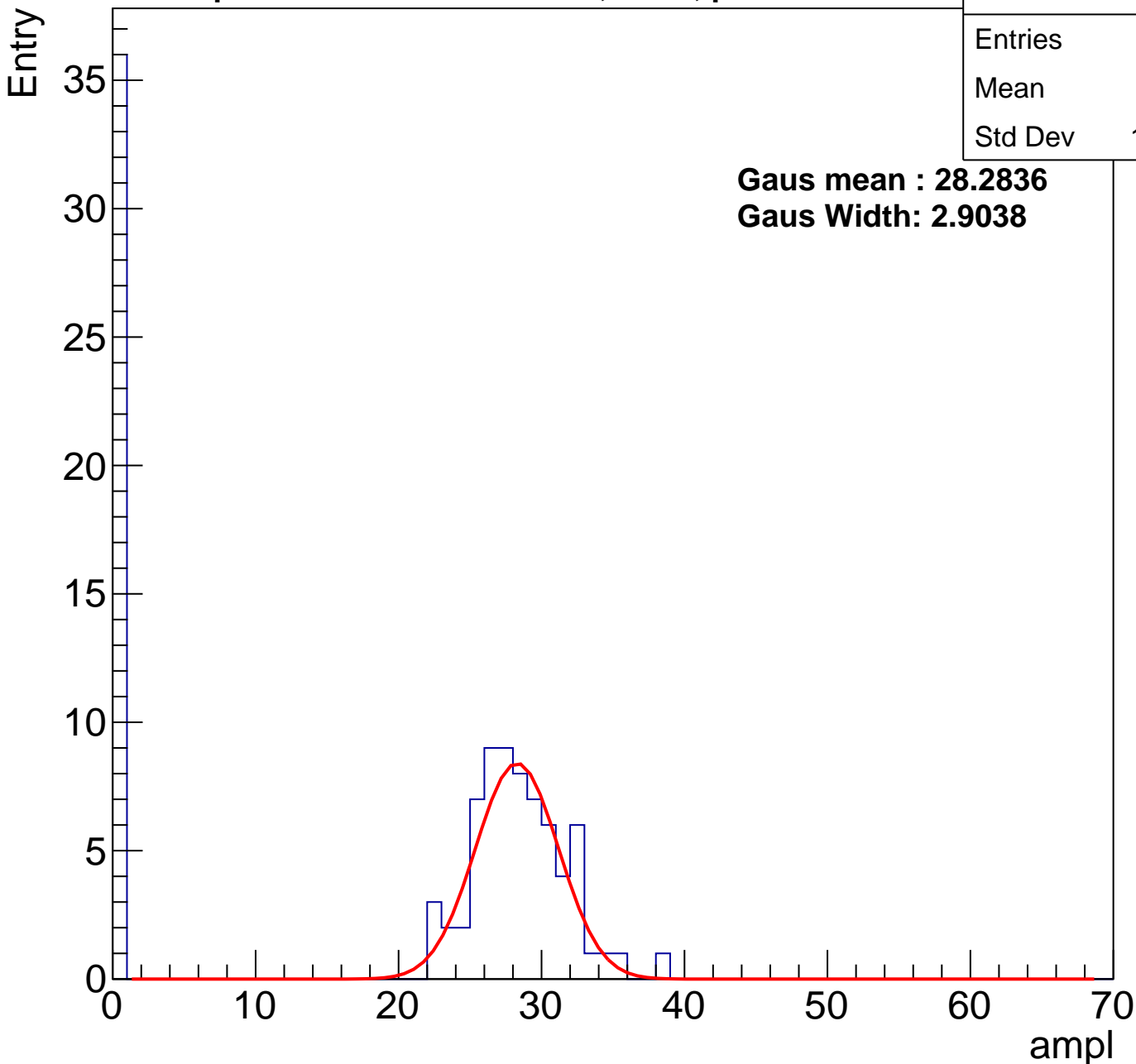
B1L103S, U1-ch20, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	18.2
Std Dev	13.59

Gaus mean : 28.2836

Gaus Width: 2.9038



B1L103S, U1-ch20, adc1

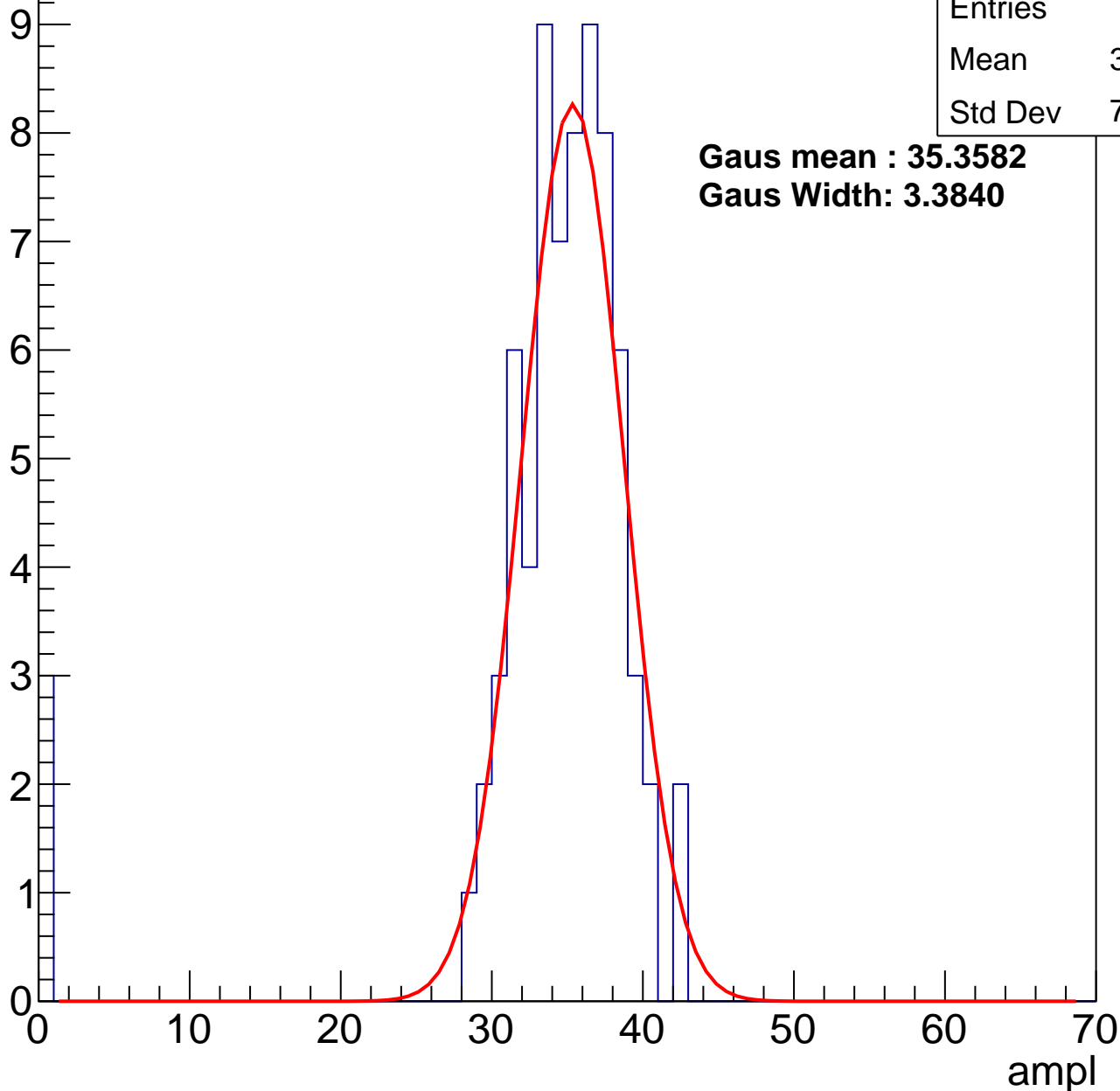
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.34
Std Dev	7.528

Gaus mean : 35.3582

Gaus Width: 3.3840



B1L103S, U1-ch20, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

14
12
10
8
6
4
2
0

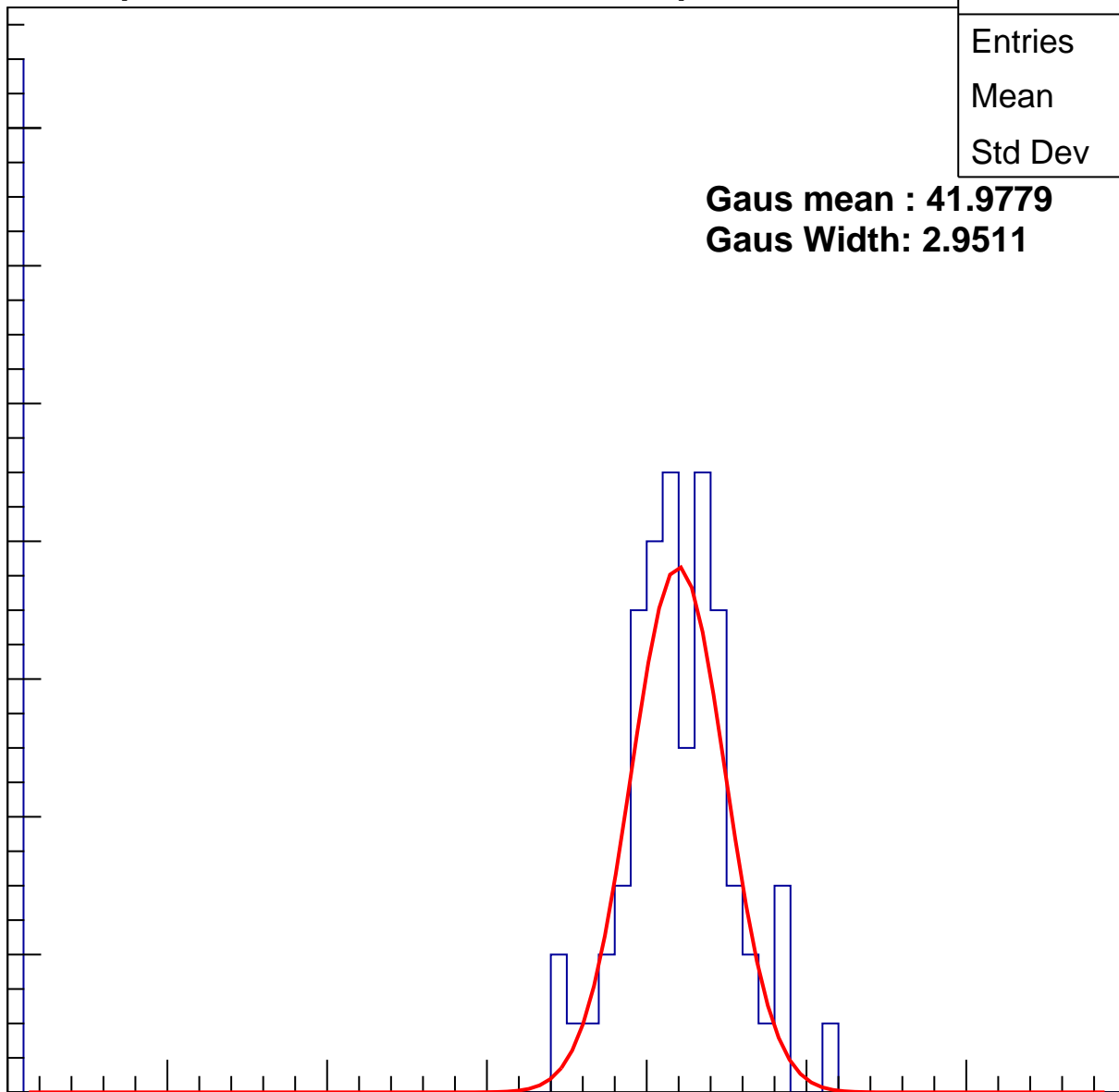
Entries	79
Mean	33.71
Std Dev	16.59

Gaus mean : 41.9779

Gaus Width: 2.9511

0 10 20 30 40 50 60 70

ampl

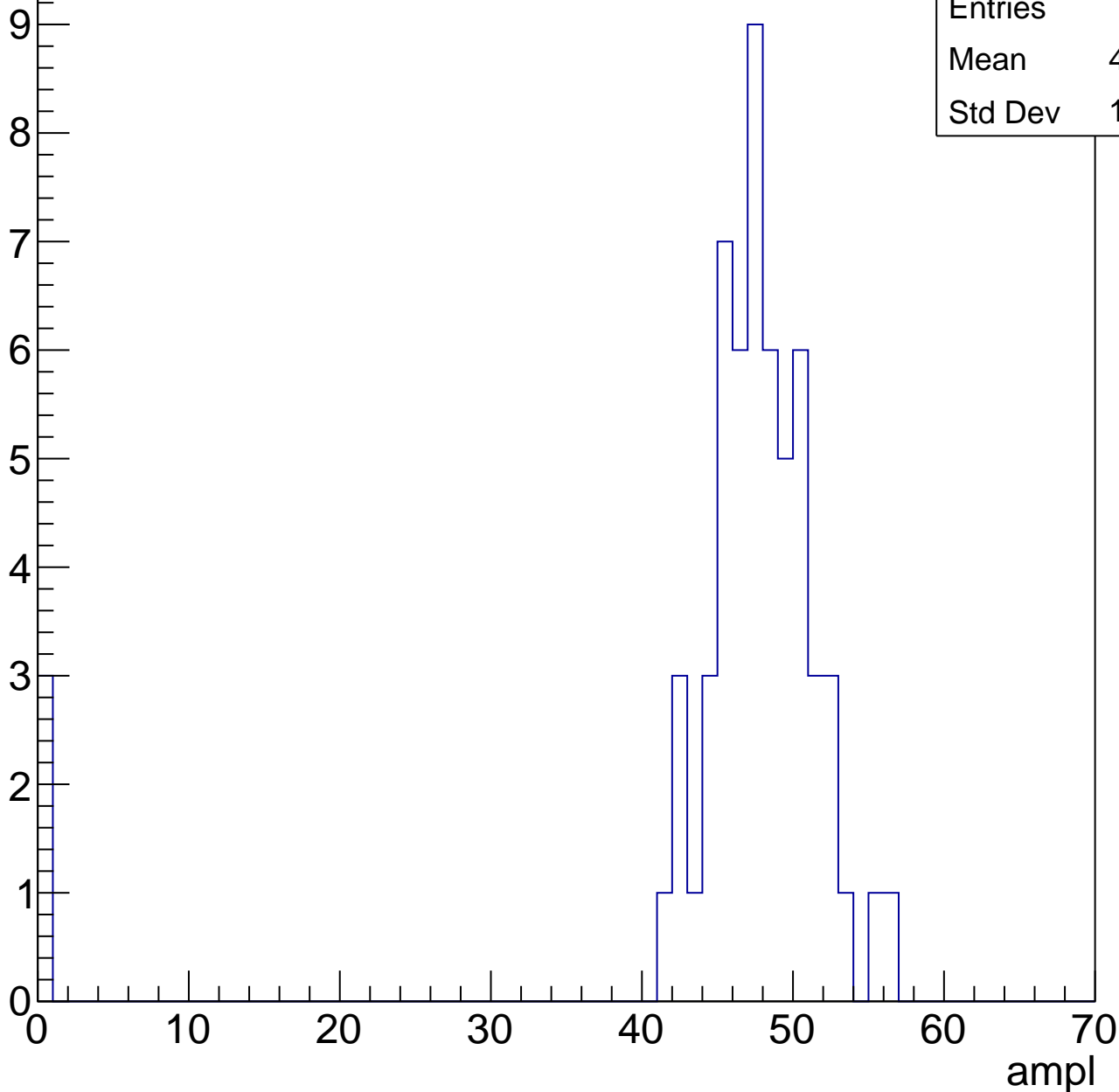


B1L103S, U1-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	45.12
Std Dev	10.88

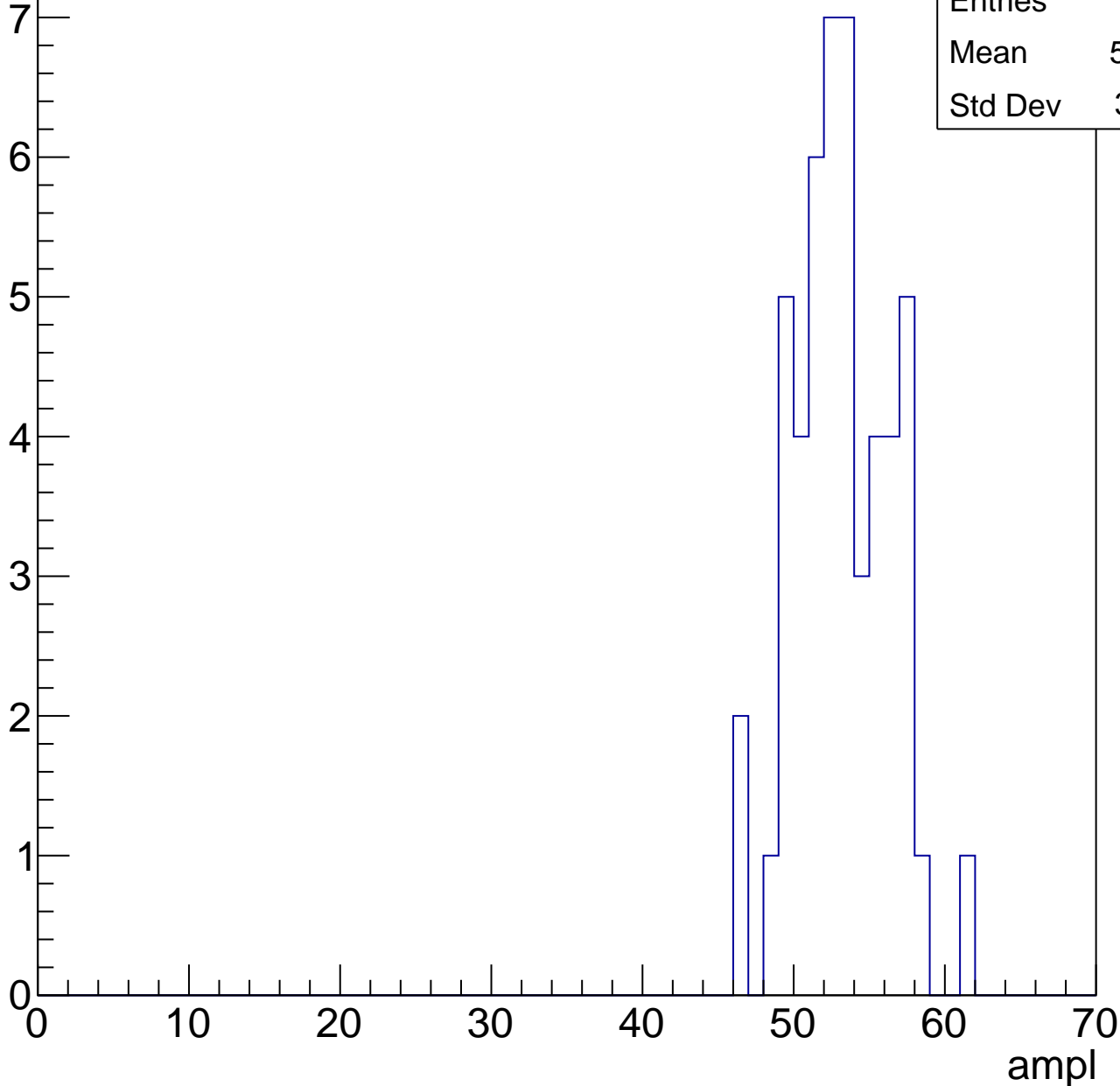


B1L103S, U1-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

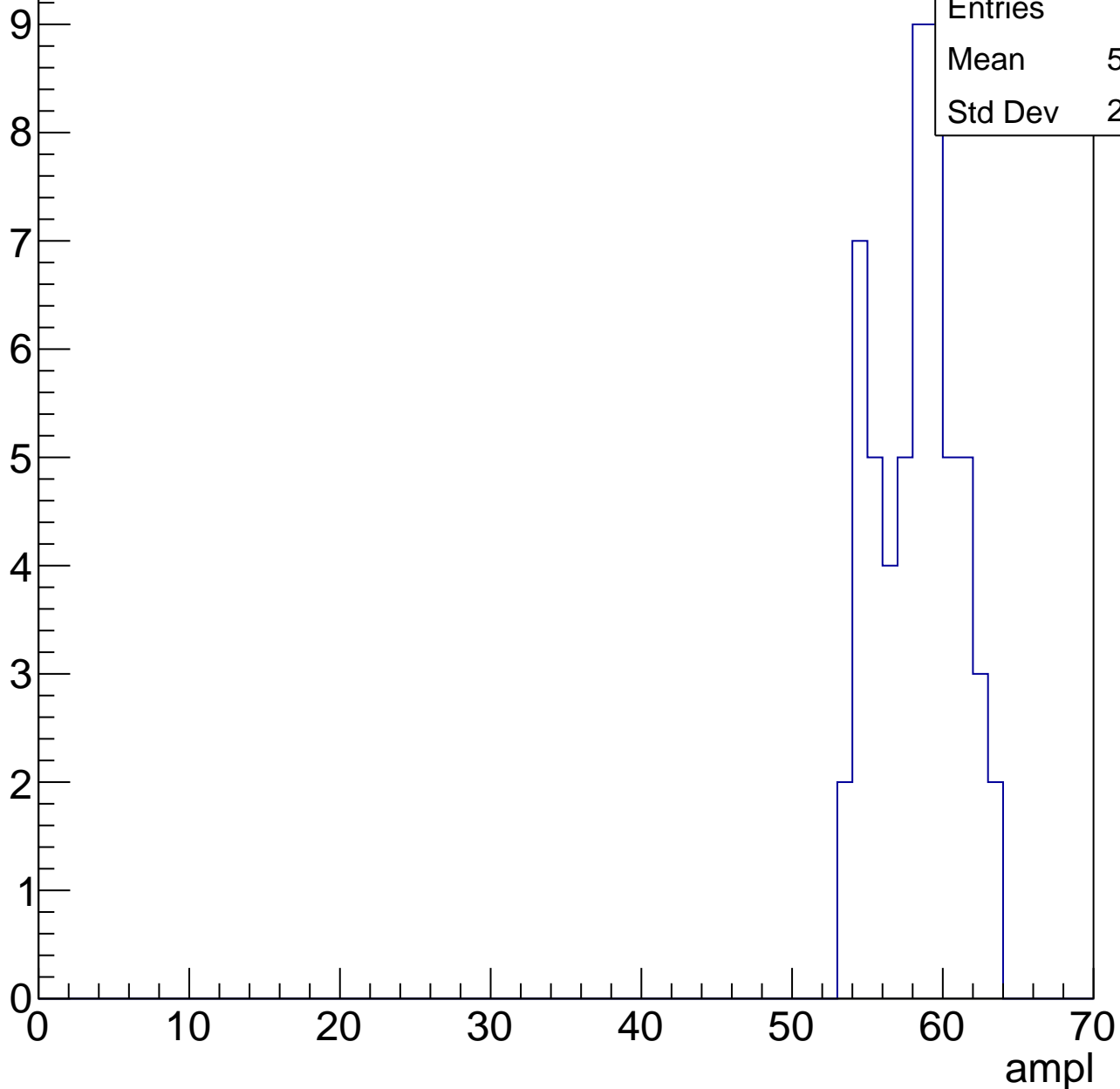
Entries	50
Mean	52.72
Std Dev	3.131



B1L103S, U1-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



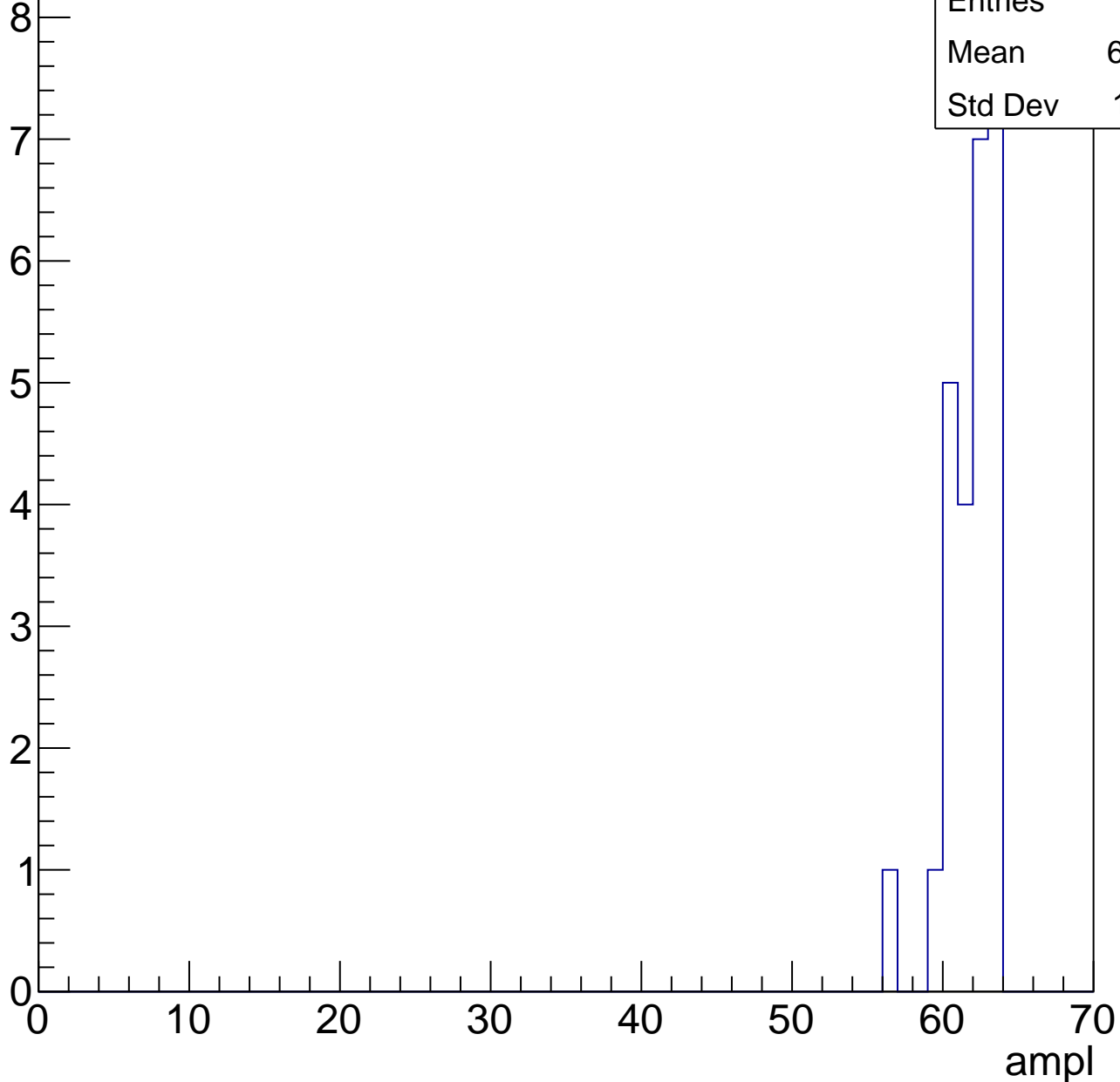
Entries	56
Mean	57.82
Std Dev	2.667

B1L103S, U1-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	61.42
Std Dev	1.621



B1L103S, U1-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch21, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	20.71
Std Dev	14.02

Gaus mean : 29.8457

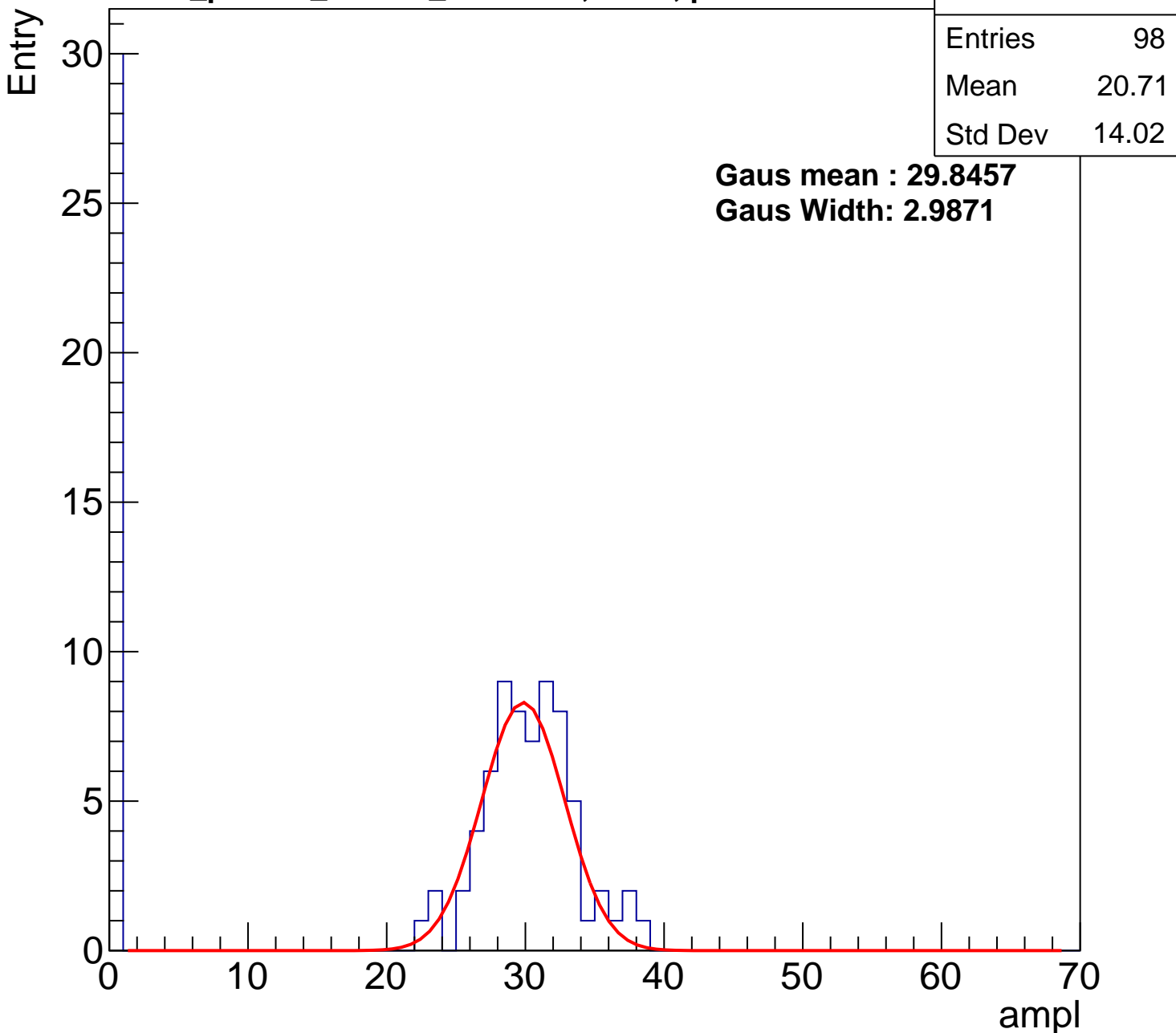
Gaus Width: 2.9871

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch21, adc1

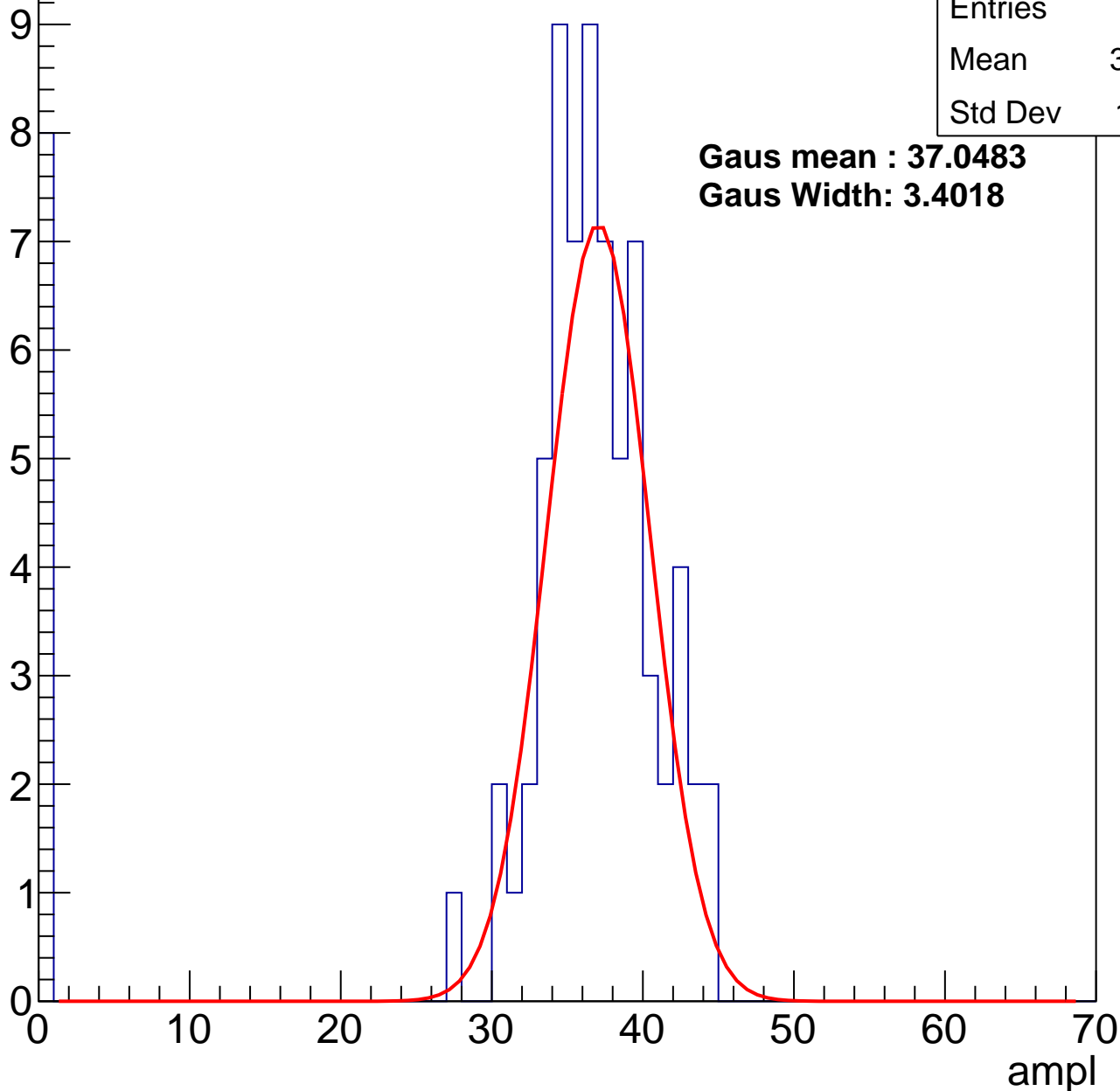
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.74
Std Dev	11.71

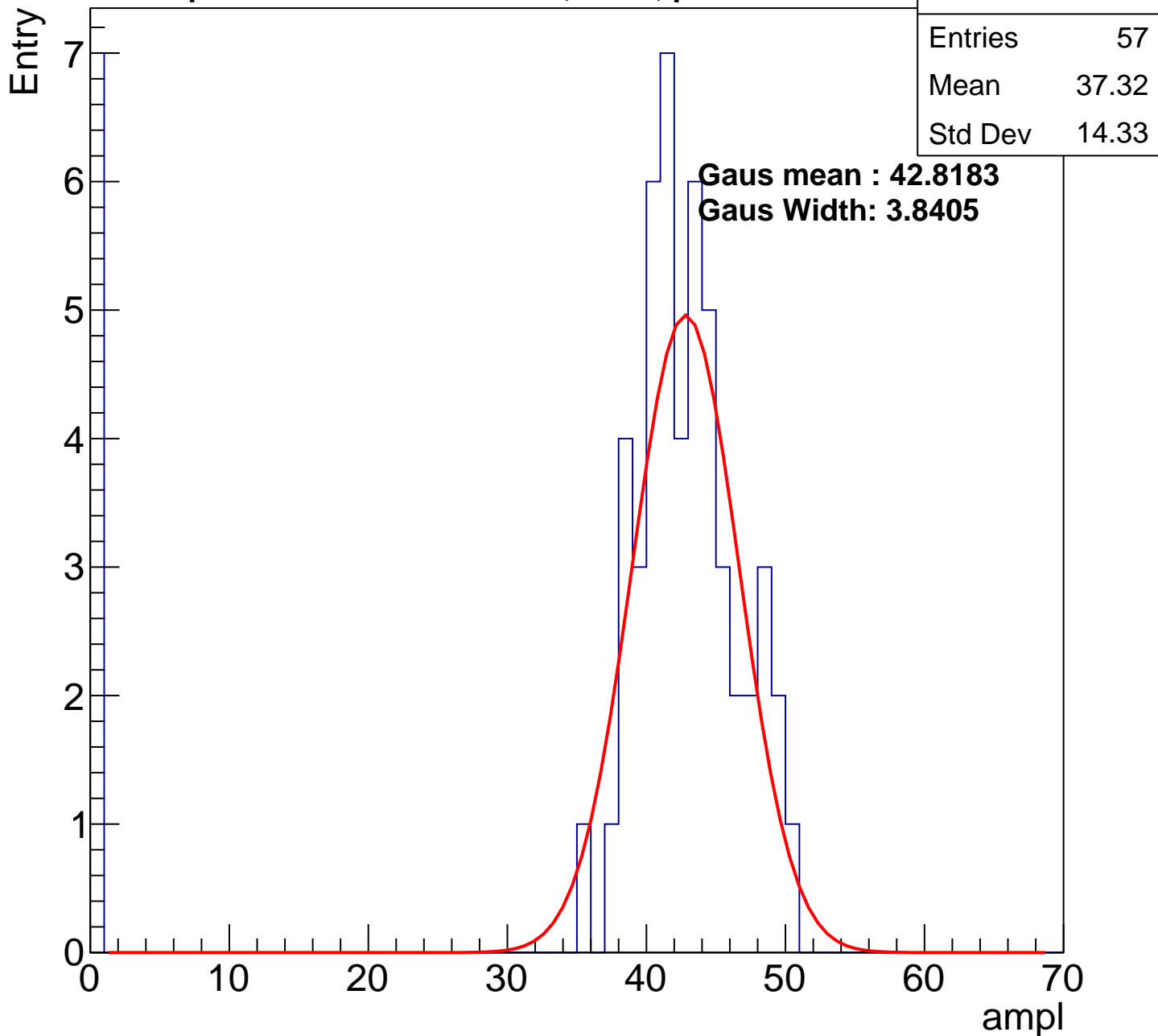
Gaus mean : 37.0483

Gaus Width: 3.4018



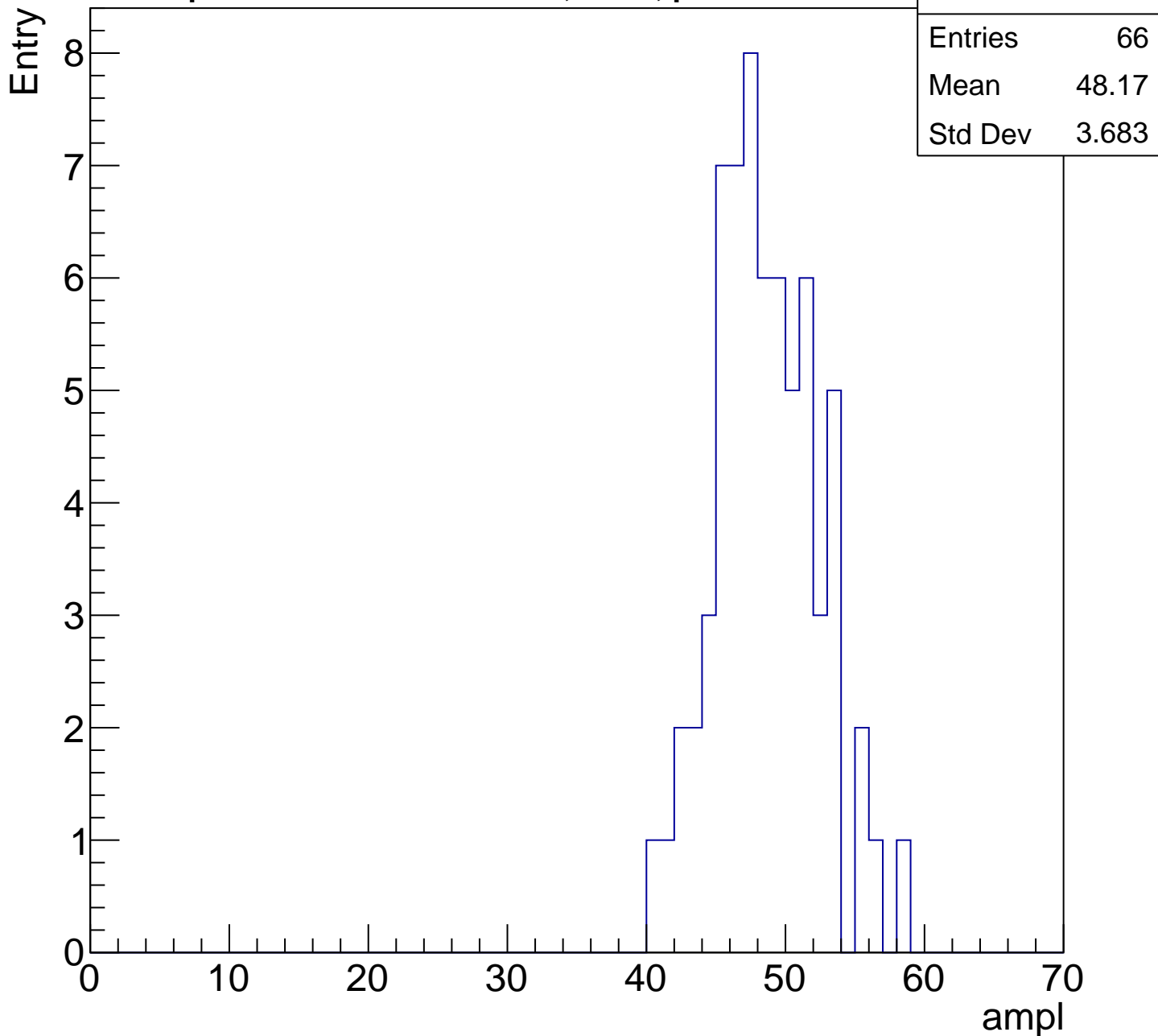
B1L103S, U1-ch21, adc2

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

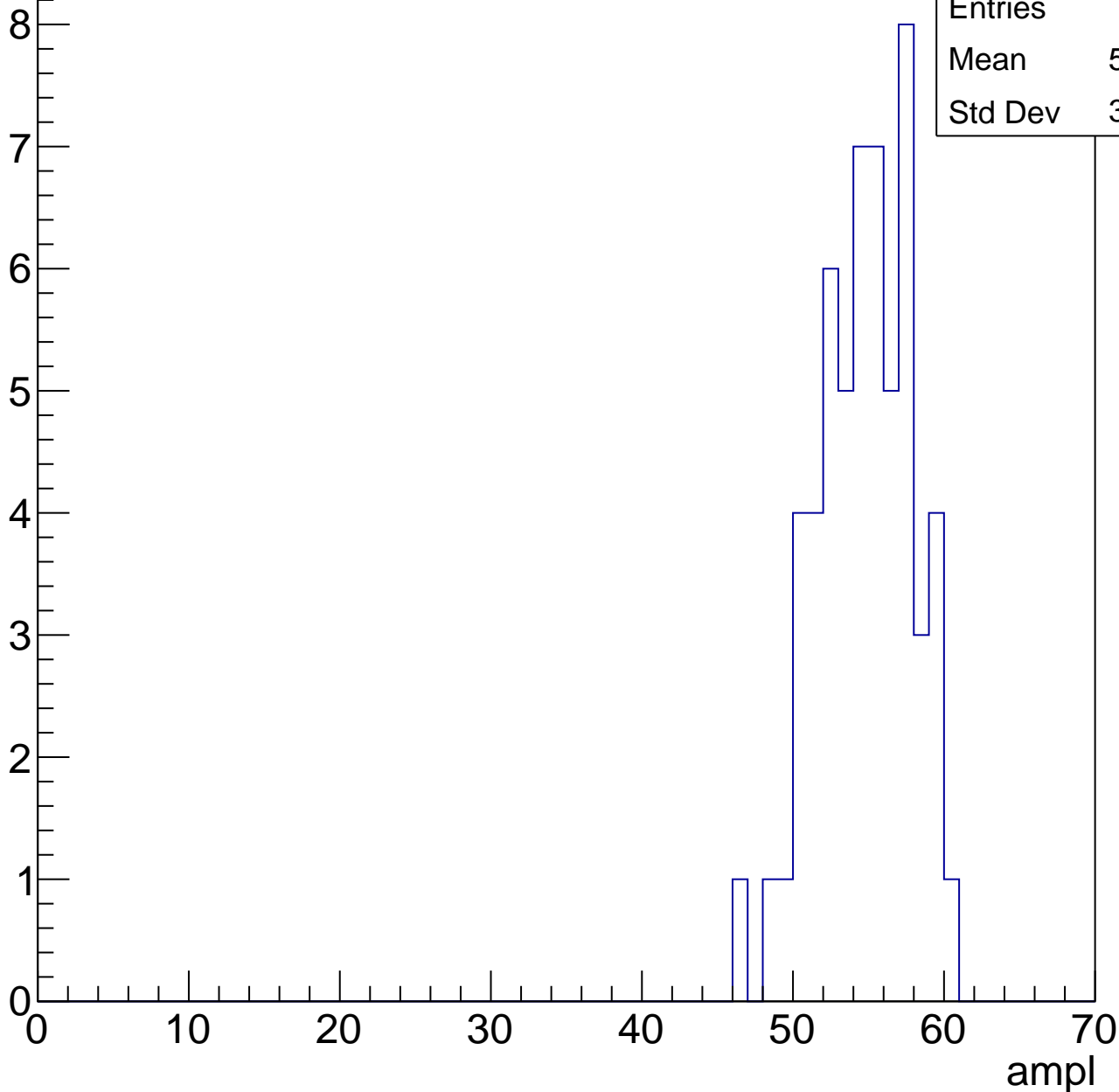


B1L103S, U1-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.26
Std Dev	3.058

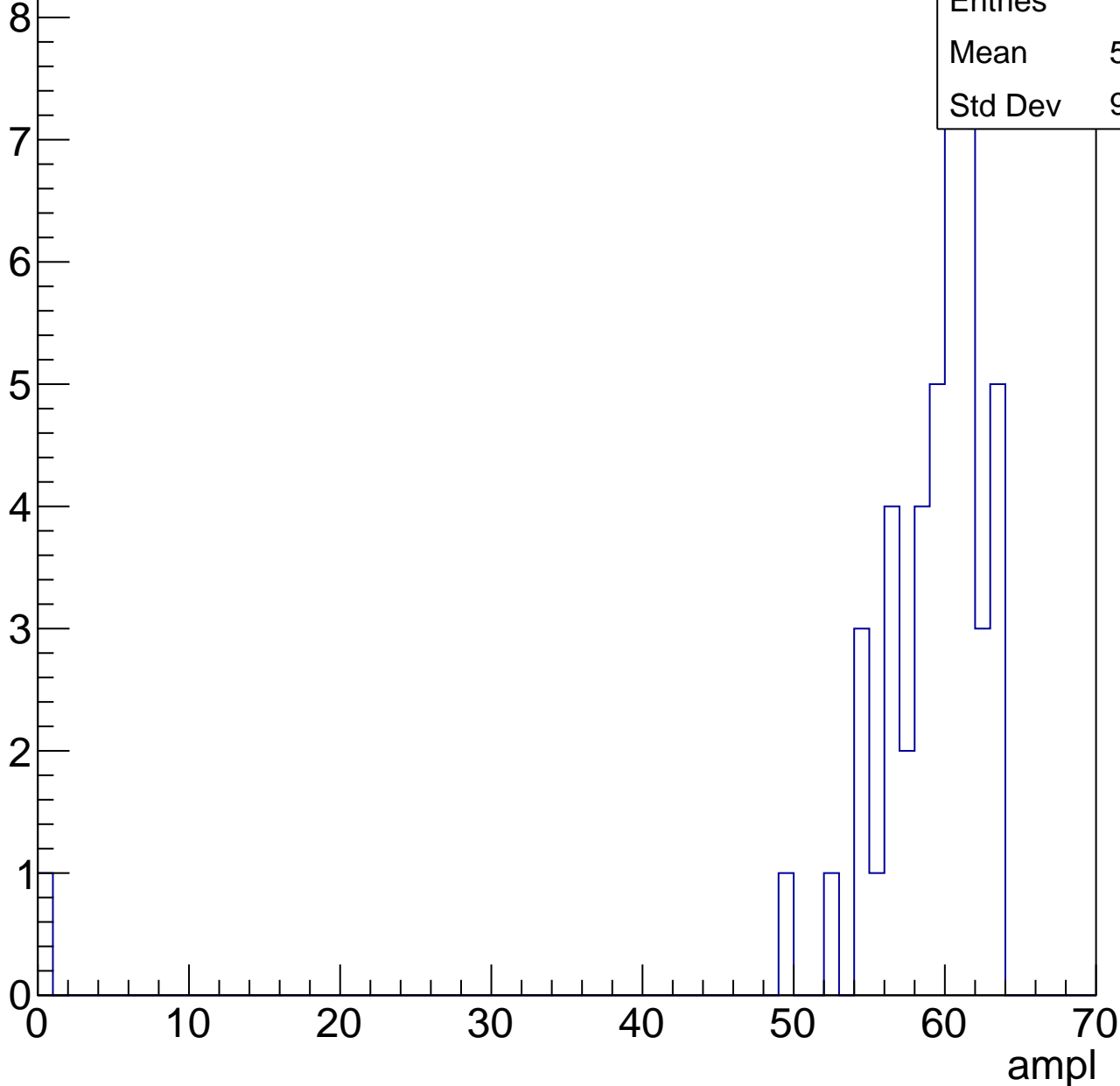


B1L103S, U1-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	57.65
Std Dev	9.128

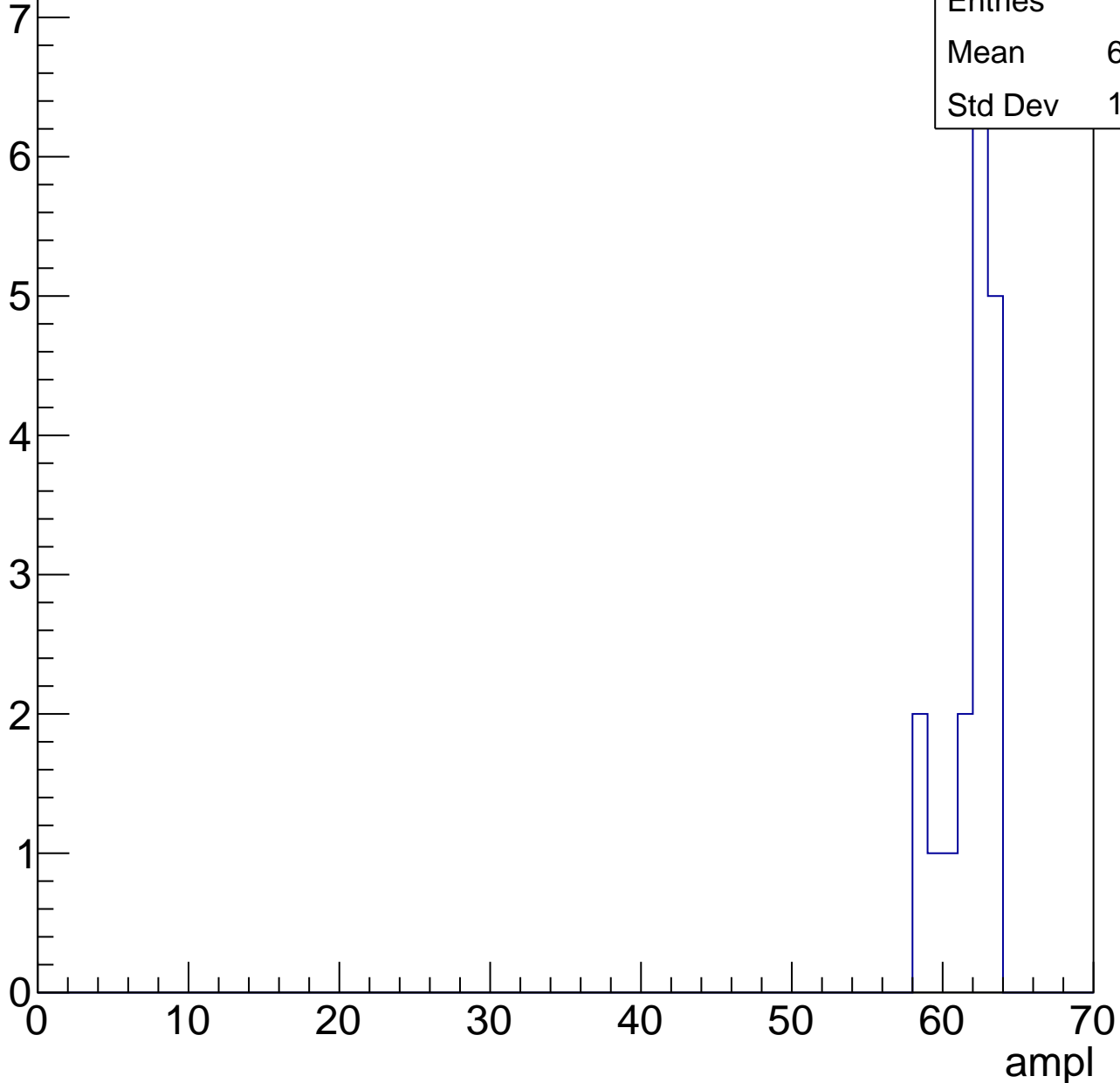


B1L103S, U1-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.44
Std Dev	1.606



B1L103S, U1-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U1-ch22, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	17.14
Std Dev	13.72

Gaus mean : 28.1723

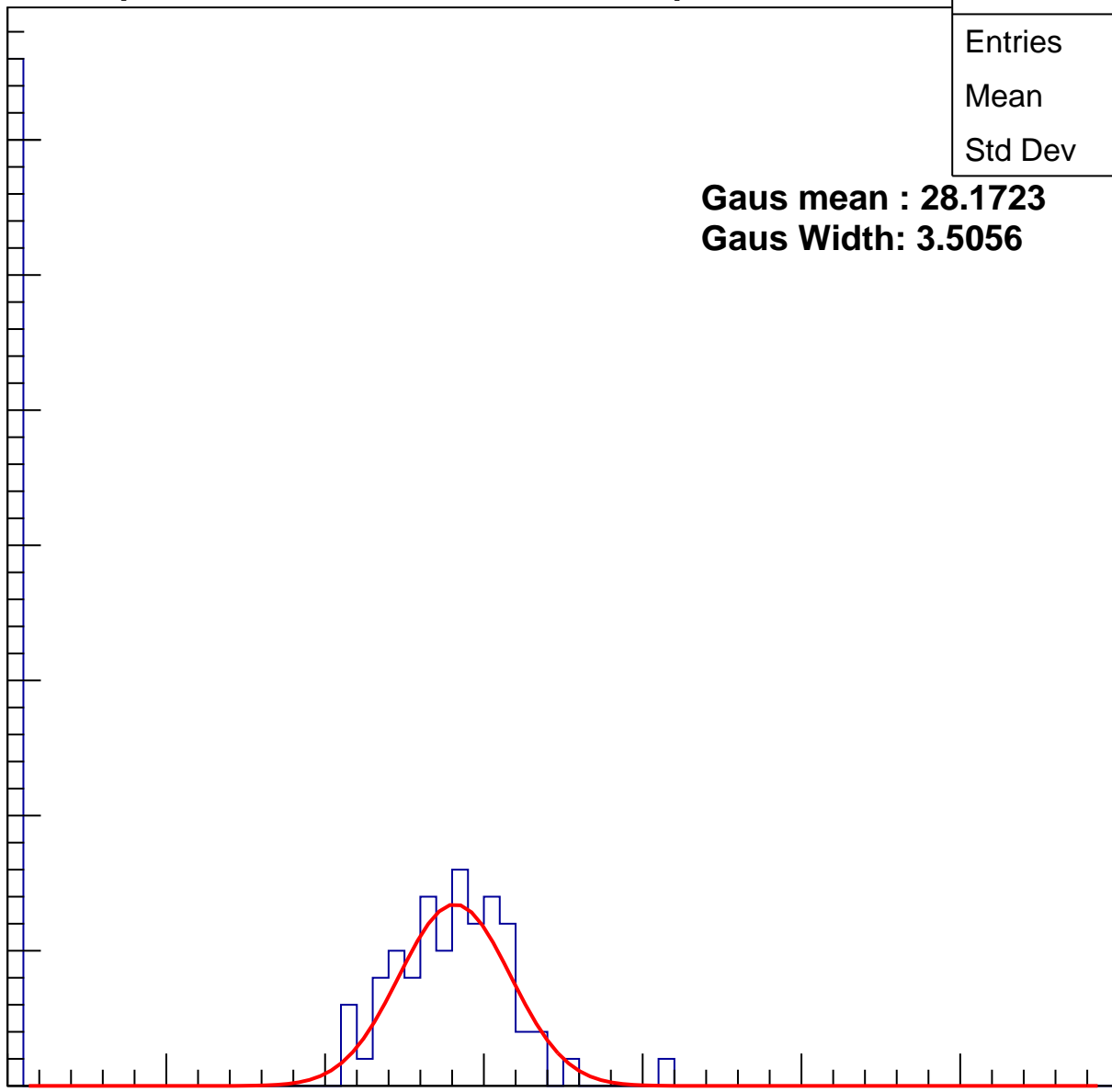
Gaus Width: 3.5056

Entry

35
30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch22, adc1

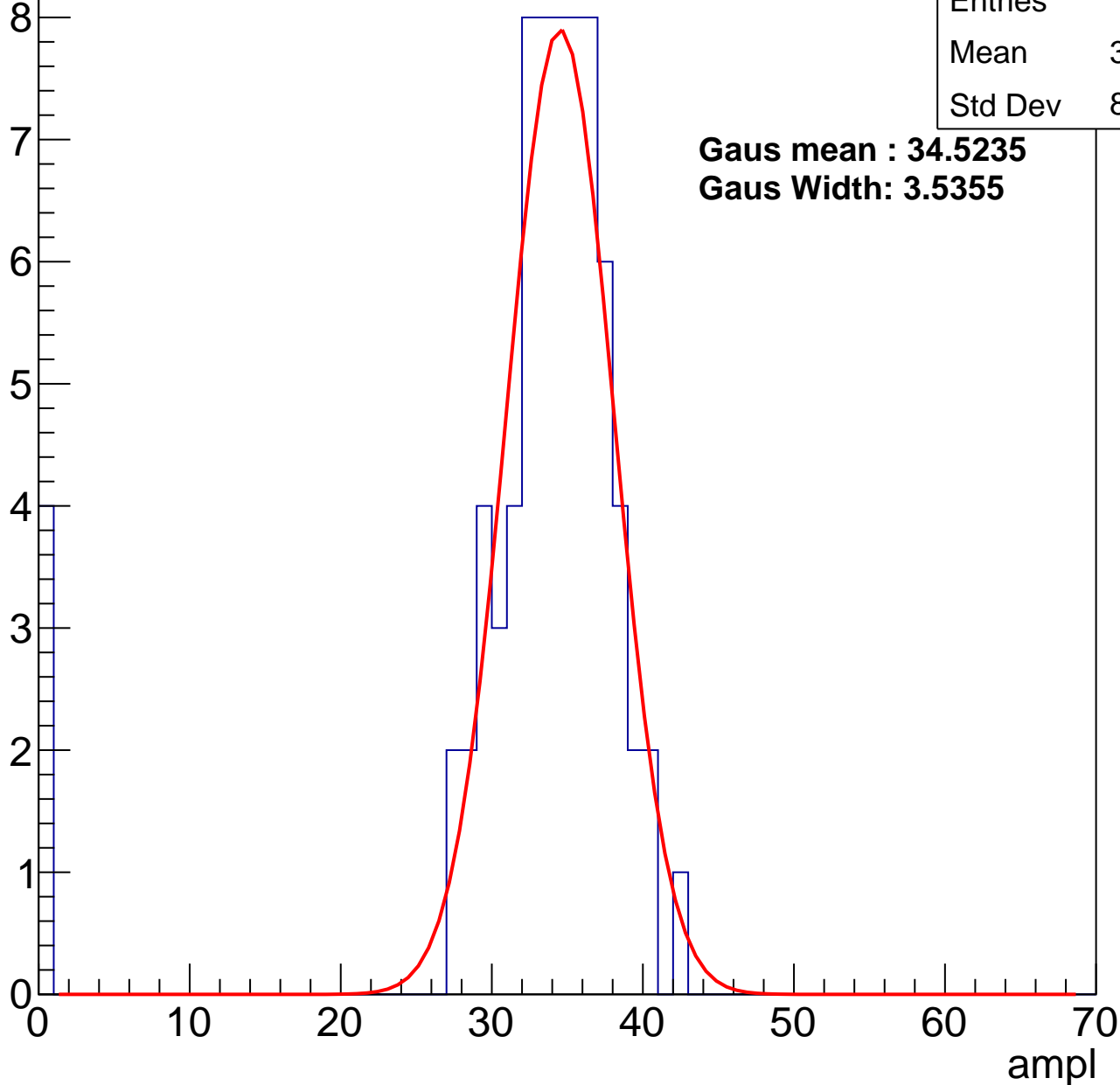
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.08
Std Dev	8.293

Gaus mean : 34.5235

Gaus Width: 3.5355



B1L103S, U1-ch22, adc2

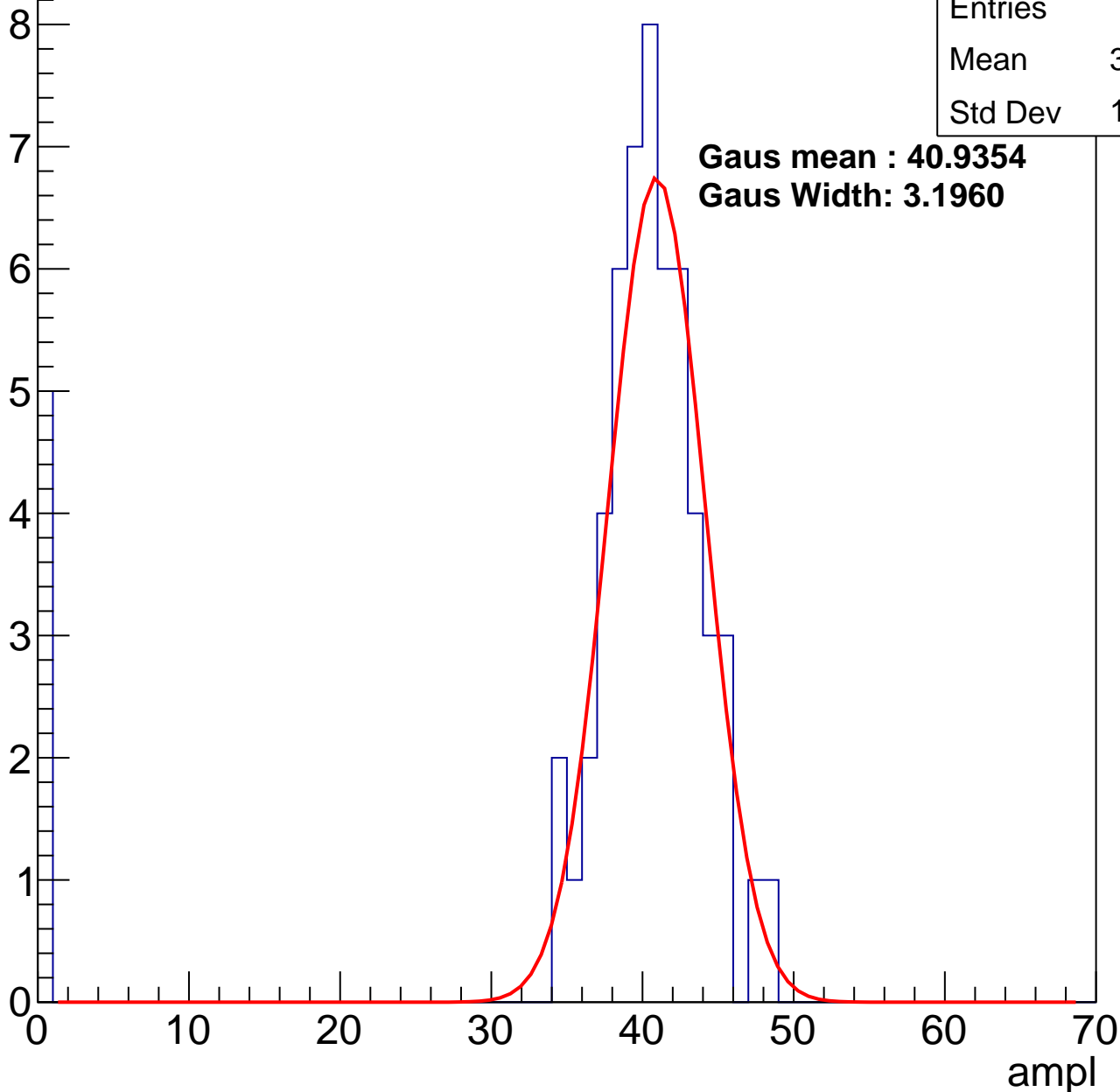
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	36.88
Std Dev	11.59

Gaus mean : 40.9354

Gaus Width: 3.1960

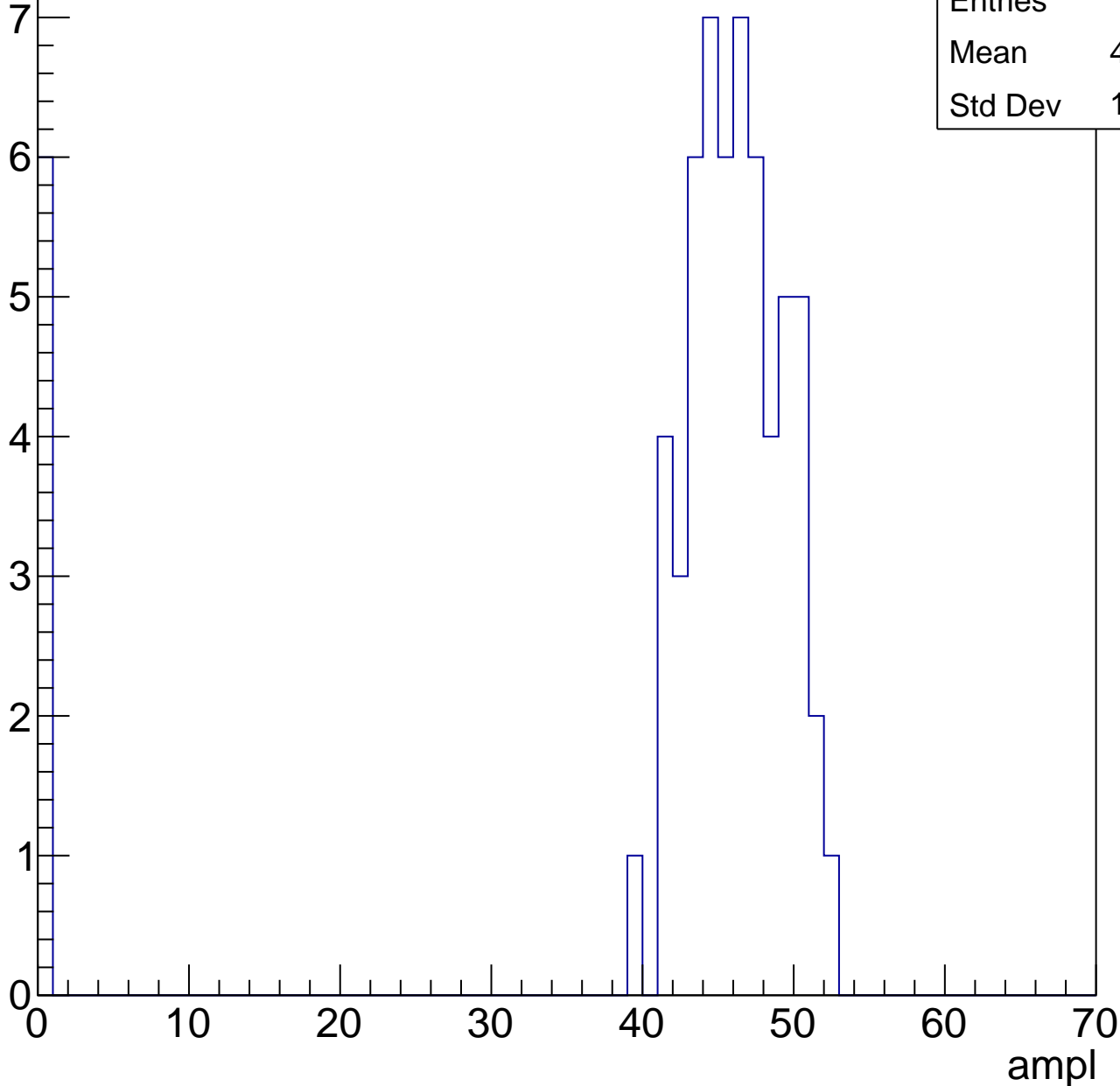


B1L103S, U1-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.43
Std Dev	13.74

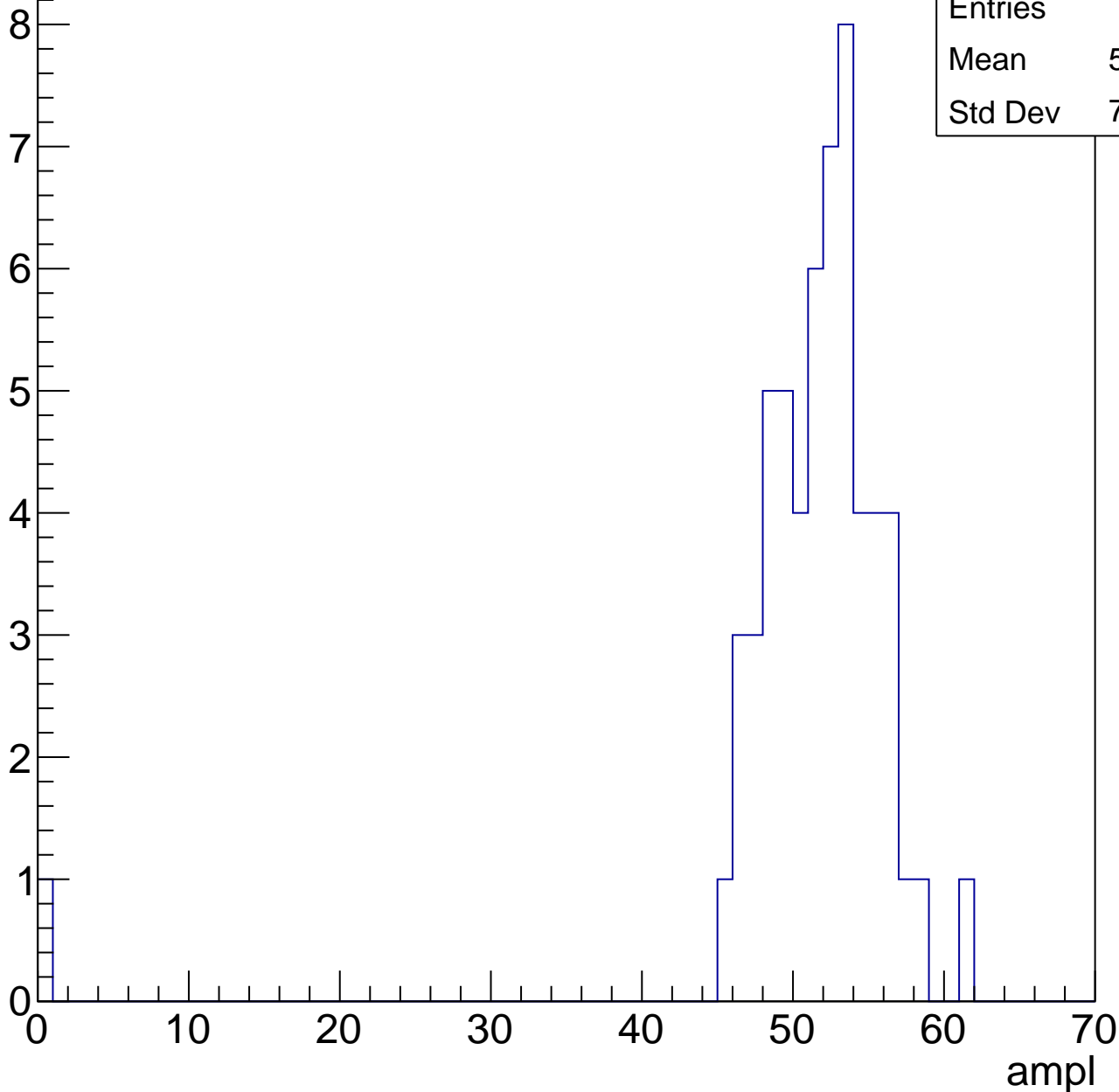


B1L103S, U1-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	50.67
Std Dev	7.482

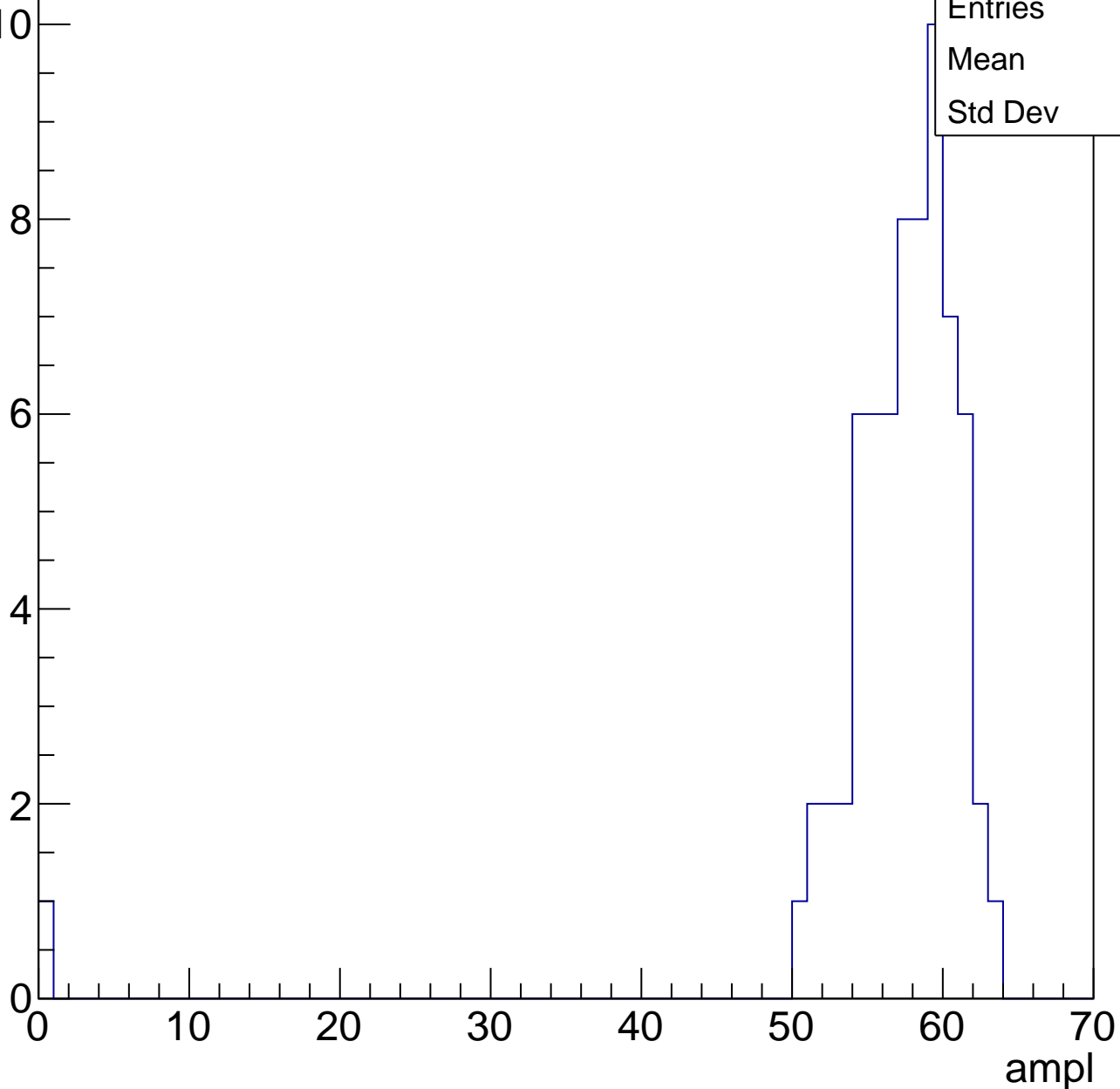


B1L103S, U1-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	56.4
Std Dev	7.48

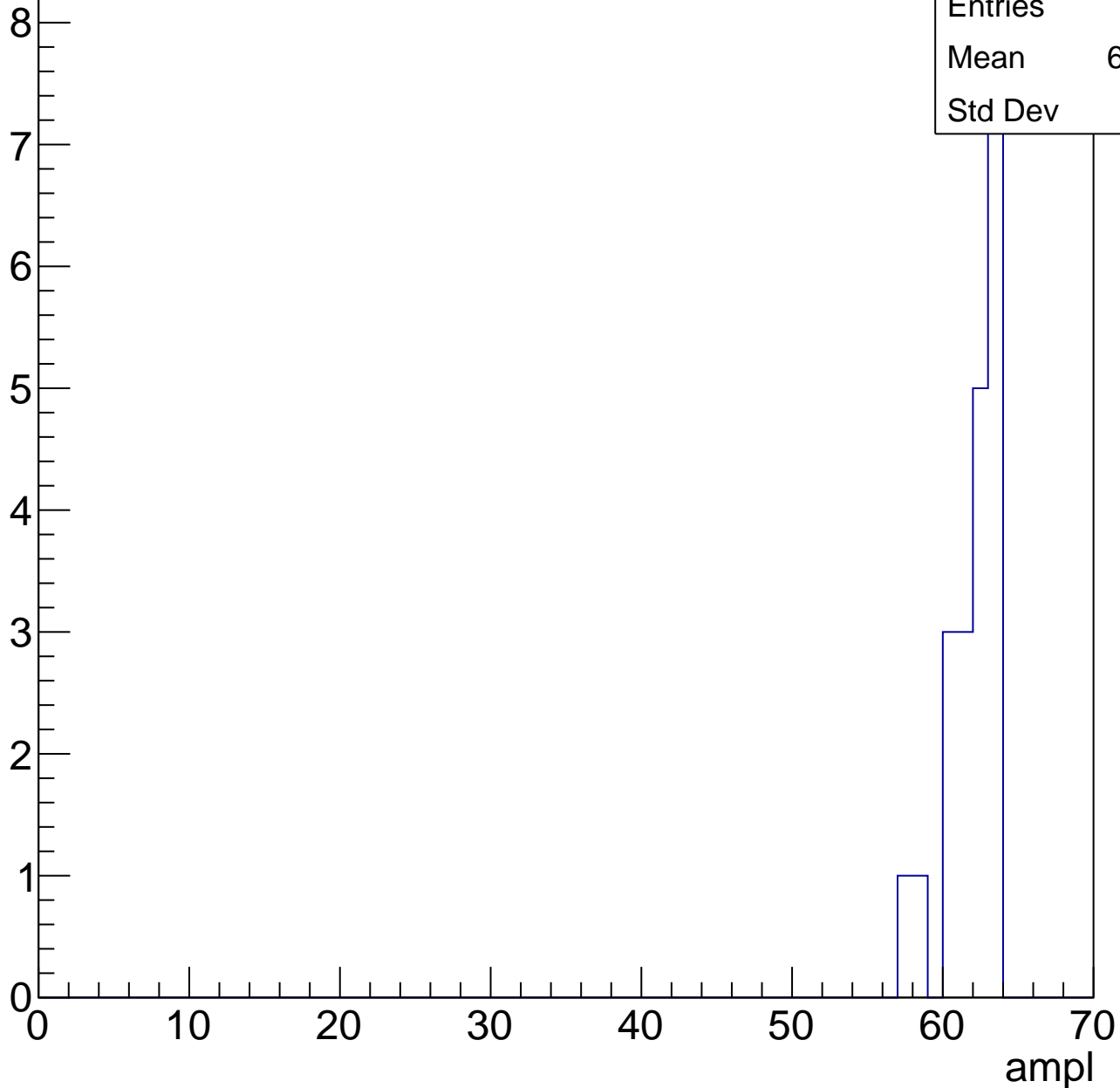


B1L103S, U1-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

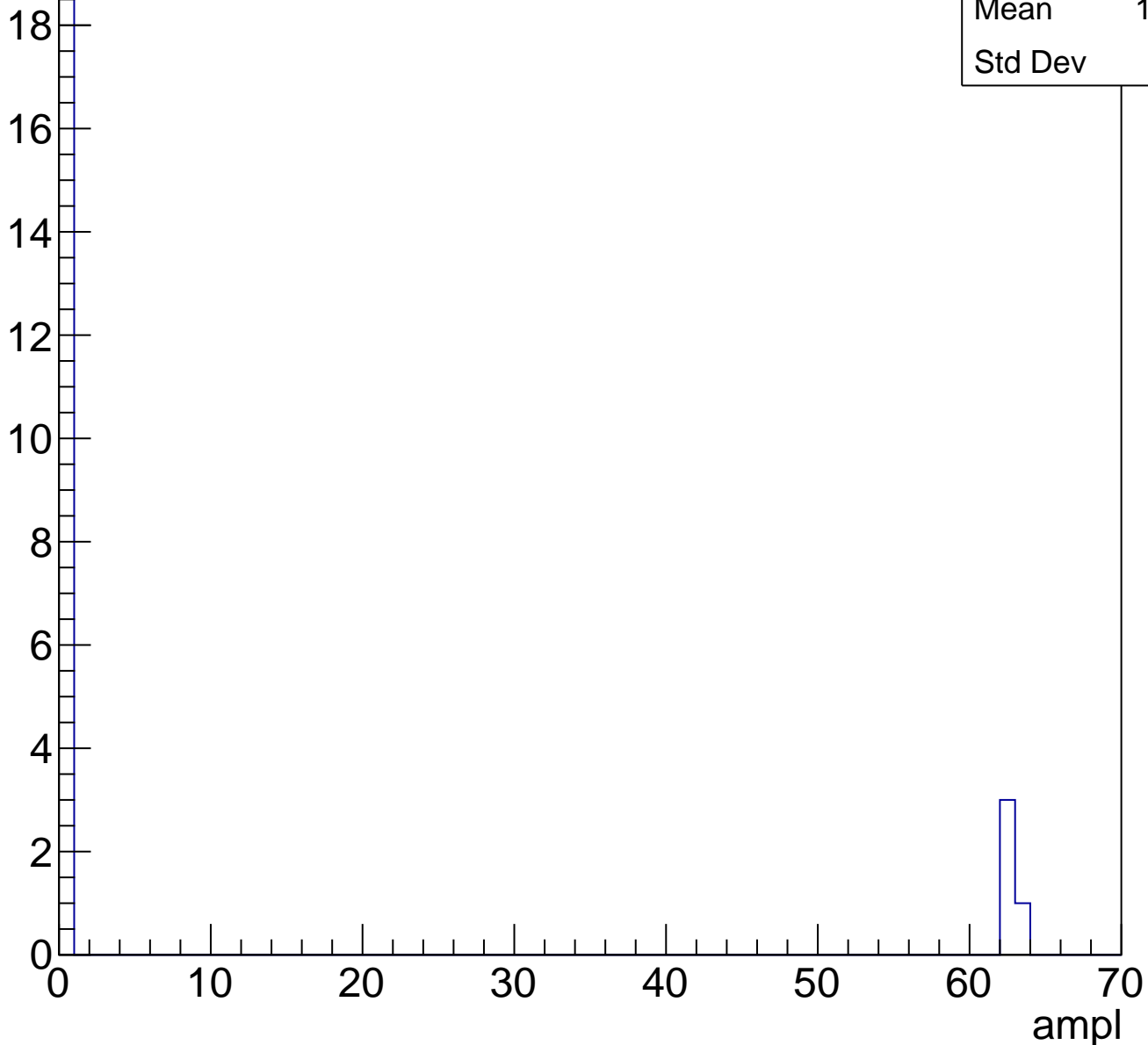
Entries	21
Mean	61.52
Std Dev	1.68



B1L103S, U1-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	23
Mean	10.83
Std Dev	23.6

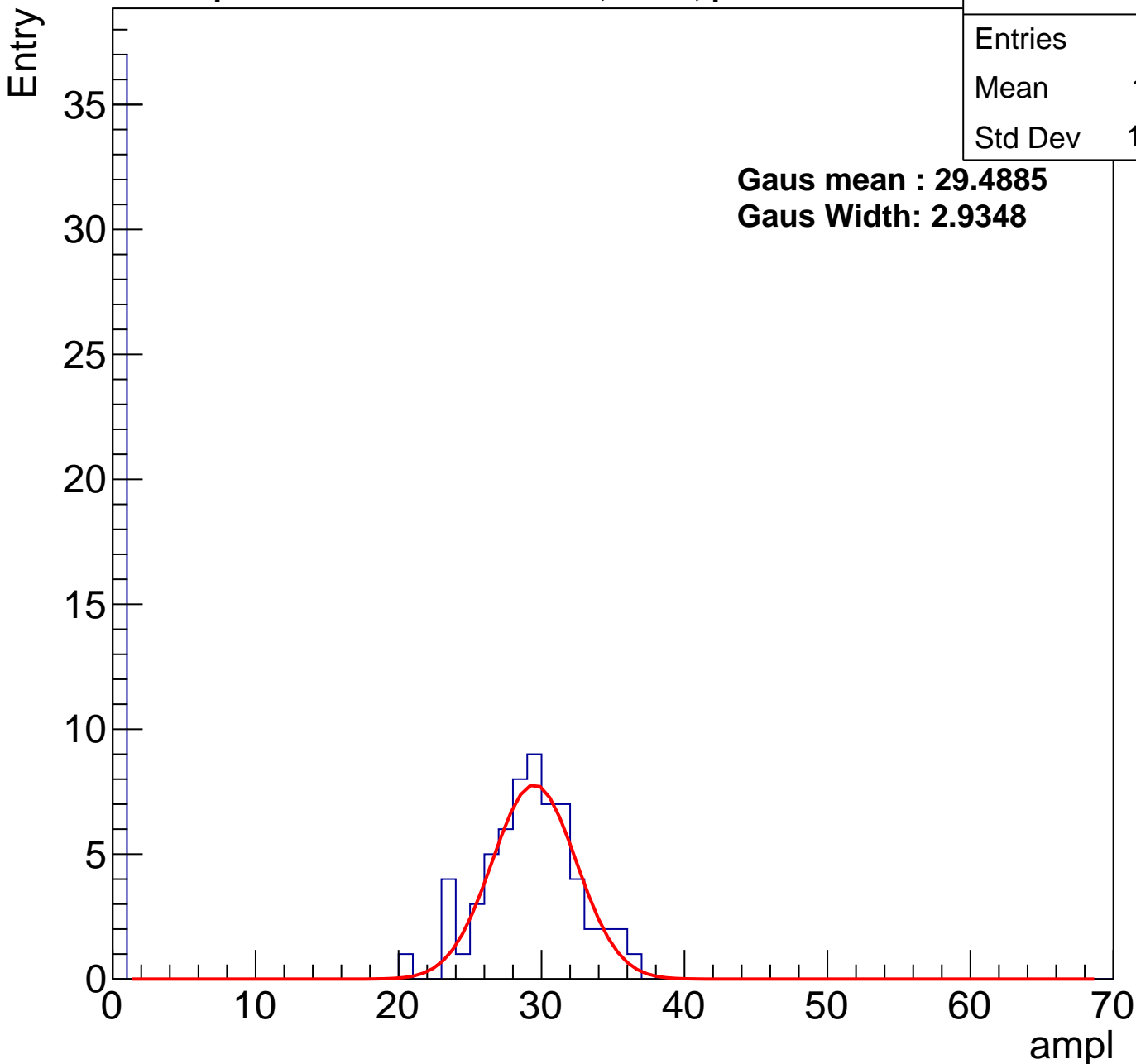
B1L103S, U1-ch23, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	18.01
Std Dev	14.15

Gaus mean : 29.4885

Gaus Width: 2.9348



B1L103S, U1-ch23, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	29.32
Std Dev	13.44

Gaus mean : 35.8106

Gaus Width: 3.9071

Entry

12

10

8

6

4

2

0

0

10

20

30

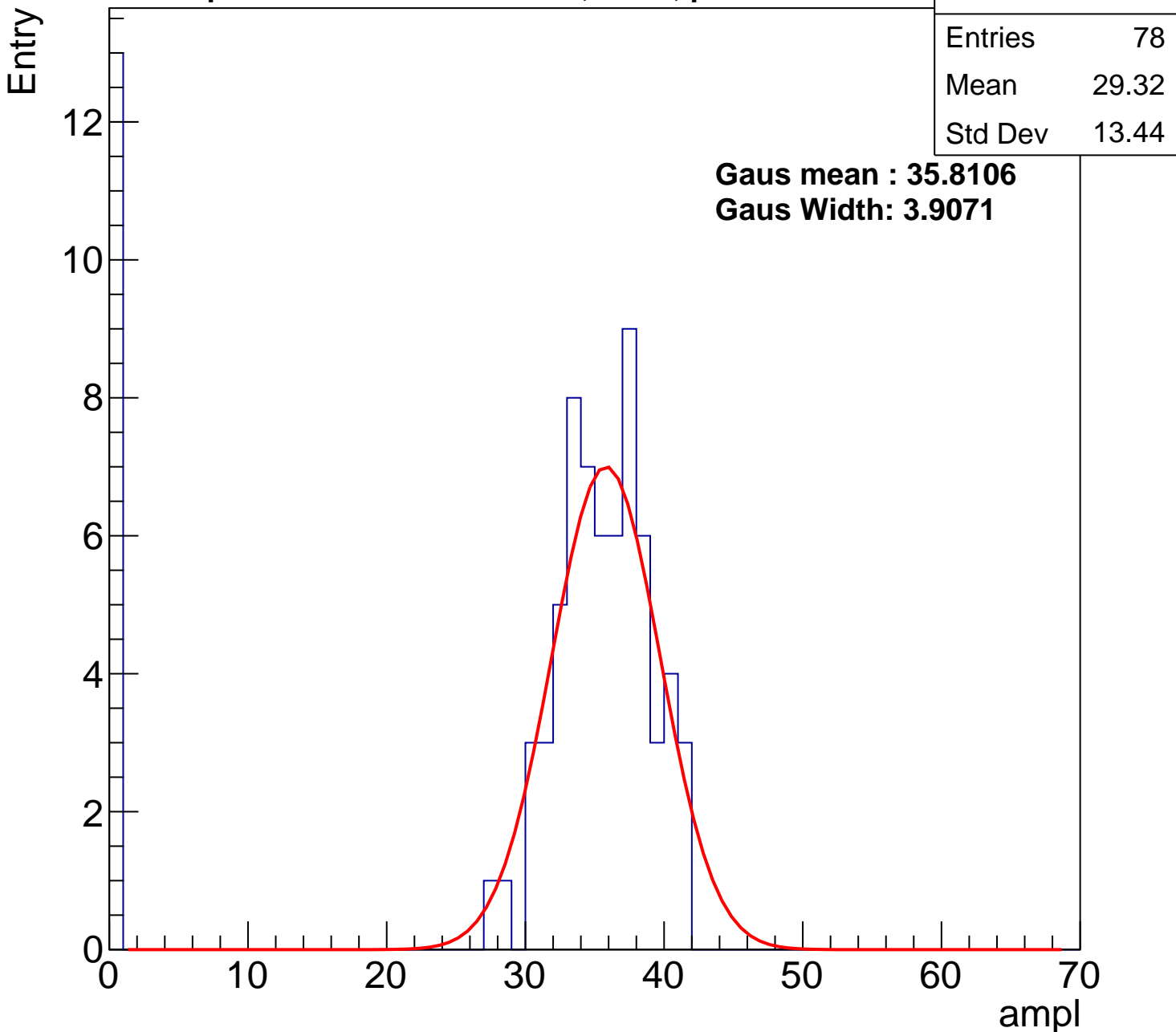
40

50

60

70

ampl



B1L103S, U1-ch23, adc2

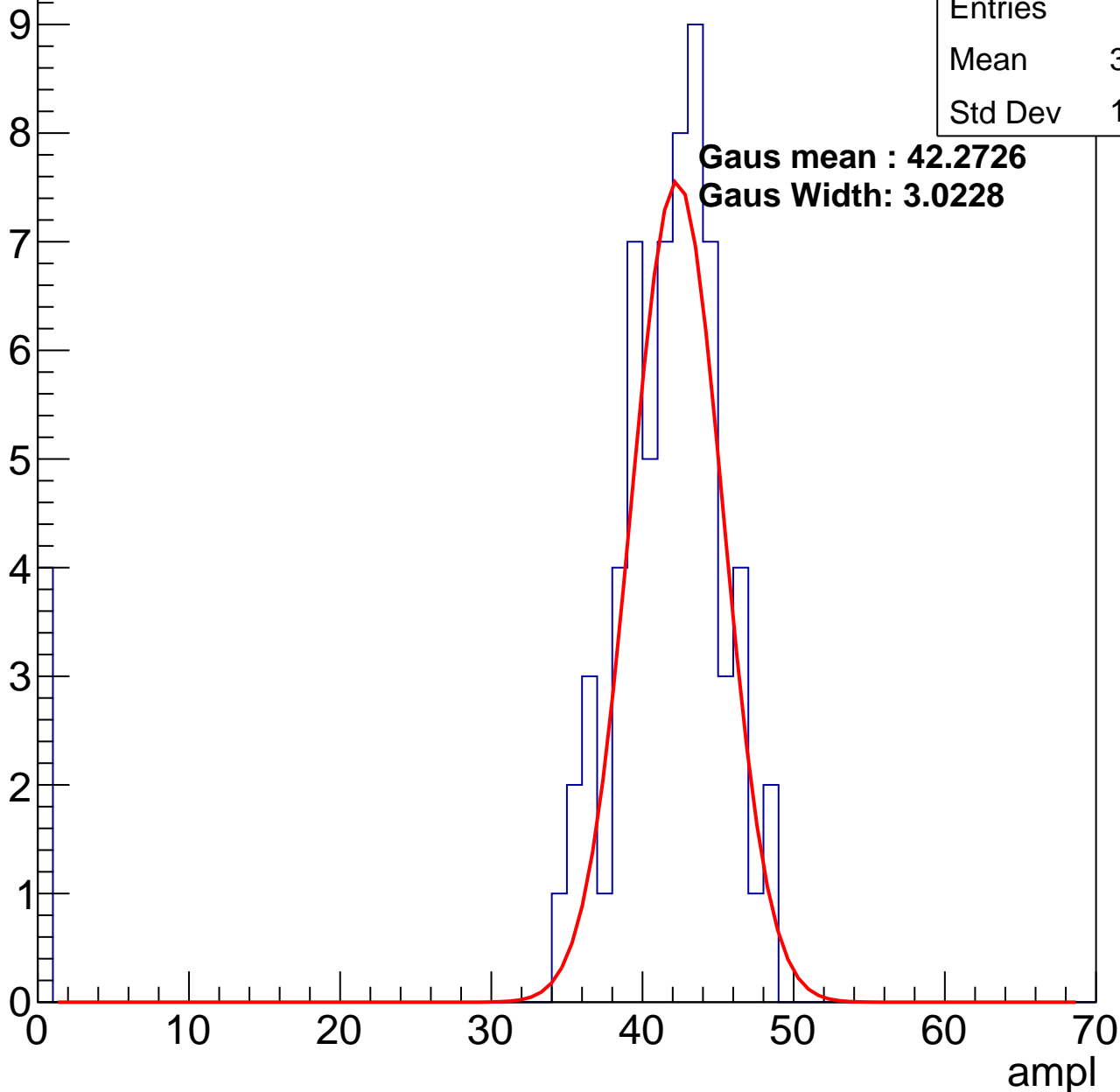
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	39.03
Std Dev	10.24

Gaus mean : 42.2726

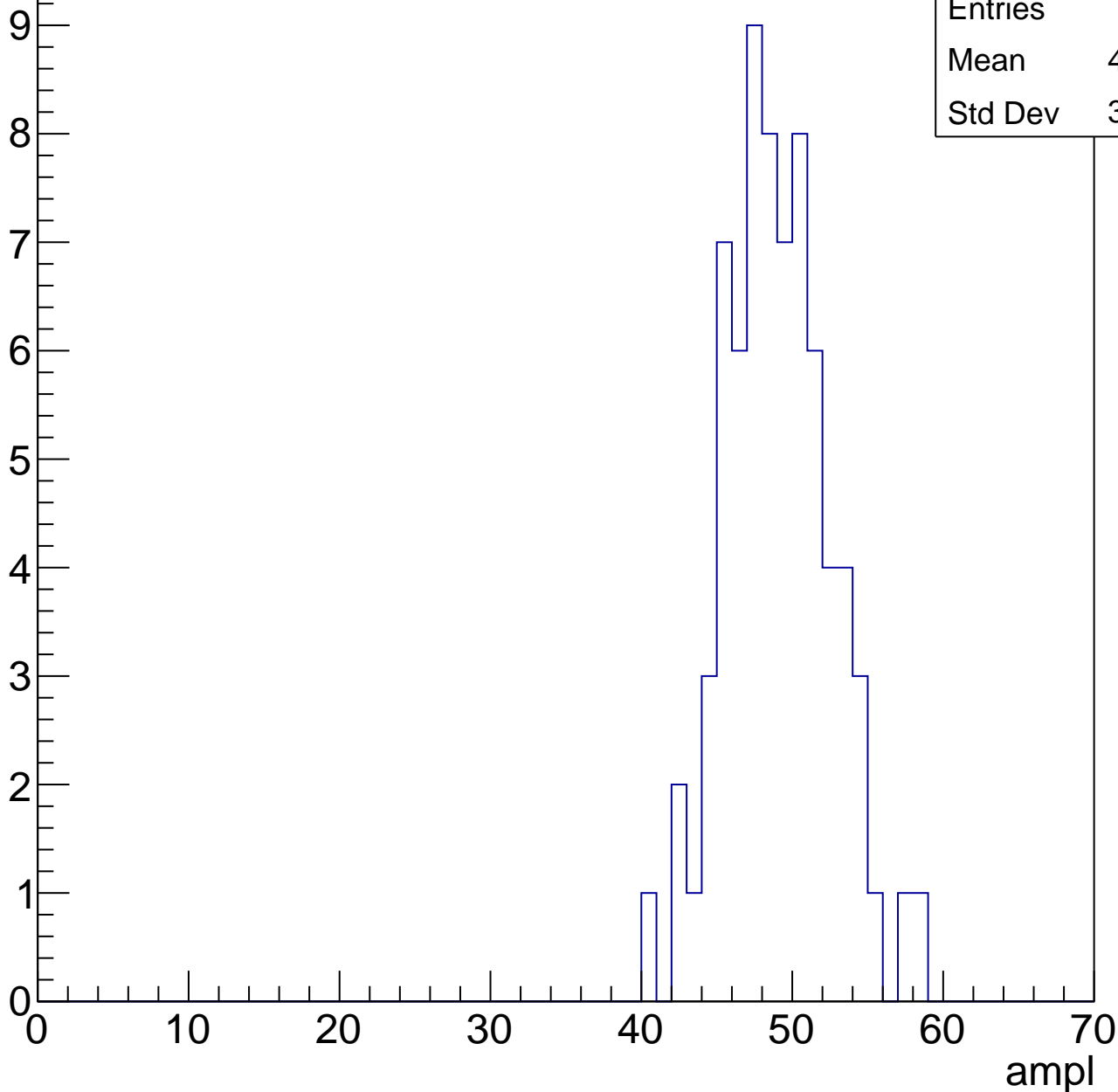
Gaus Width: 3.0228



B1L103S, U1-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

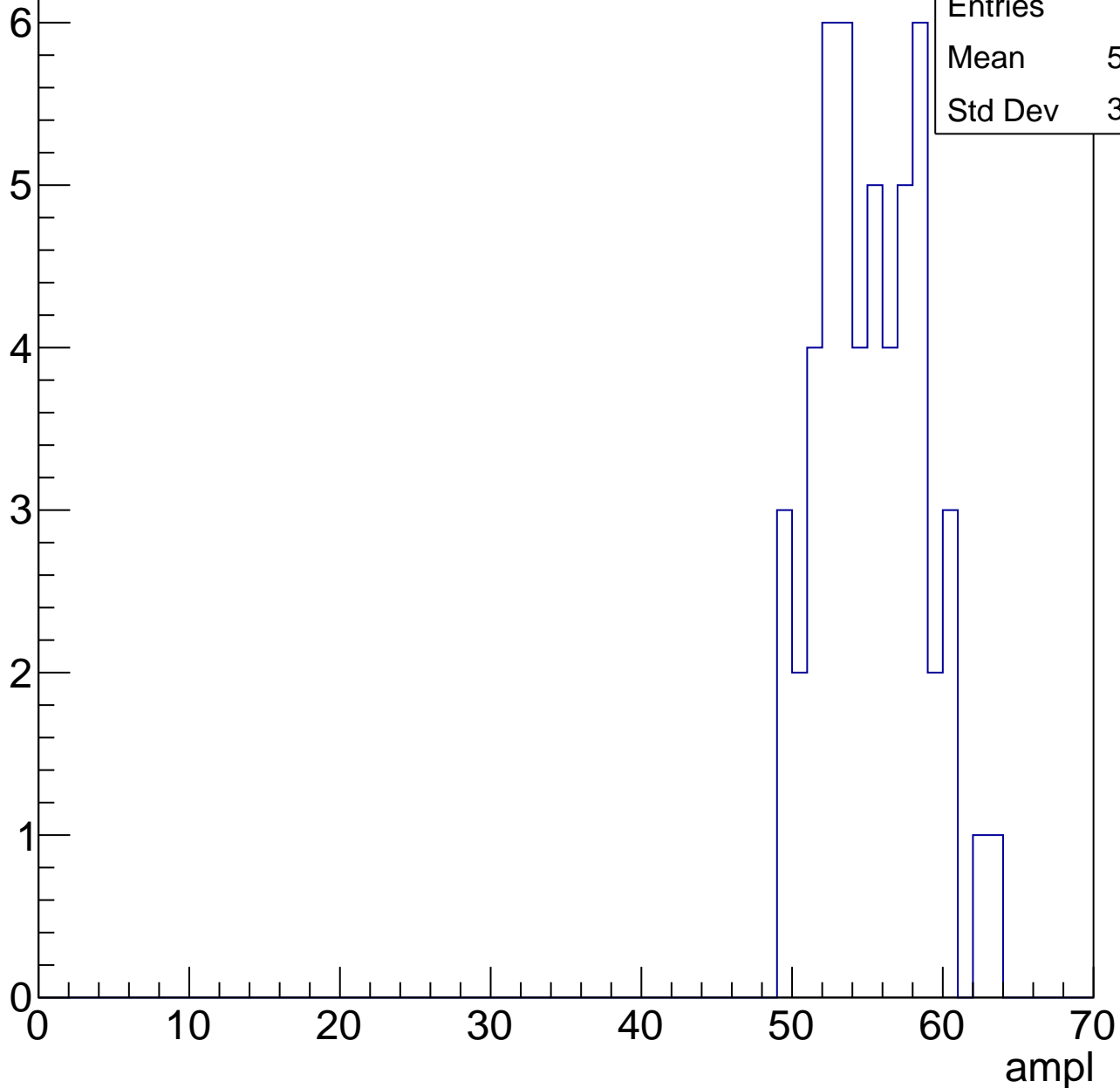
Entry



B1L103S, U1-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

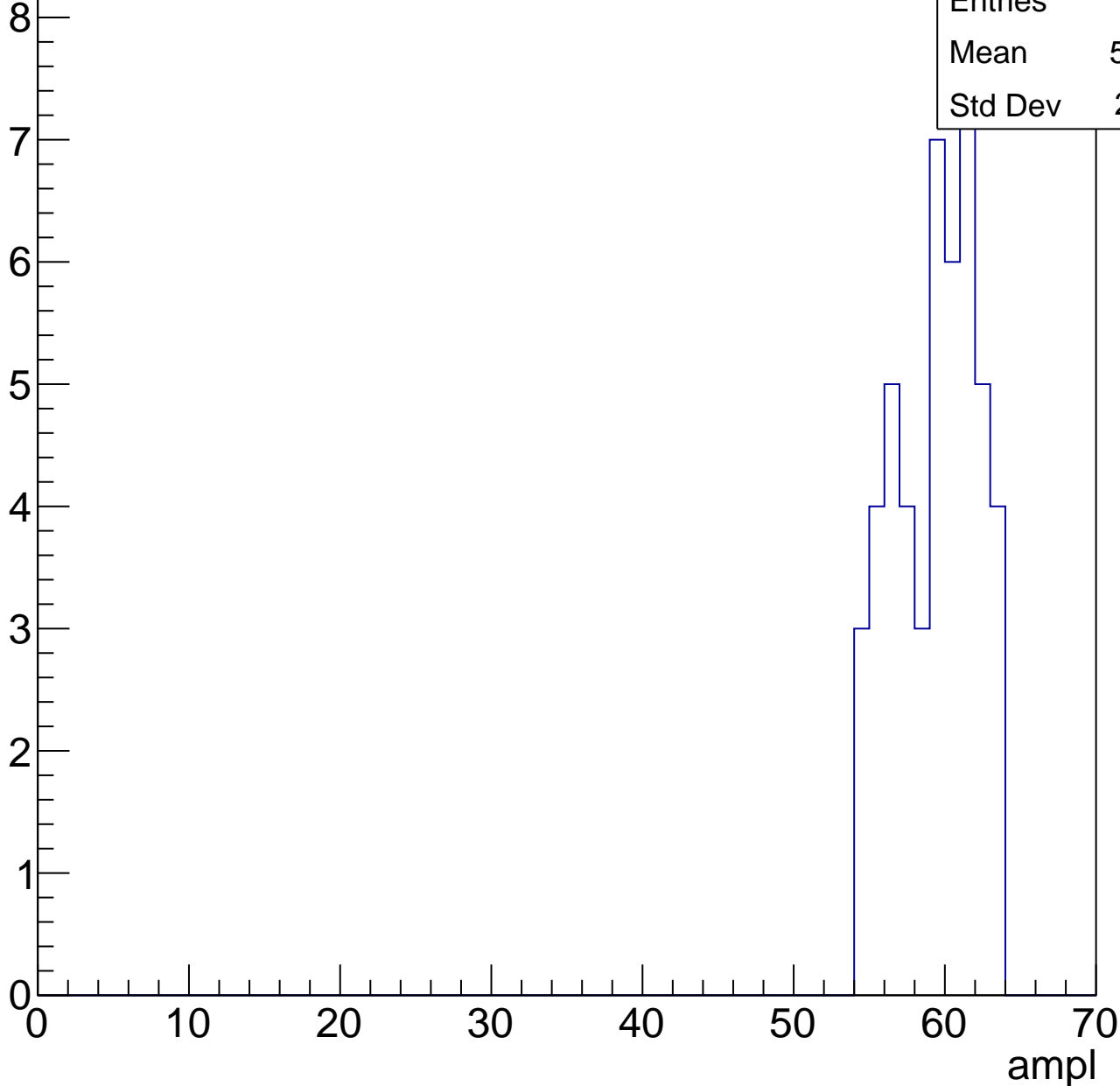


B1L103S, U1-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.92
Std Dev	2.671

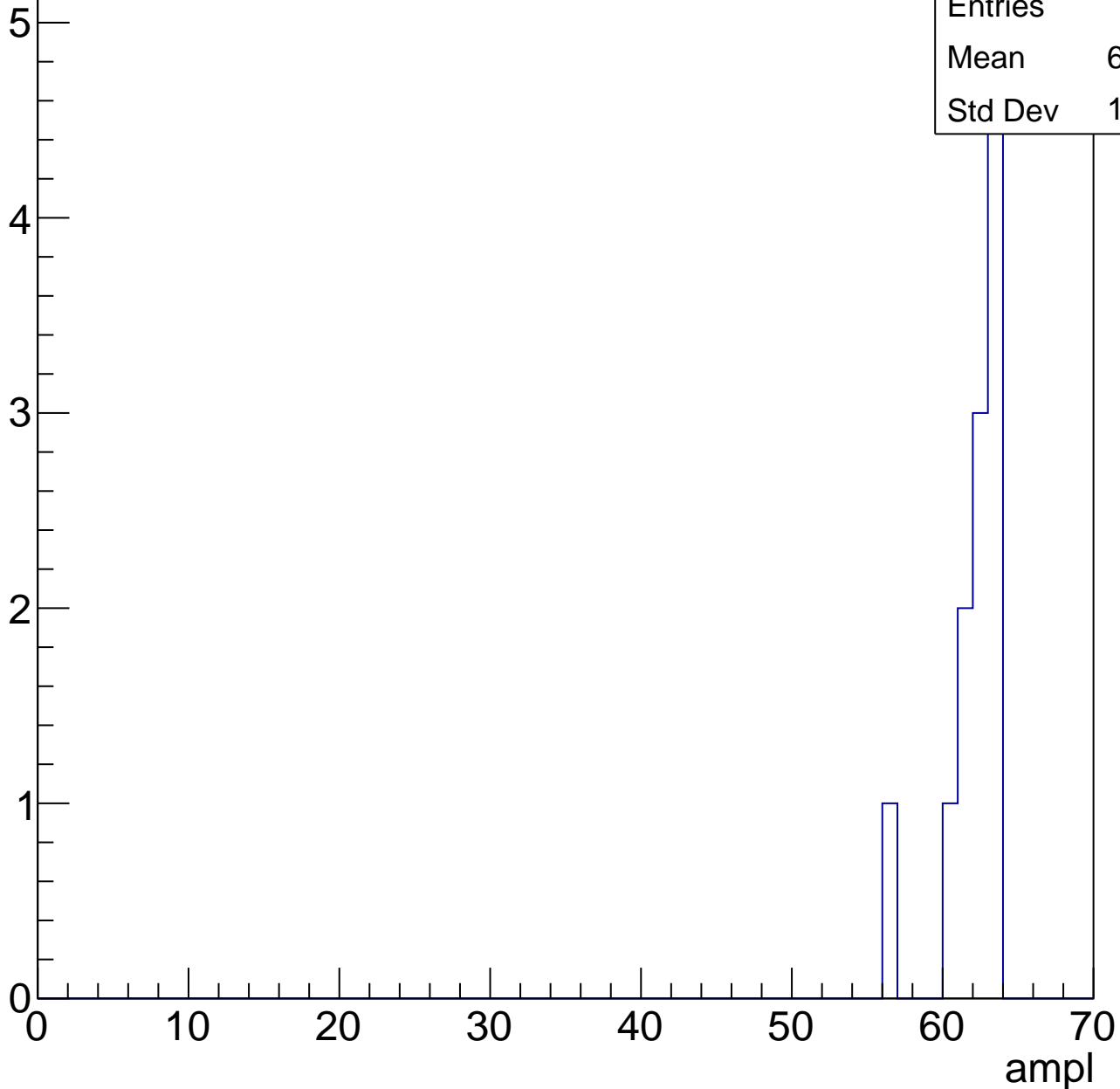


B1L103S, U1-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.935



B1L103S, U1-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

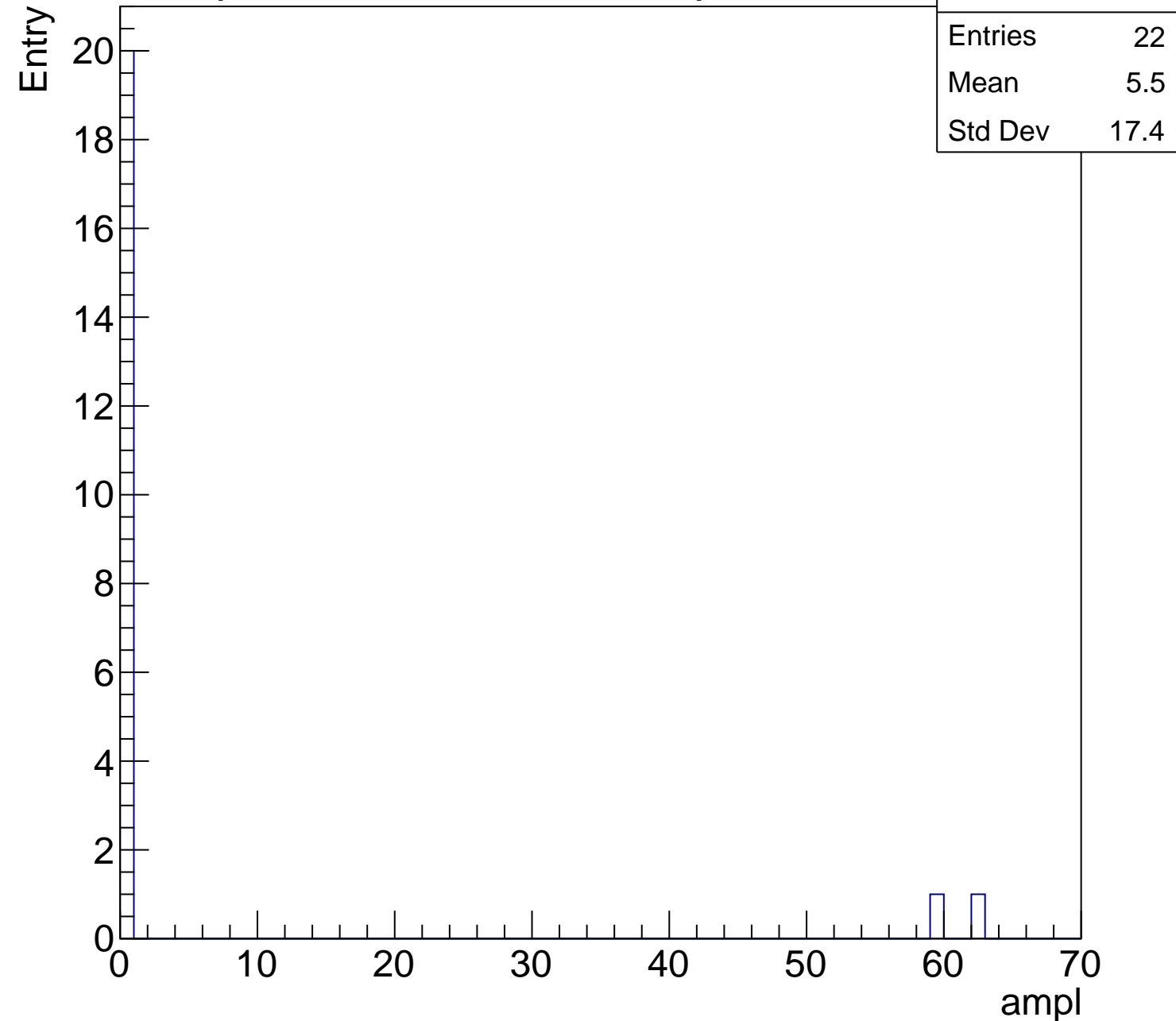
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	22
Mean	5.5
Std Dev	17.4

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch24, adc0

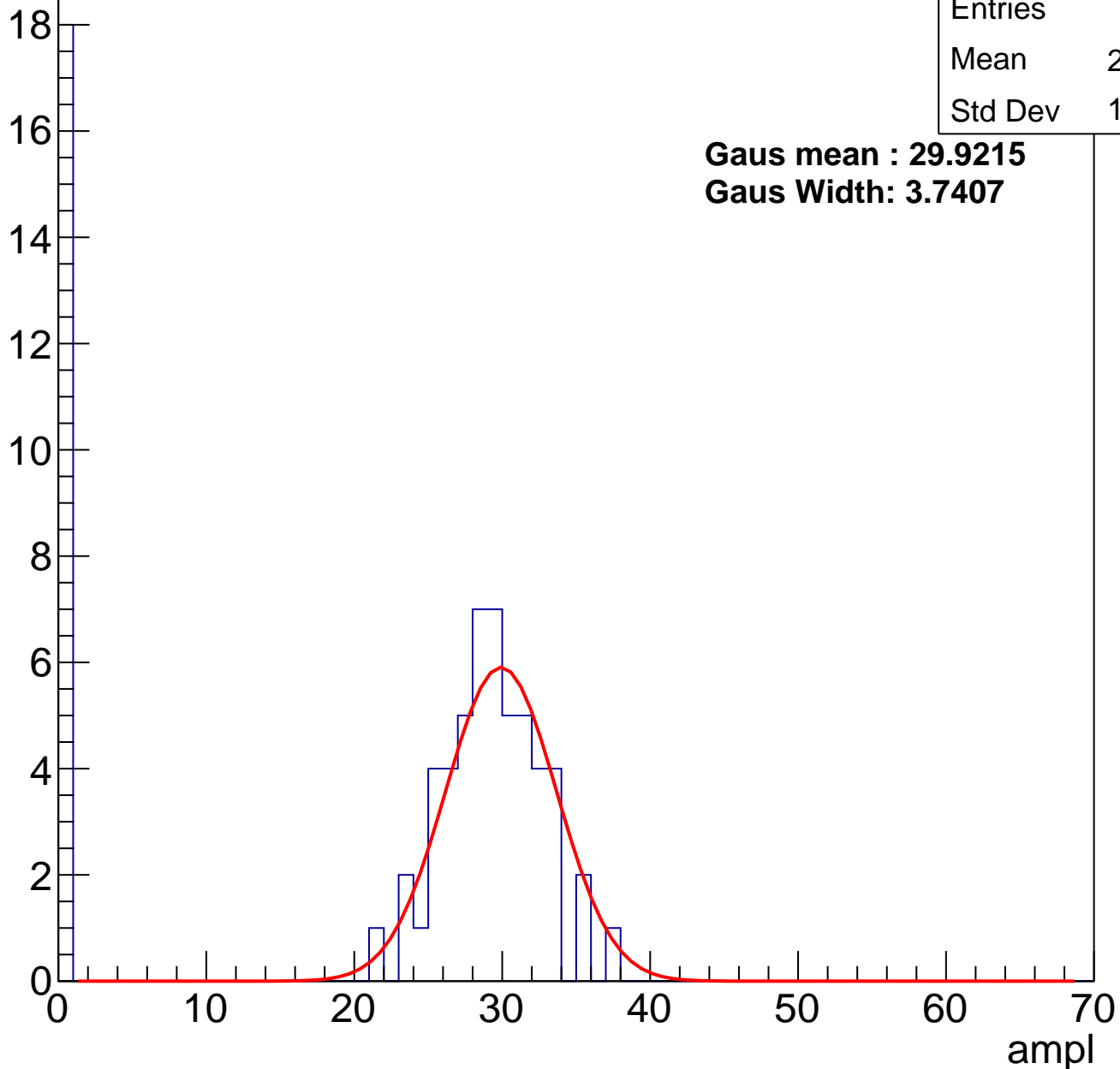
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	21.44
Std Dev	12.92

Gaus mean : 29.9215

Gaus Width: 3.7407

Entry



B1L103S, U1-ch24, adc1

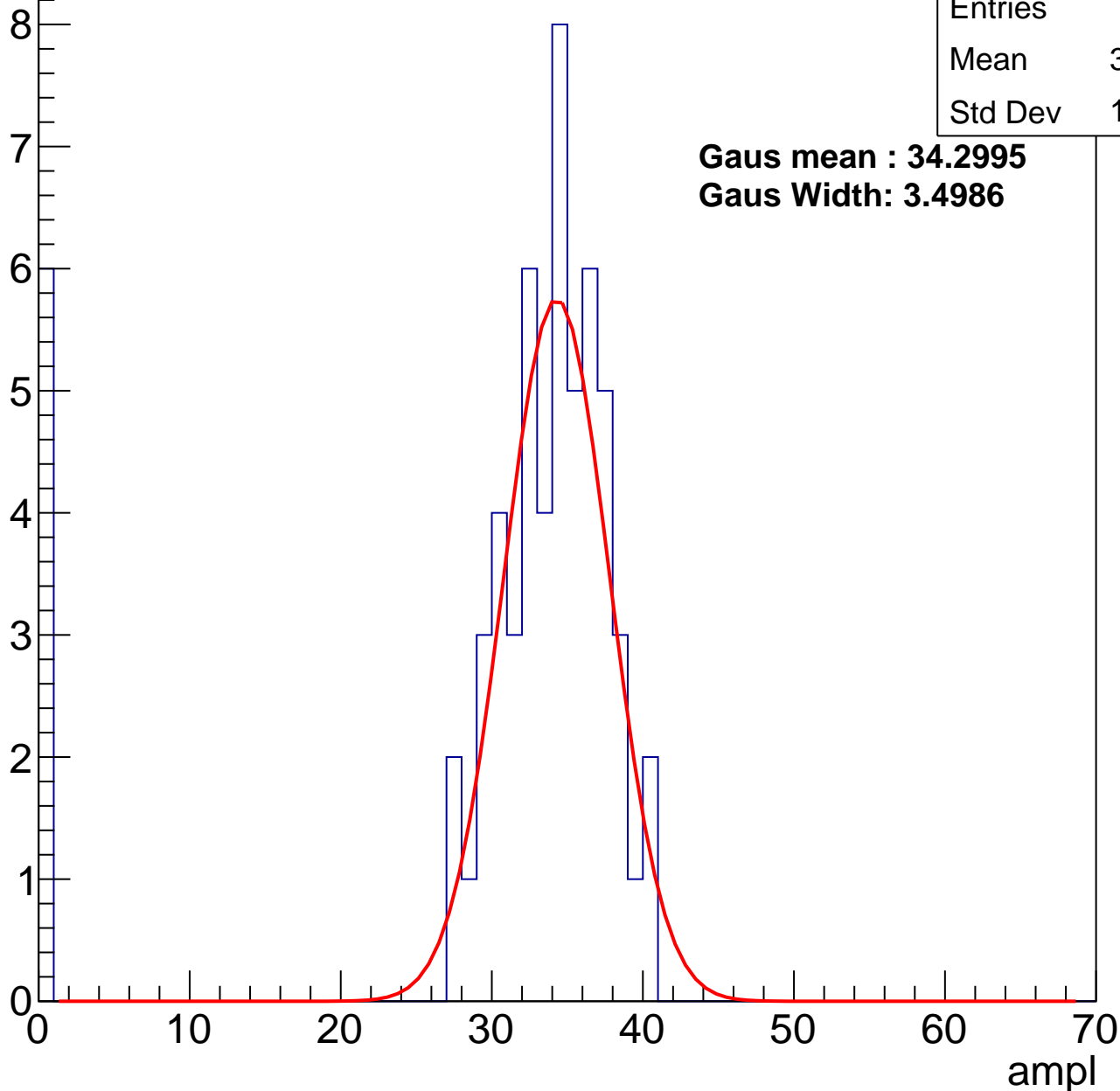
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	30.29
Std Dev	10.63

Gaus mean : 34.2995

Gaus Width: 3.4986



B1L103S, U1-ch24, adc2

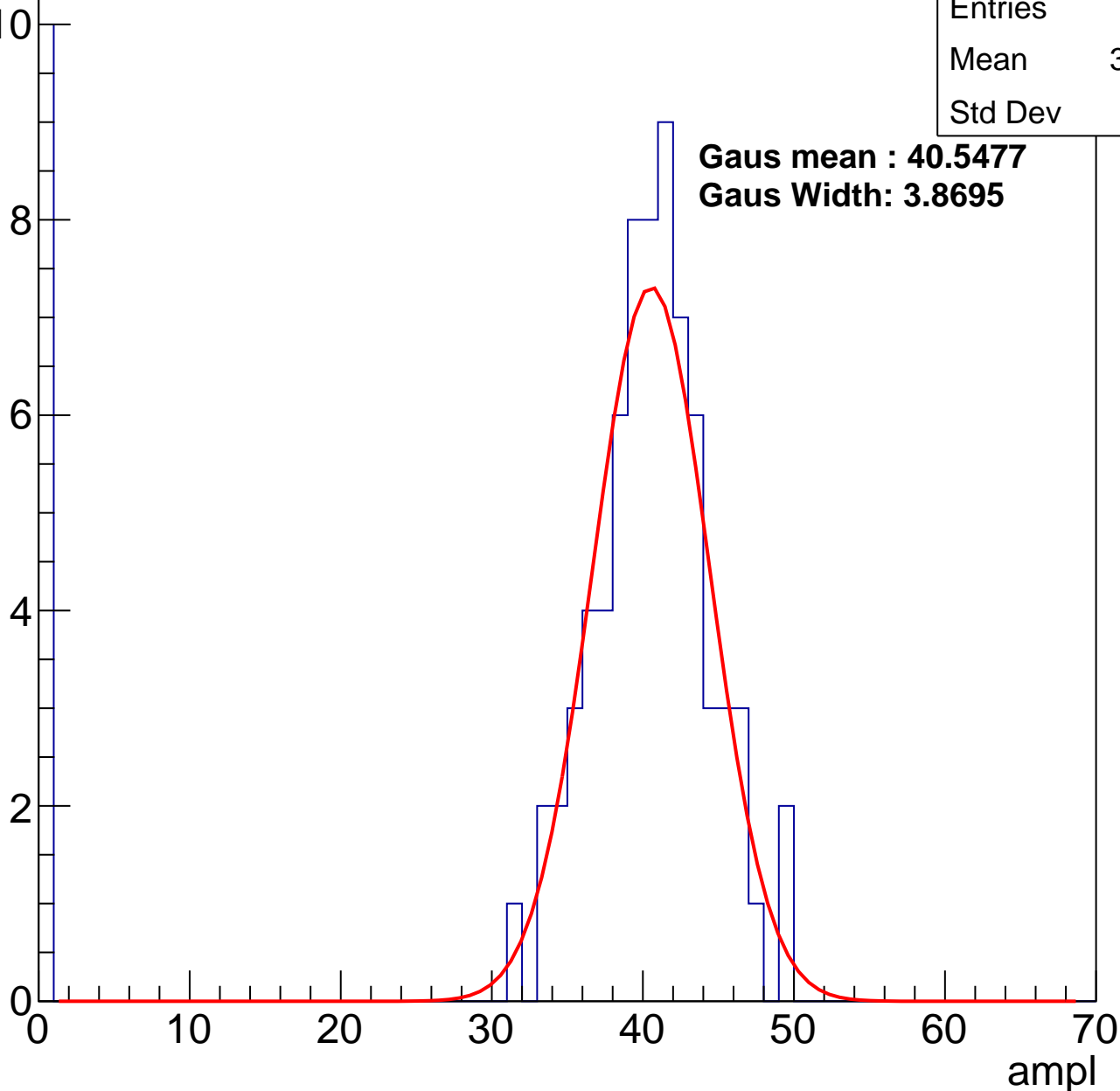
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	35.28
Std Dev	13.6

Gaus mean : 40.5477

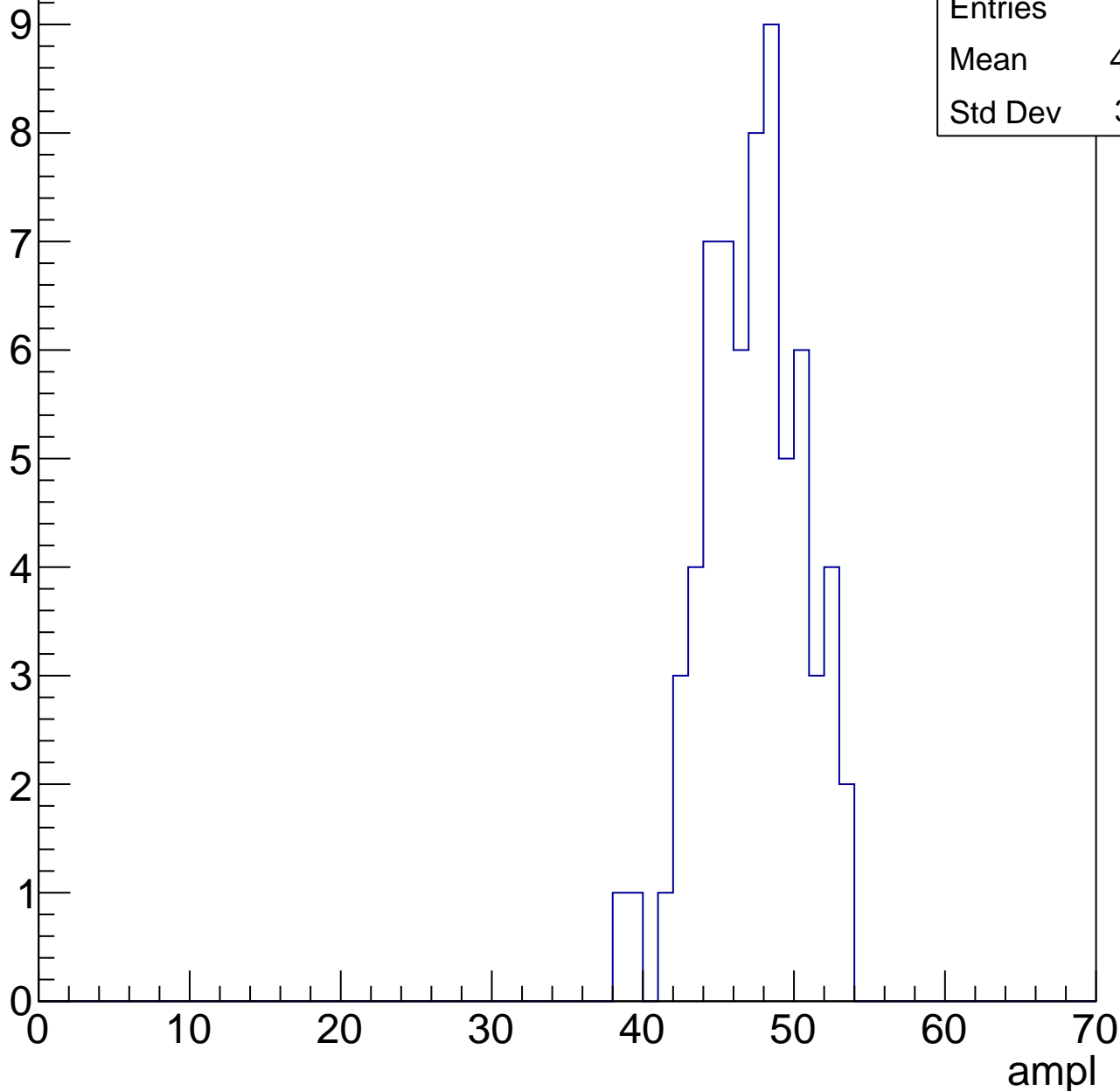
Gaus Width: 3.8695



B1L103S, U1-ch24, adc3

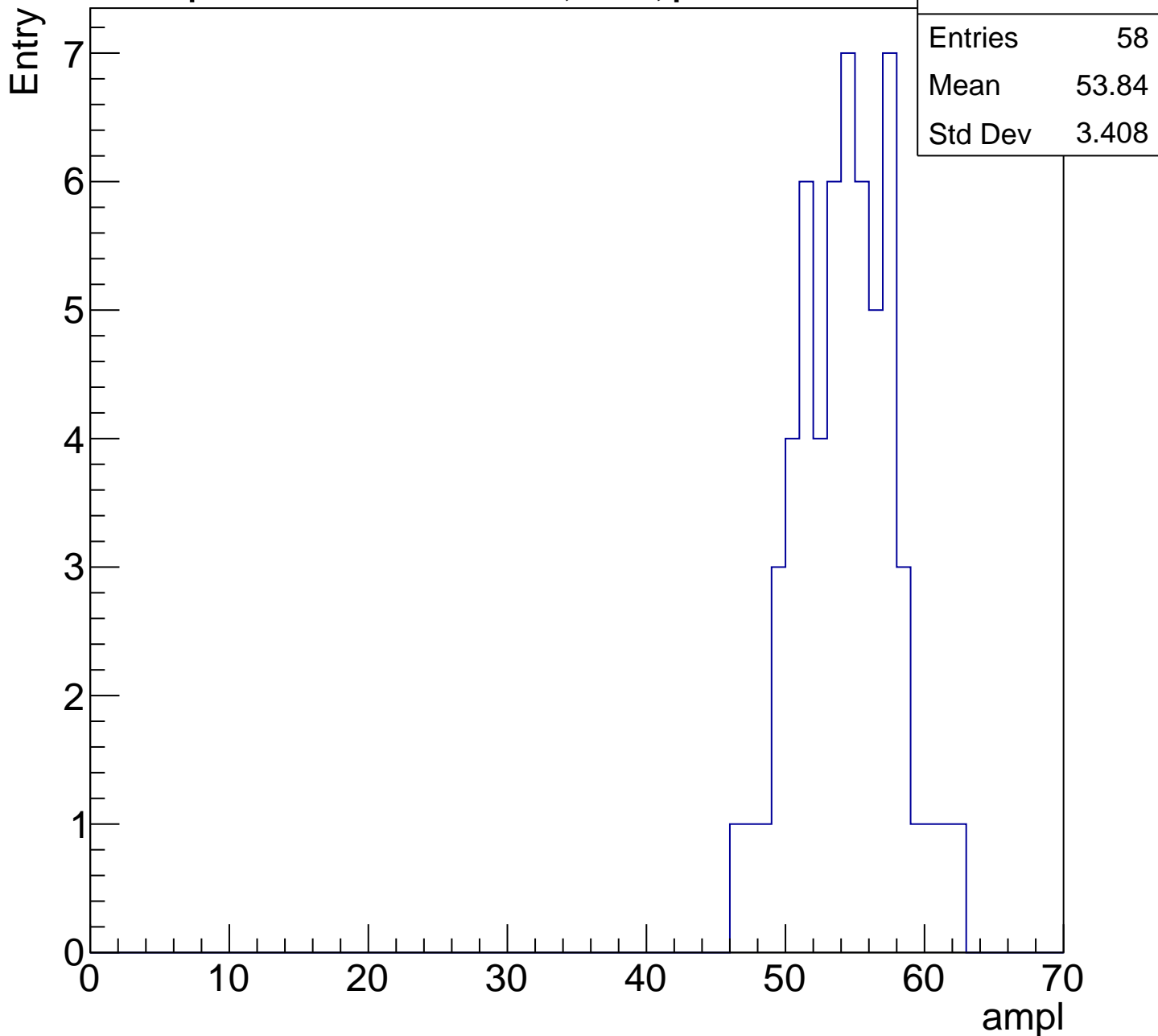
calib_packv5_041523_1651.root, FC#0, port C2

Entry



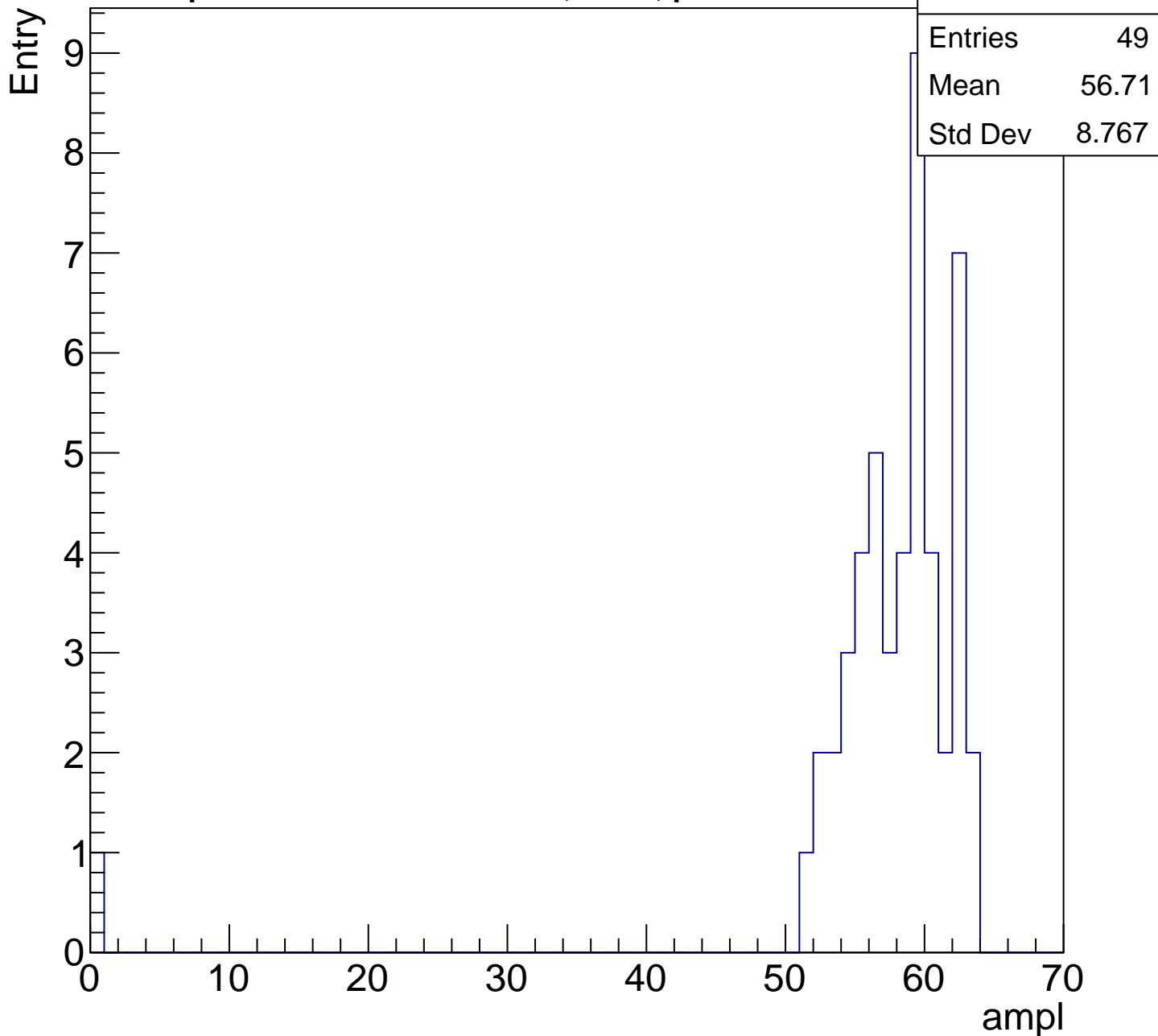
B1L103S, U1-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

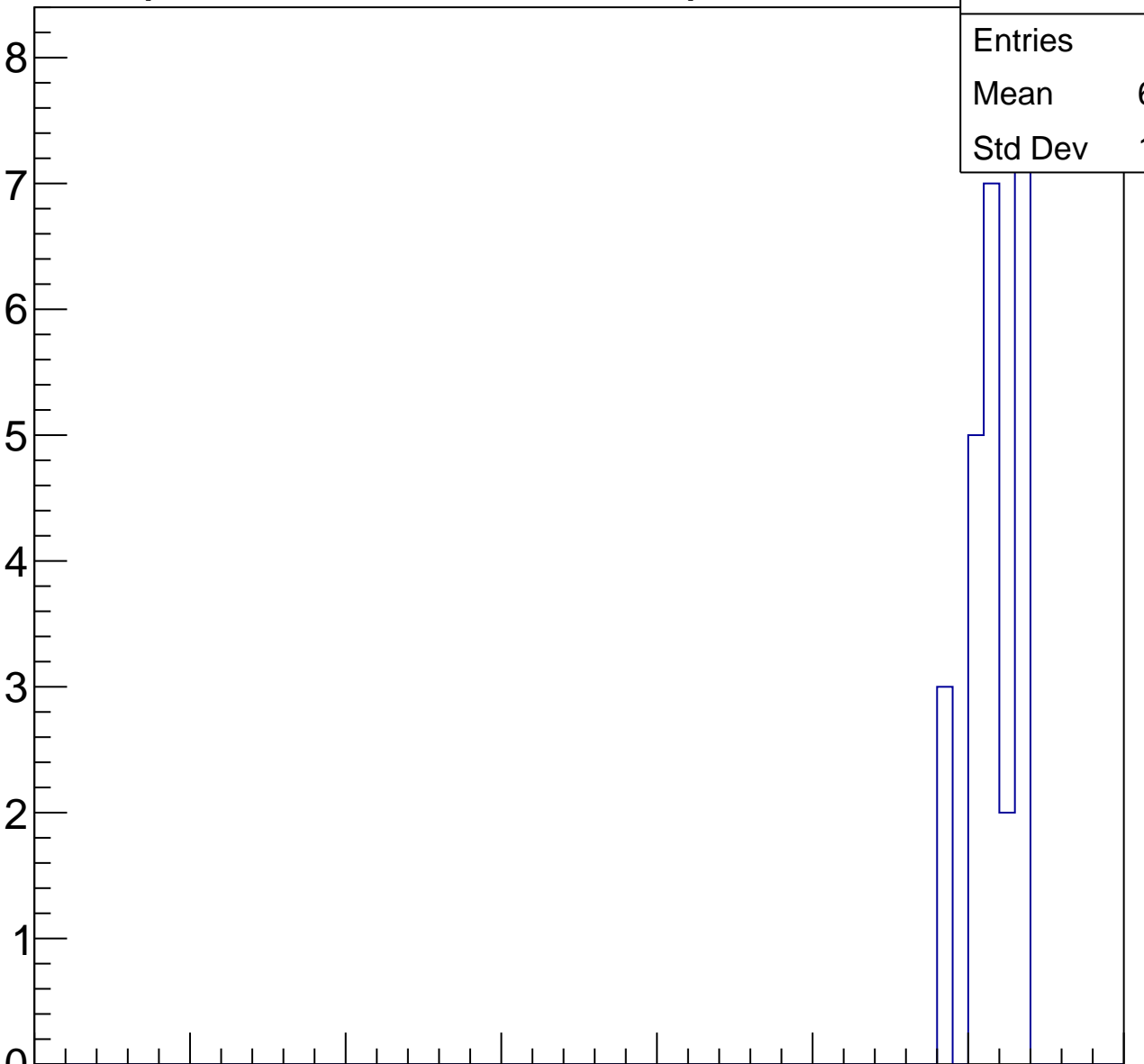
Entry

8
7
6
5
4
3
2
1
0

Entries	25
Mean	61.16
Std Dev	1.617

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

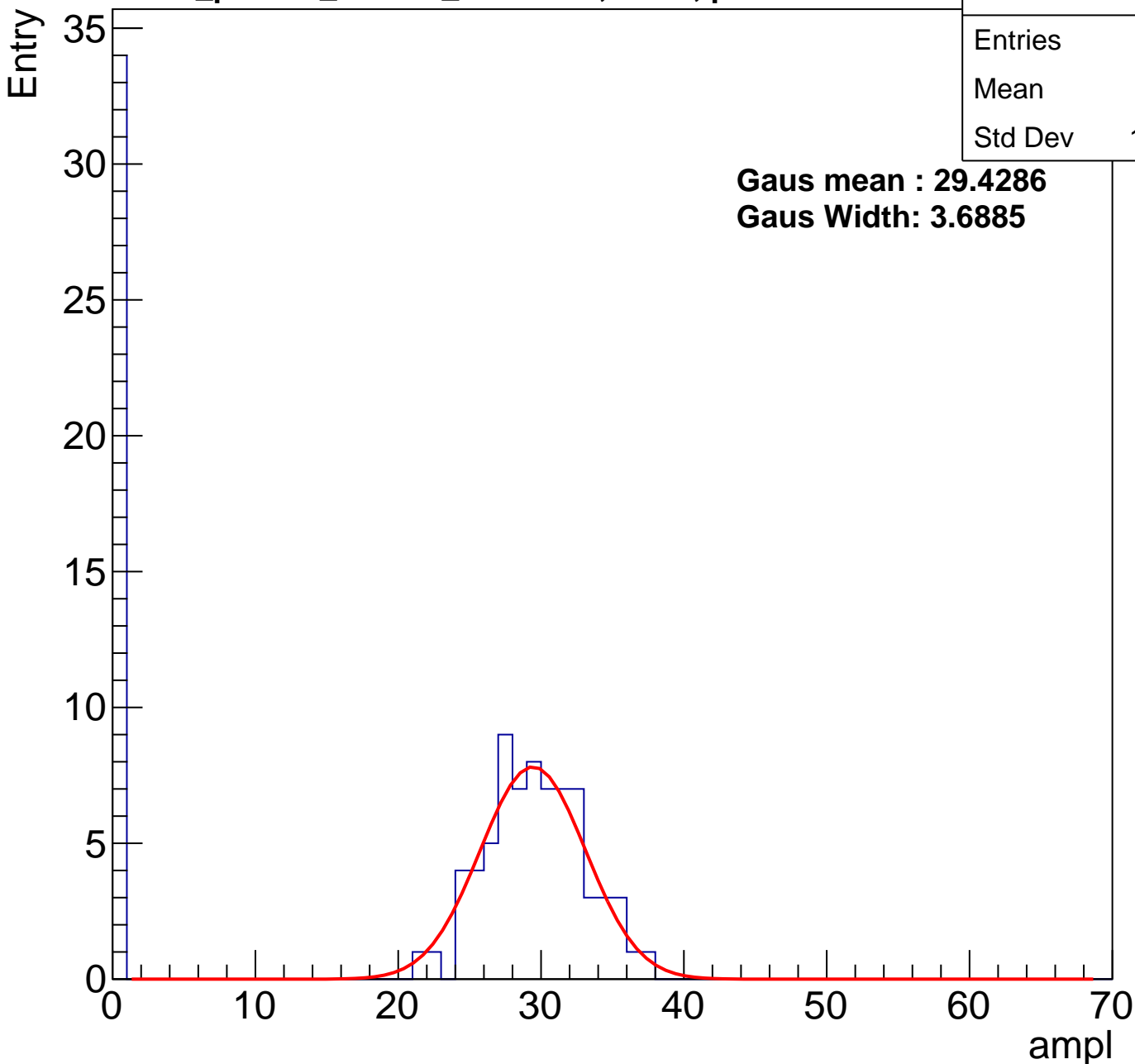
B1L103S, U1-ch25, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	19.71
Std Dev	13.92

Gaus mean : 29.4286

Gaus Width: 3.6885



B1L103S, U1-ch25, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	30.21
Std Dev	13.33

Gaus mean : 36.6435

Gaus Width: 3.5991

Entry

10

8

6

4

2

0

0

10

20

30

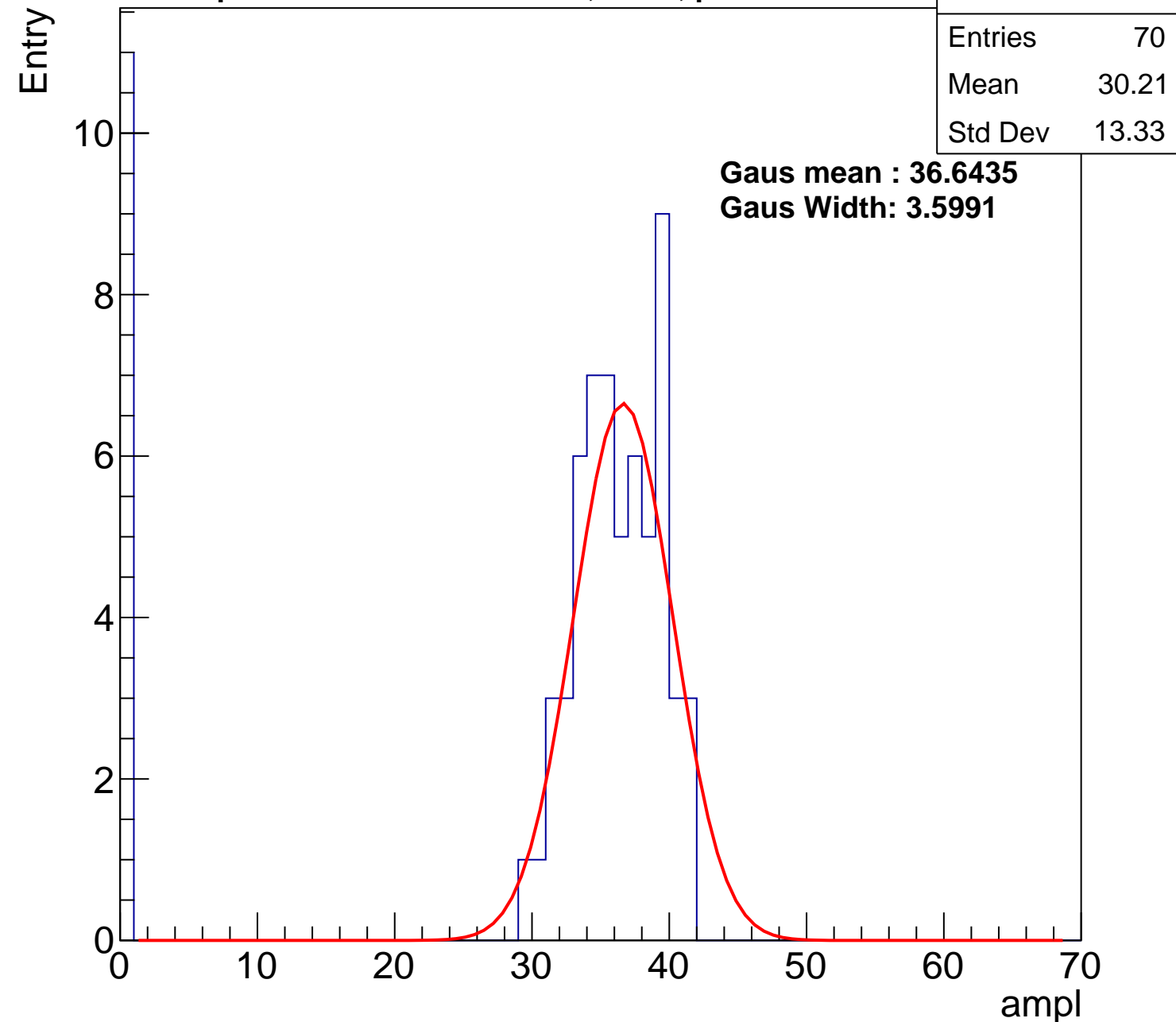
40

50

60

70

ampl



B1L103S, U1-ch25, adc2

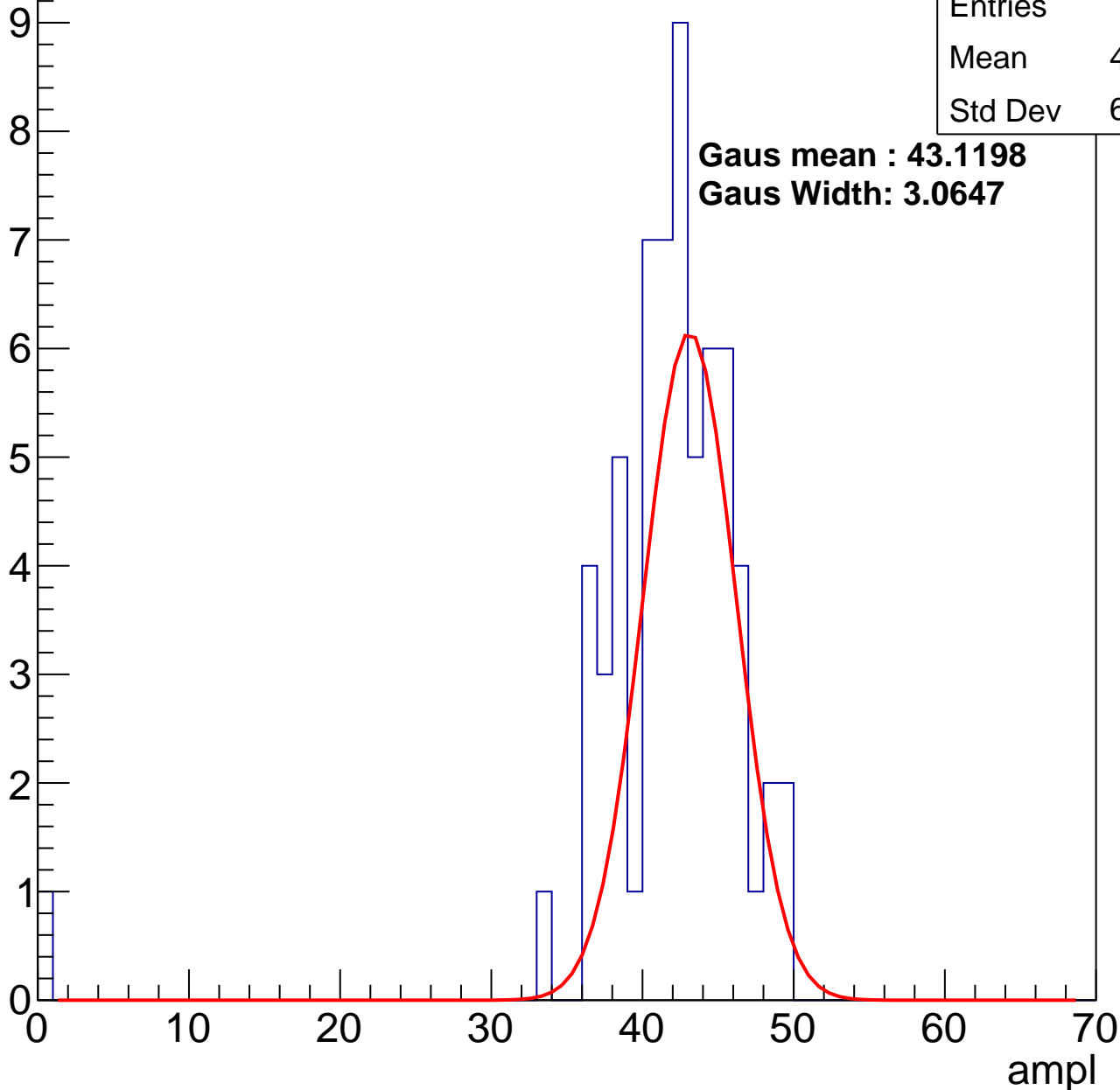
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	41.19
Std Dev	6.235

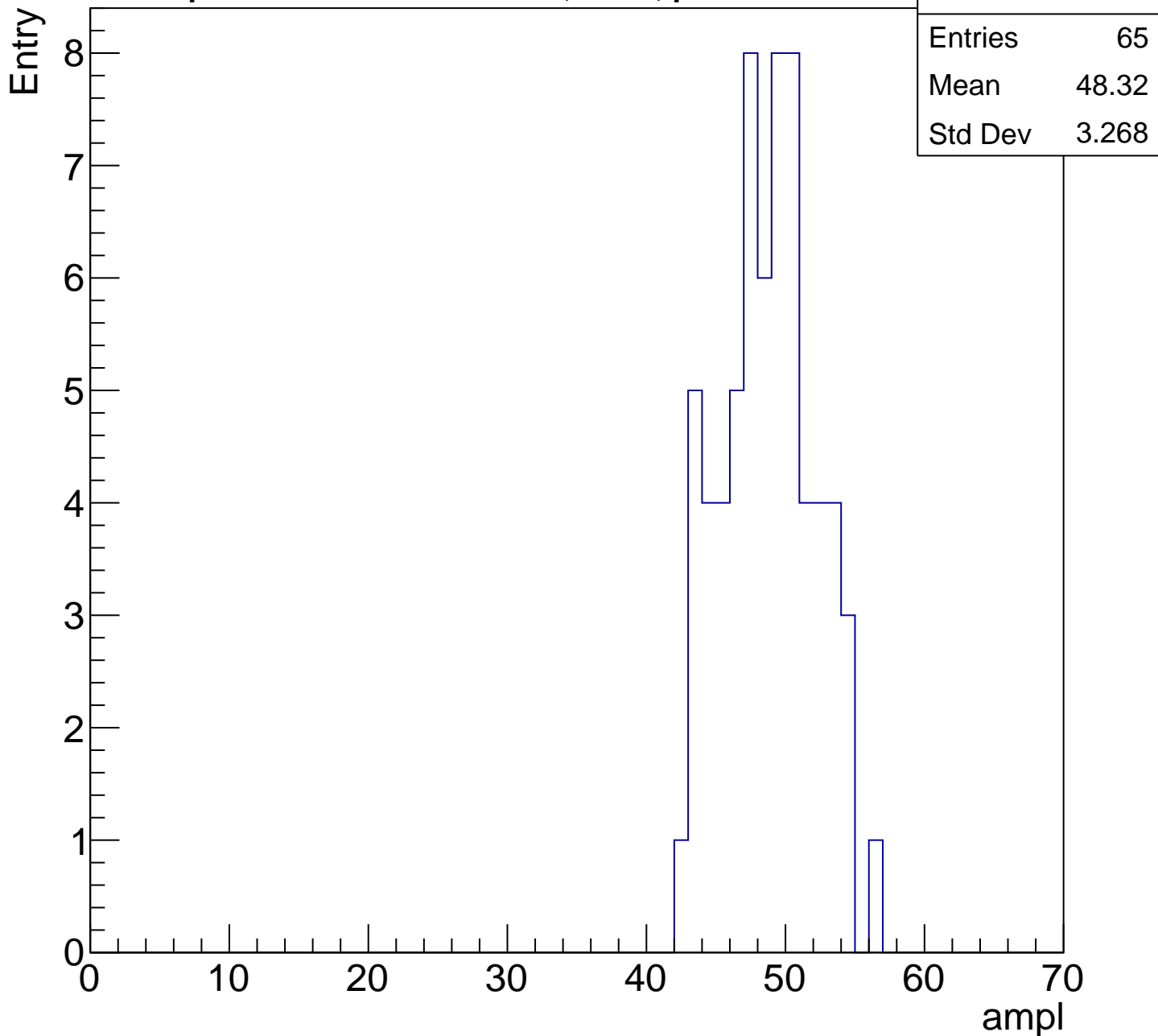
Gaus mean : 43.1198

Gaus Width: 3.0647



B1L103S, U1-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

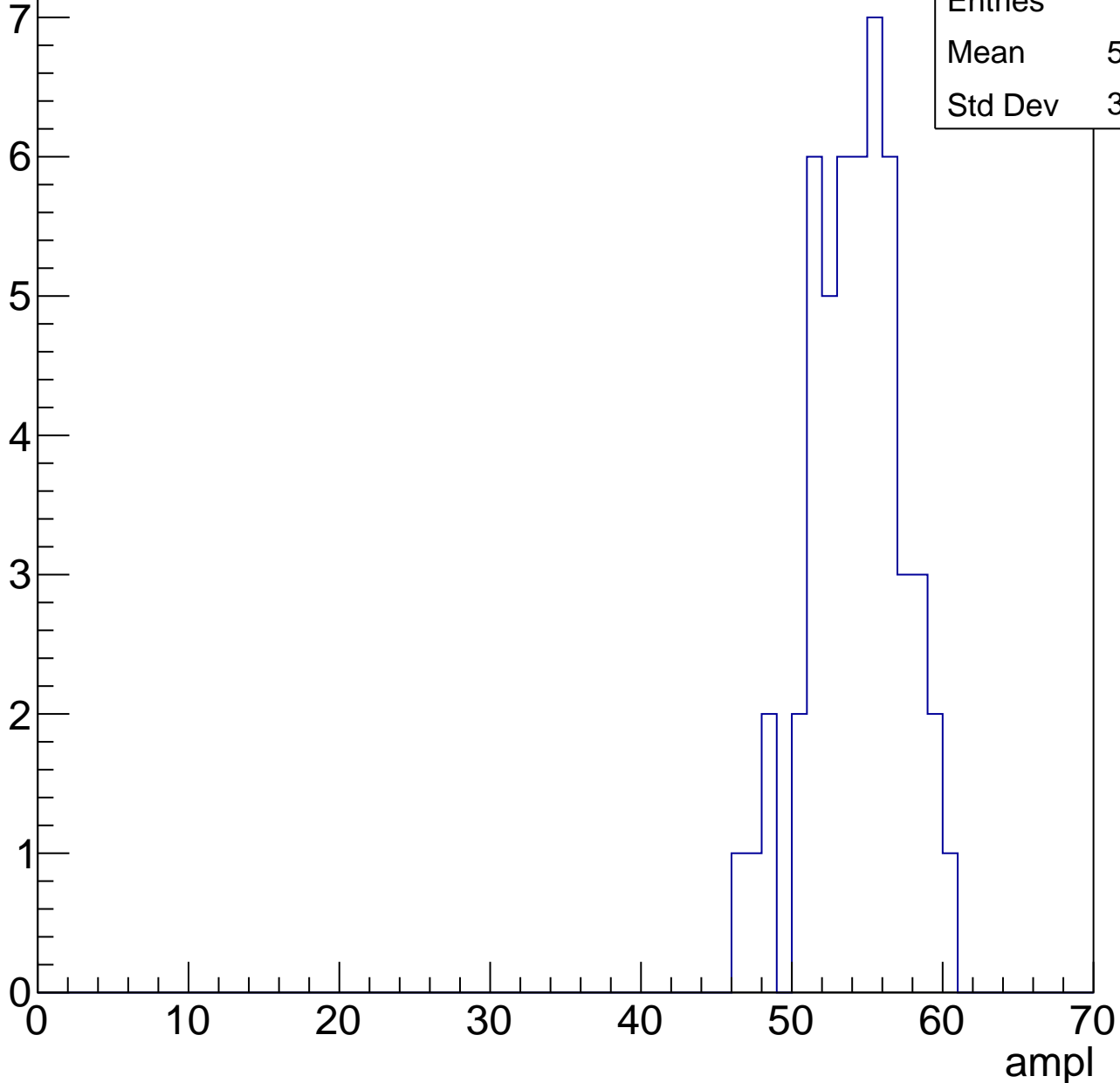


B1L103S, U1-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	53.75
Std Dev	3.086



B1L103S, U1-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

47

Mean

59

Std Dev

2.609

0

10

20

30

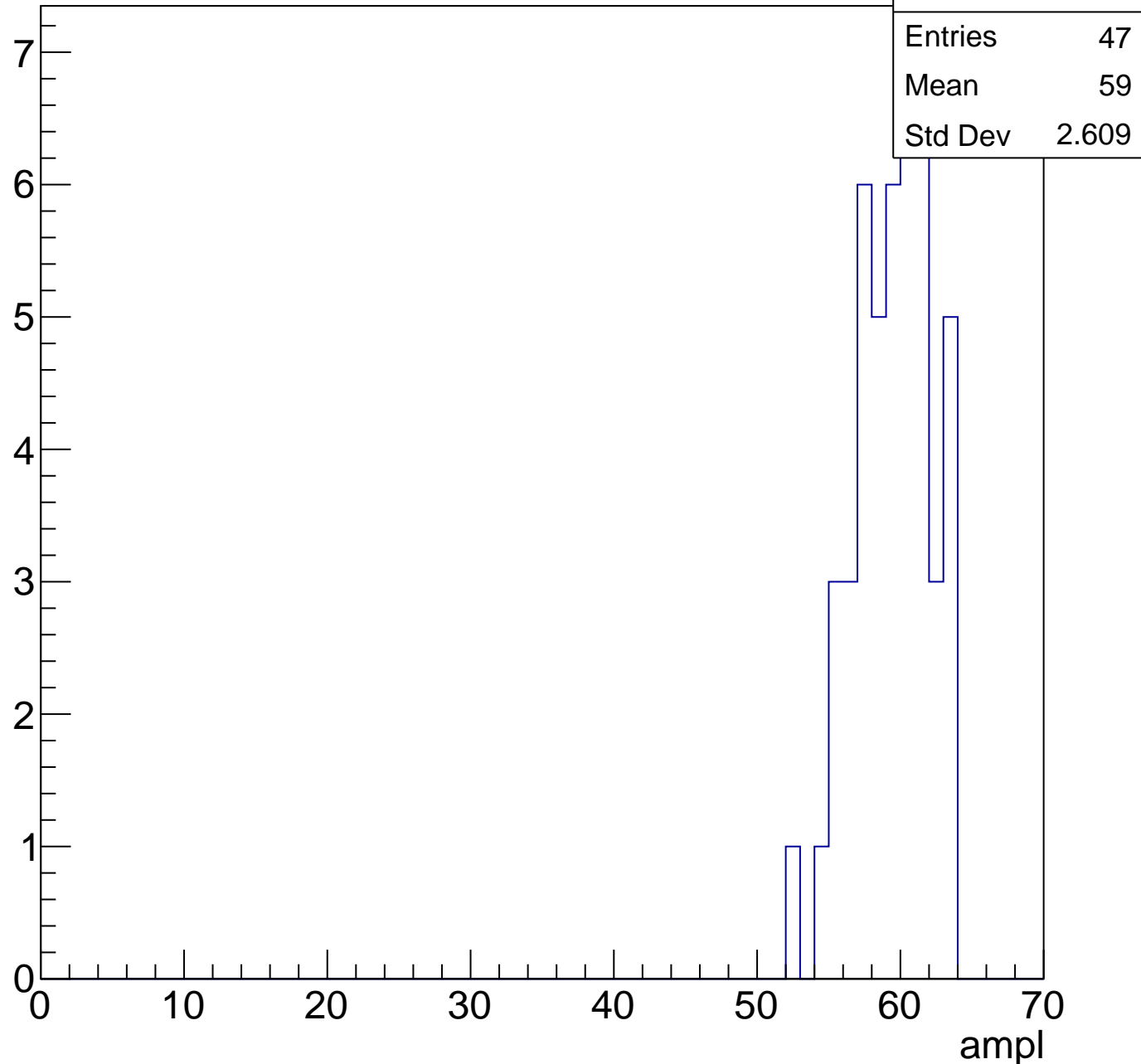
40

50

60

70

ampl

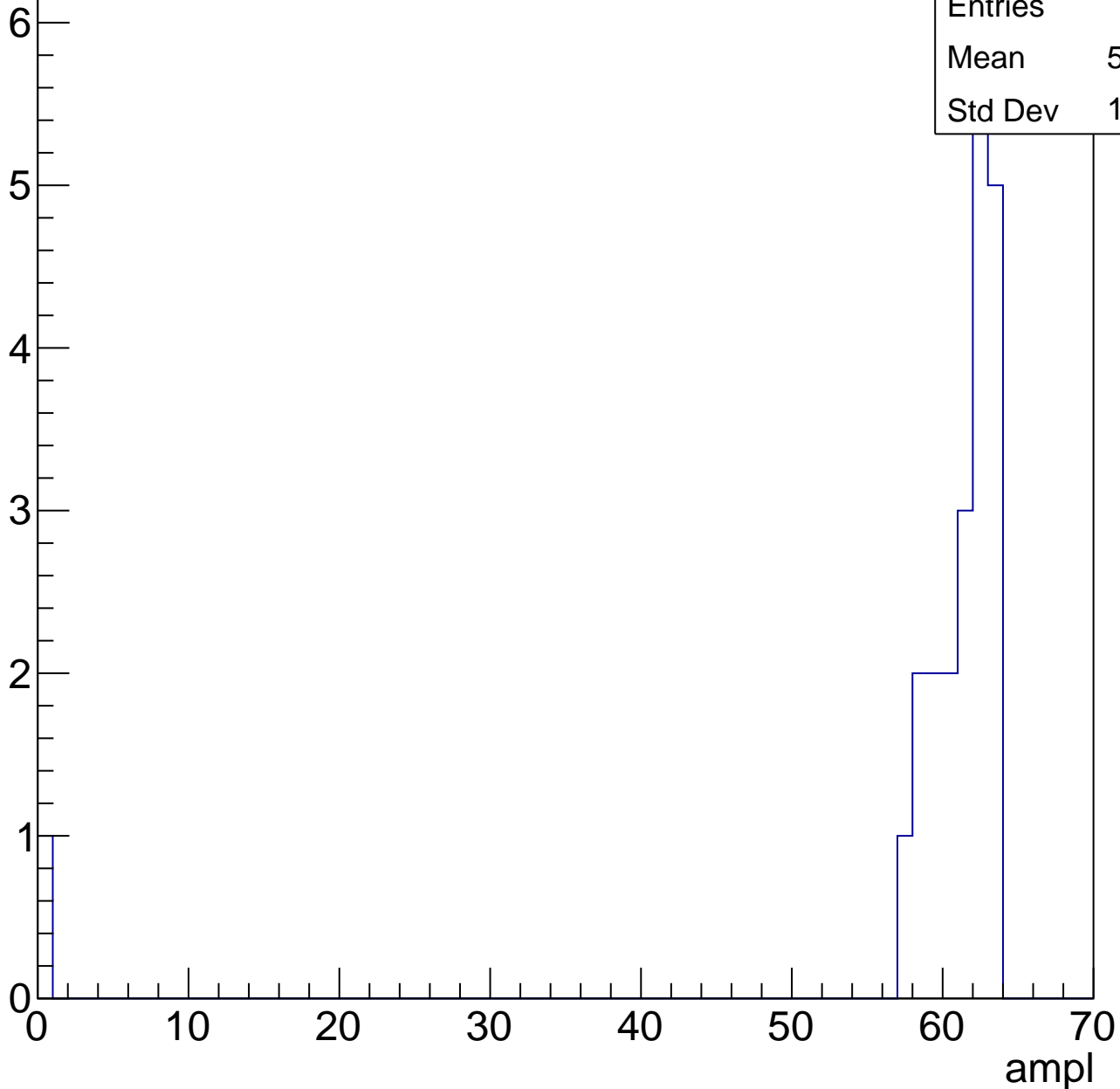


B1L103S, U1-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

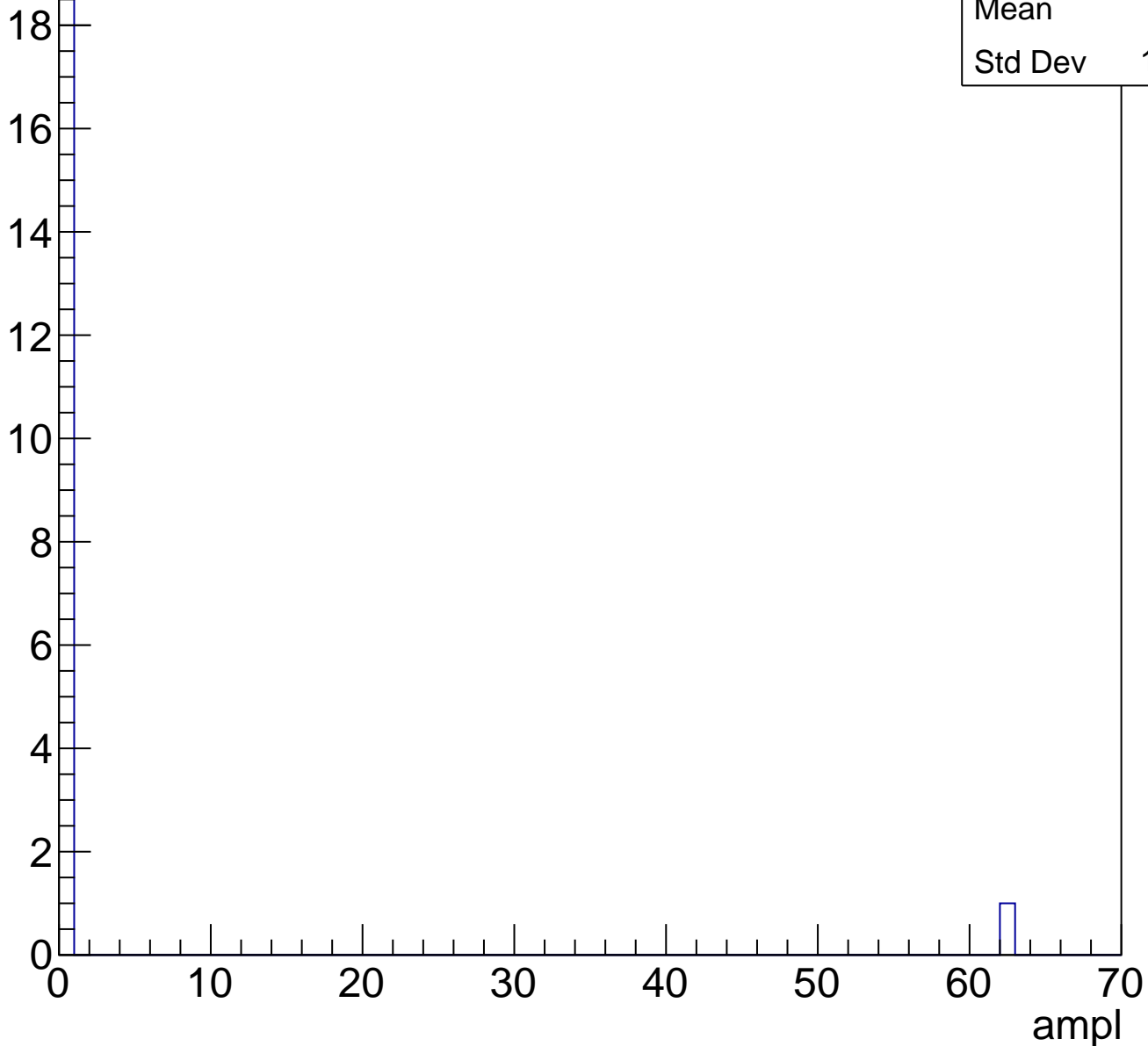
Entries	22
Mean	58.23
Std Dev	12.83



B1L103S, U1-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.1
Std Dev	13.51

B1L103S, U1-ch26, adc0

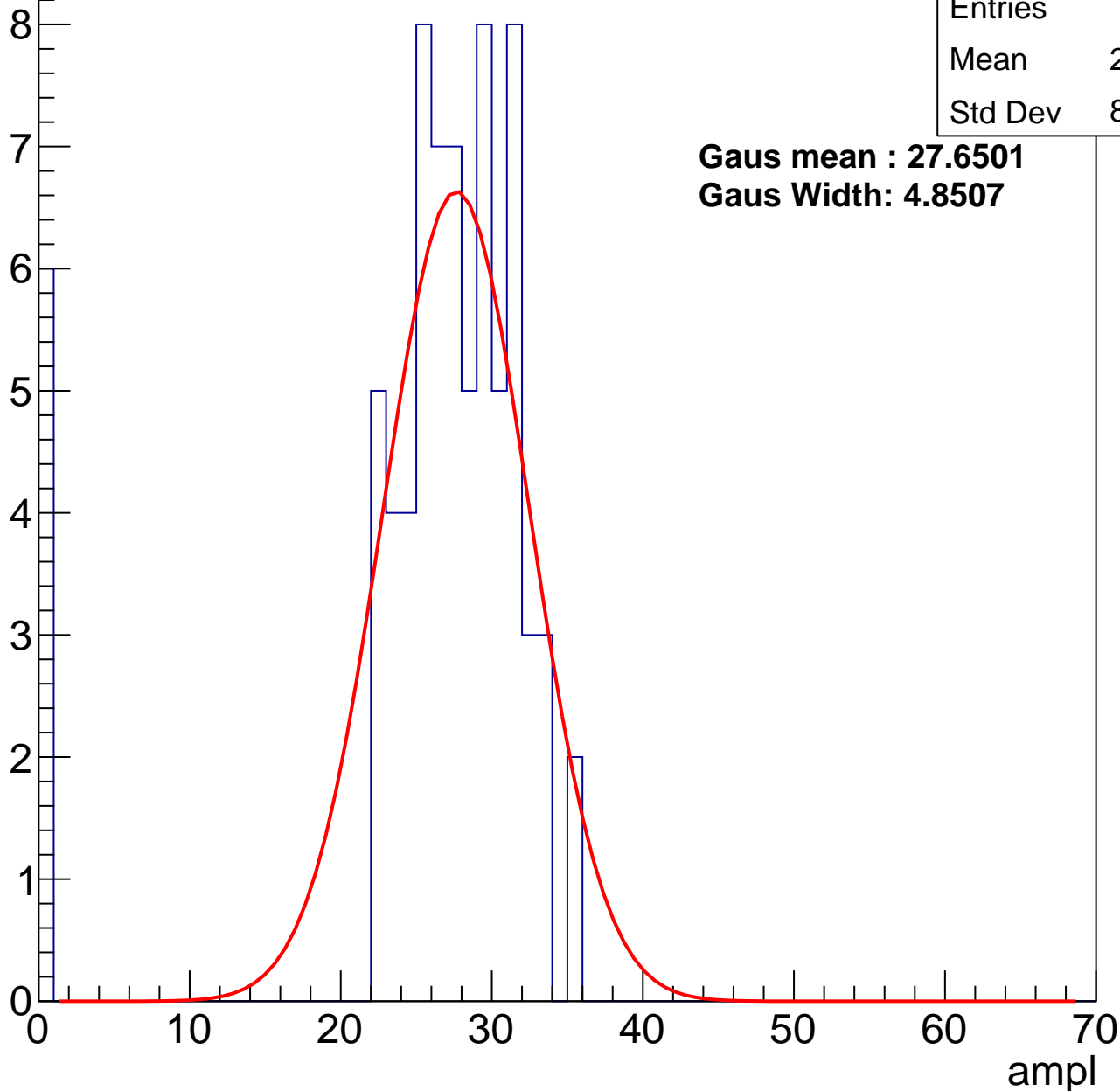
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.39
Std Dev	8.135

Gaus mean : 27.6501

Gaus Width: 4.8507



B1L103S, U1-ch26, adc1

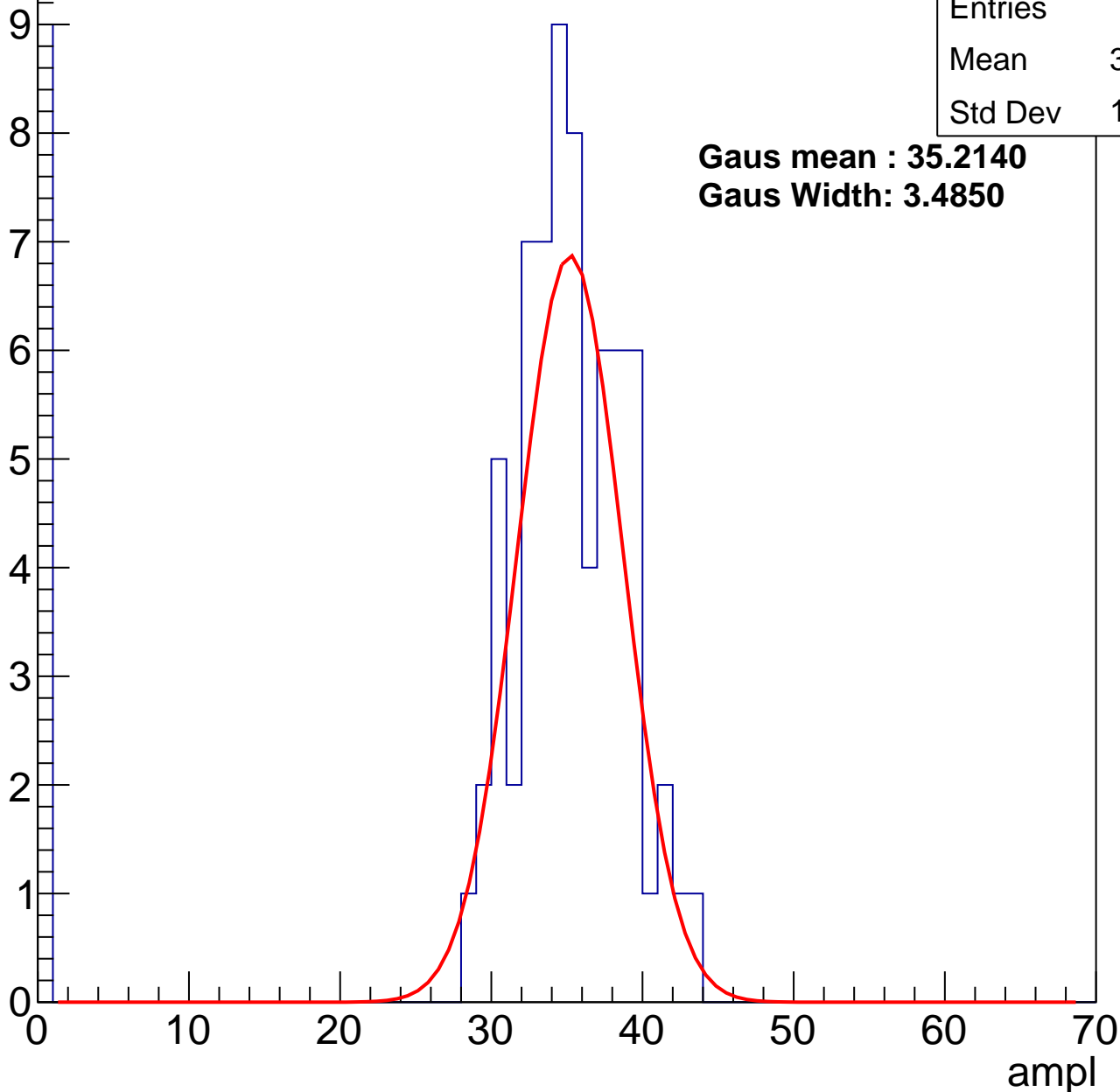
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	30.83
Std Dev	11.65

Gaus mean : 35.2140

Gaus Width: 3.4850



B1L103S, U1-ch26, adc2

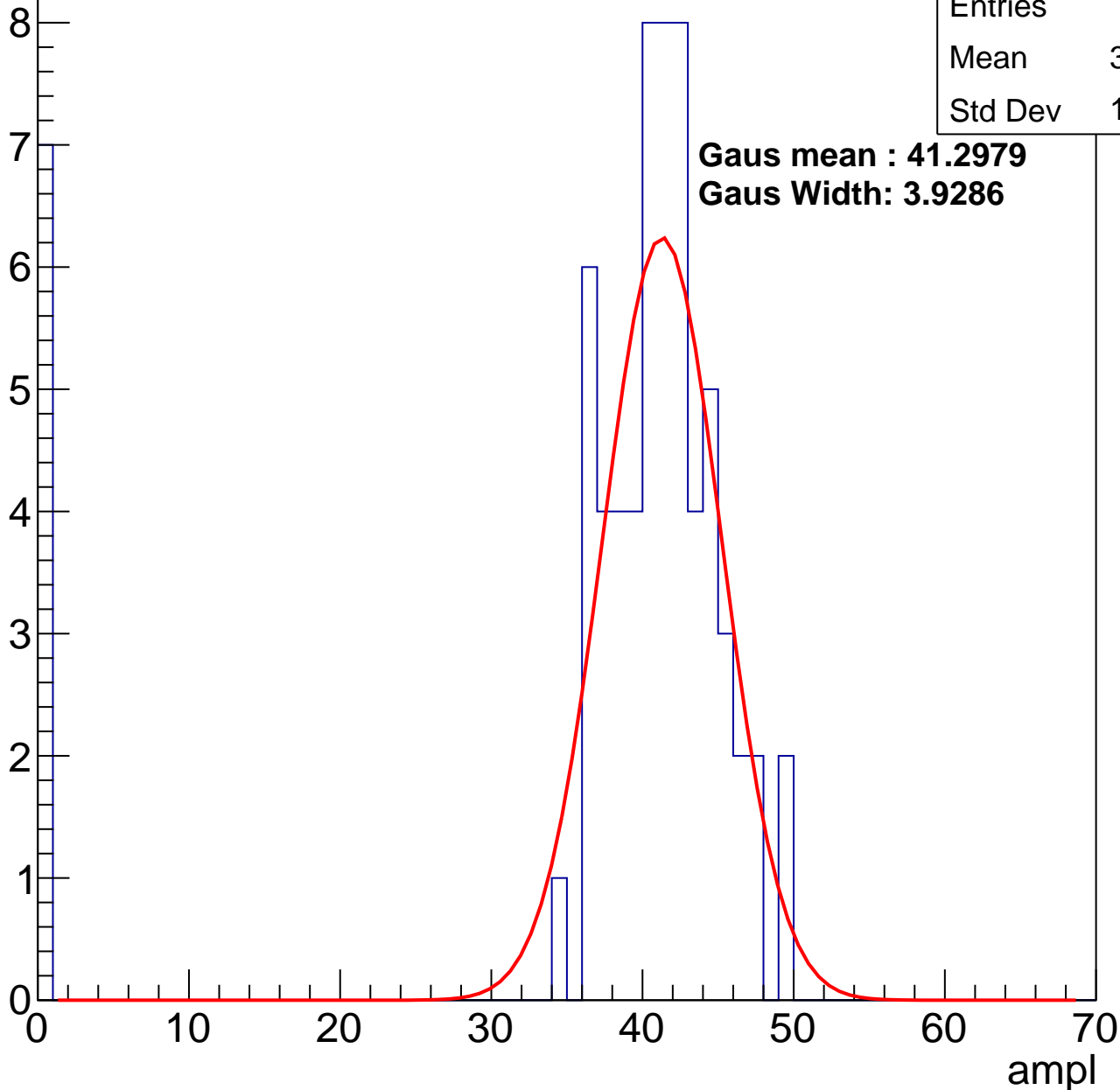
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.78
Std Dev	12.86

Gaus mean : 41.2979

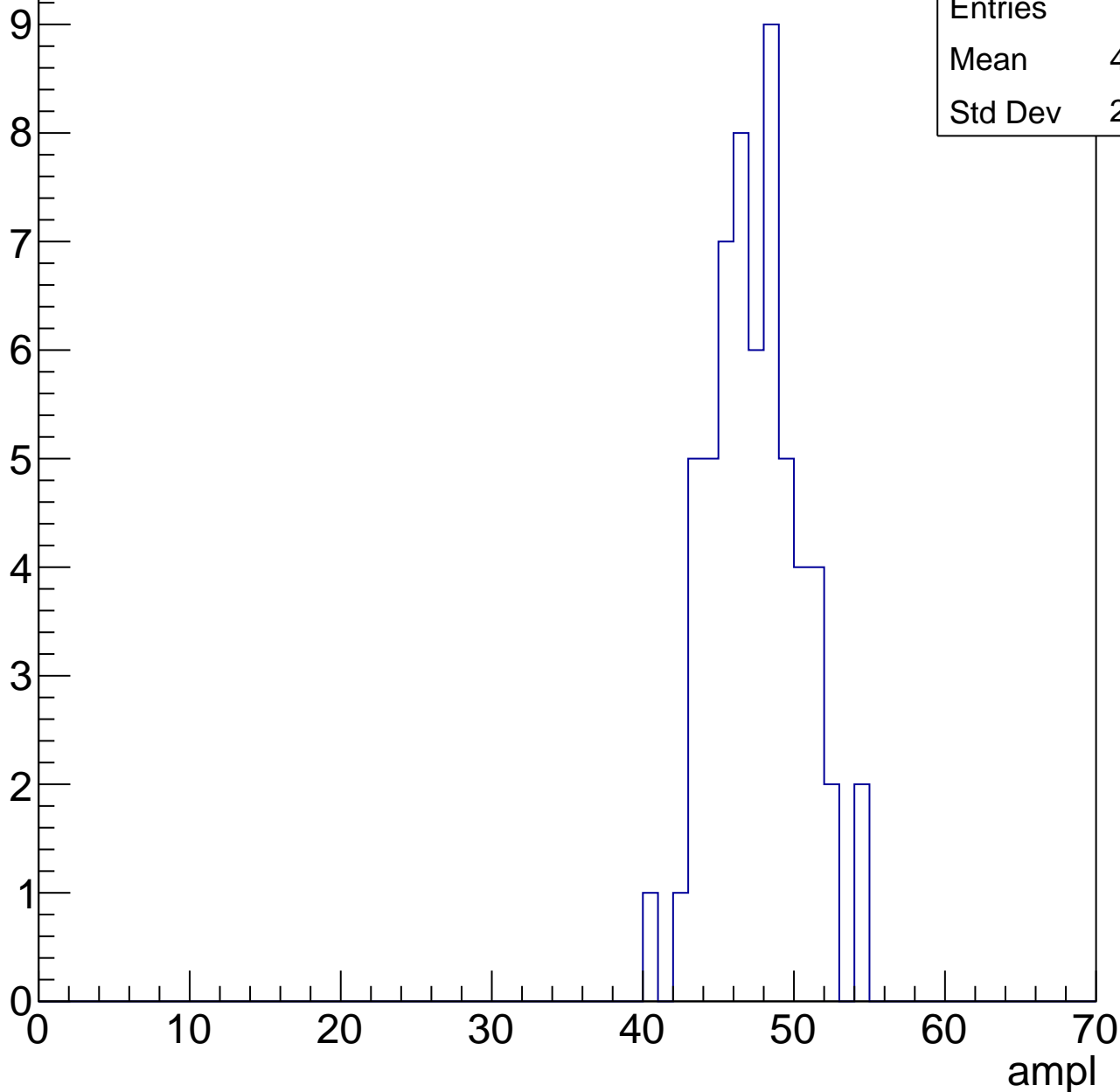
Gaus Width: 3.9286



B1L103S, U1-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

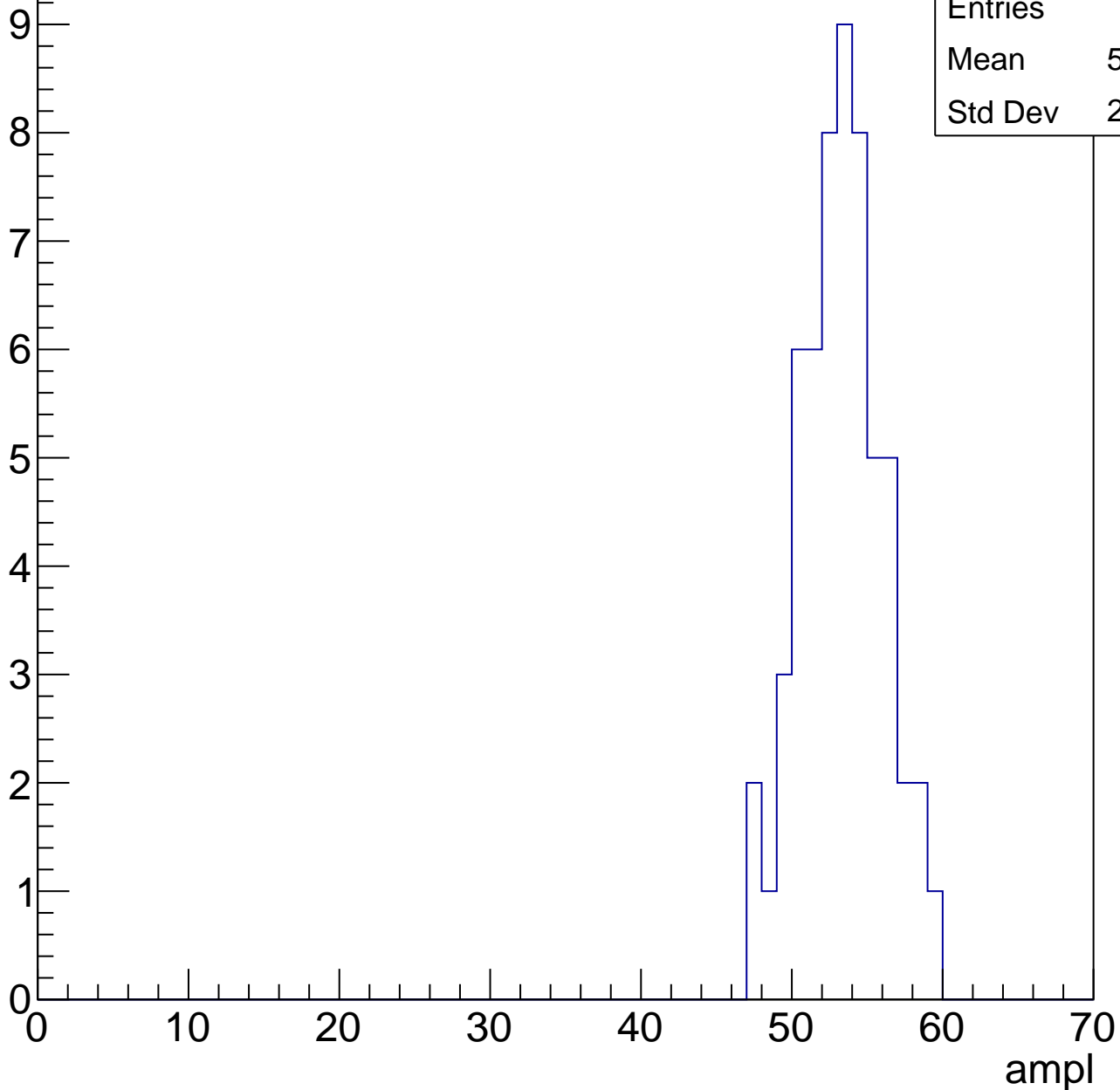


B1L103S, U1-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.83
Std Dev	2.692

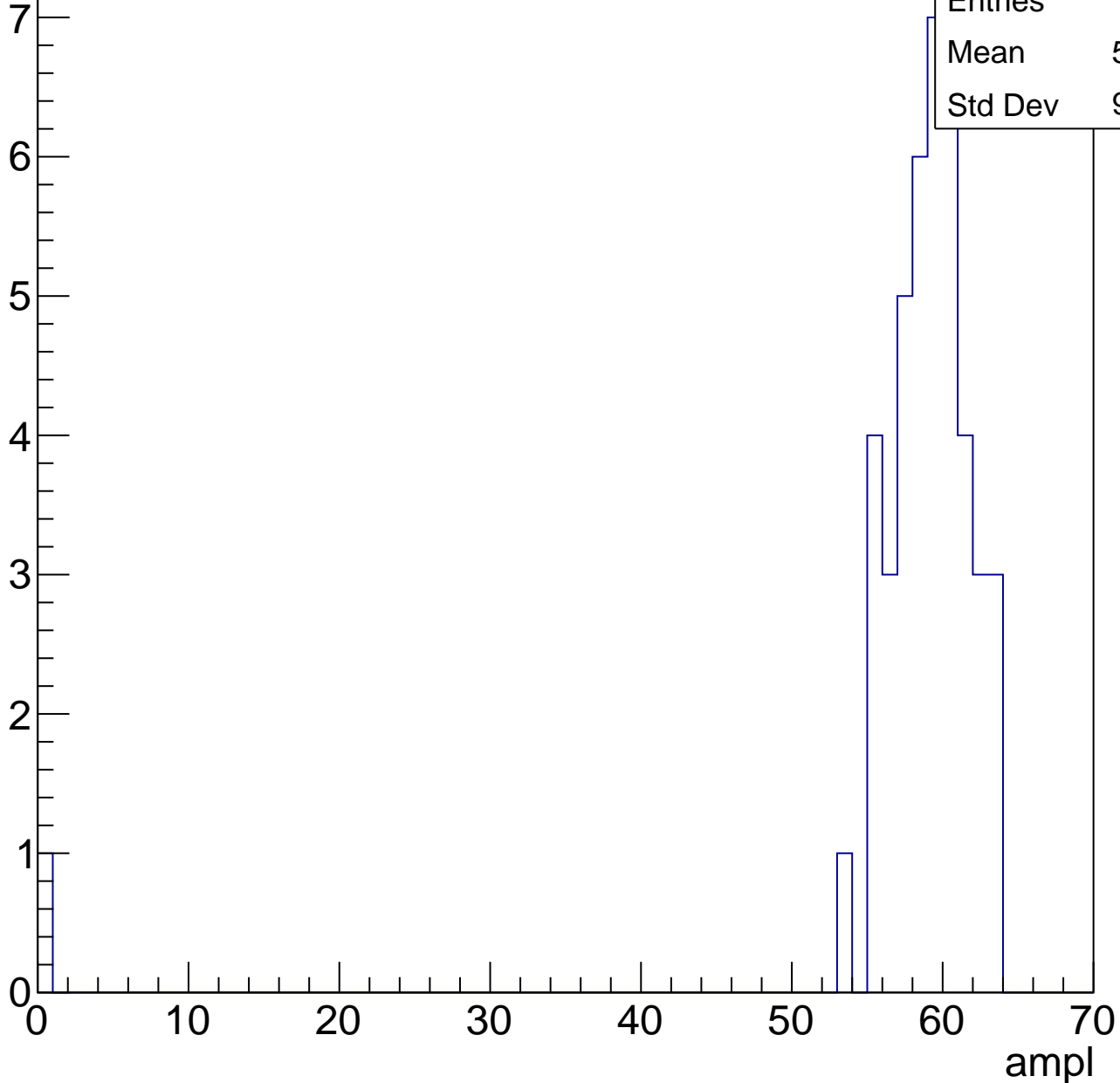


B1L103S, U1-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	57.41
Std Dev	9.071

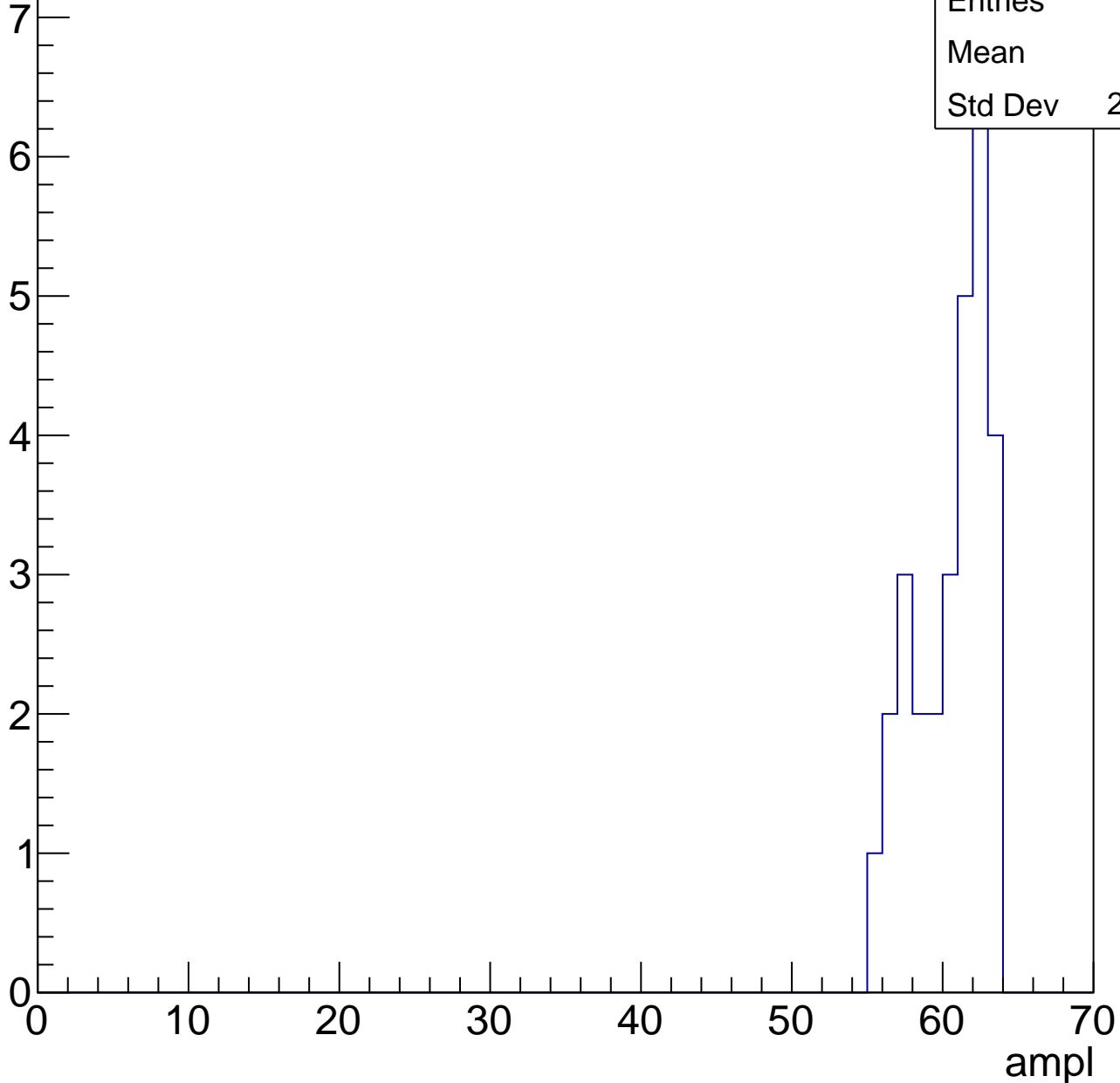


B1L103S, U1-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

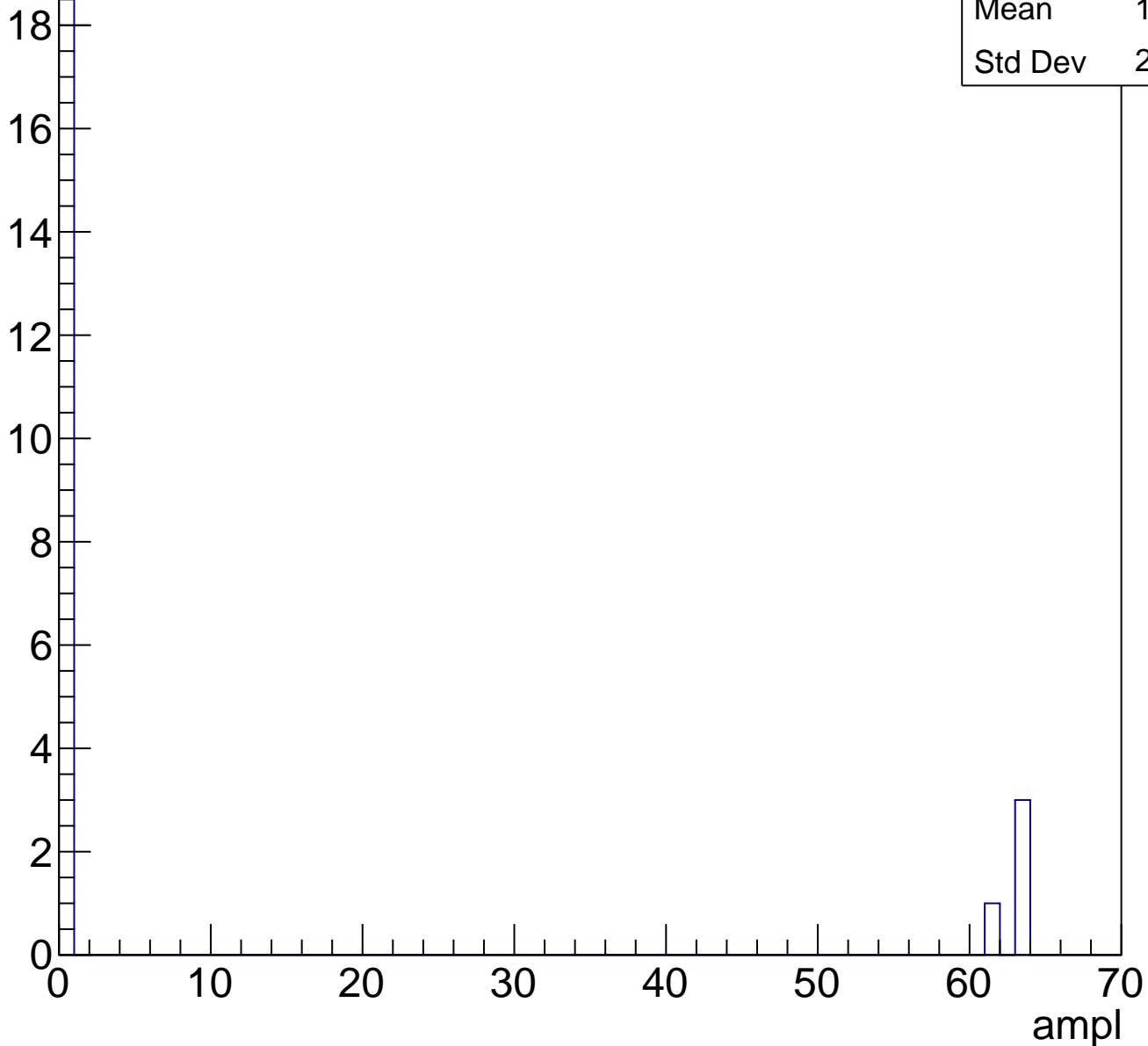
Entries	29
Mean	60.1
Std Dev	2.369



B1L103S, U1-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	23
Mean	10.87
Std Dev	23.69

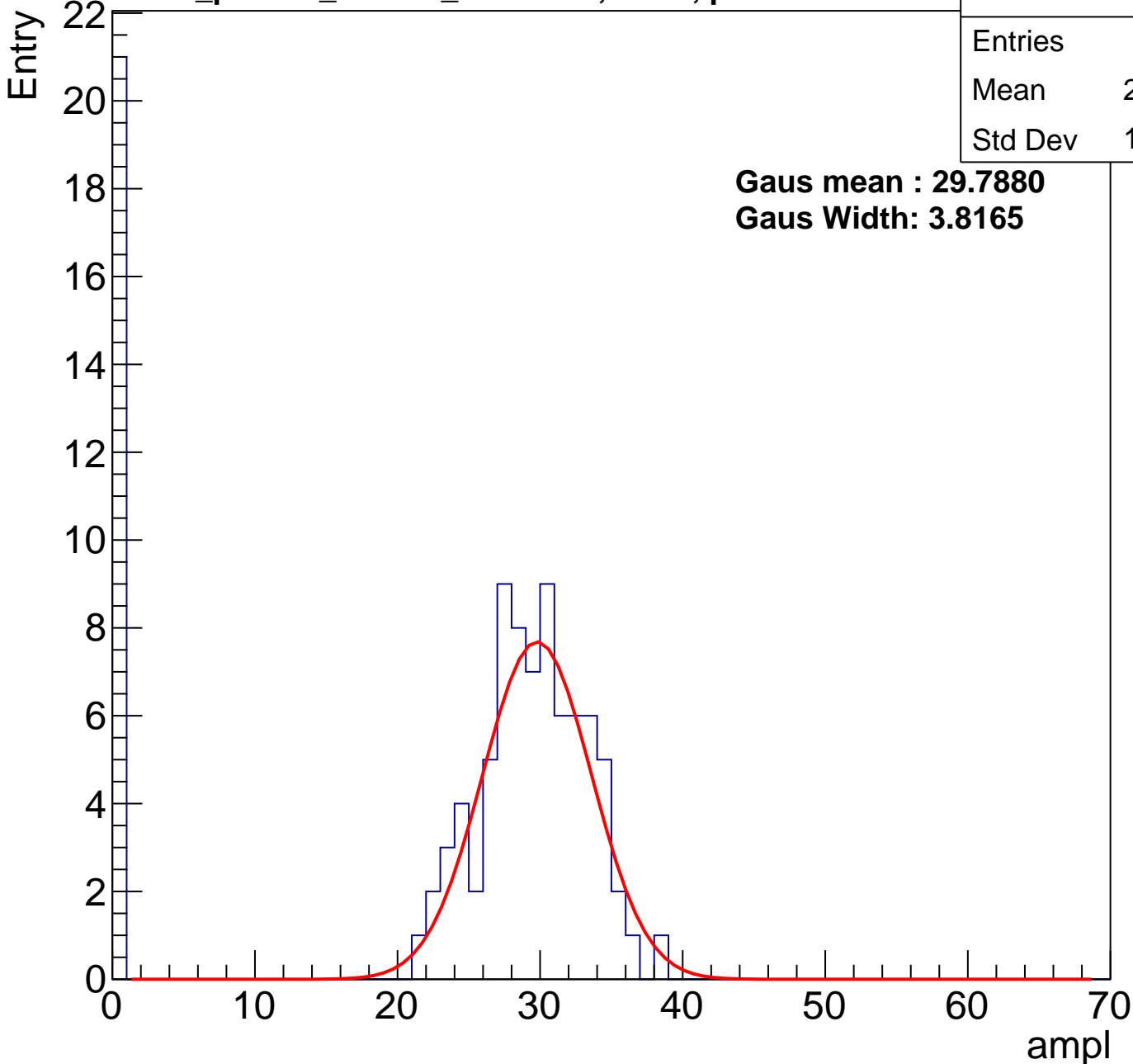
B1L103S, U1-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	22.86
Std Dev	12.36

Gaus mean : 29.7880

Gaus Width: 3.8165



B1L103S, U1-ch27, adc1

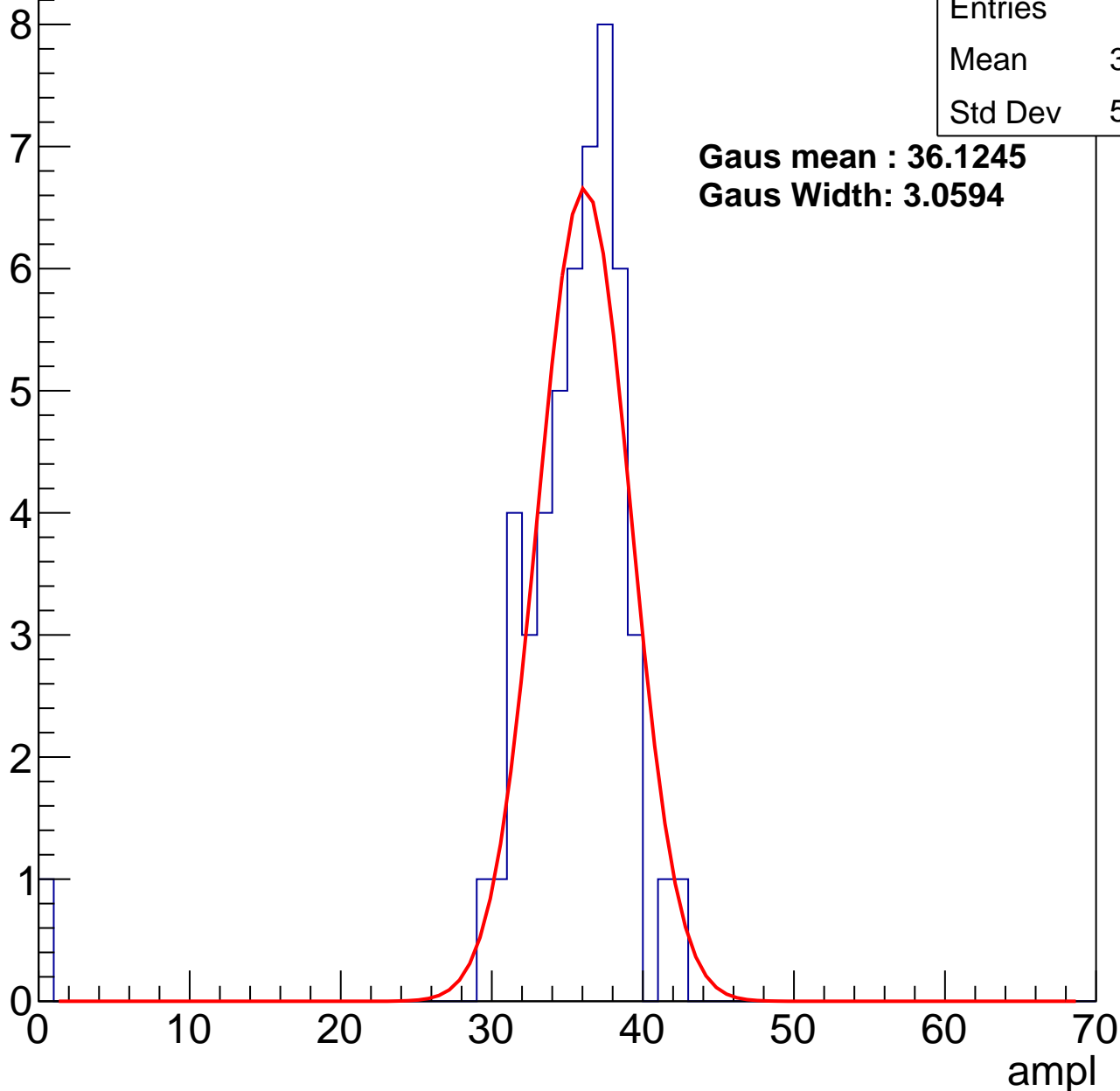
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	34.65
Std Dev	5.632

Gaus mean : 36.1245

Gaus Width: 3.0594



B1L103S, U1-ch27, adc2

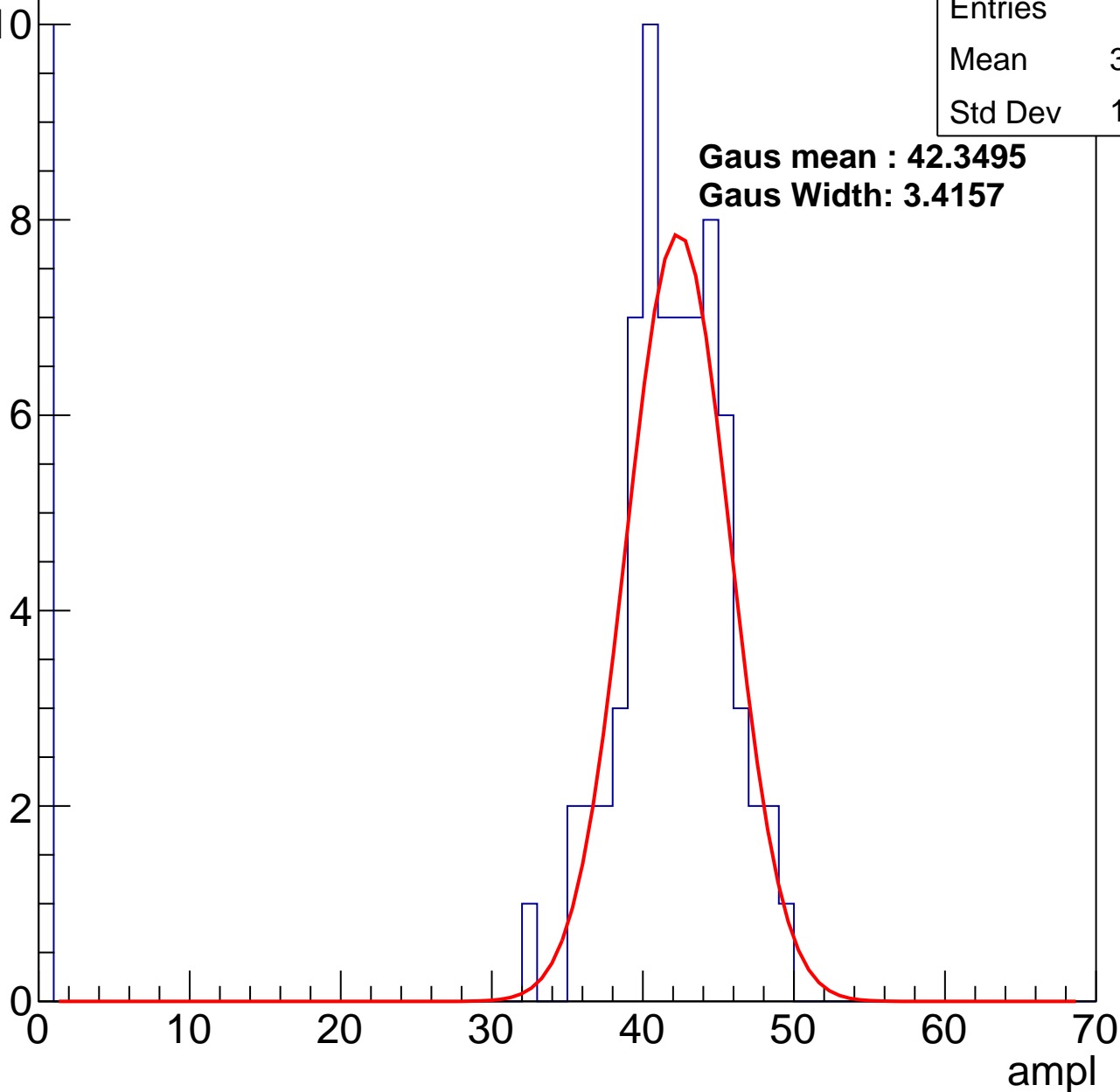
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	36.45
Std Dev	14.13

Gaus mean : 42.3495

Gaus Width: 3.4157

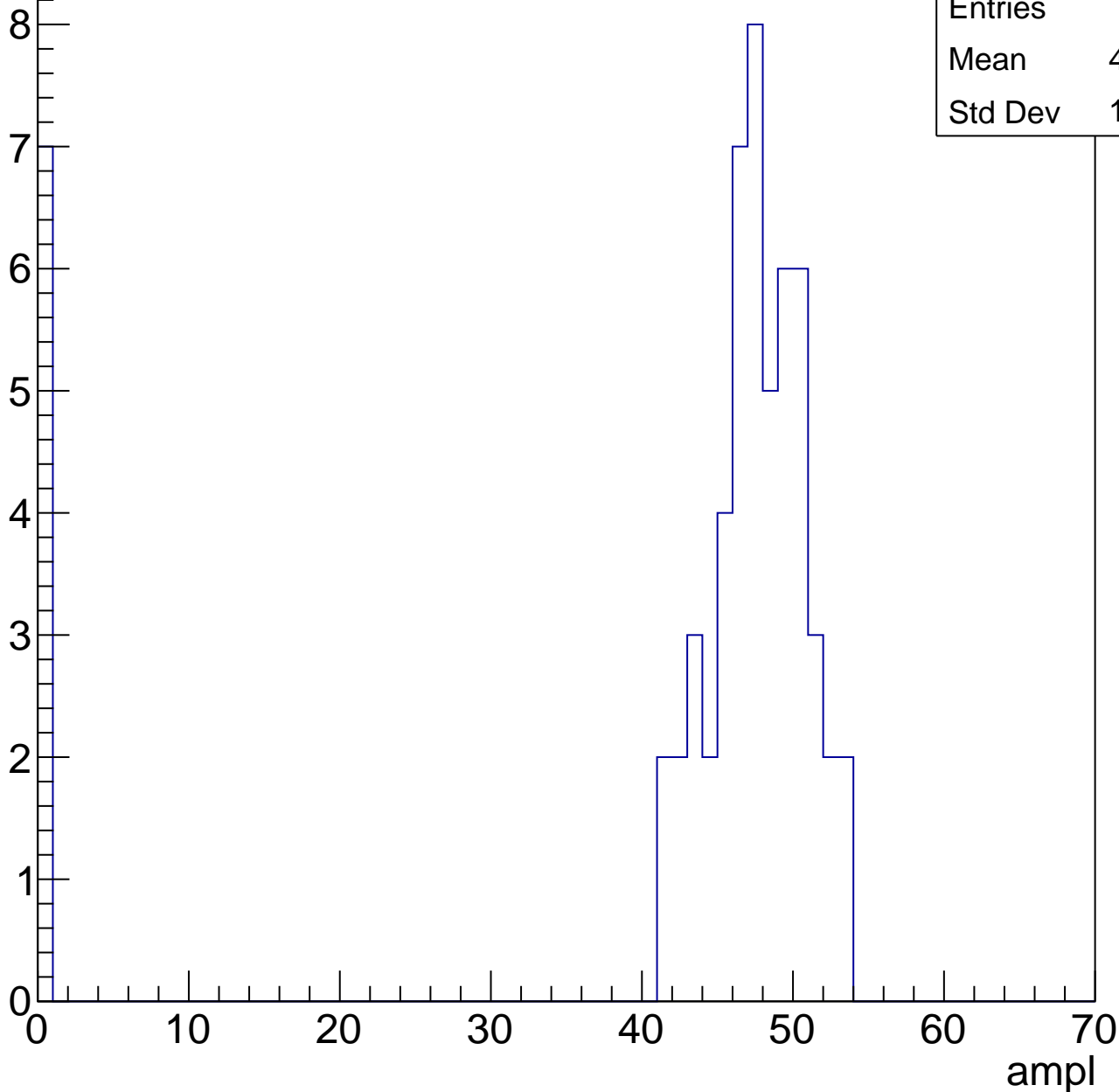


B1L103S, U1-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	41.66
Std Dev	15.54

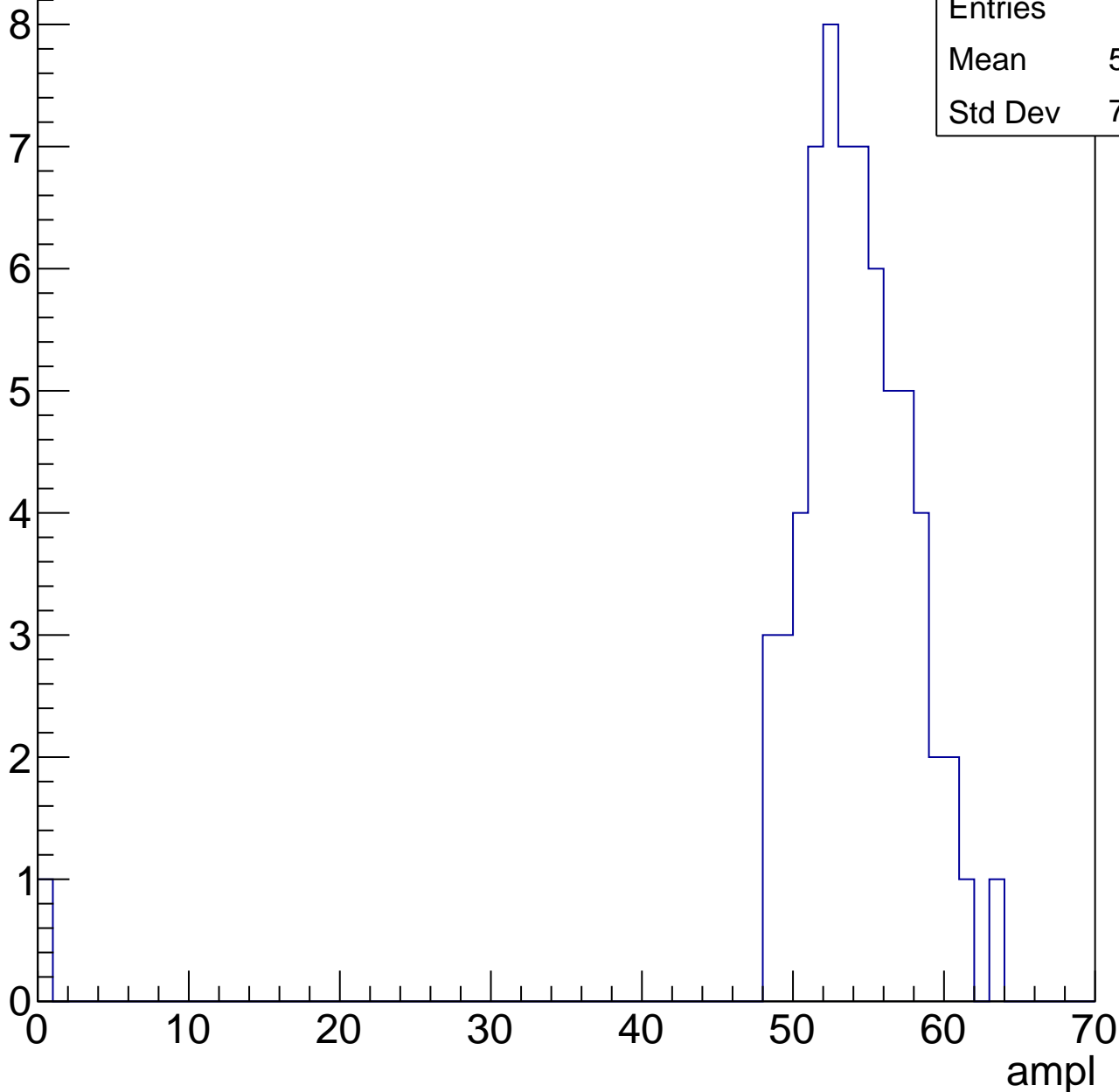


B1L103S, U1-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

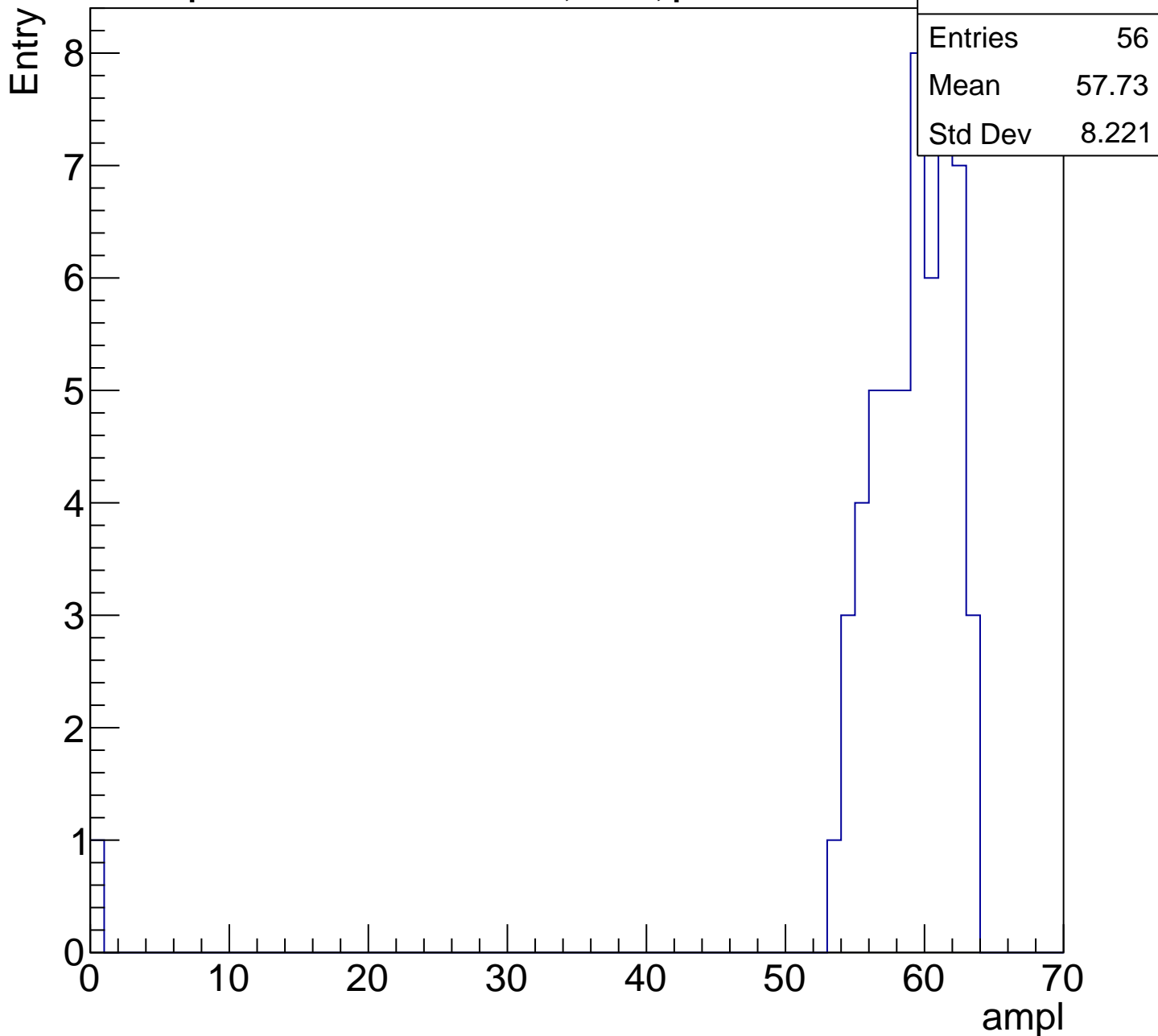
Entry

Entries	66
Mean	53.06
Std Dev	7.379



B1L103S, U1-ch27, adc5

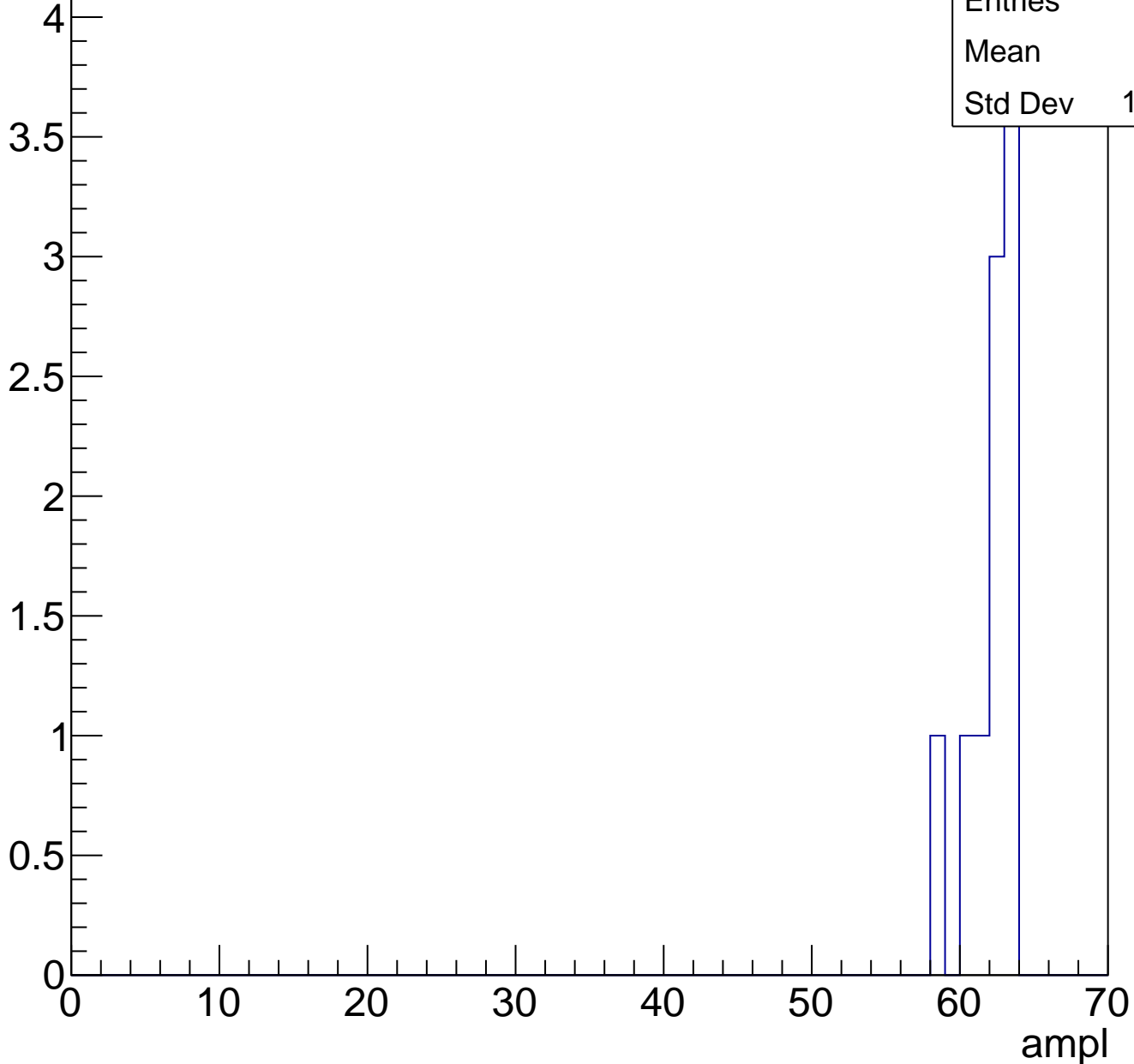
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

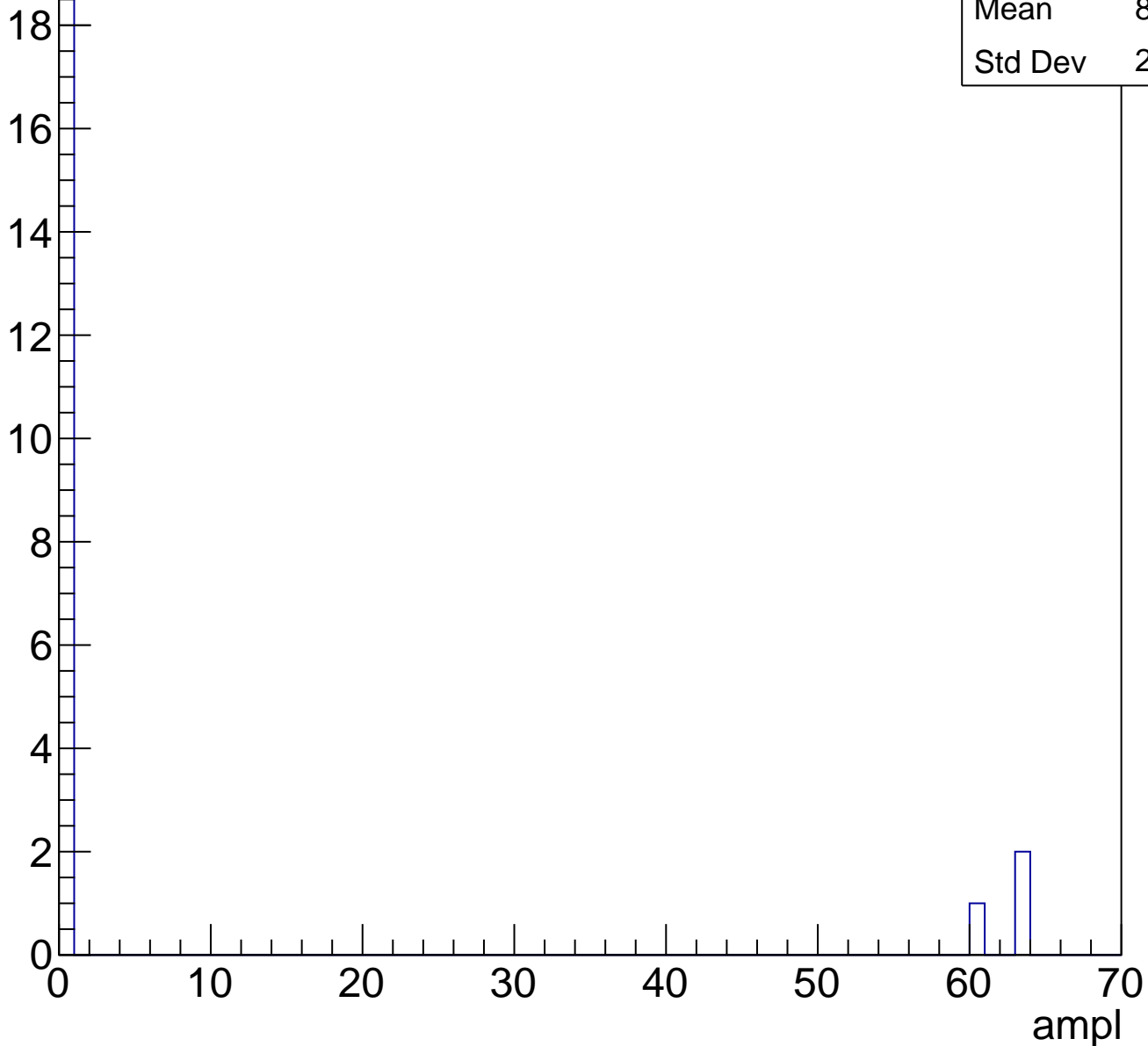


B1L103S, U1-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U1-ch28, adc0

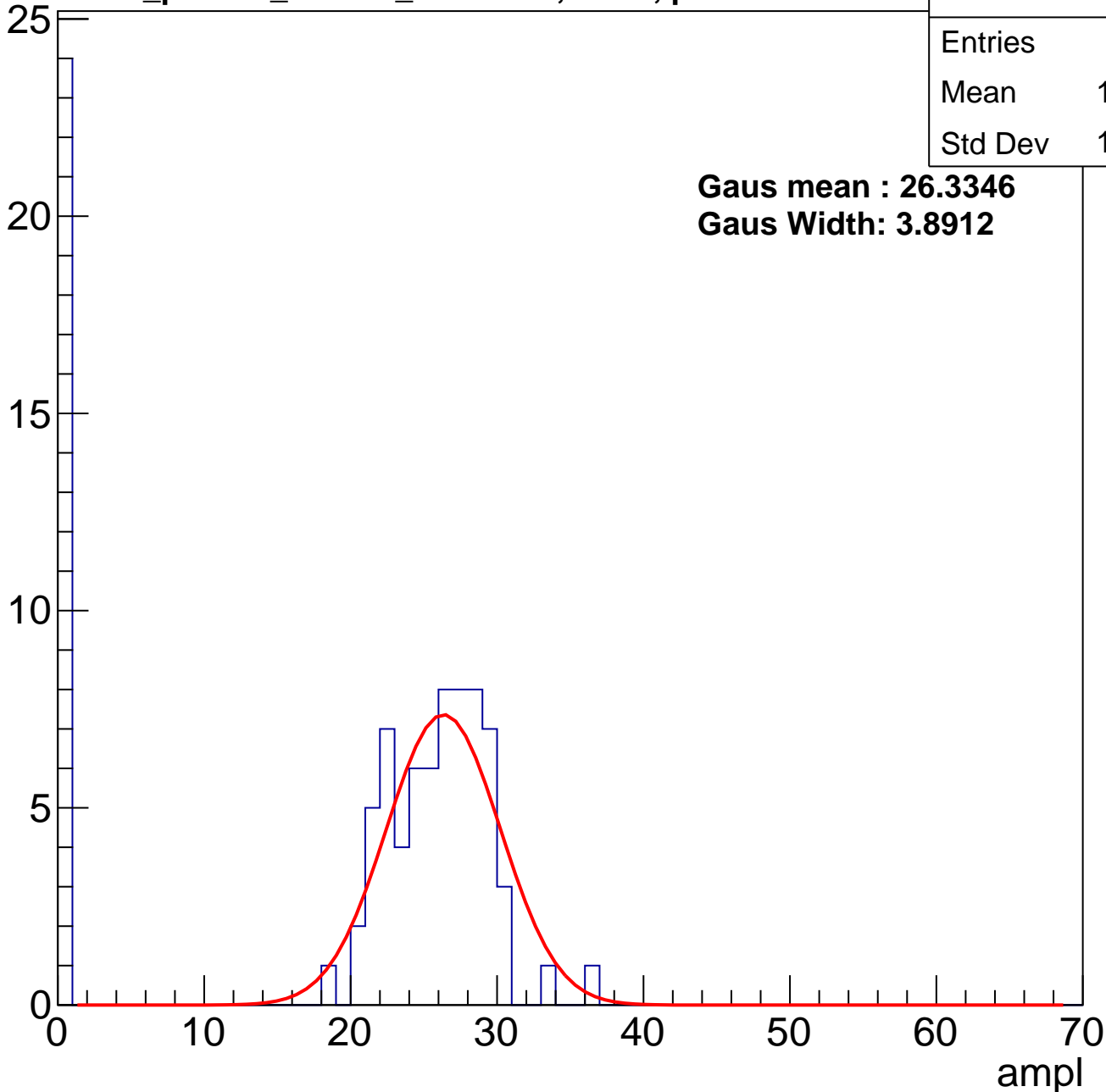
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	18.82
Std Dev	11.62

Gaus mean : 26.3346

Gaus Width: 3.8912

Entry



B1L103S, U1-ch28, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	31.66
Std Dev	7.704

Gaus mean : 34.7652

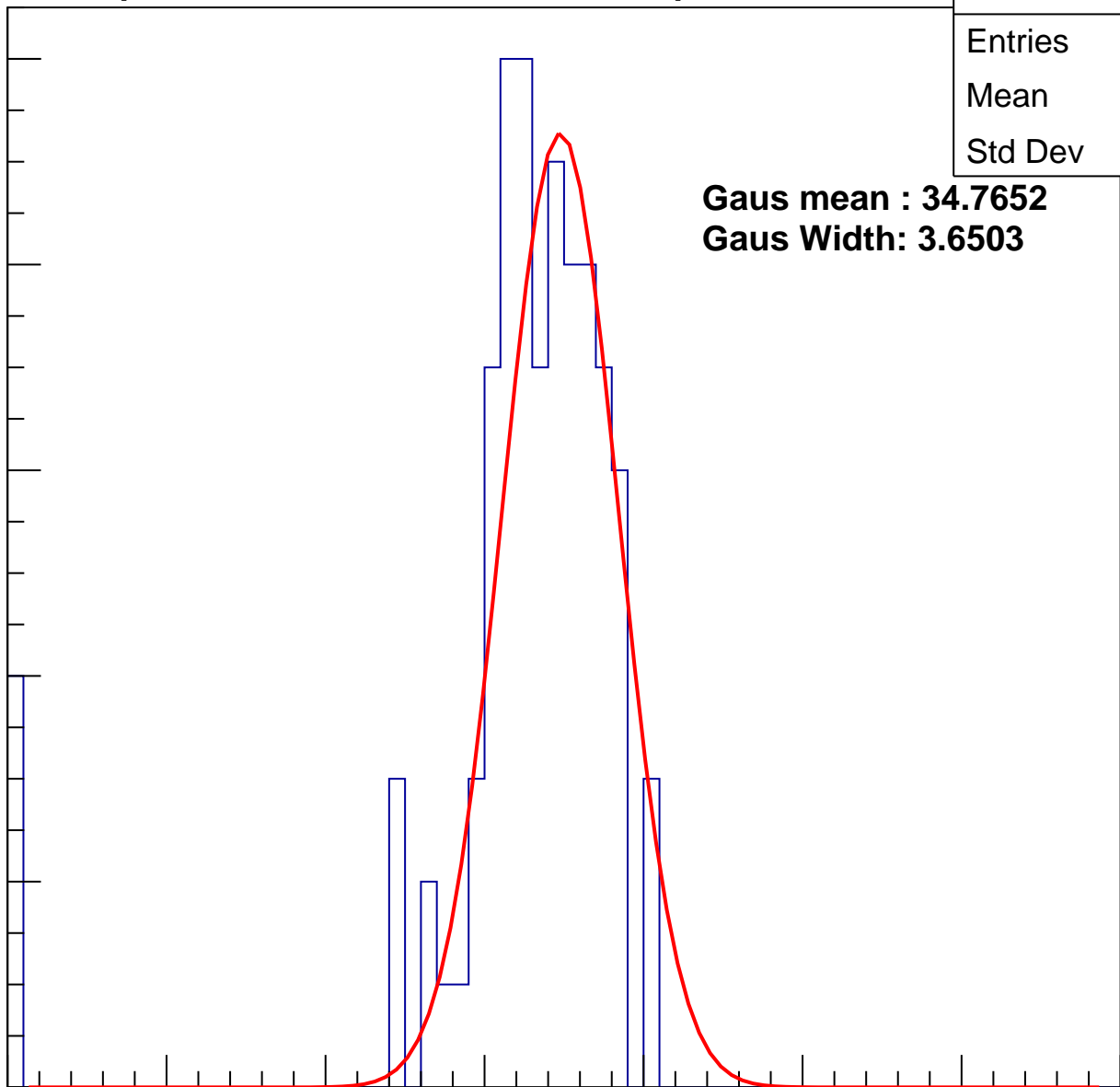
Gaus Width: 3.6503

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch28, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	33.03
Std Dev	15.61

Gaus mean : 40.9872

Gaus Width: 2.9319

Entry

10

8

6

4

2

0

0

10

20

30

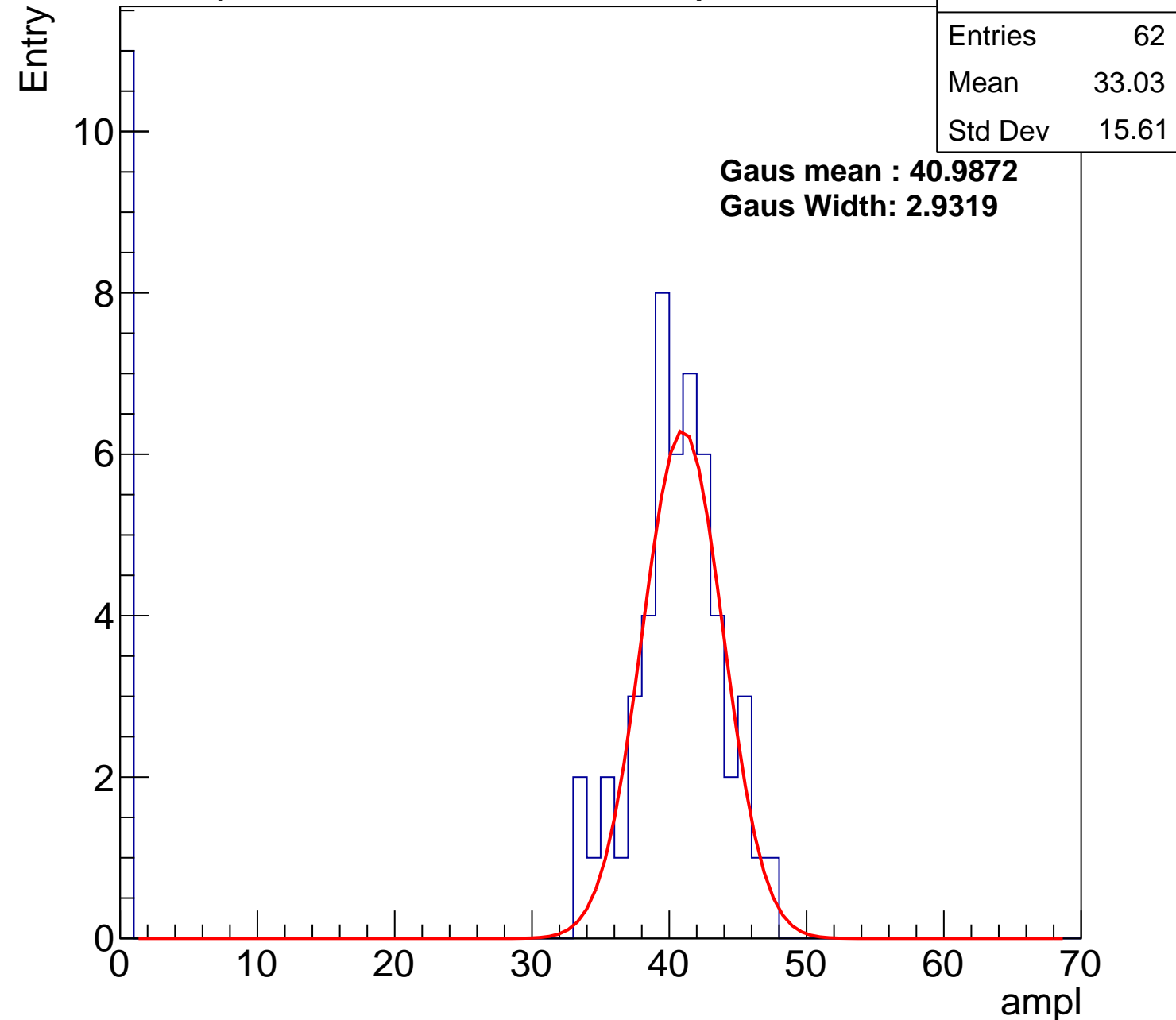
40

50

60

70

ampl

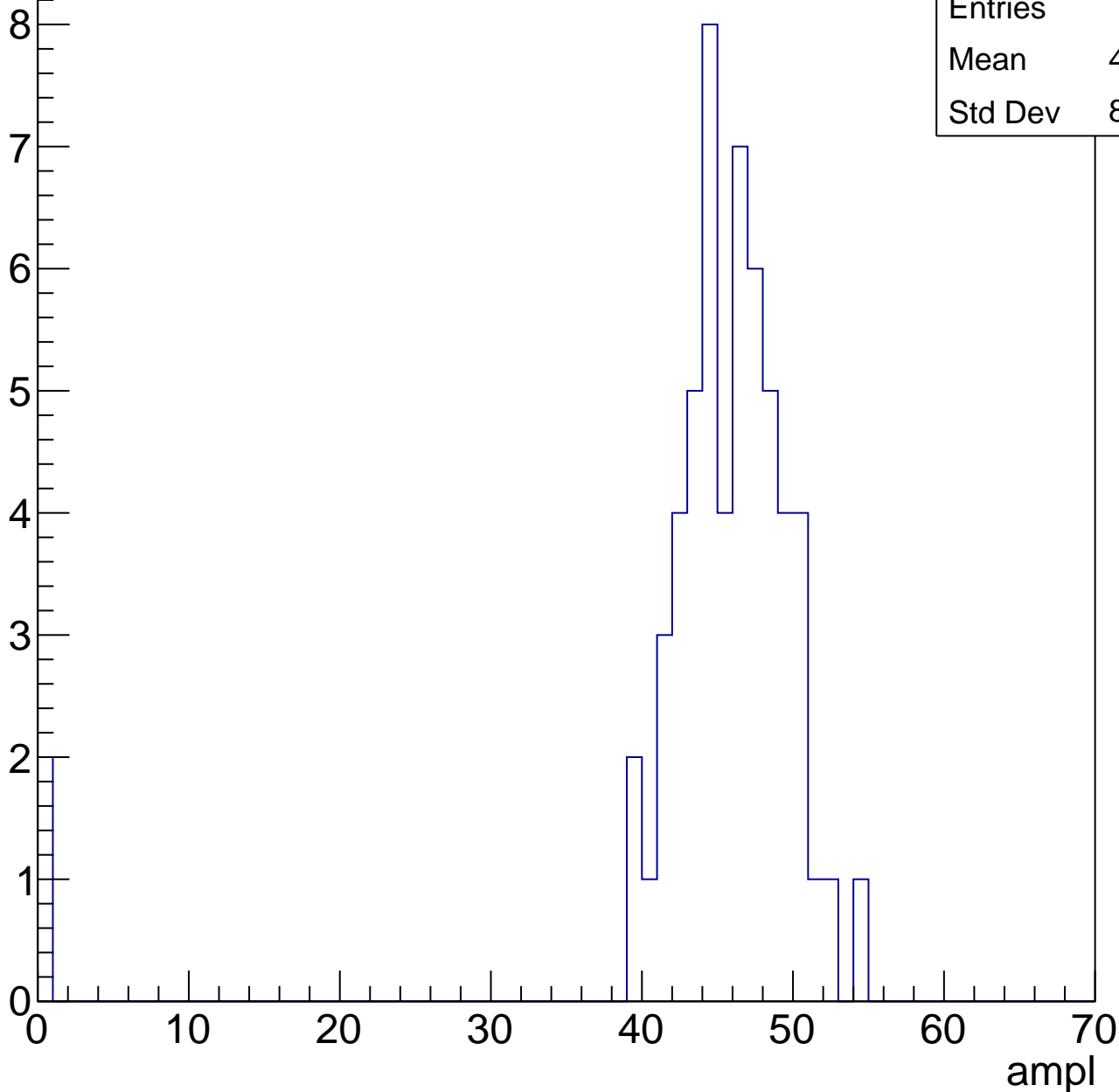


B1L103S, U1-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	44.02
Std Dev	8.914

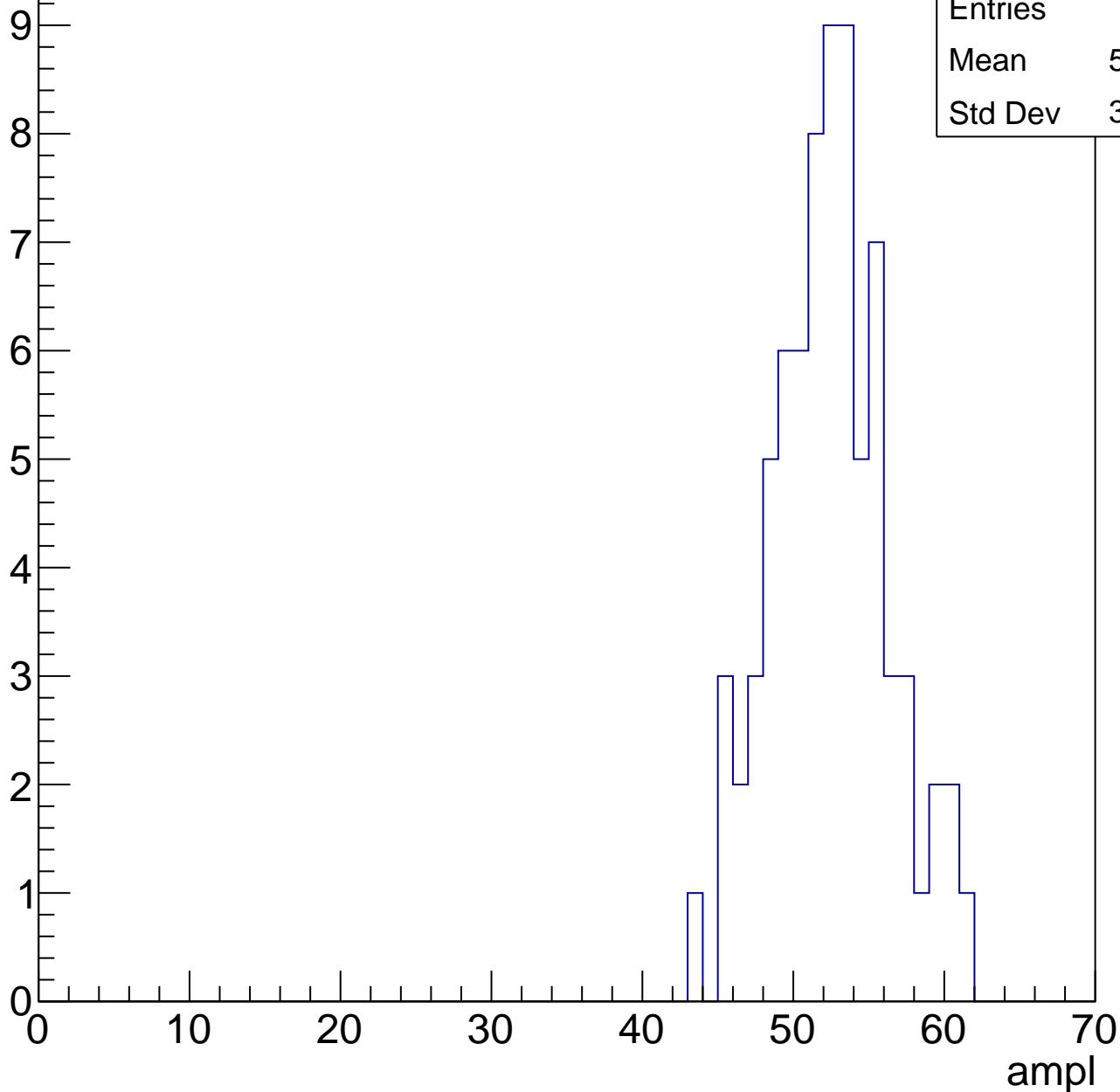


B1L103S, U1-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	51.96
Std Dev	3.802

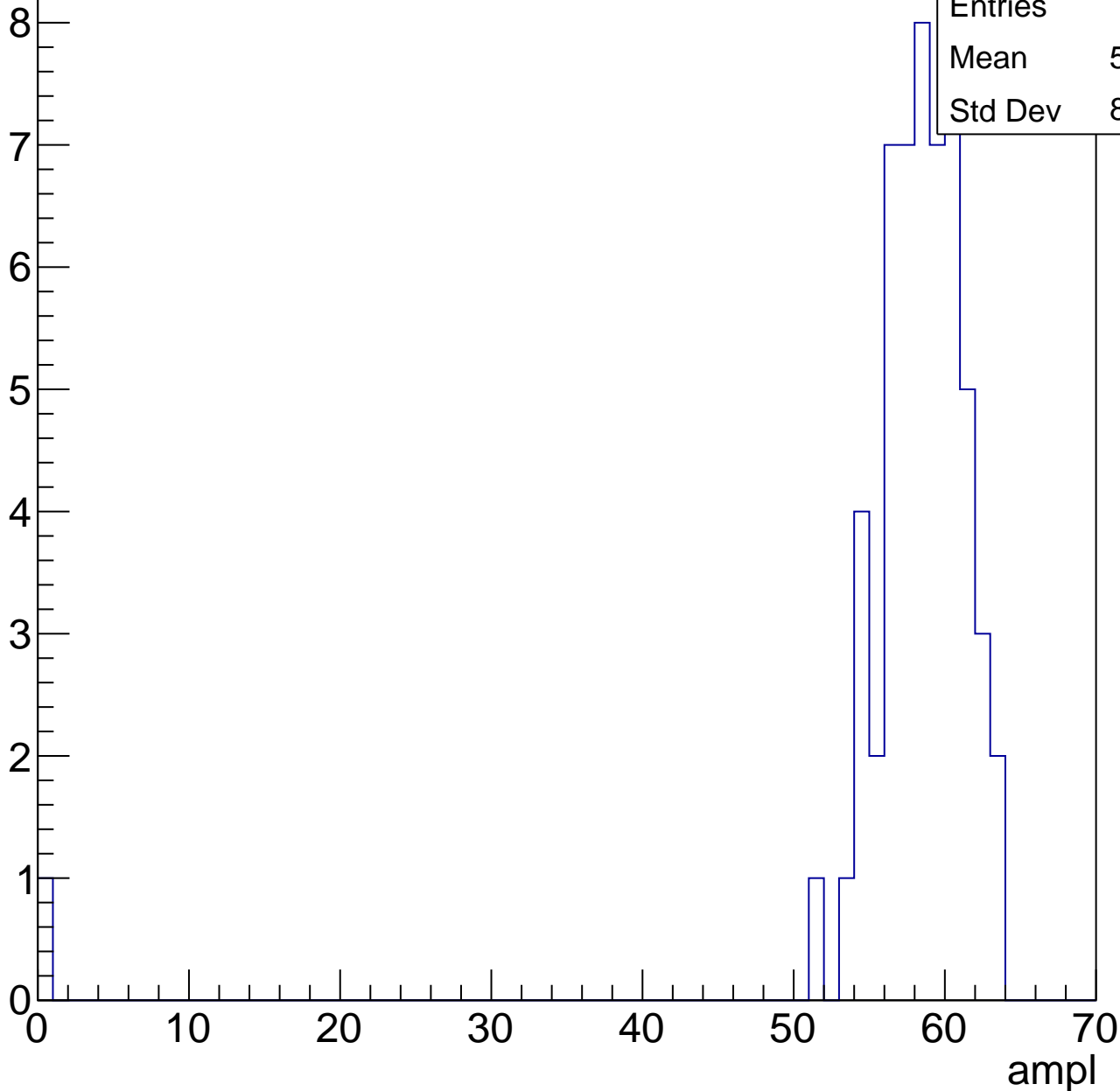


B1L103S, U1-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	57.05
Std Dev	8.114

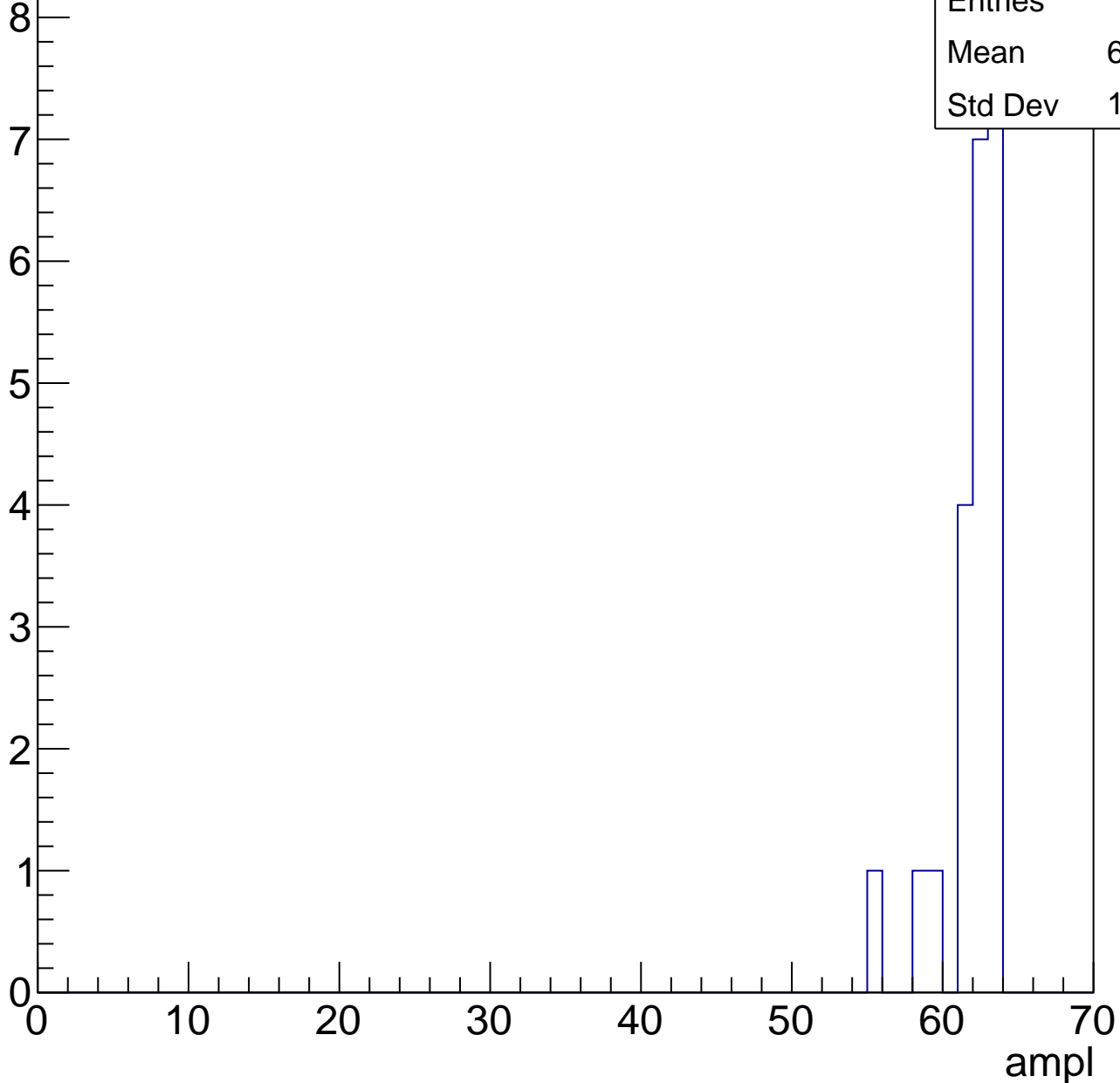


B1L103S, U1-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	61.55
Std Dev	1.924



B1L103S, U1-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch29, adc0

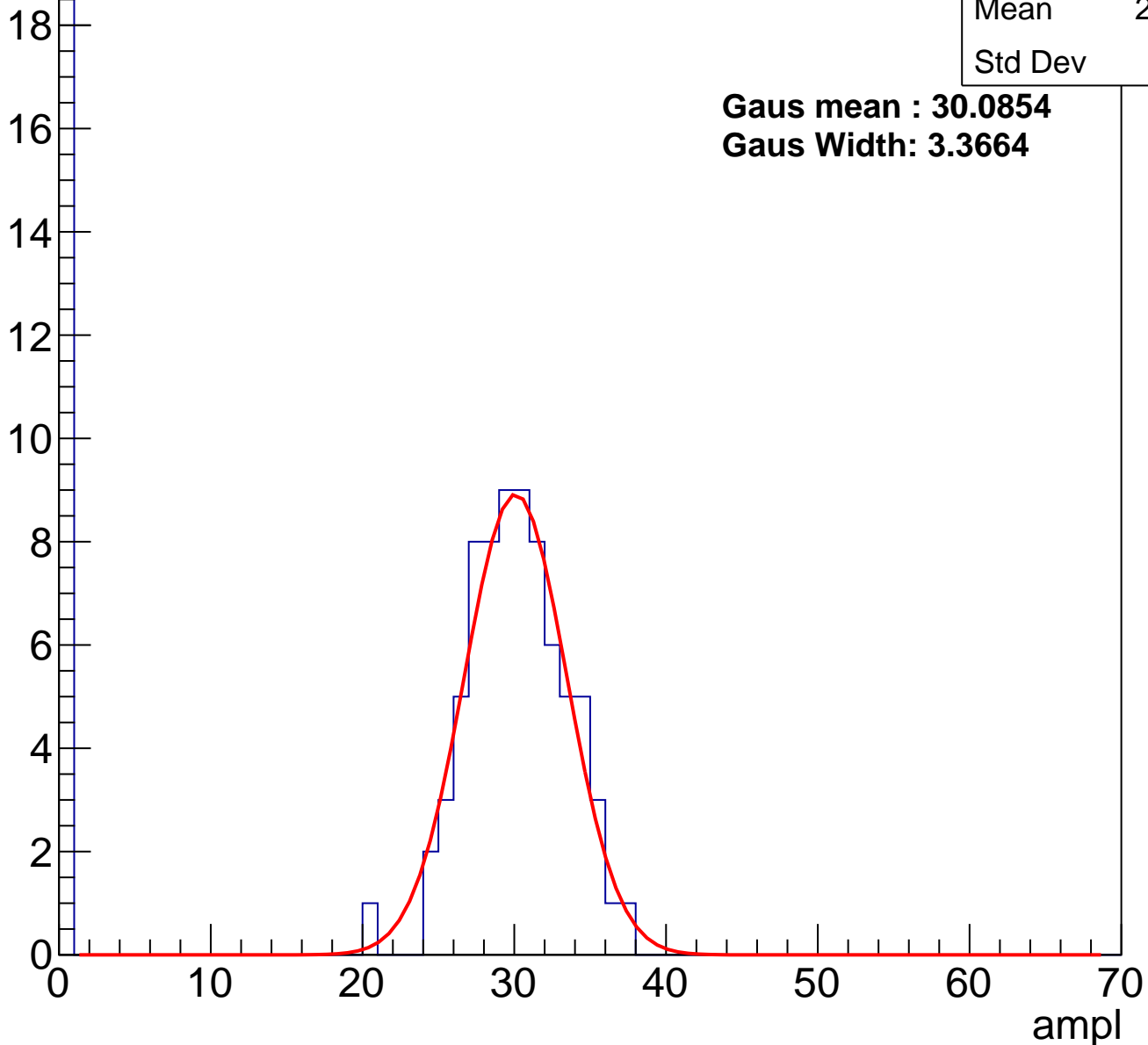
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	23.62
Std Dev	12.3

Gaus mean : 30.0854

Gaus Width: 3.3664

Entry



B1L103S, U1-ch29, adc1

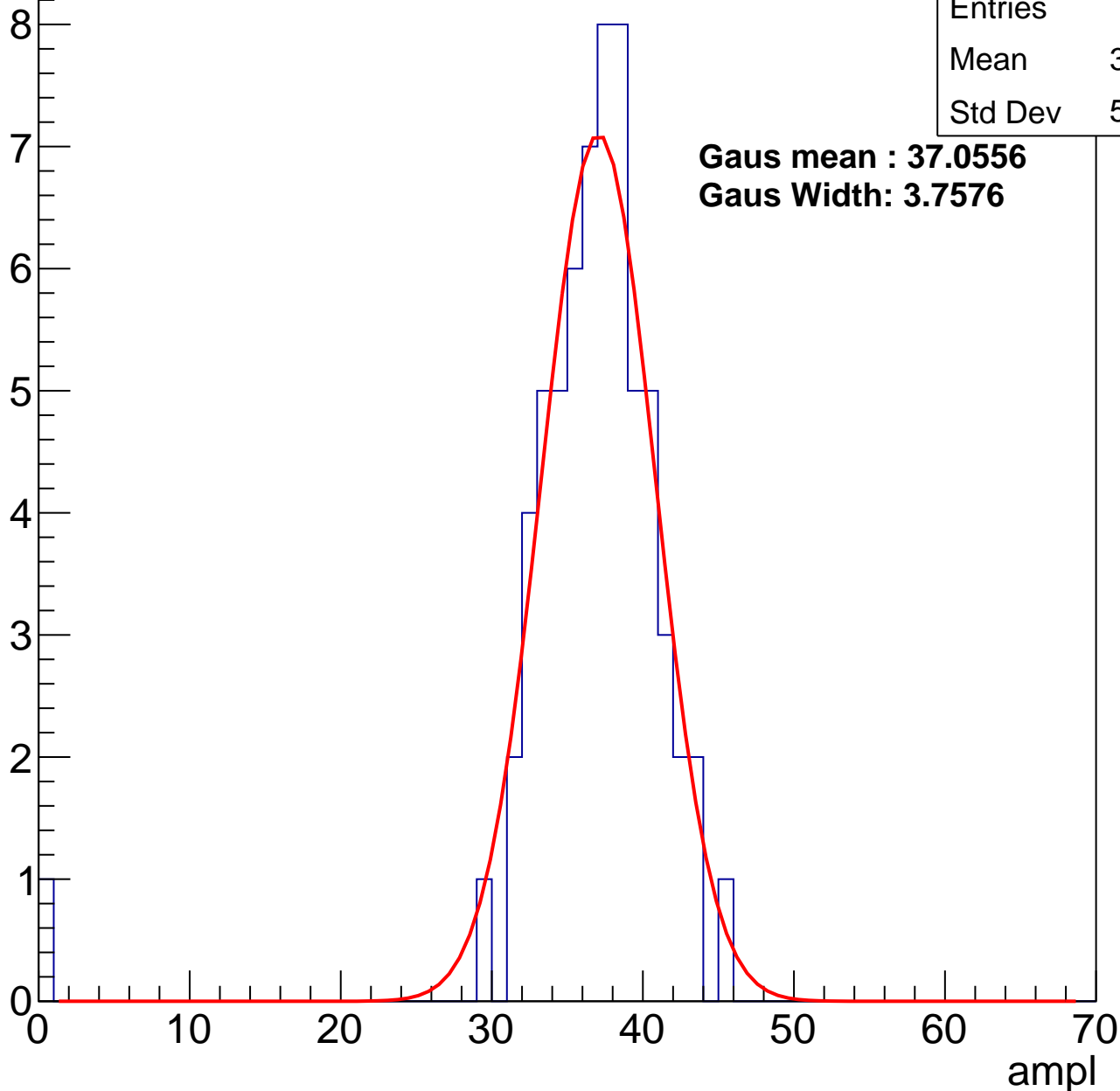
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.14
Std Dev	5.574

Gaus mean : 37.0556

Gaus Width: 3.7576



B1L103S, U1-ch29, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	36.84
Std Dev	15.38

Gaus mean : 43.7467

Gaus Width: 3.8590

10

8

6

4

2

0

0

10

20

30

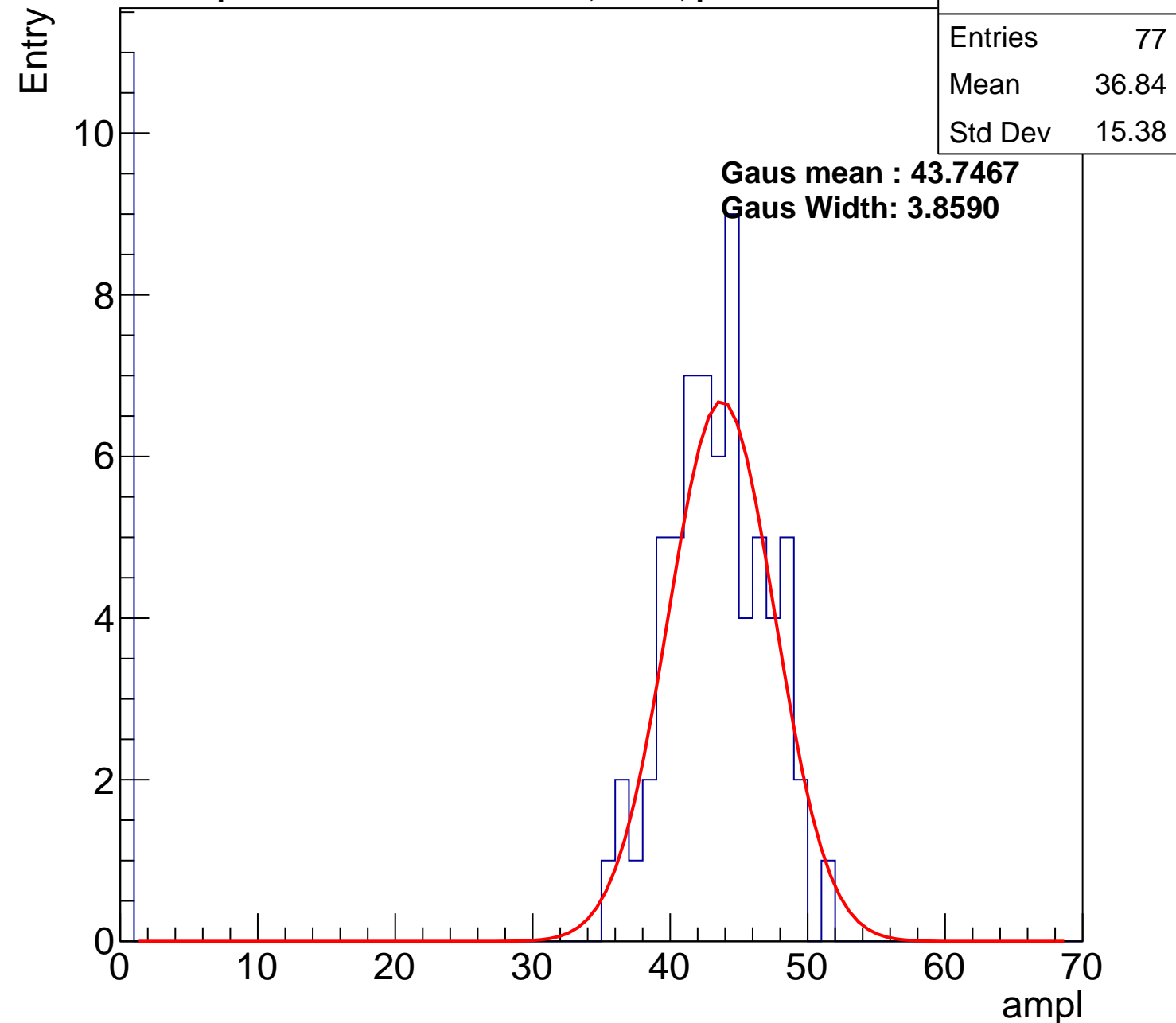
40

50

60

70

ampl

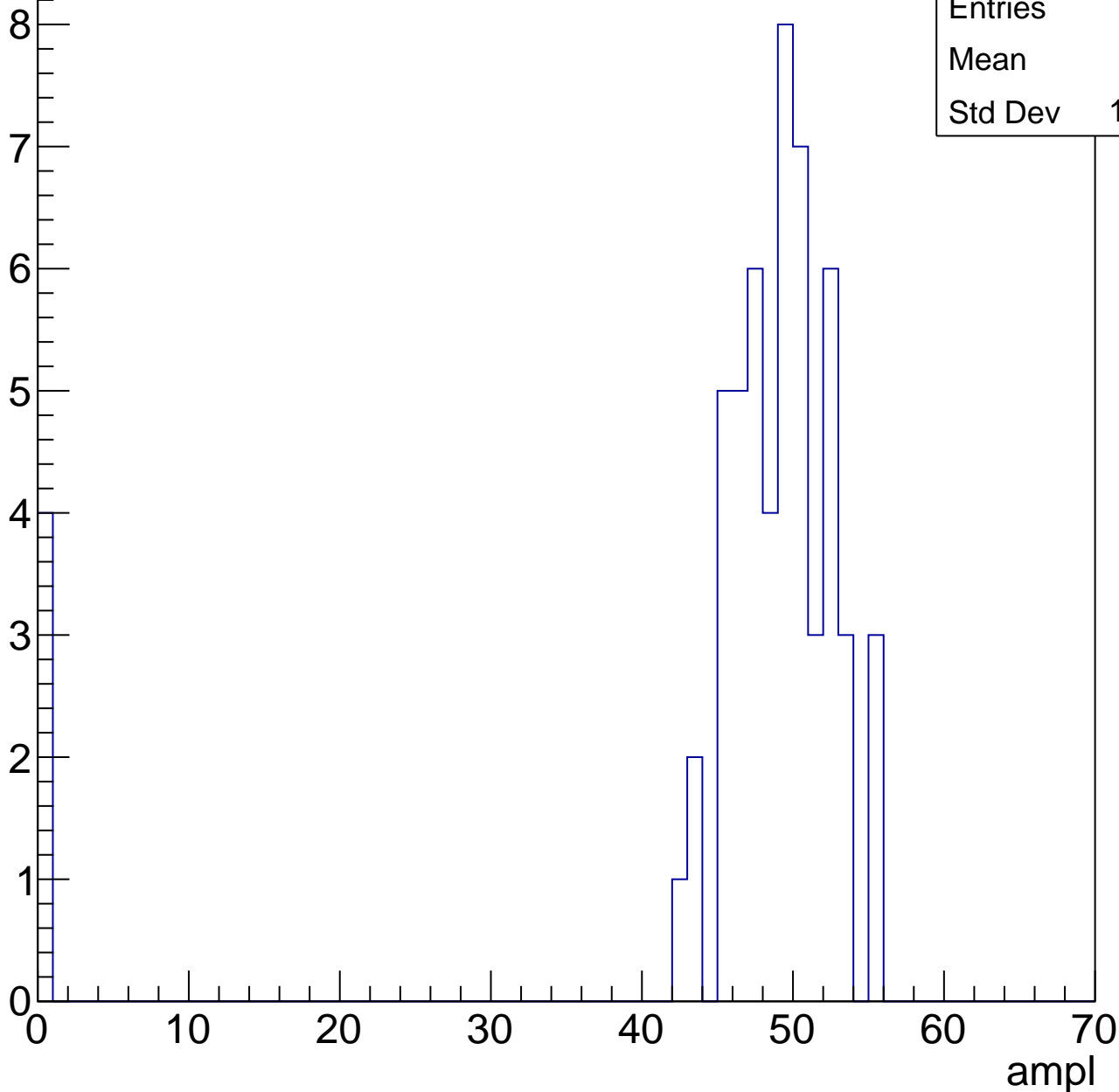


B1L103S, U1-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	45.4
Std Dev	12.82

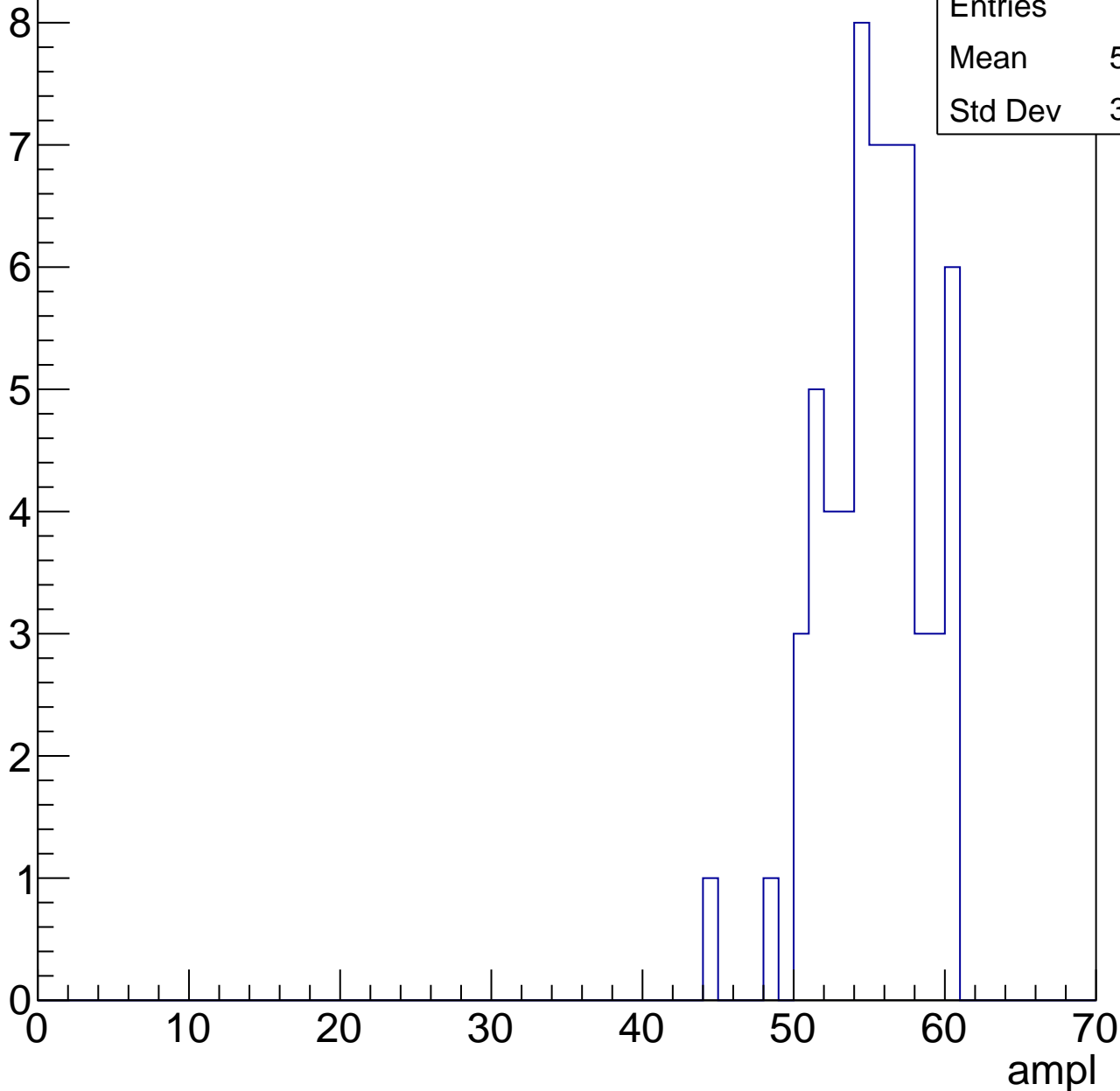


B1L103S, U1-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.85
Std Dev	3.303

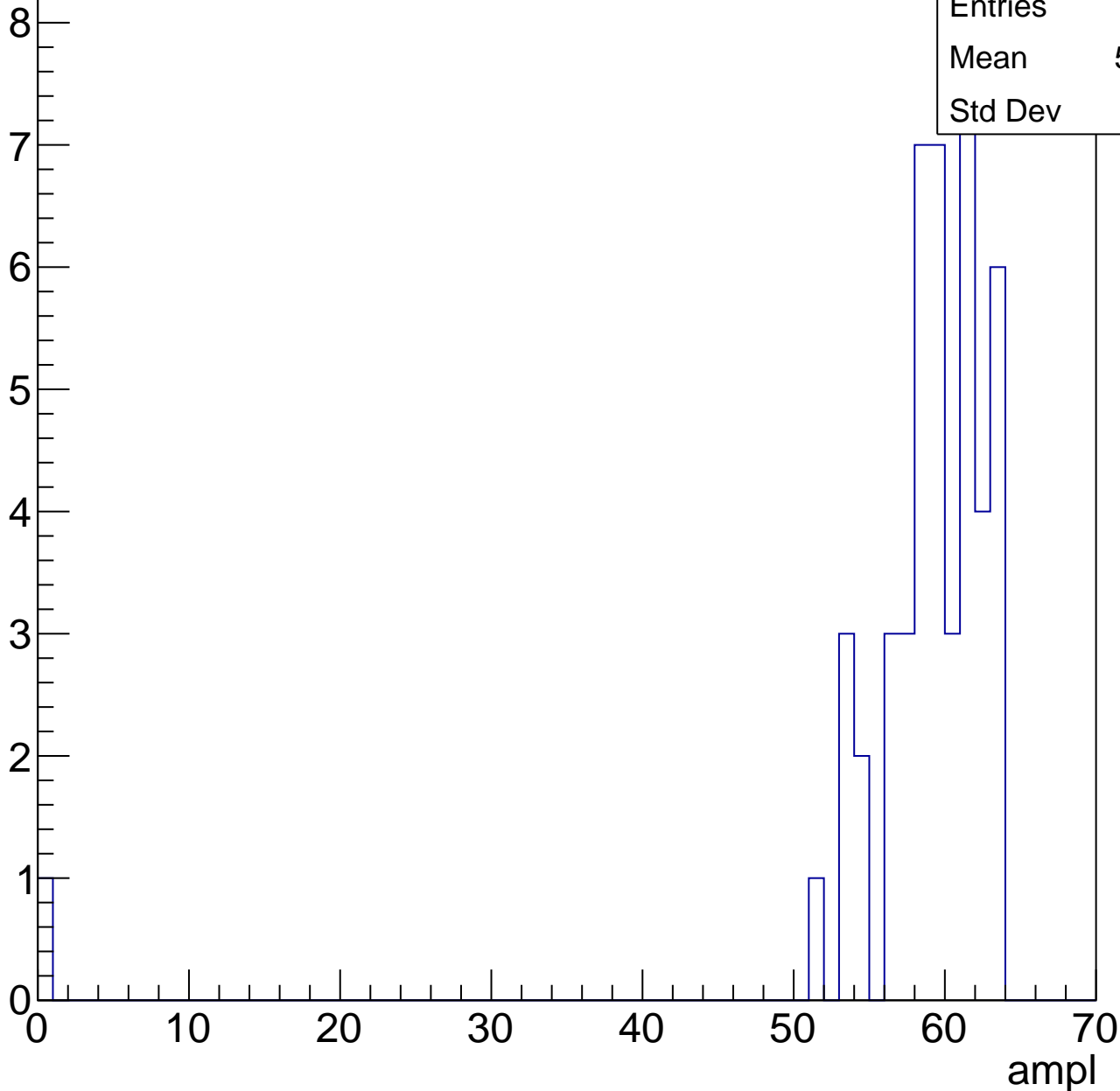


B1L103S, U1-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	57.71
Std Dev	8.94

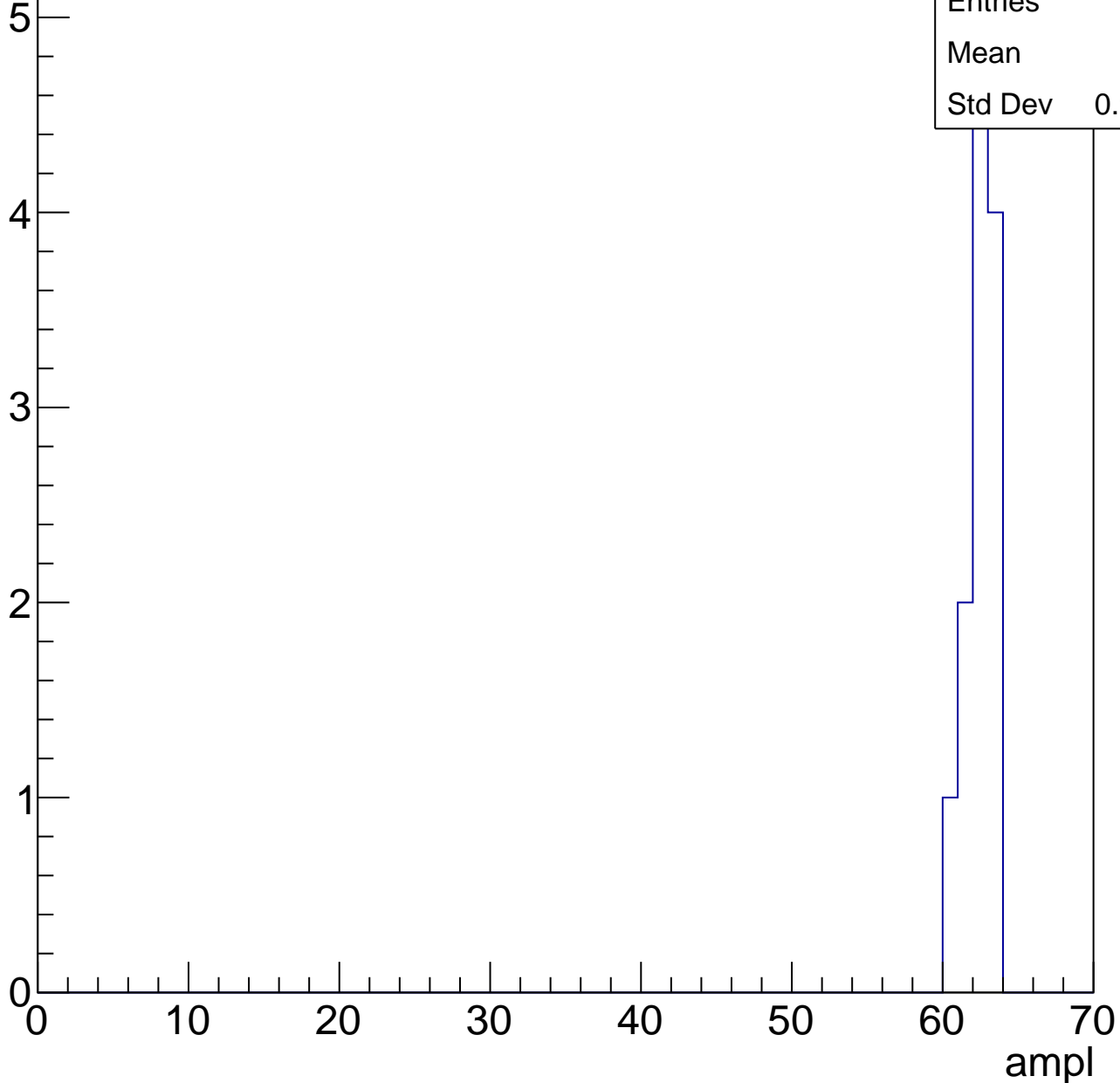


B1L103S, U1-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62
Std Dev	0.9129



B1L103S, U1-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U1-ch30, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	20.76
Std Dev	11.35

Gaus mean : 26.9397

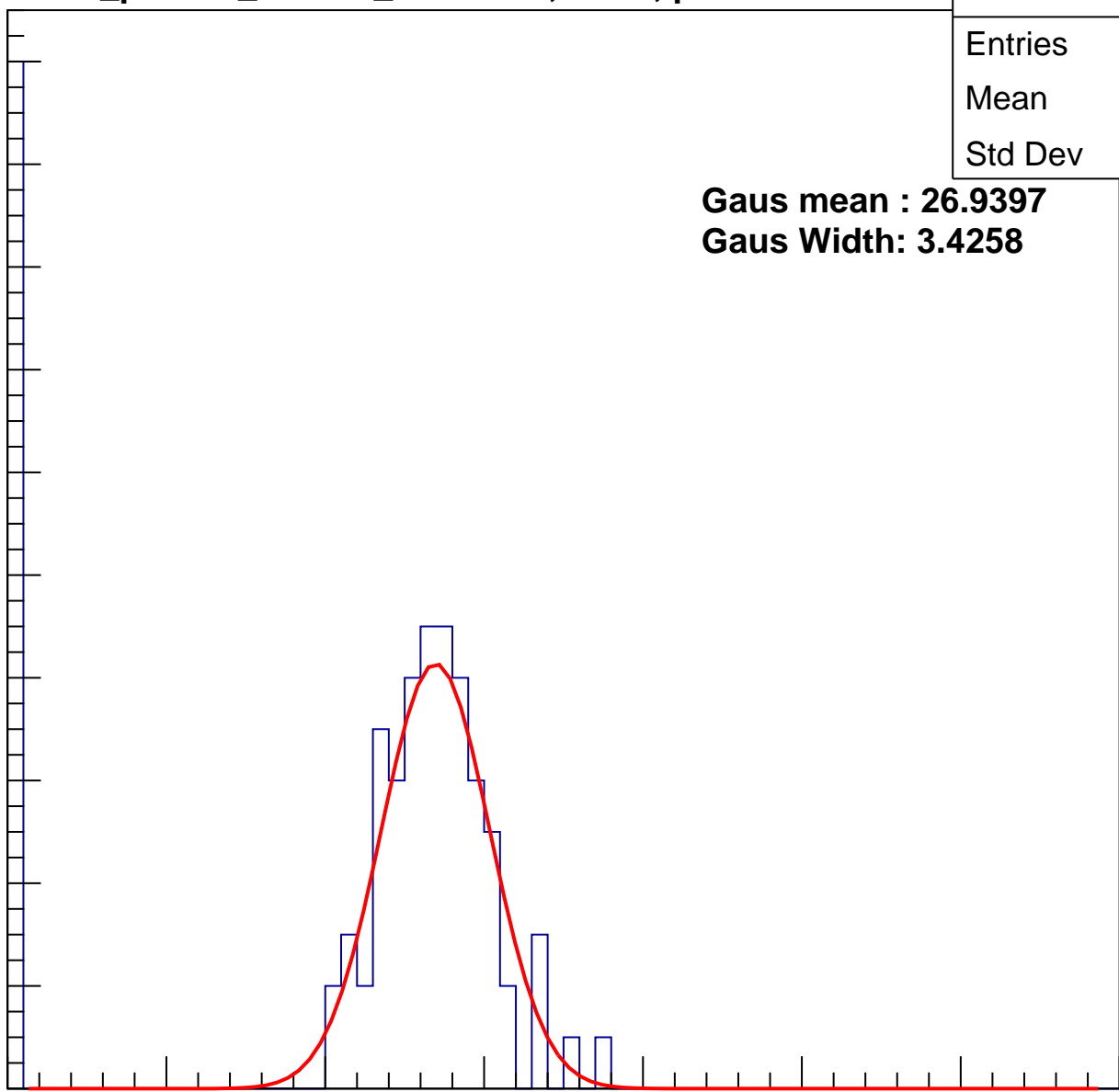
Gaus Width: 3.4258

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



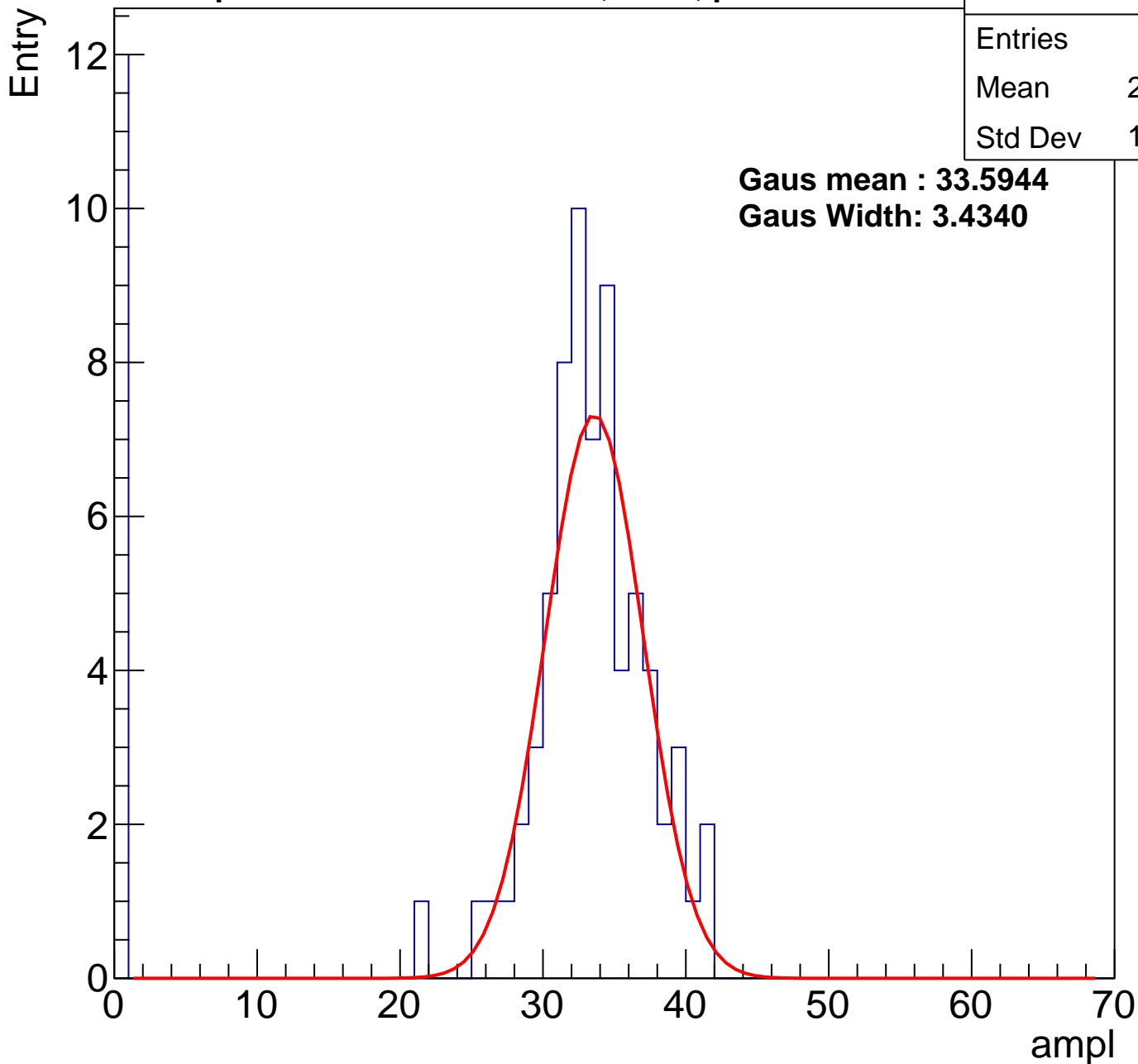
B1L103S, U1-ch30, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	28.15
Std Dev	12.23

Gaus mean : 33.5944

Gaus Width: 3.4340



B1L103S, U1-ch30, adc2

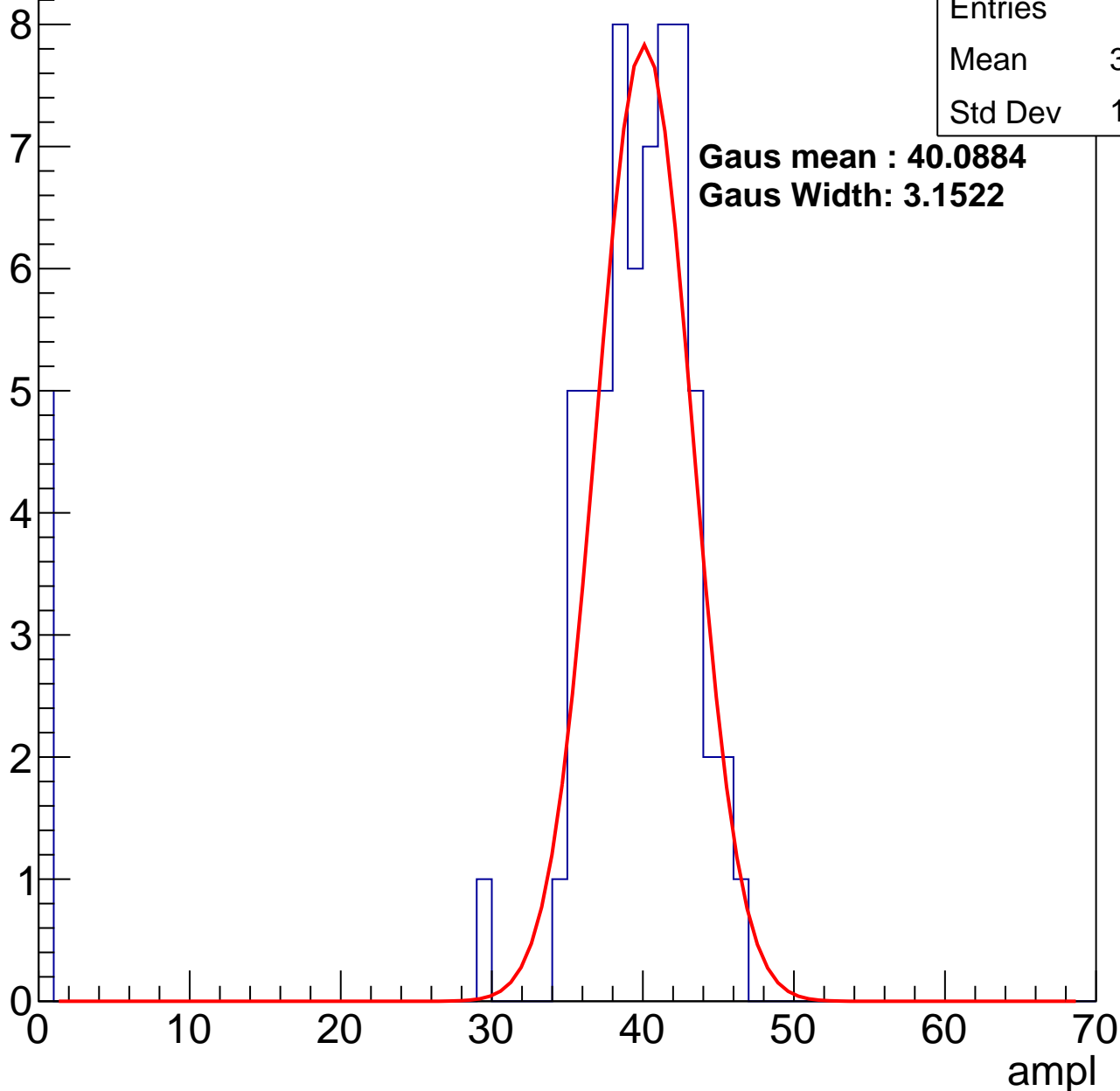
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.58
Std Dev	10.66

Gaus mean : 40.0884

Gaus Width: 3.1522

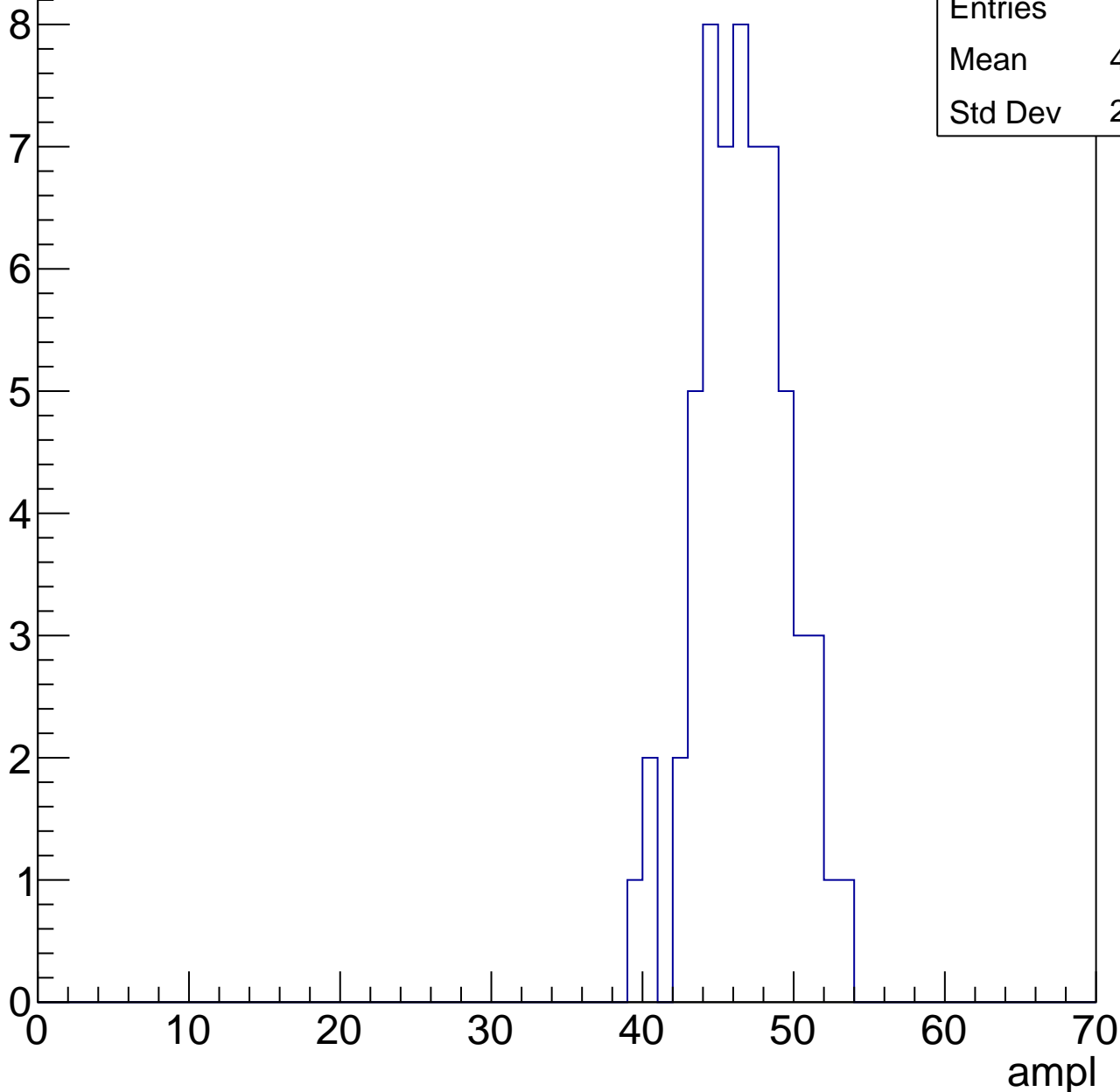


B1L103S, U1-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	46.18
Std Dev	2.952

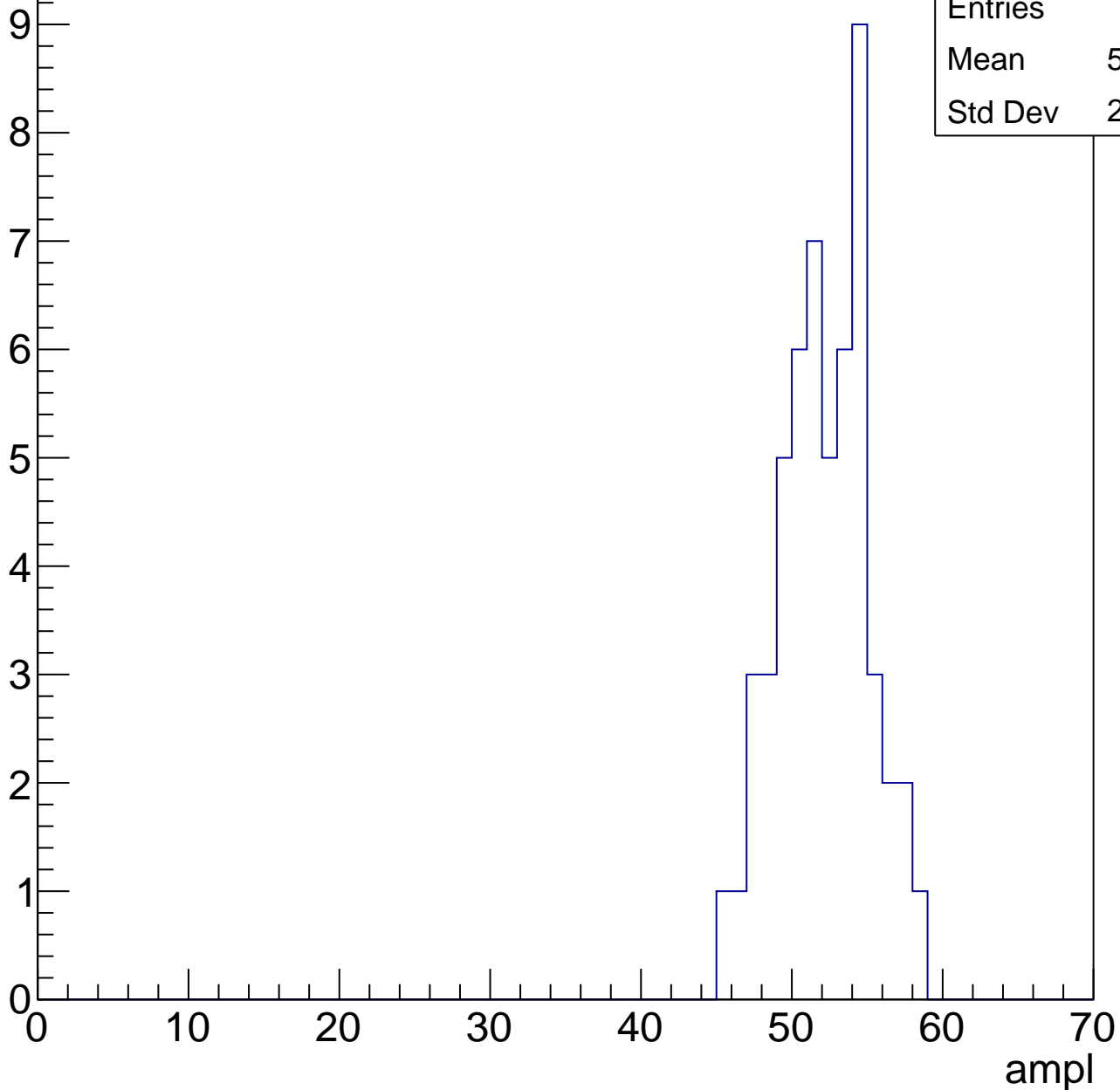


B1L103S, U1-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	51.69
Std Dev	2.937

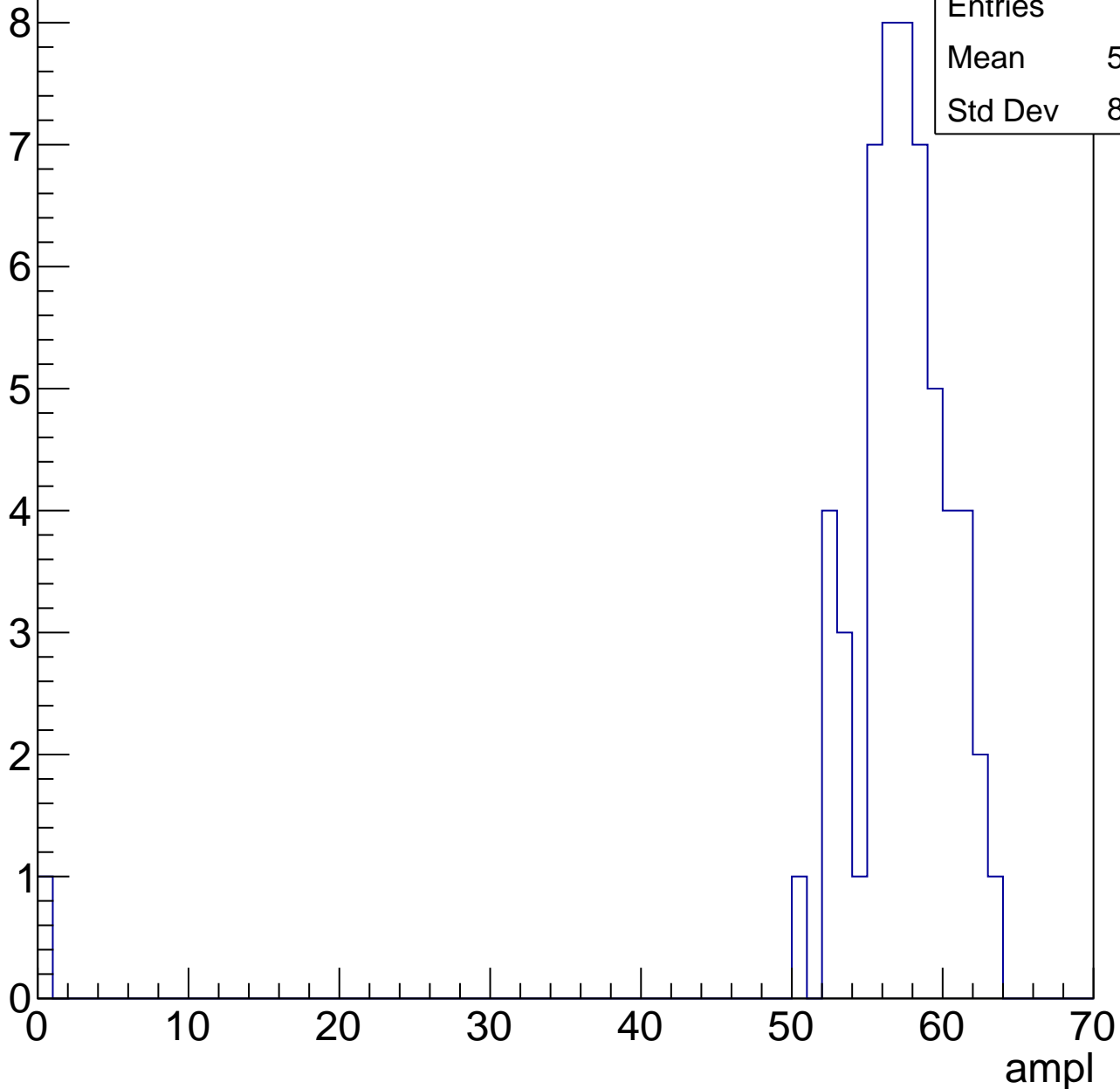


B1L103S, U1-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.93
Std Dev	8.062

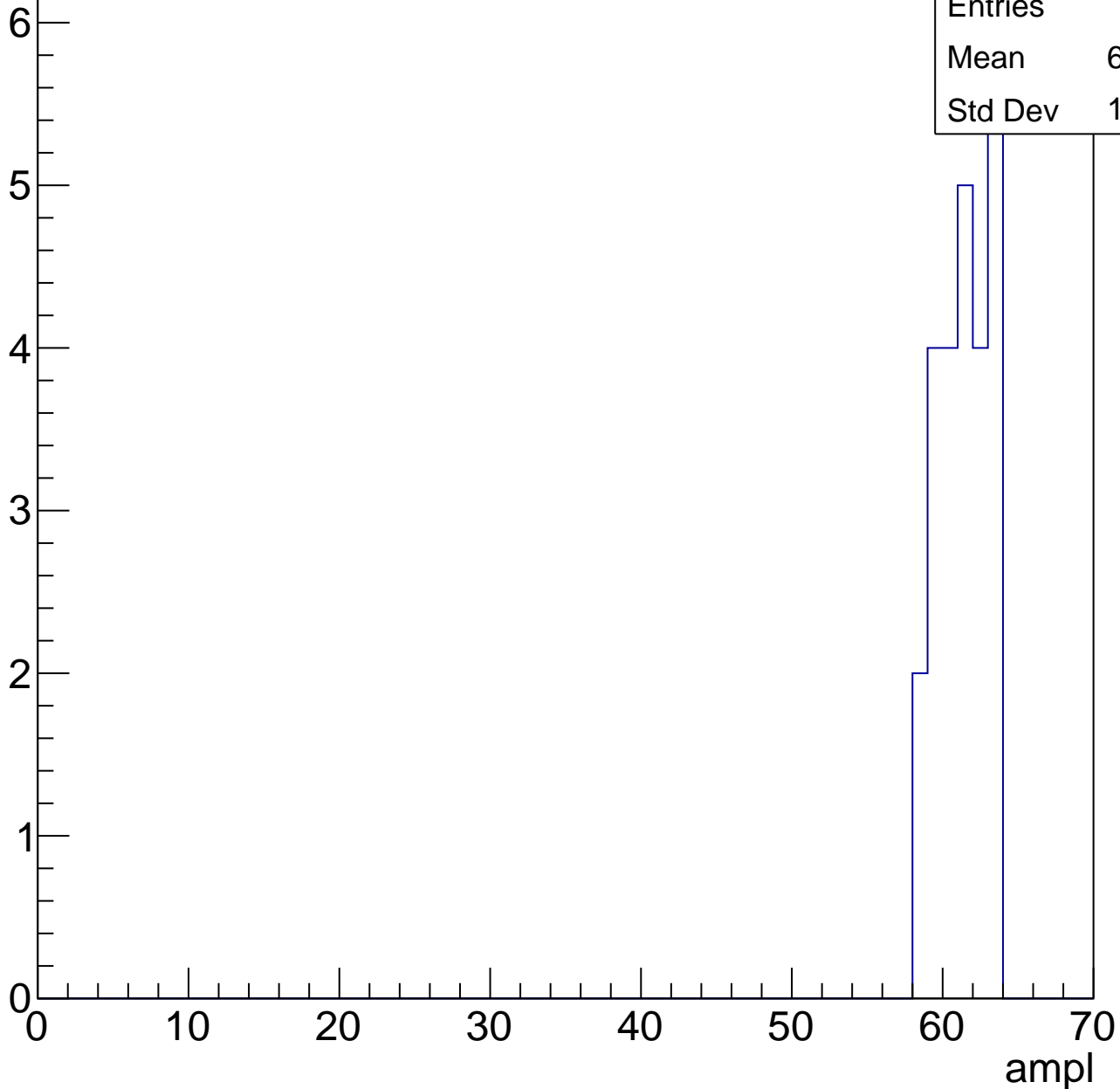


B1L103S, U1-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	60.92
Std Dev	1.623

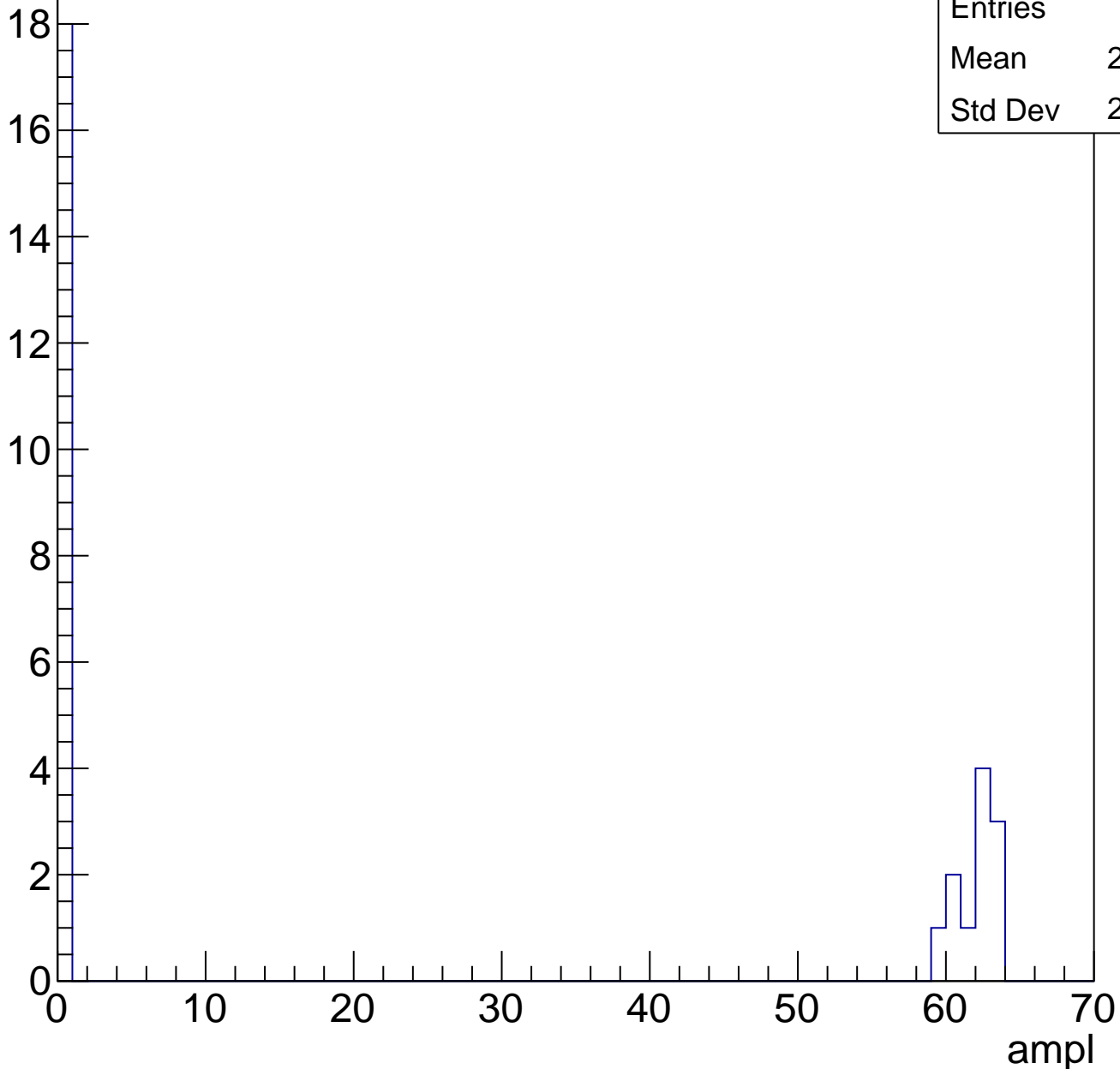


B1L103S, U1-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	29
Mean	23.34
Std Dev	29.87

Entry



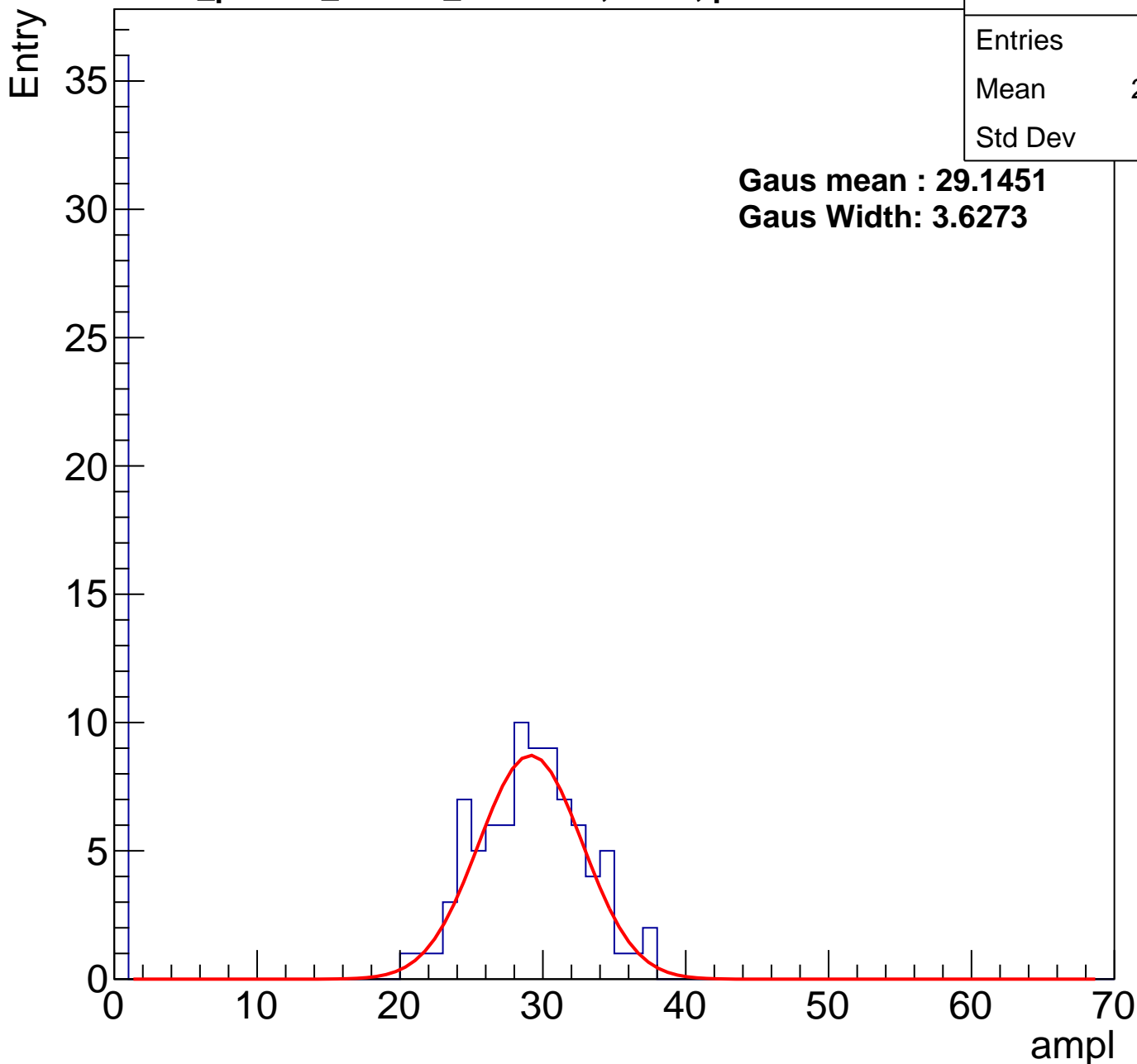
B1L103S, U1-ch31, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	120
Mean	20.08
Std Dev	13.5

Gaus mean : 29.1451

Gaus Width: 3.6273



B1L103S, U1-ch31, adc1

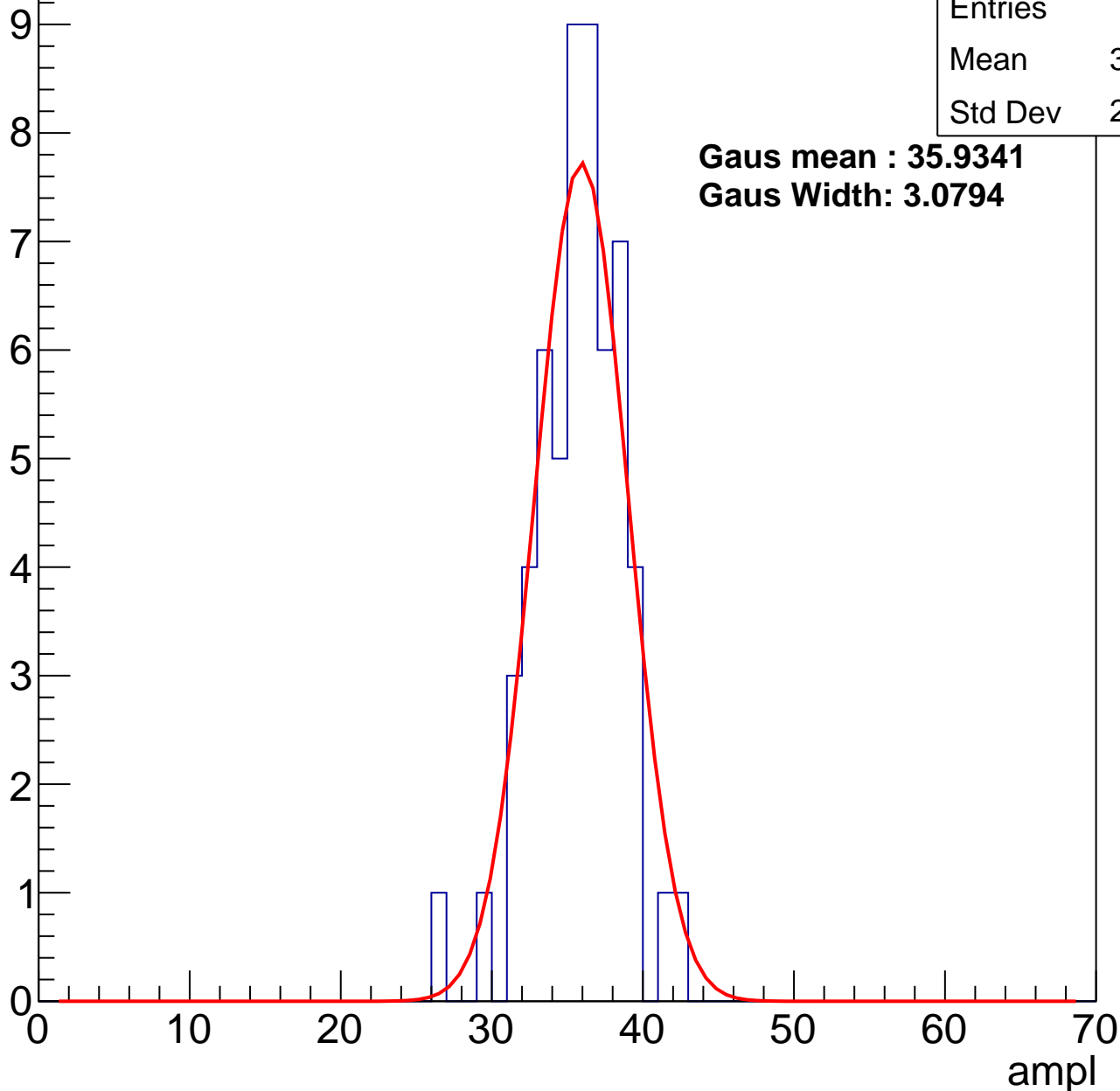
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	35.26
Std Dev	2.875

Gaus mean : 35.9341

Gaus Width: 3.0794



B1L103S, U1-ch31, adc2

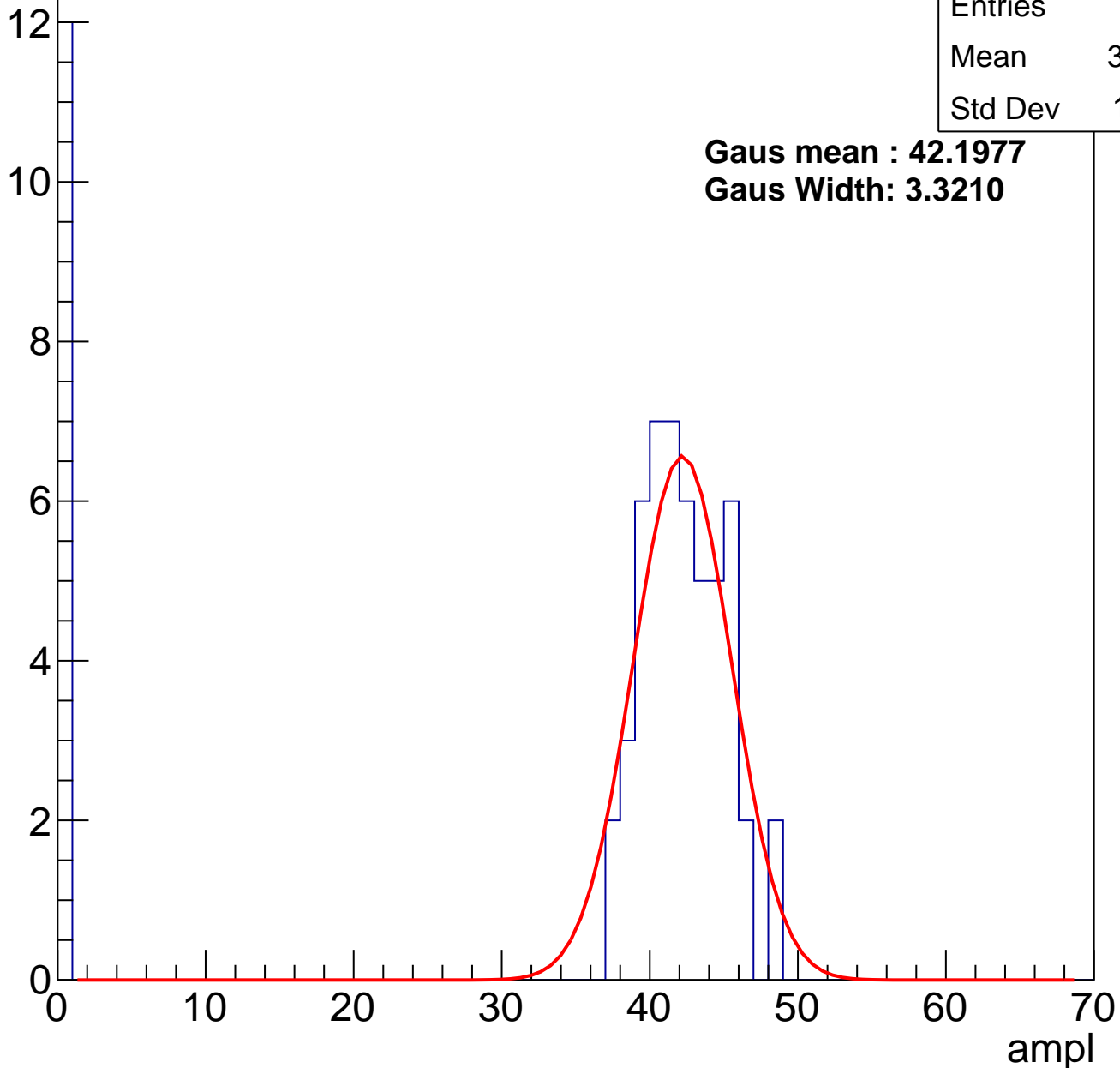
calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	33.87
Std Dev	16.61

Gaus mean : 42.1977

Gaus Width: 3.3210

Entry

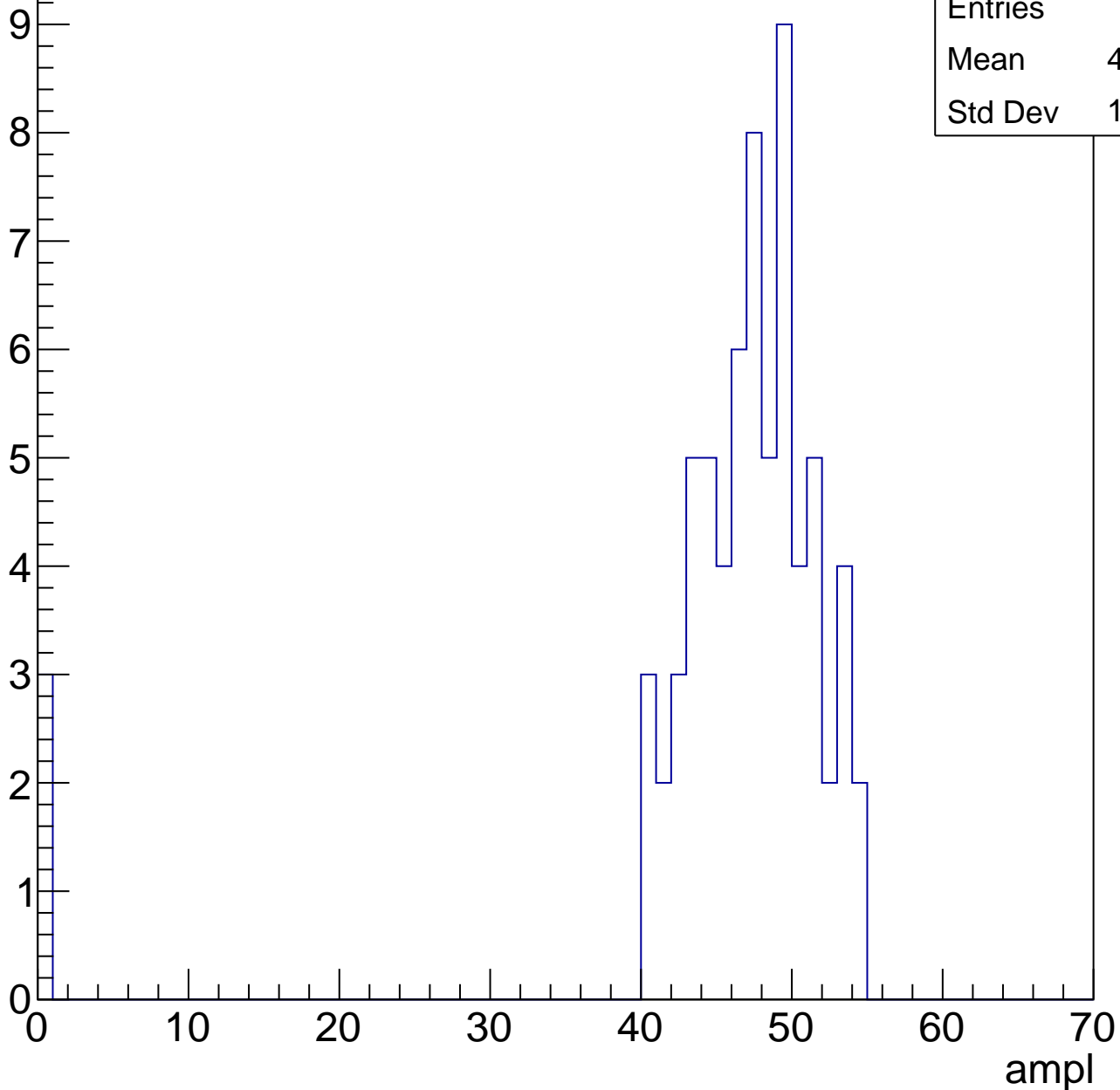


B1L103S, U1-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.07
Std Dev	10.18

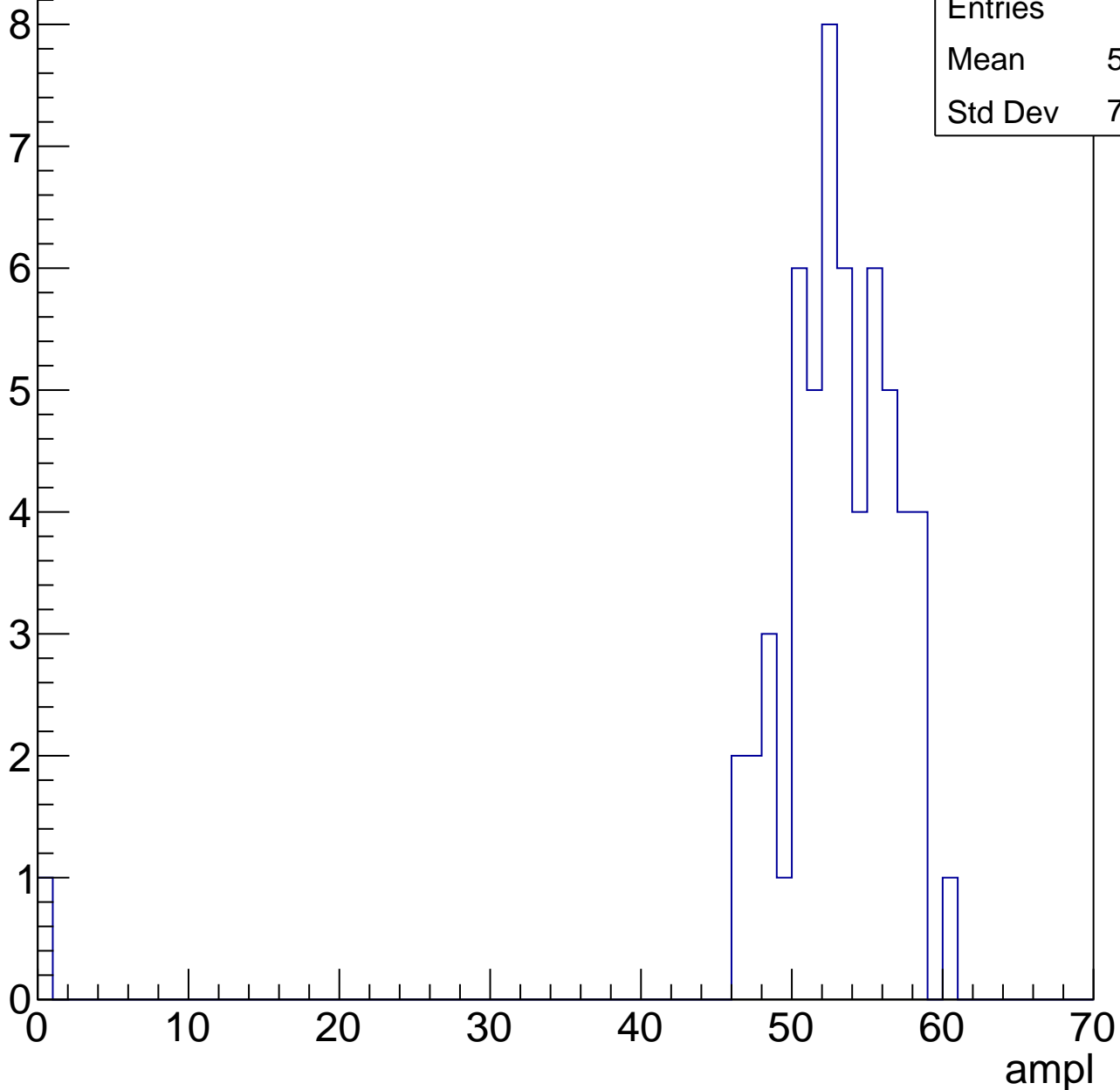


B1L103S, U1-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	51.97
Std Dev	7.629

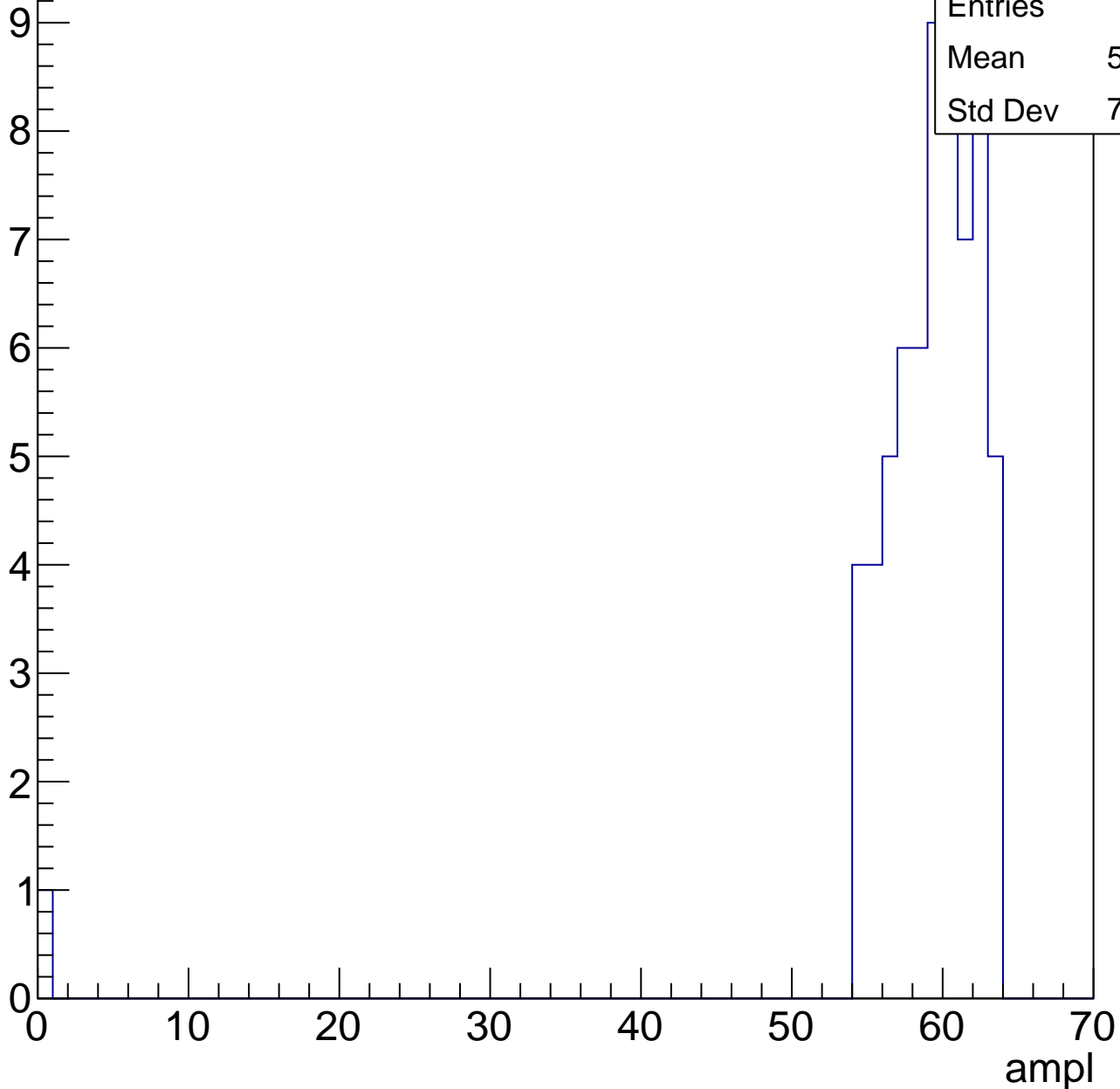


B1L103S, U1-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	58.02
Std Dev	7.814

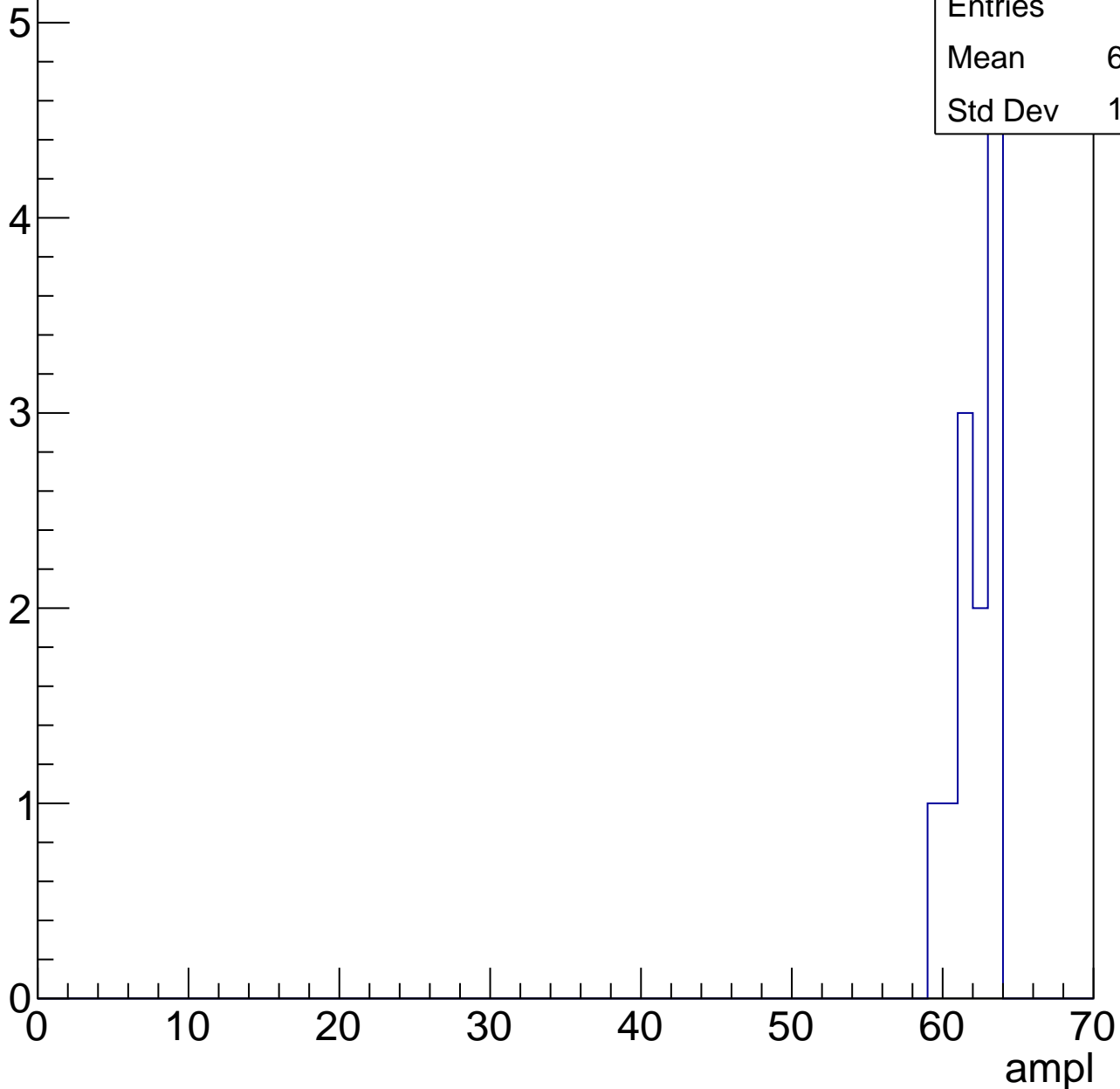


B1L103S, U1-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.299



B1L103S, U1-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

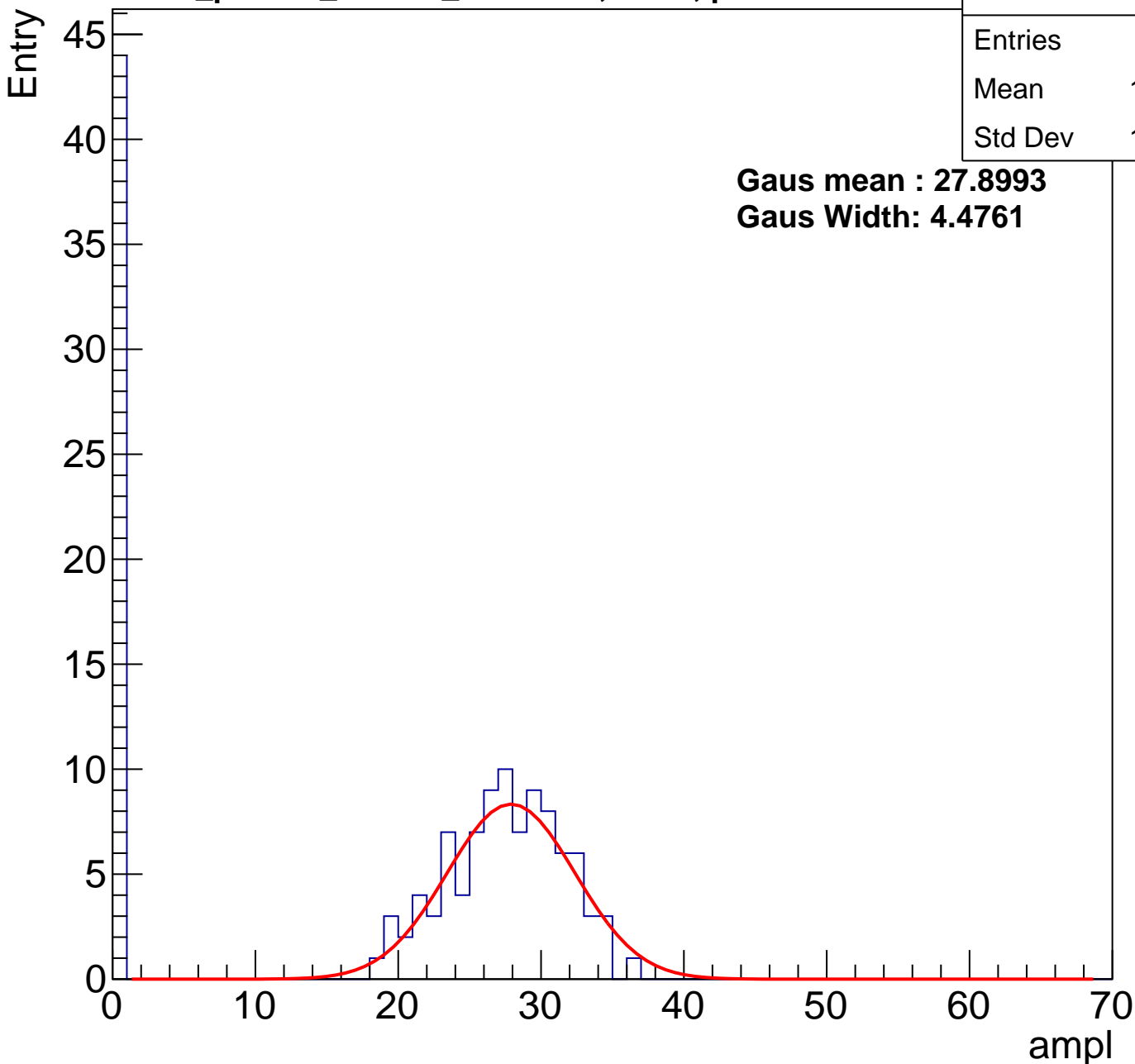
B1L103S, U1-ch32, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	137
Mean	18.34
Std Dev	13.04

Gaus mean : 27.8993

Gaus Width: 4.4761



B1L103S, U1-ch32, adc1

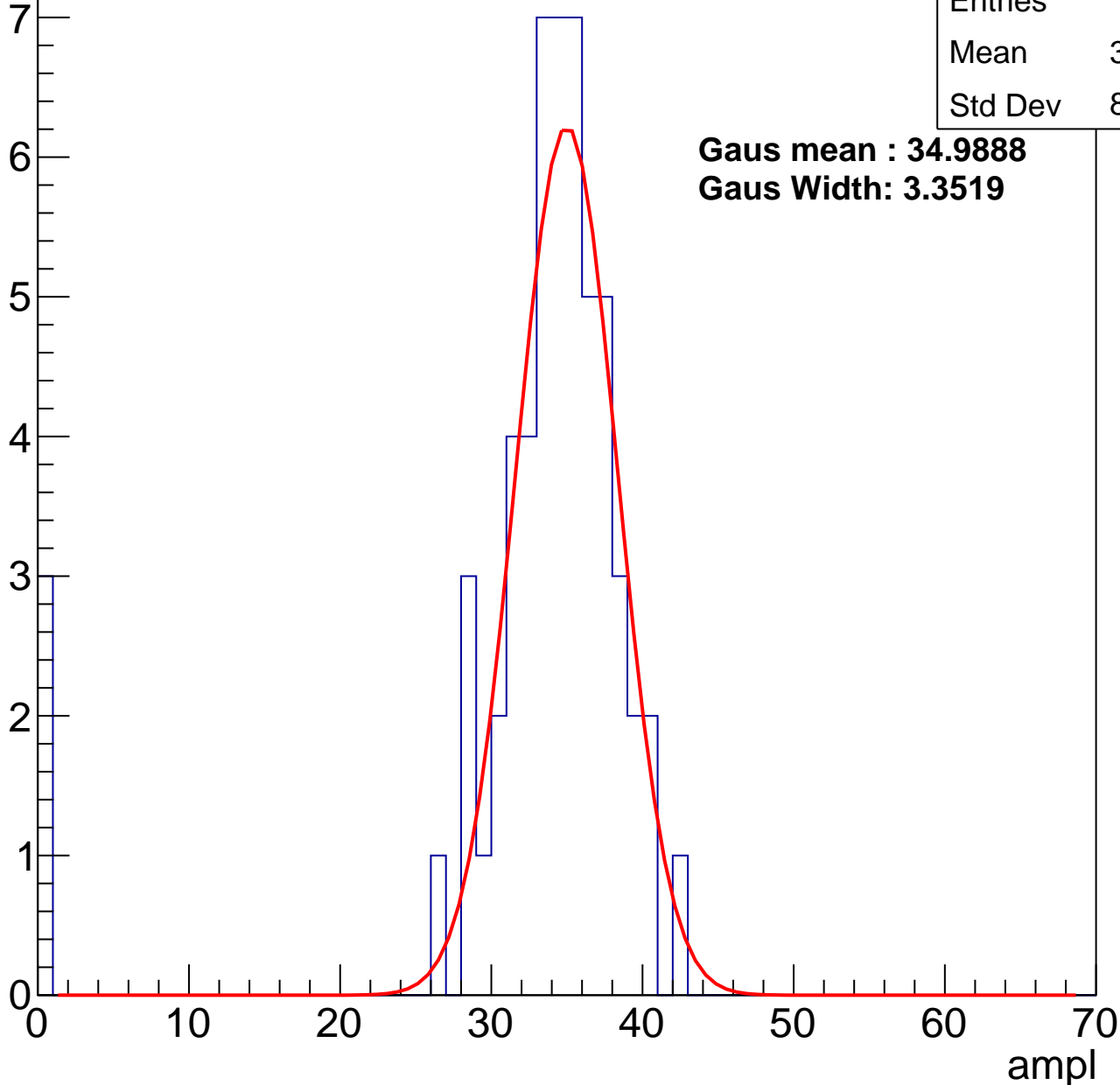
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	32.35
Std Dev	8.279

Gaus mean : 34.9888

Gaus Width: 3.3519



B1L103S, U1-ch32, adc2

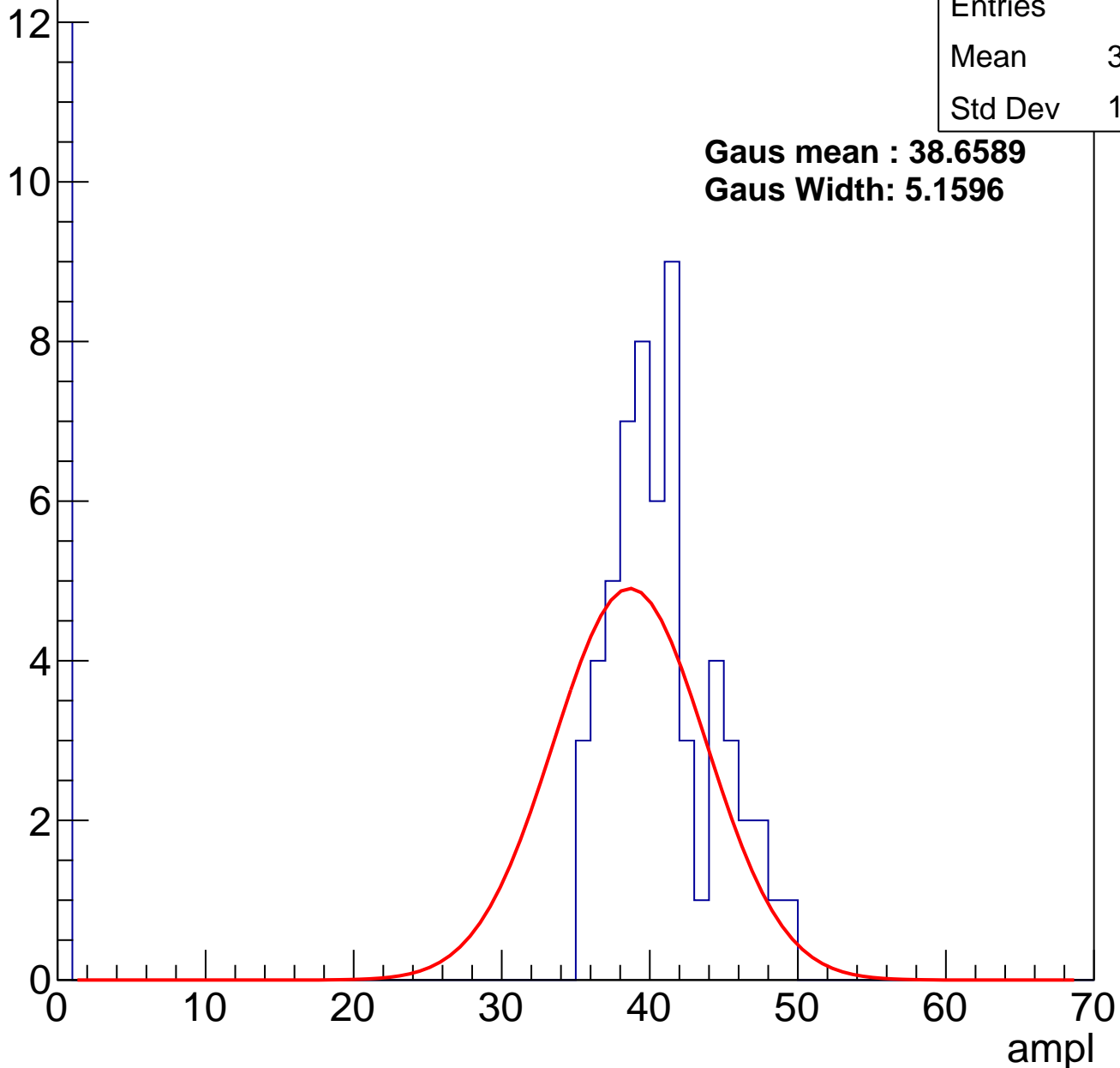
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	33.58
Std Dev	15.46

Gaus mean : 38.6589

Gaus Width: 5.1596

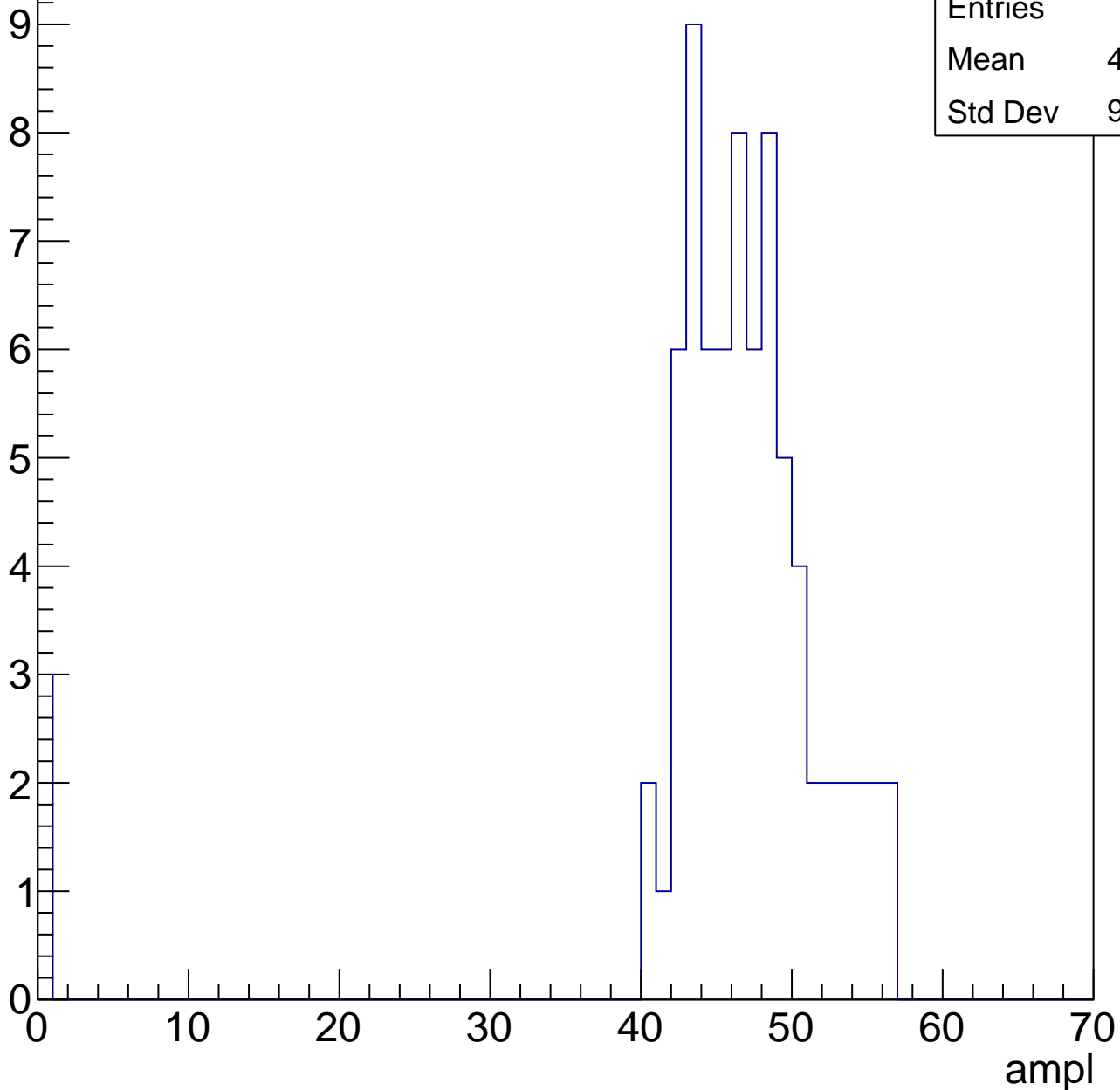
Entry



B1L103S, U1-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



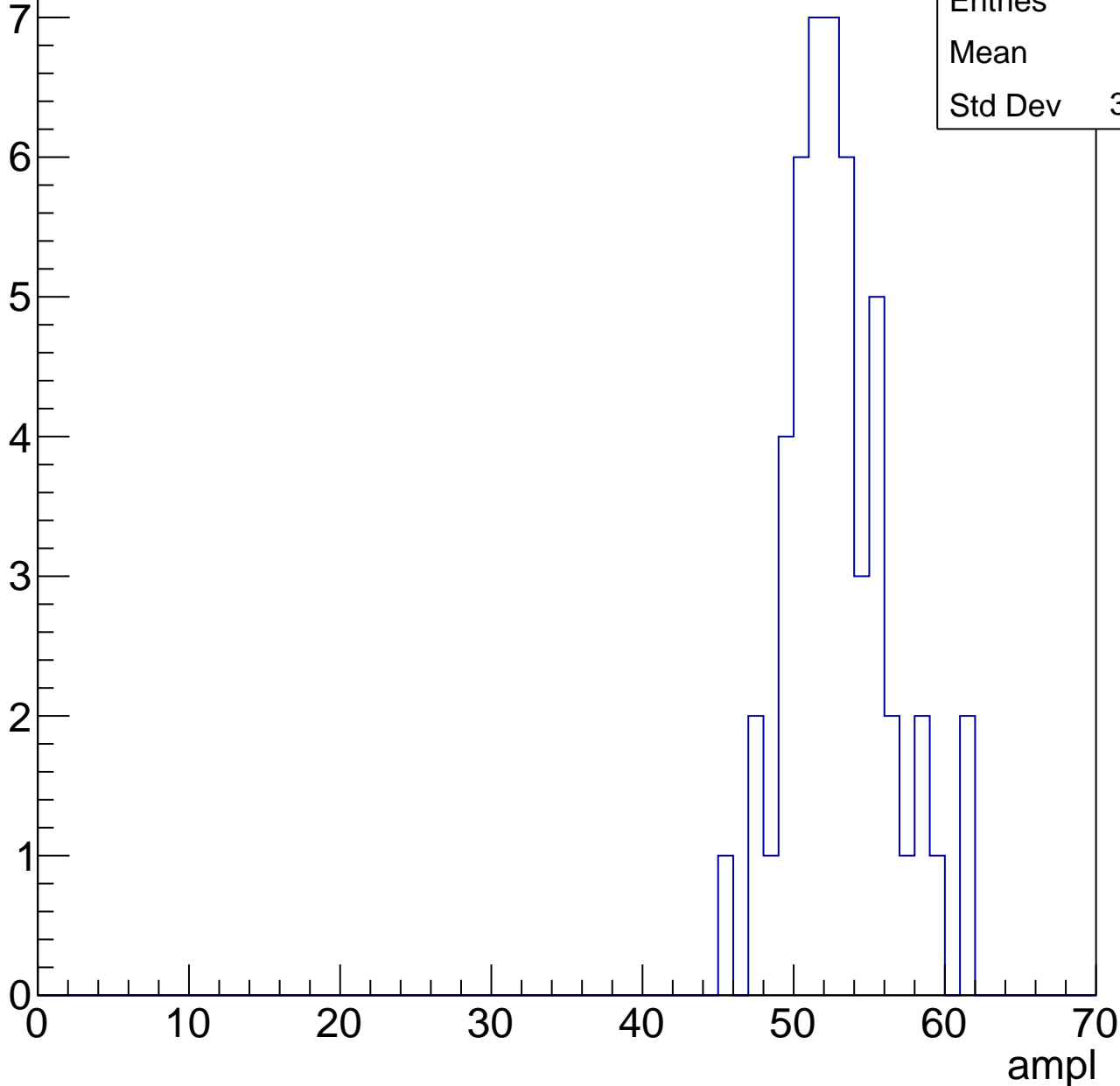
Entries	76
Mean	44.93
Std Dev	9.883

B1L103S, U1-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

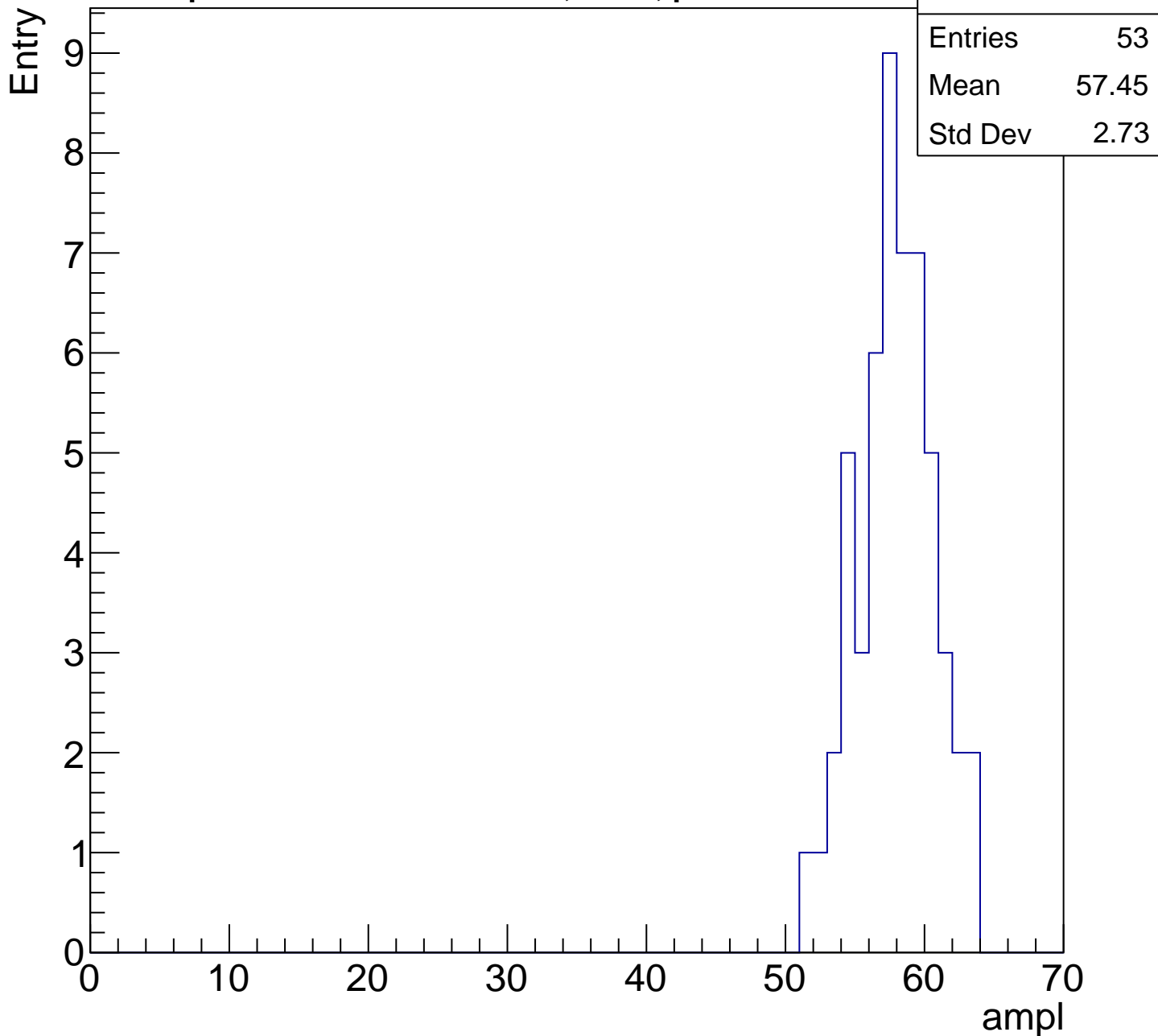
Entry

Entries	50
Mean	52.5
Std Dev	3.384



B1L103S, U1-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

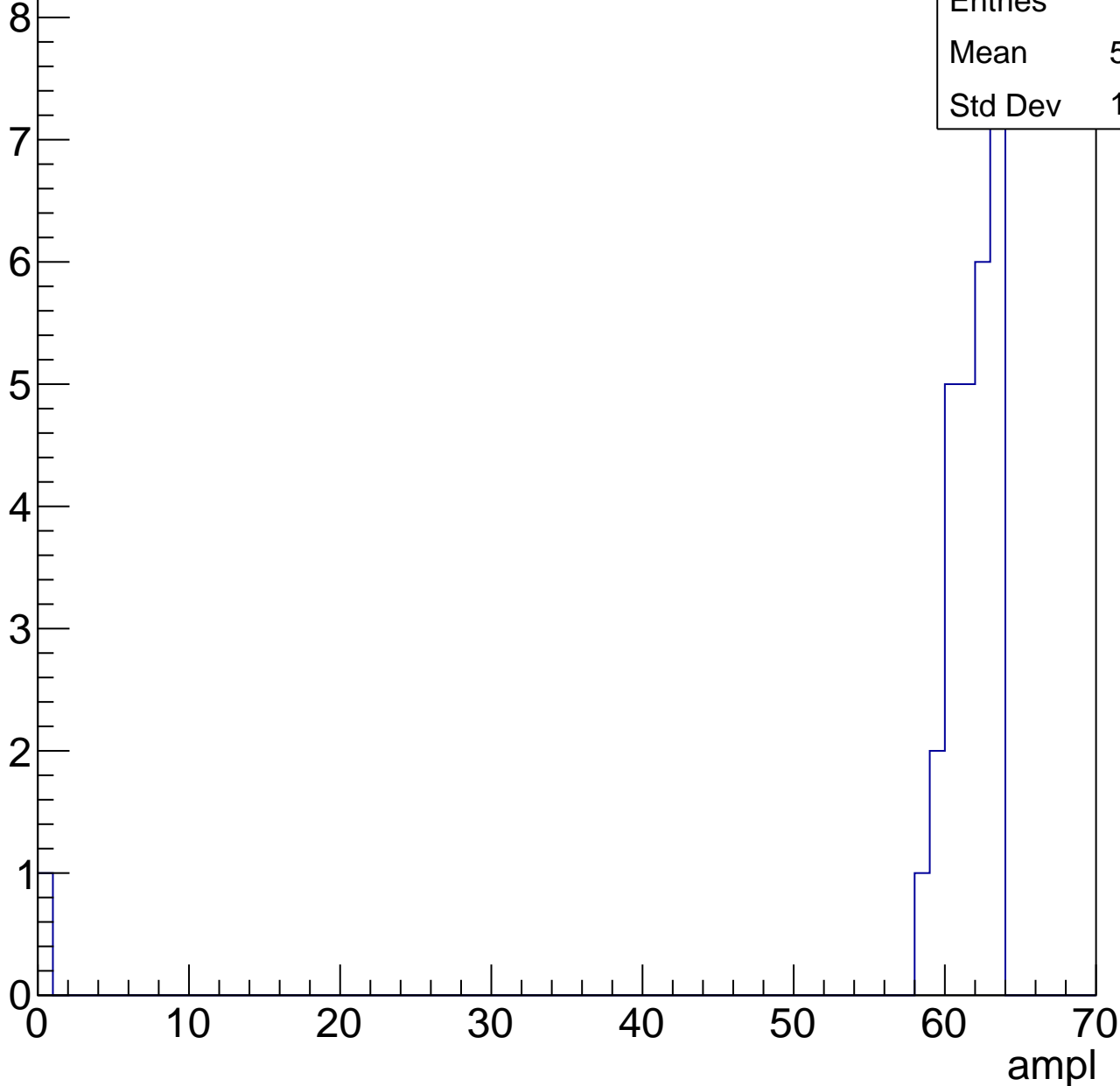


B1L103S, U1-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

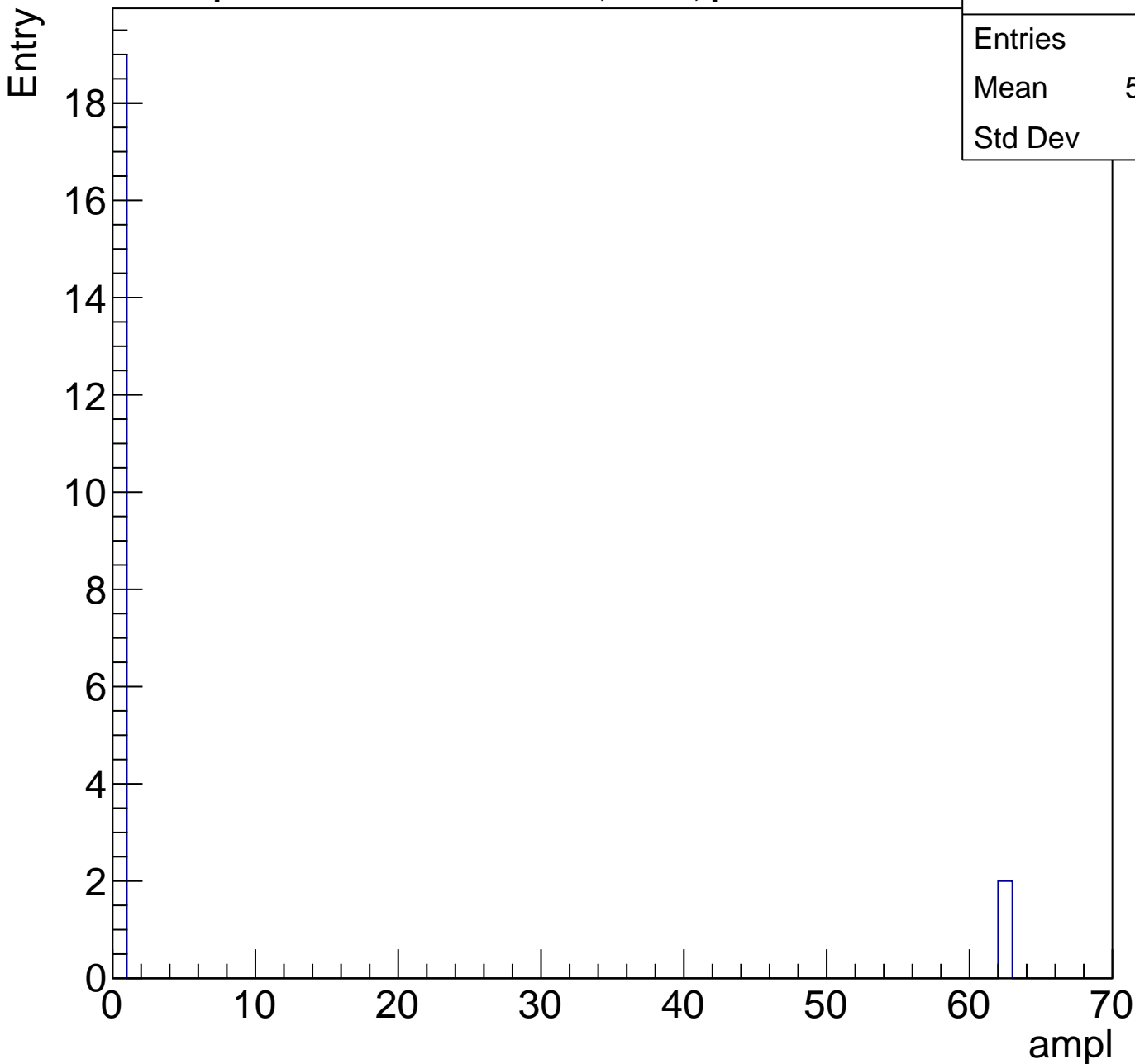
Entries	28
Mean	59.18
Std Dev	11.48



B1L103S, U1-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2



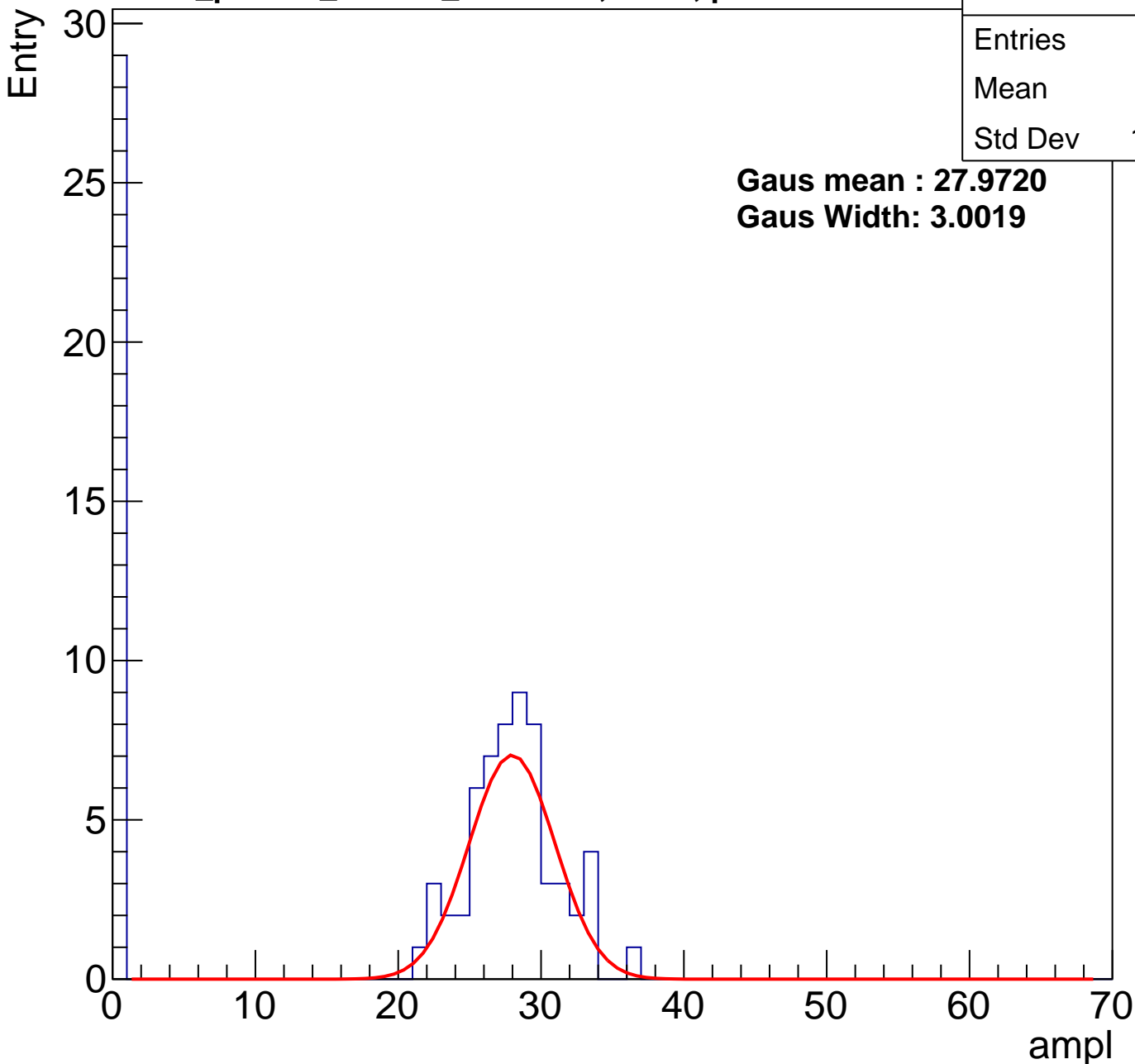
B1L103S, U1-ch33, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	18.5
Std Dev	13.21

Gaus mean : 27.9720

Gaus Width: 3.0019



B1L103S, U1-ch33, adc1

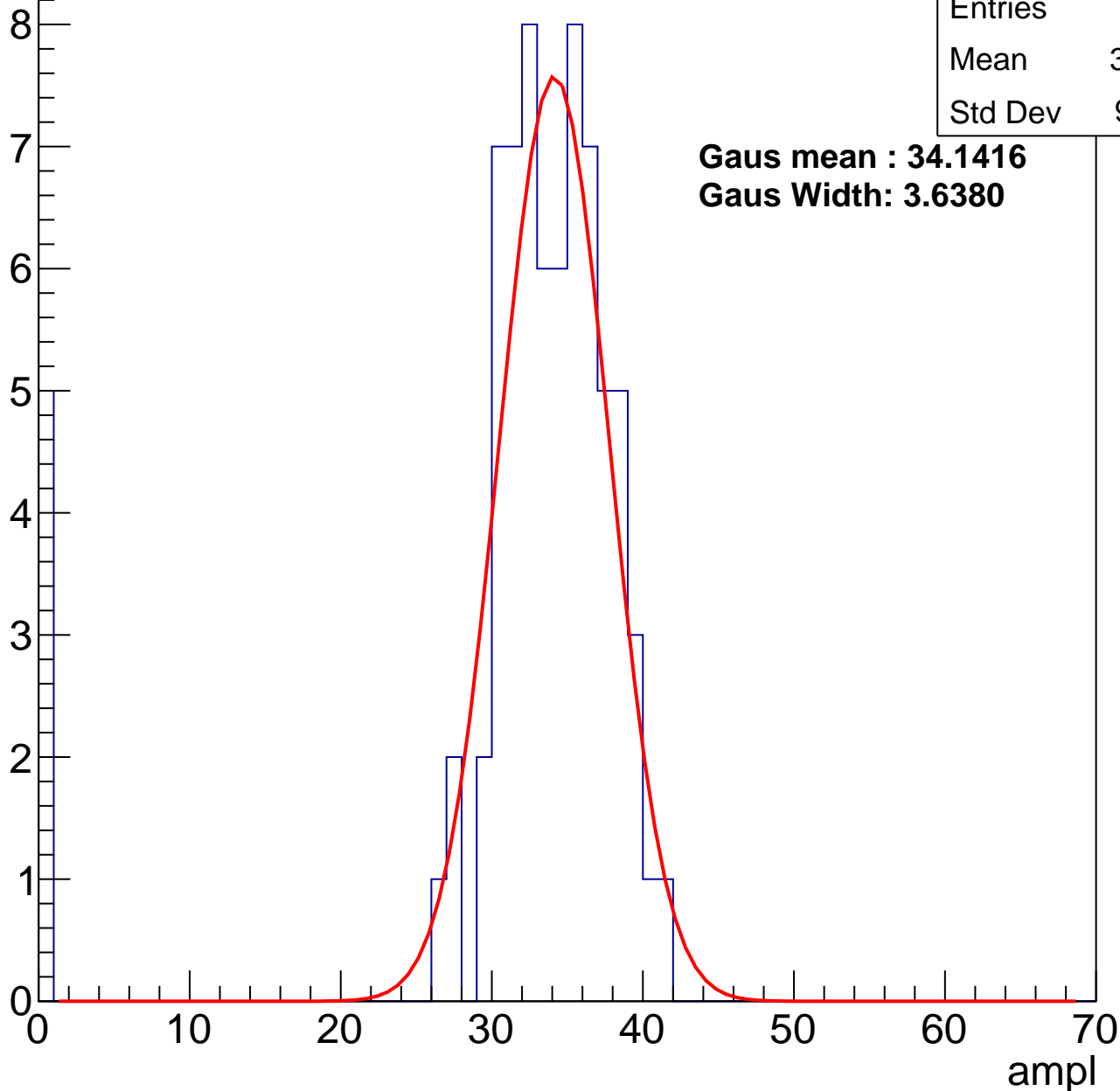
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	31.46
Std Dev	9.041

Gaus mean : 34.1416

Gaus Width: 3.6380



B1L103S, U1-ch33, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	33.48
Std Dev	15.14

Gaus mean : 40.6127

Gaus Width: 3.4193

Entry

10

8

6

4

2

0

0

10

20

30

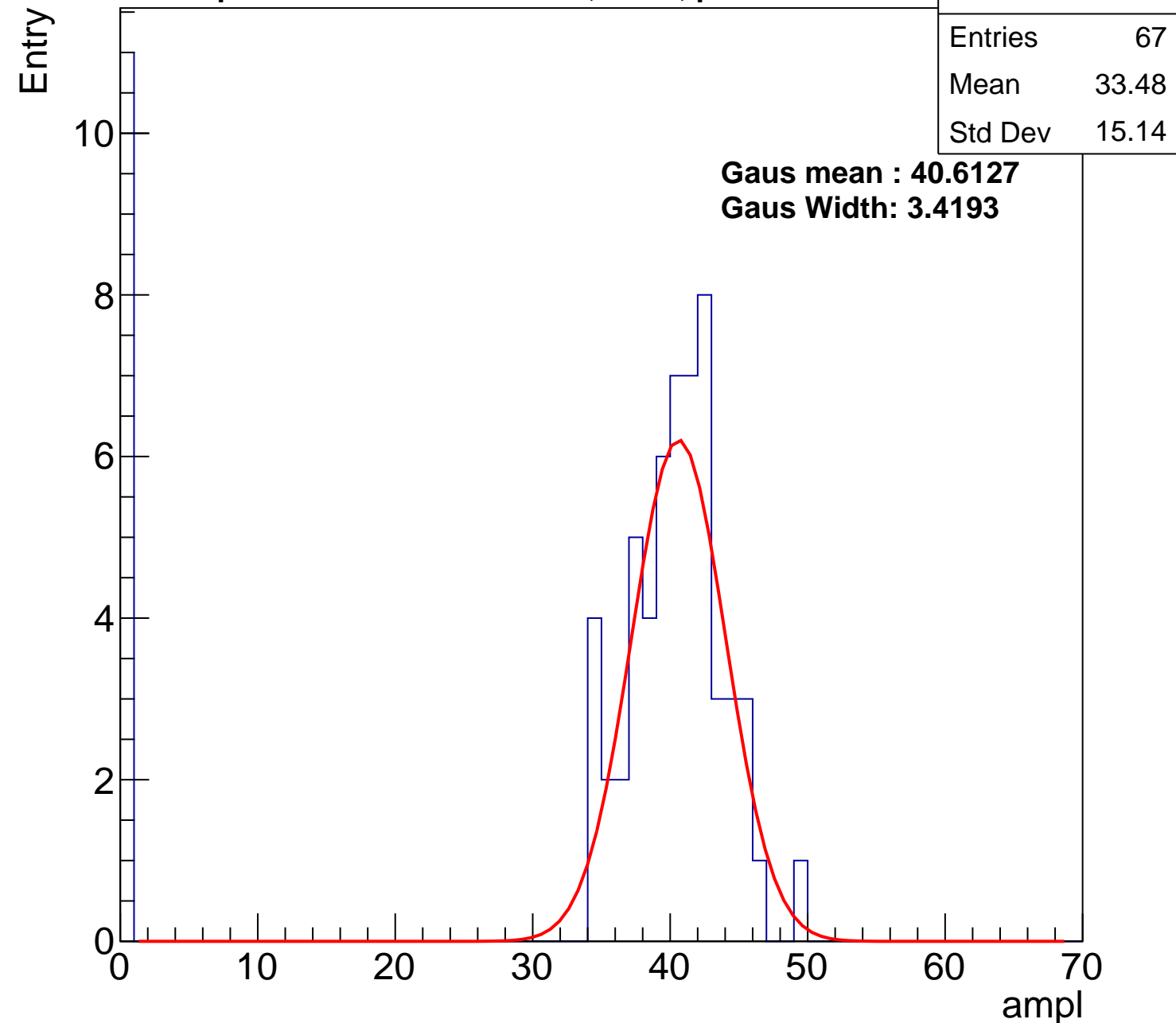
40

50

60

70

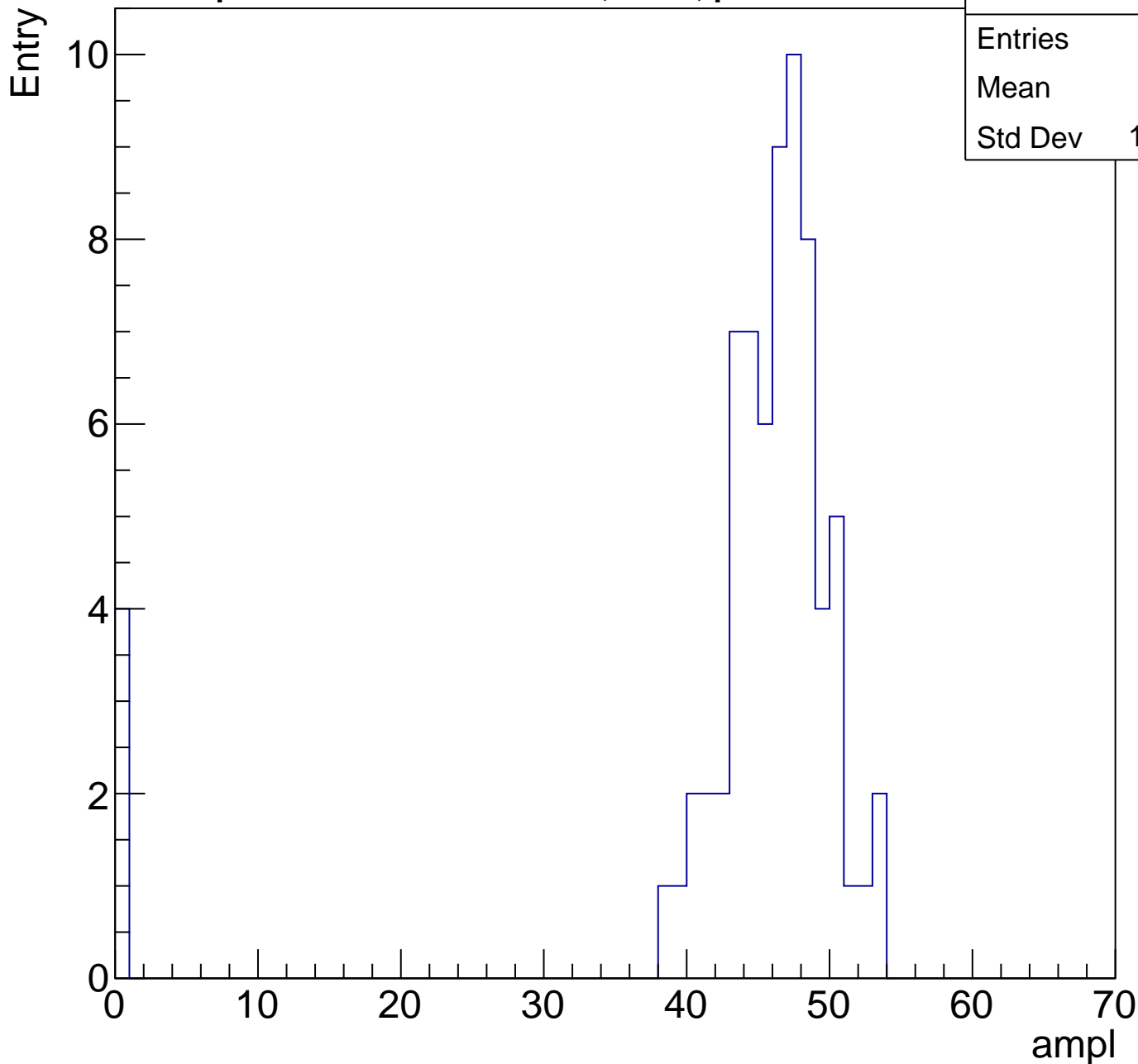
ampl



B1L103S, U1-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	43.4
Std Dev	10.97

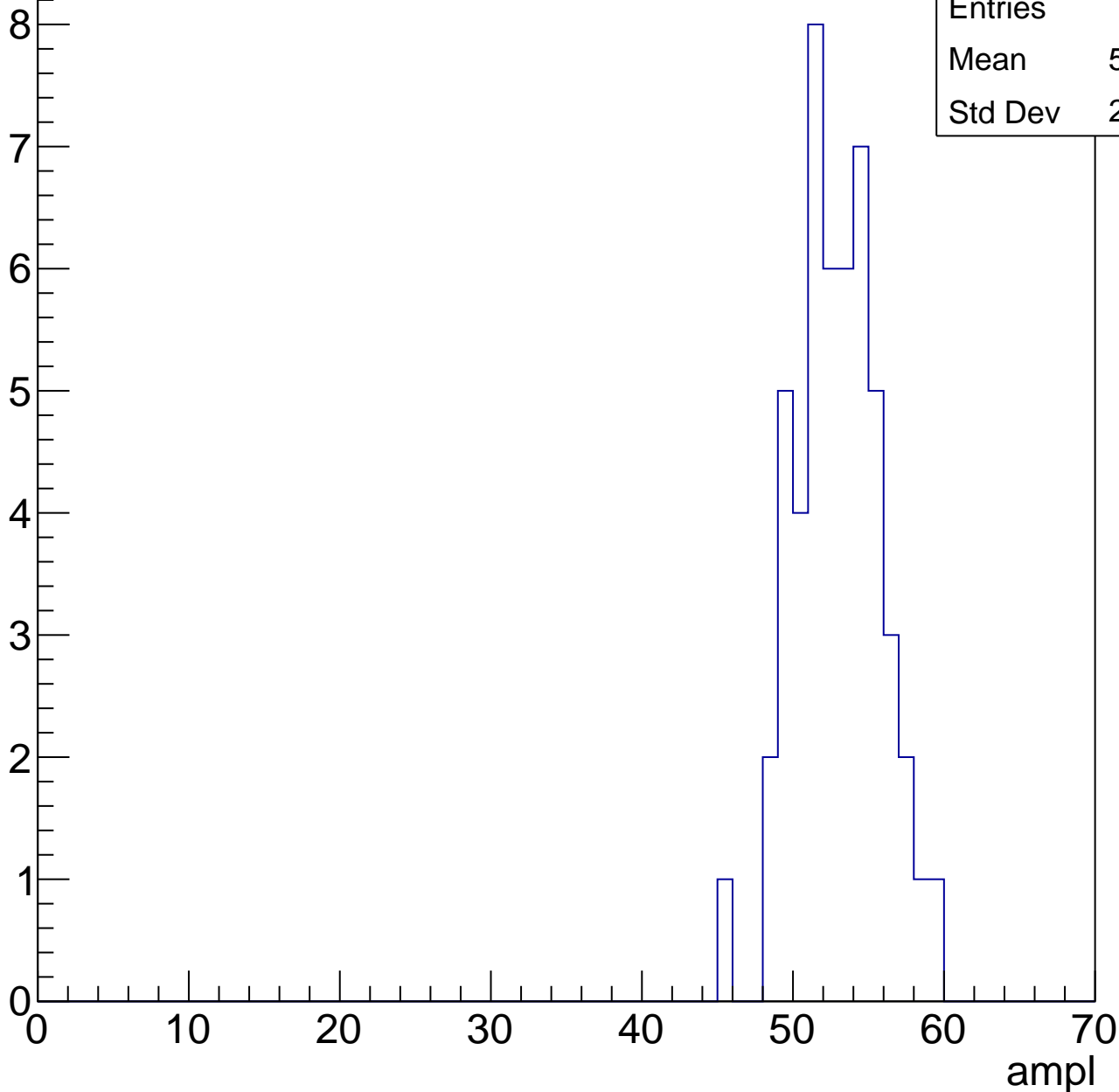


B1L103S, U1-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	52.47
Std Dev	2.803

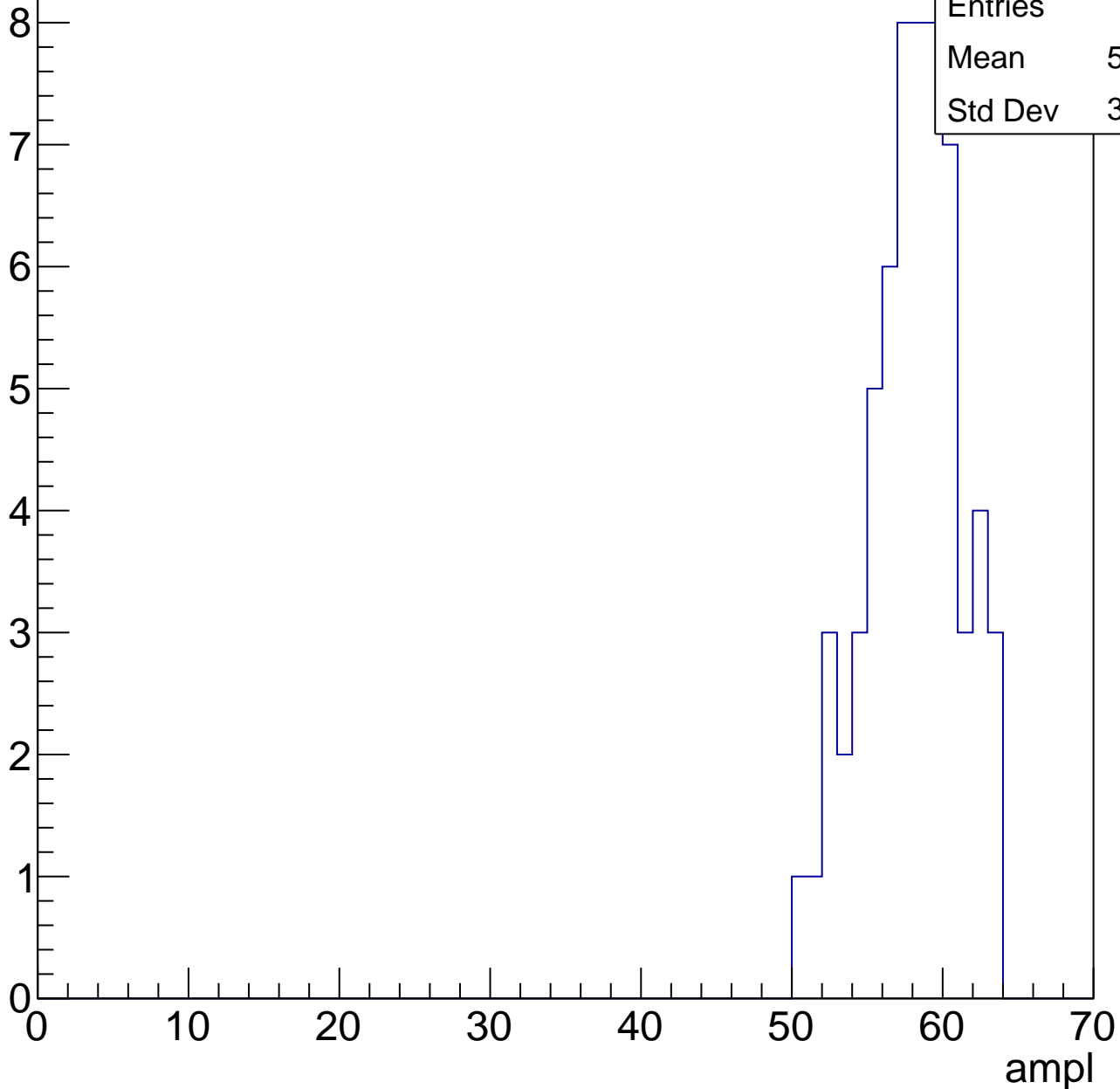


B1L103S, U1-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	57.55
Std Dev	3.073

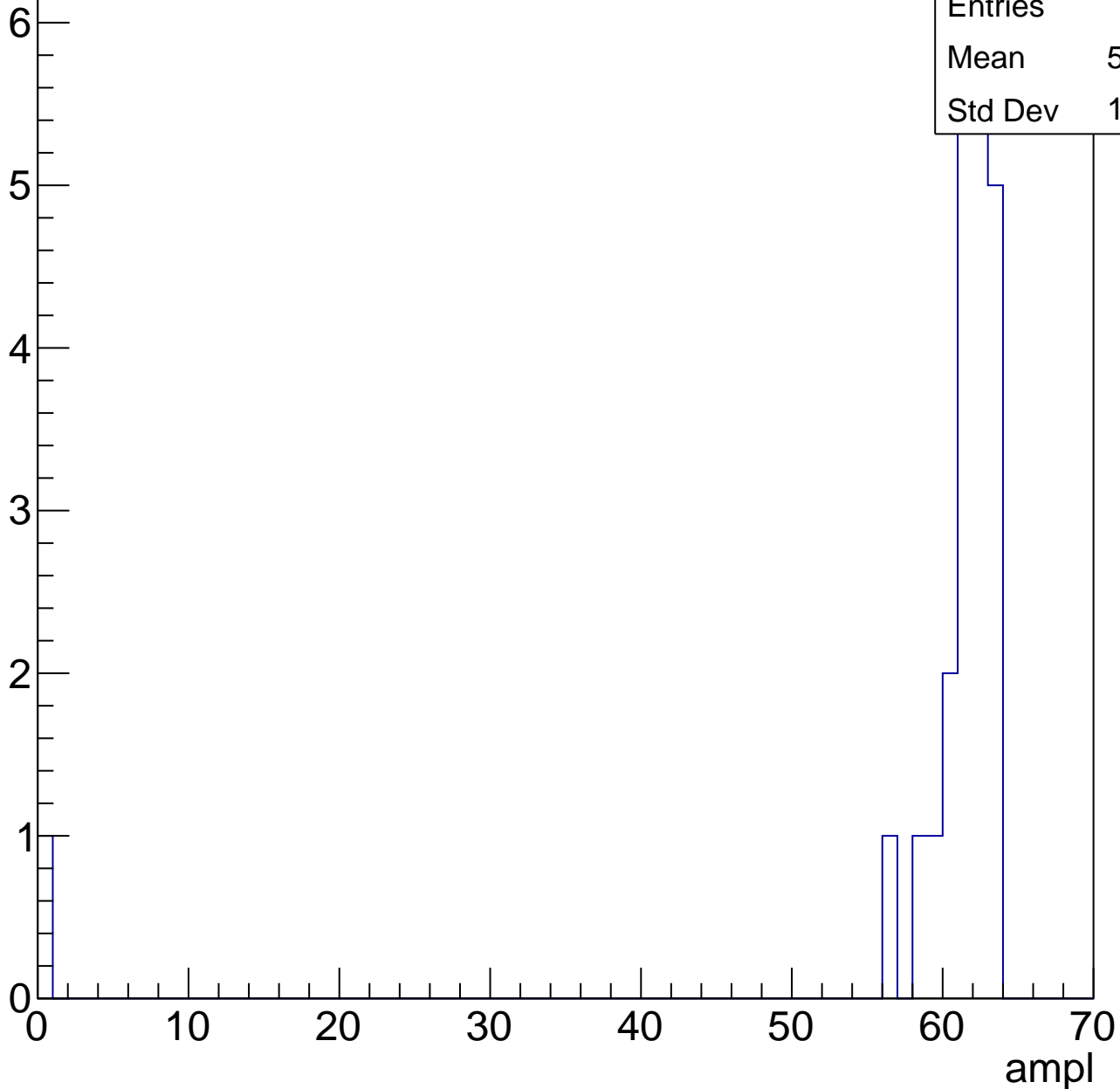


B1L103S, U1-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.52
Std Dev	12.59

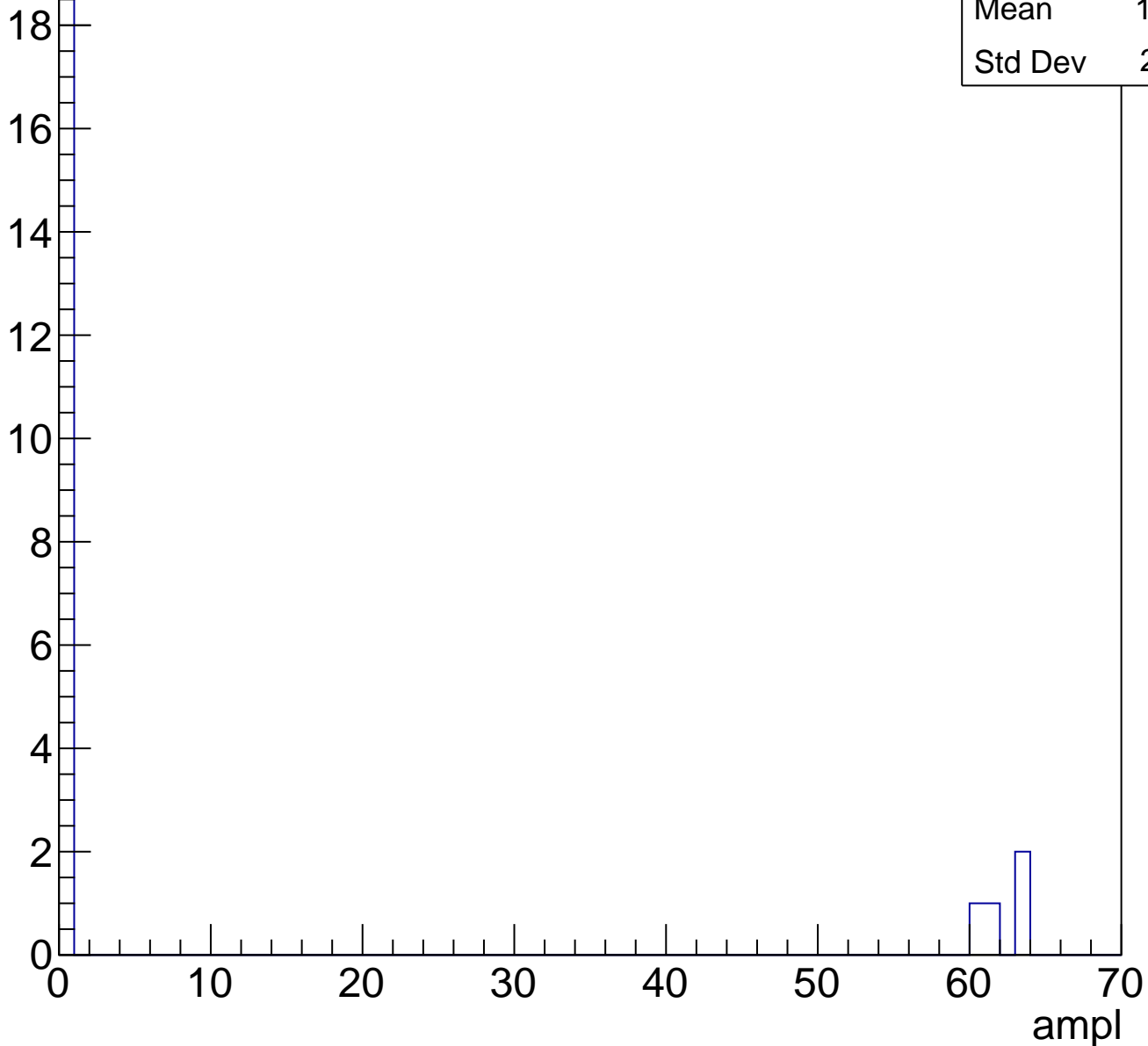


B1L103S, U1-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.74
Std Dev	23.41

Entry



B1L103S, U1-ch34, adc0

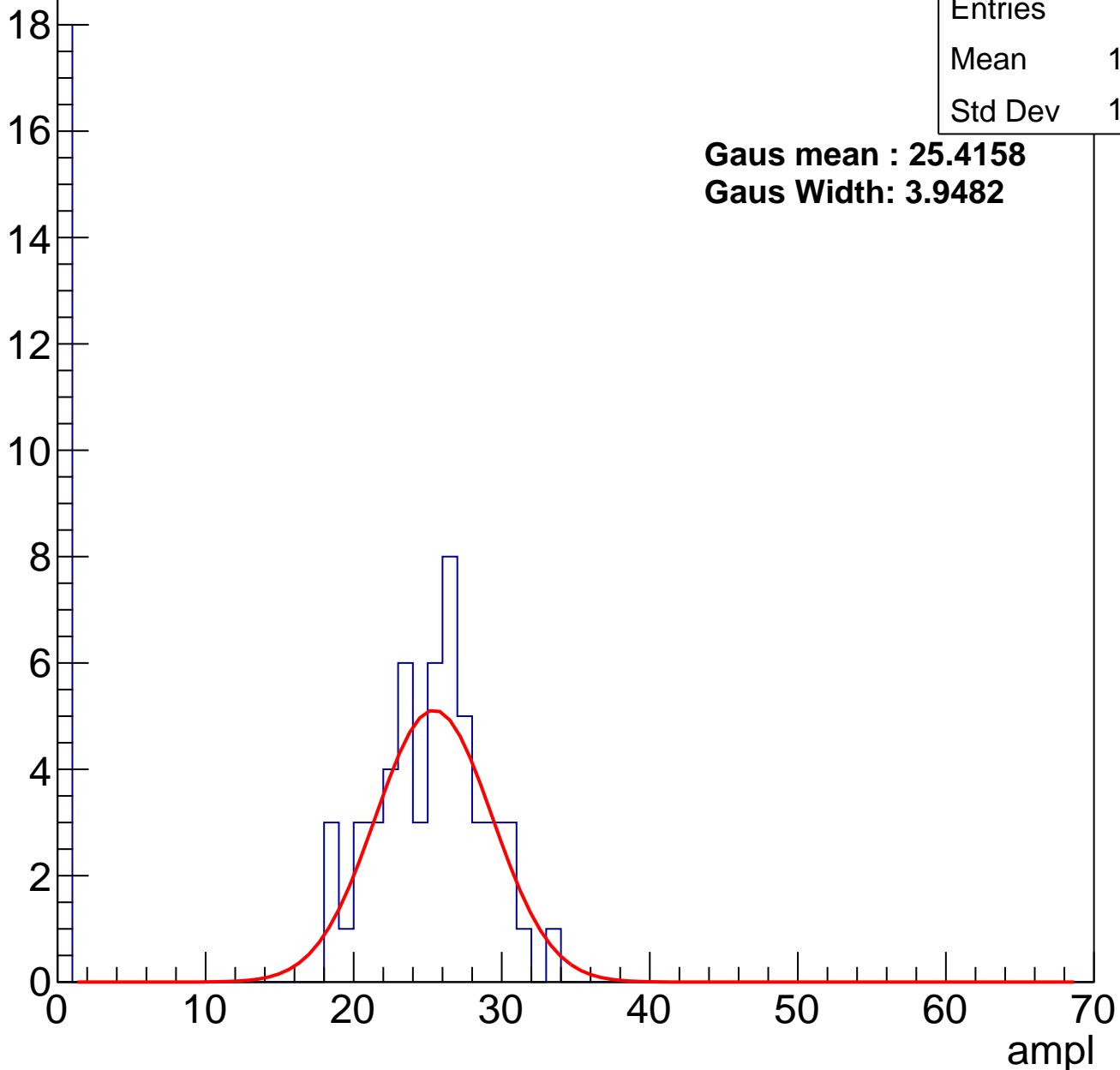
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	18.48
Std Dev	11.18

Gaus mean : 25.4158

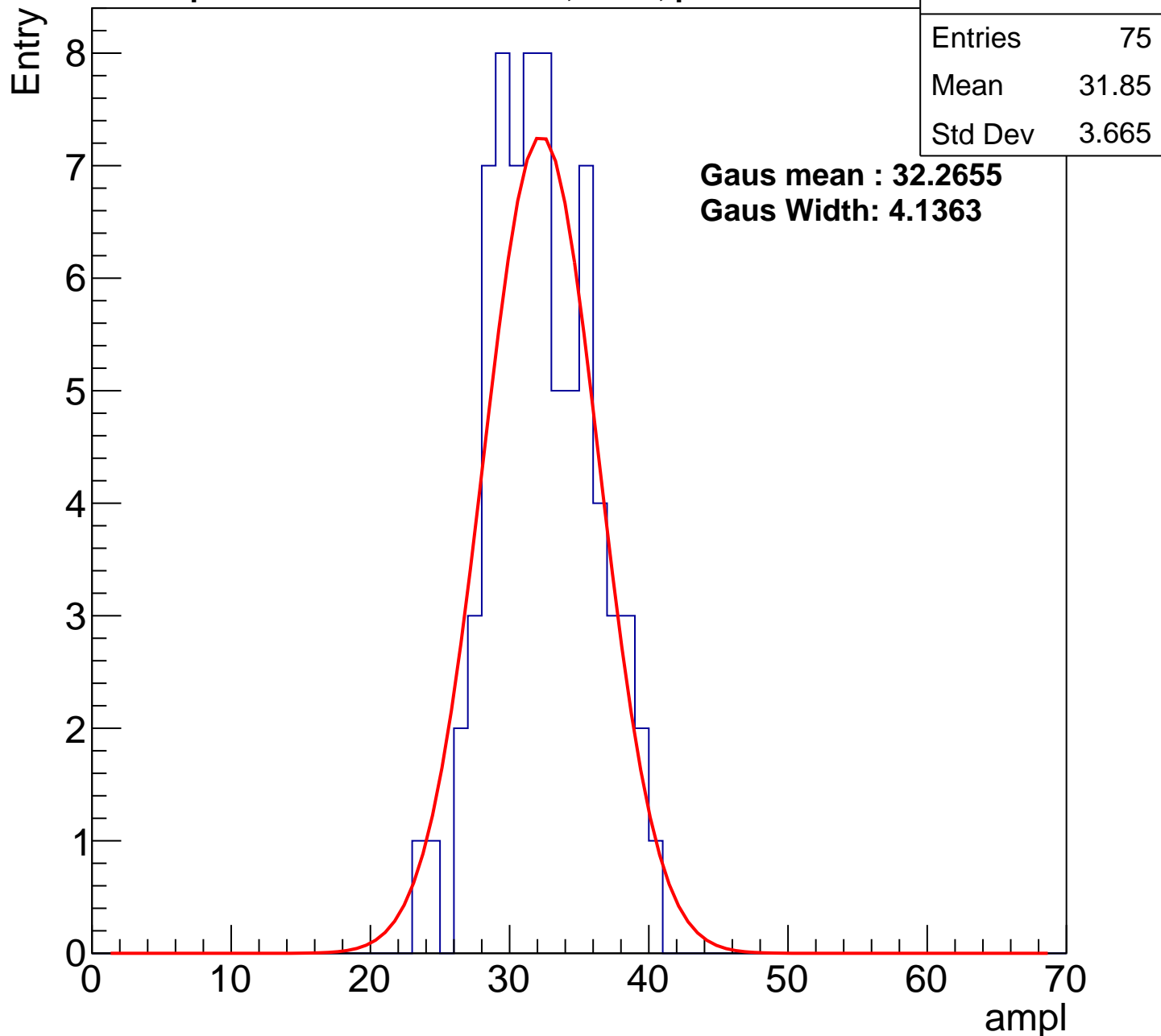
Gaus Width: 3.9482

Entry



B1L103S, U1-ch34, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch34, adc2

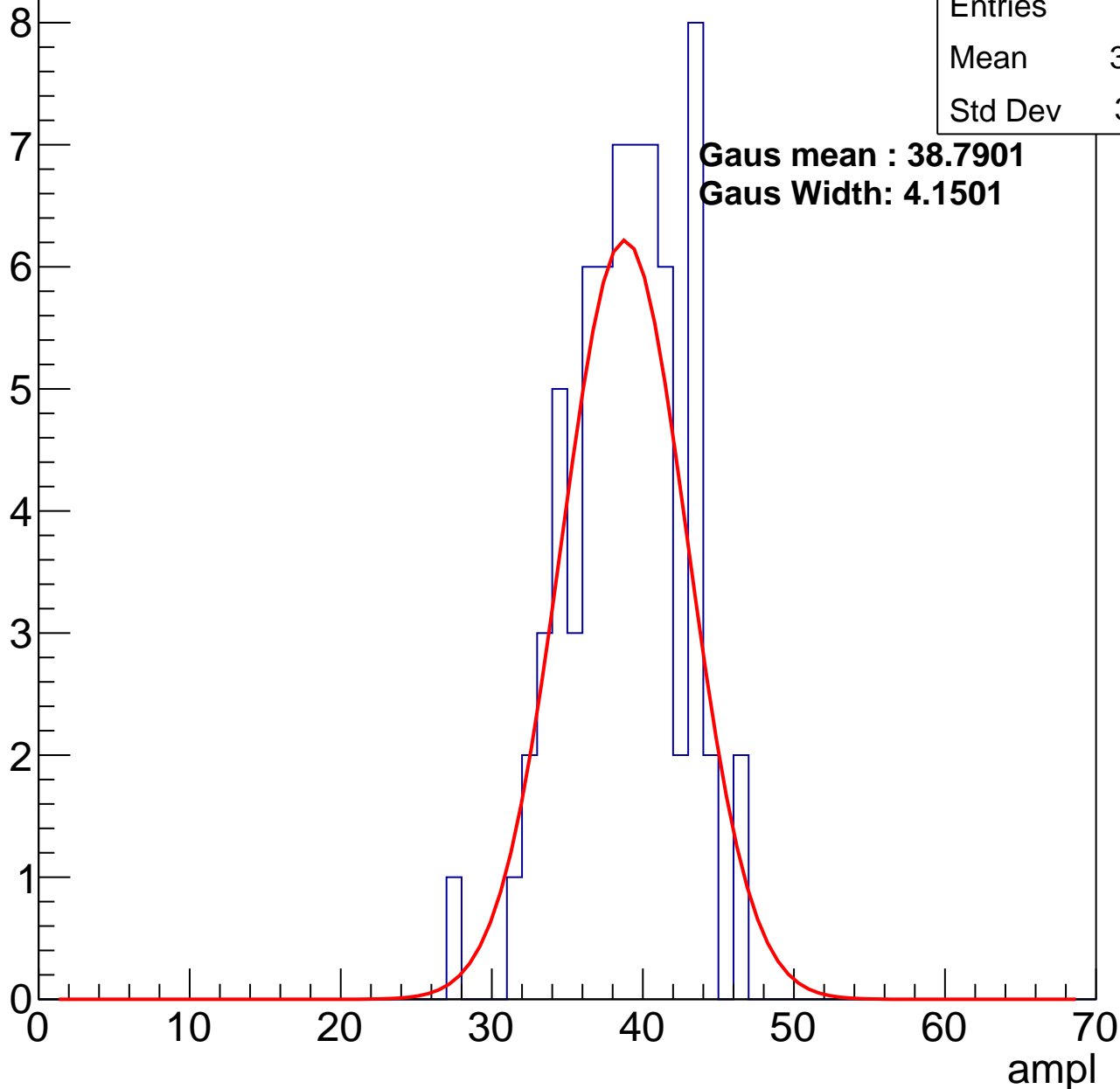
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.34
Std Dev	3.771

Gaus mean : 38.7901

Gaus Width: 4.1501



B1L103S, U1-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

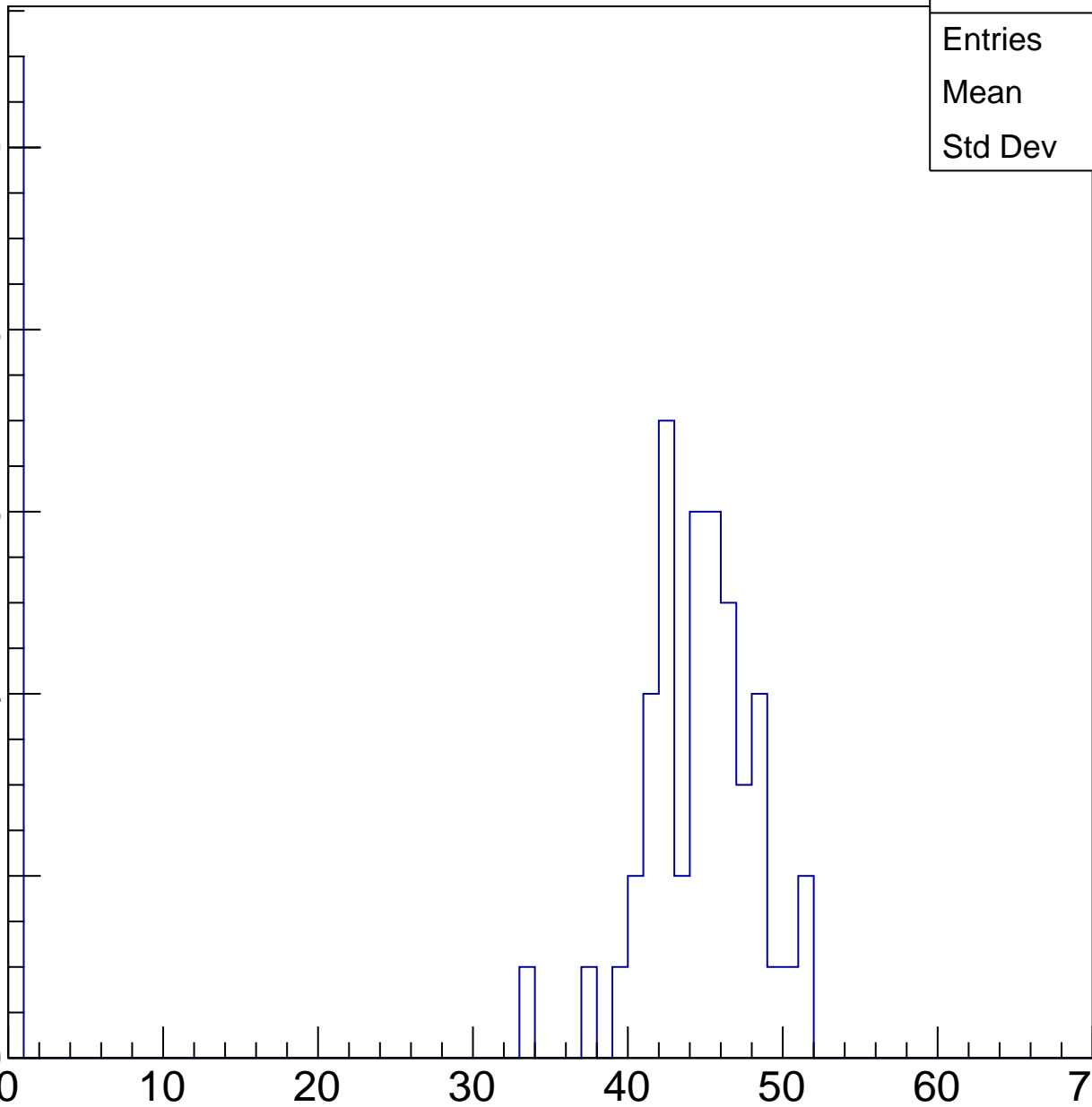
4

2

0

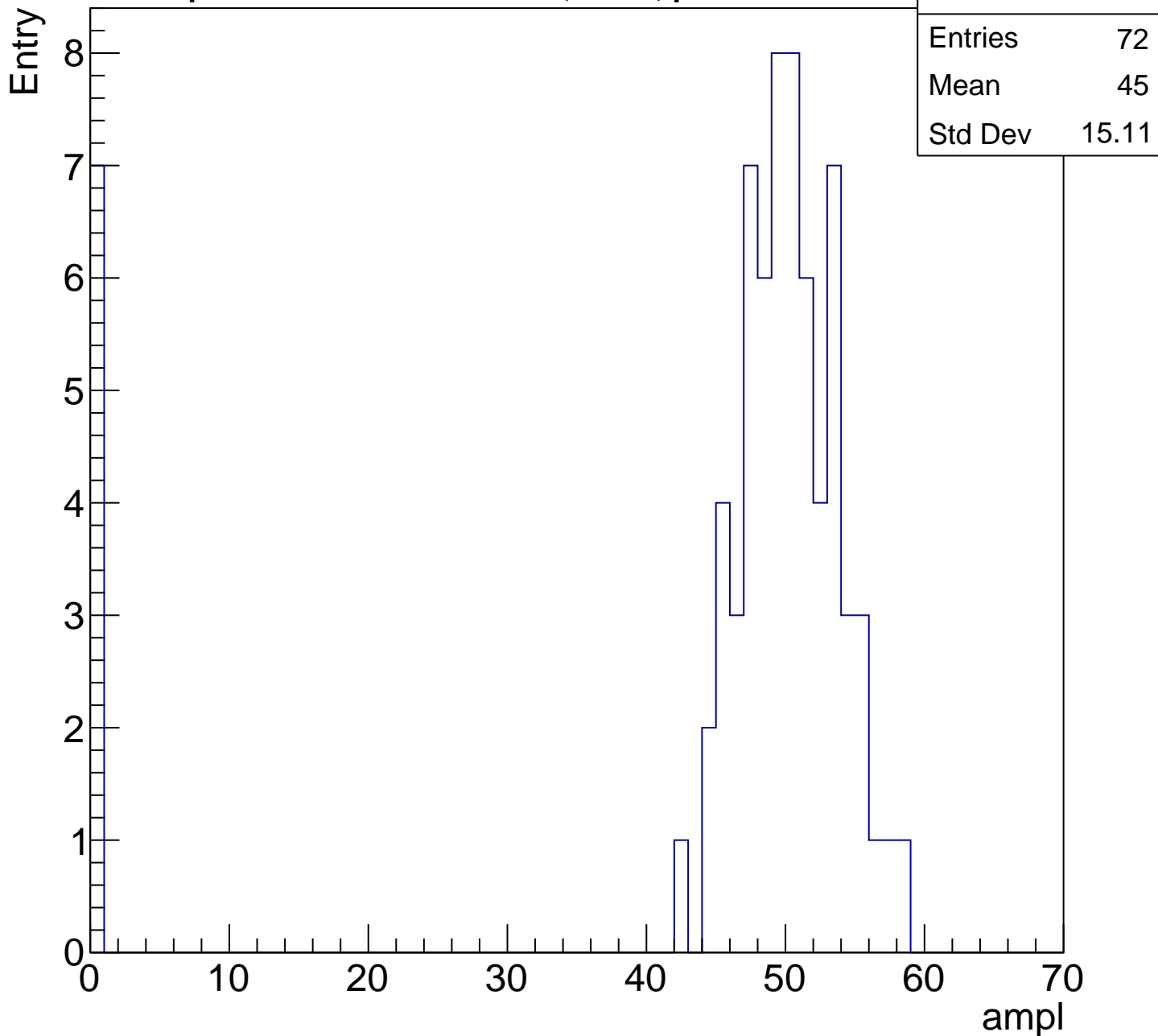
Entries	57
Mean	35.63
Std Dev	17.71

ampl



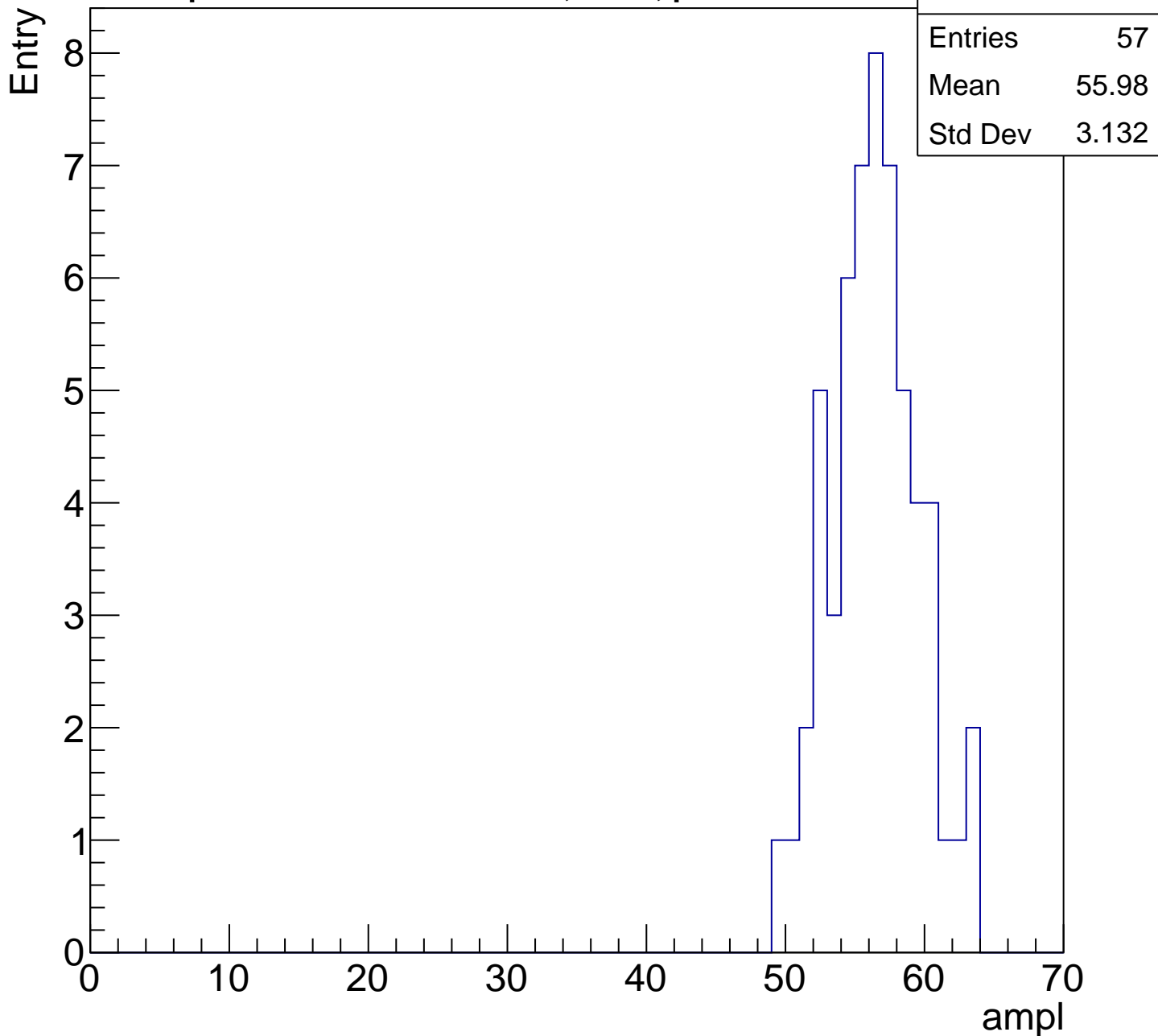
B1L103S, U1-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

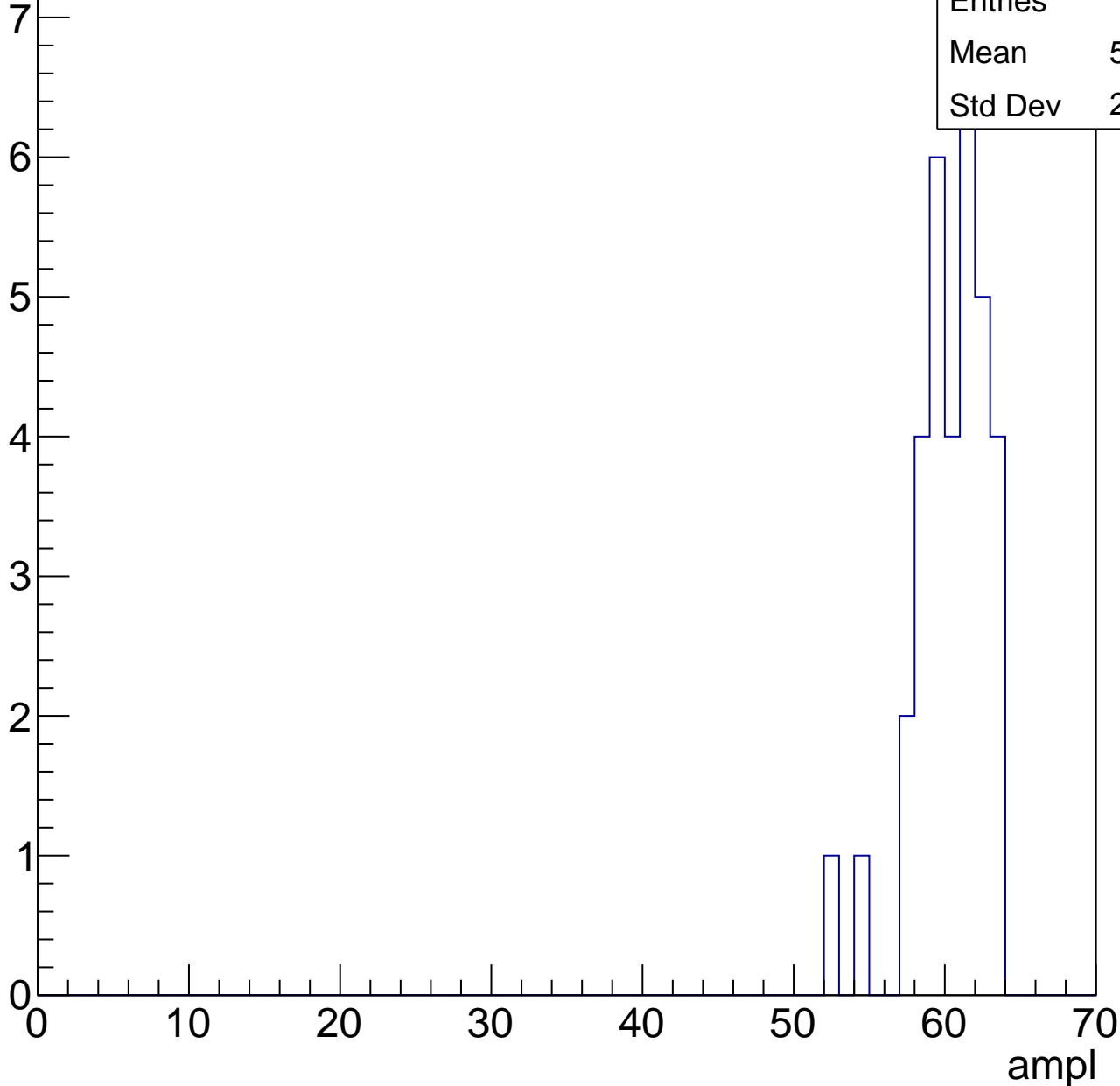


B1L103S, U1-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

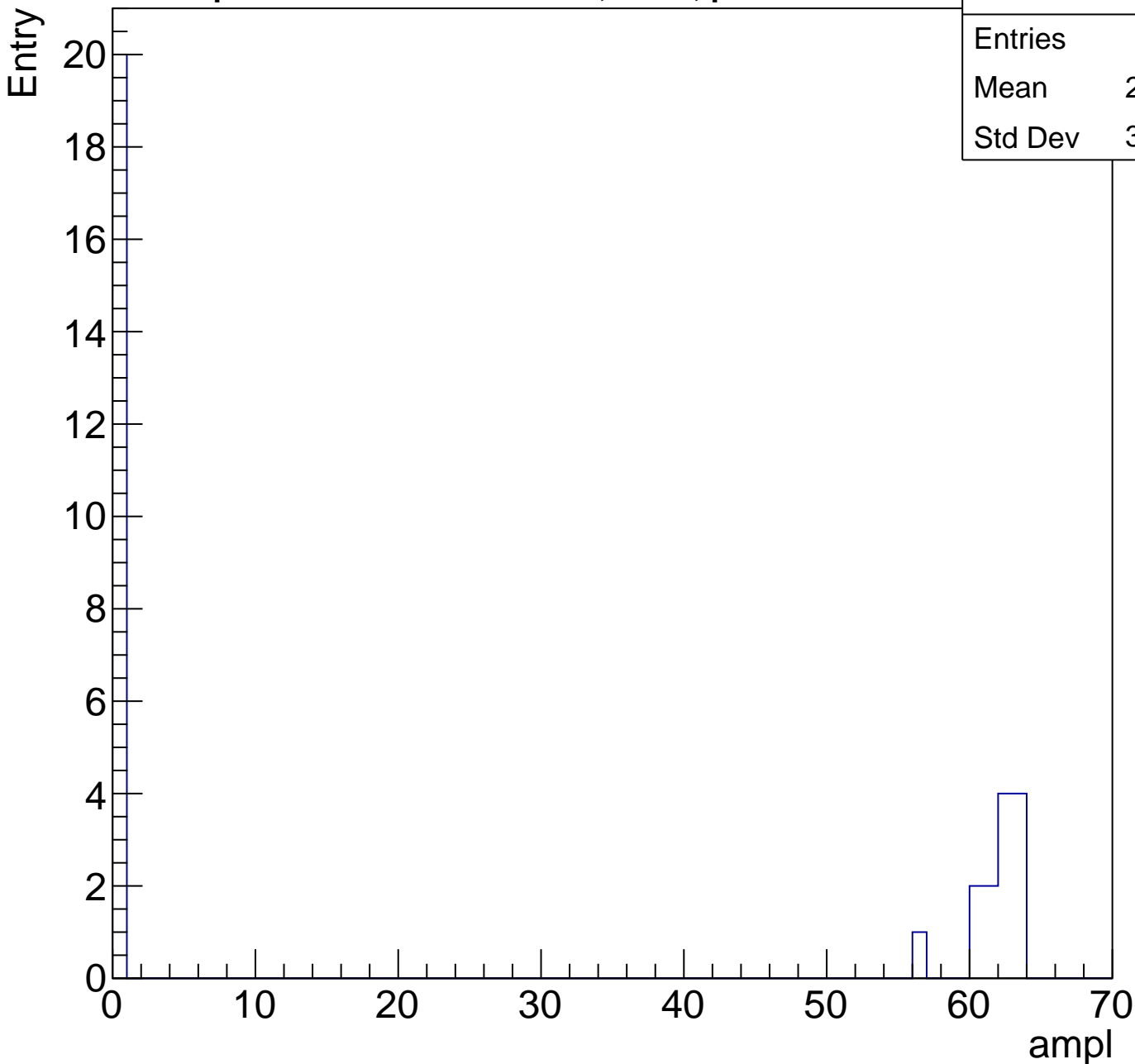
Entries	34
Mean	59.85
Std Dev	2.439



B1L103S, U1-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	33
Mean	24.18
Std Dev	30.02



B1L103S, U1-ch35, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	19.29
Std Dev	13.56

Gaus mean : 29.4835

Gaus Width: 3.4779

Entry

30

25

20

15

10

5

0

0

10

20

30

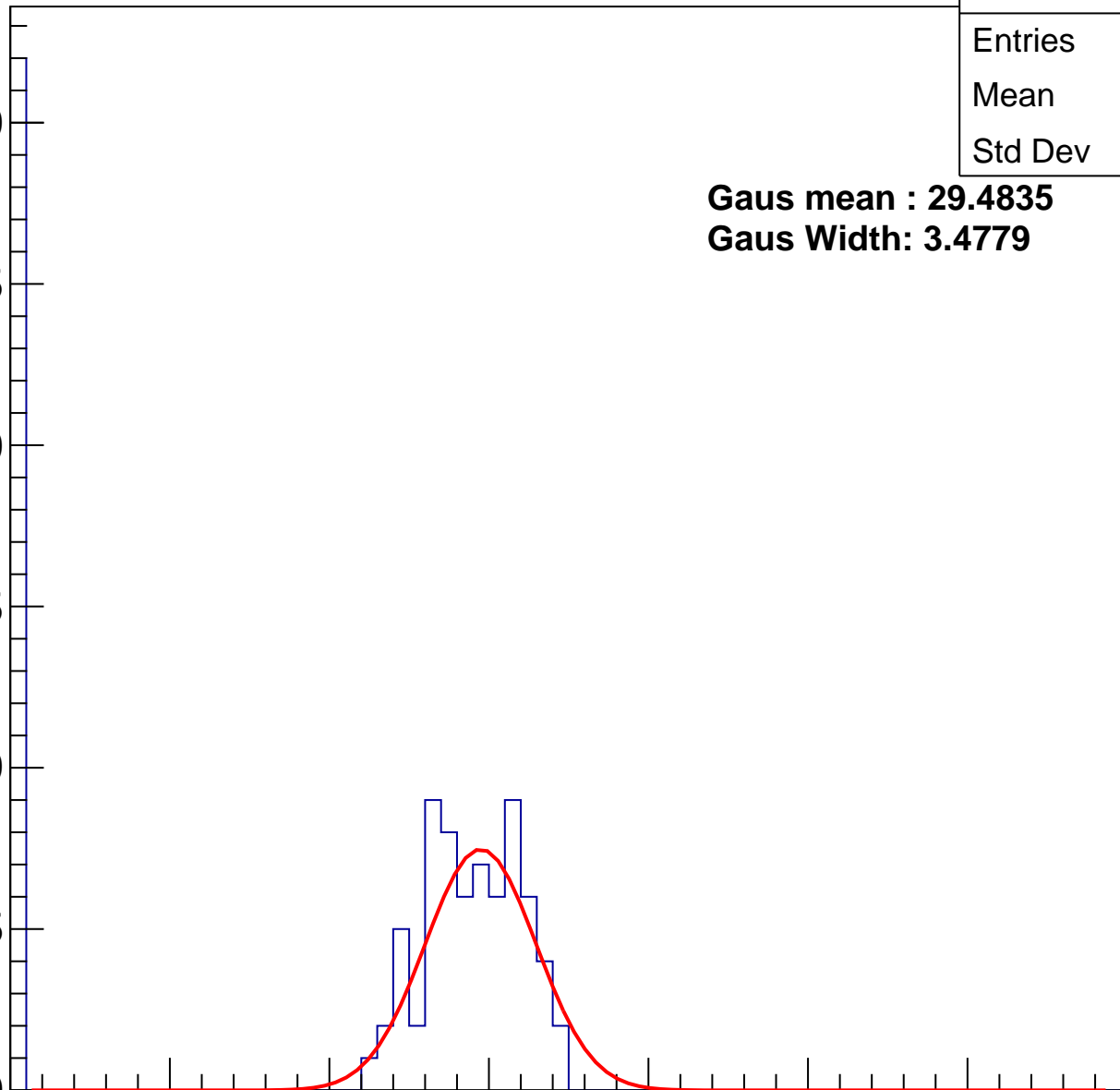
40

50

60

70

ampl



B1L103S, U1-ch35, adc1

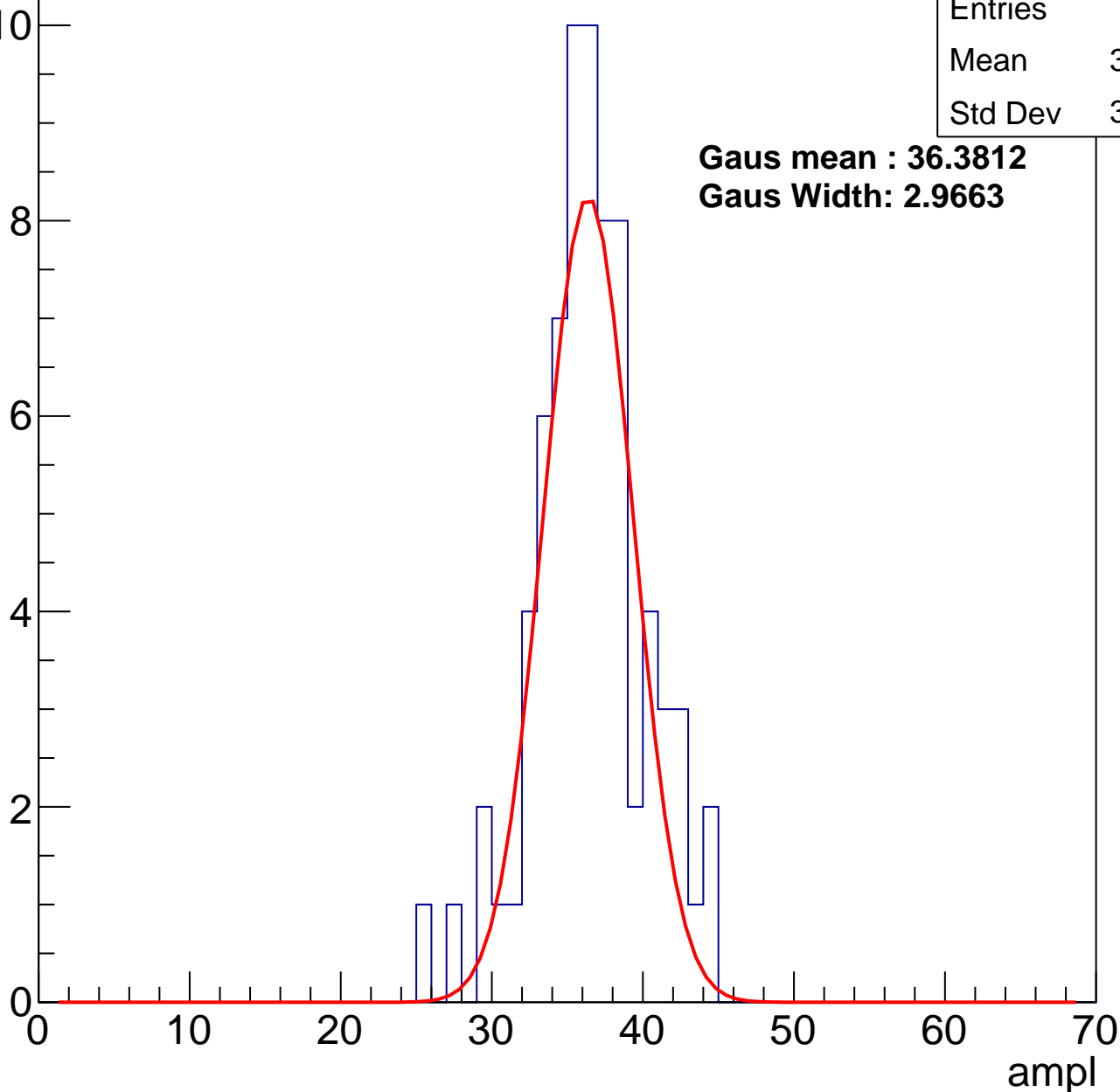
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	35.99
Std Dev	3.674

Gaus mean : 36.3812

Gaus Width: 2.9663



B1L103S, U1-ch35, adc2

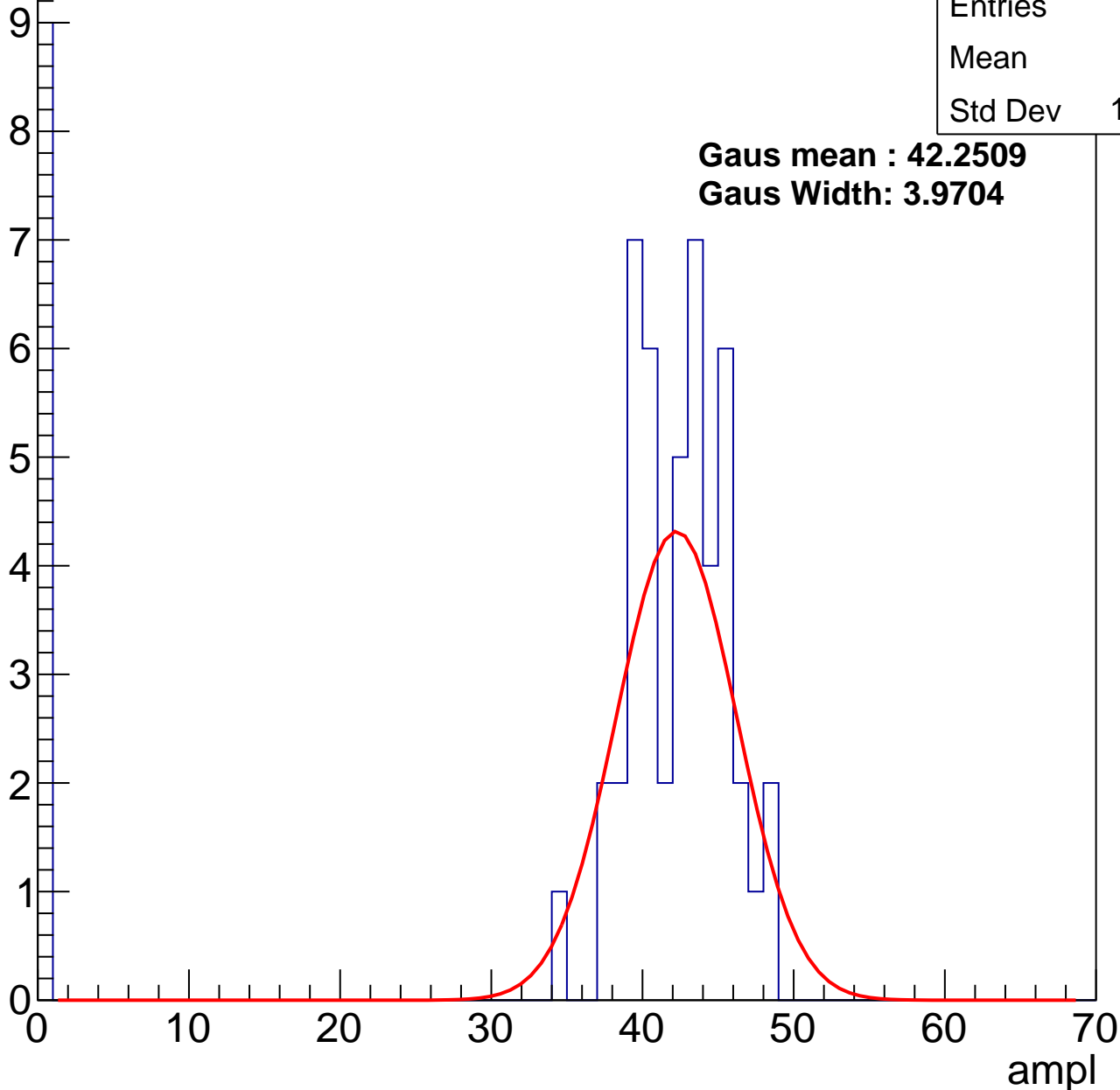
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	35.2
Std Dev	15.66

Gaus mean : 42.2509

Gaus Width: 3.9704

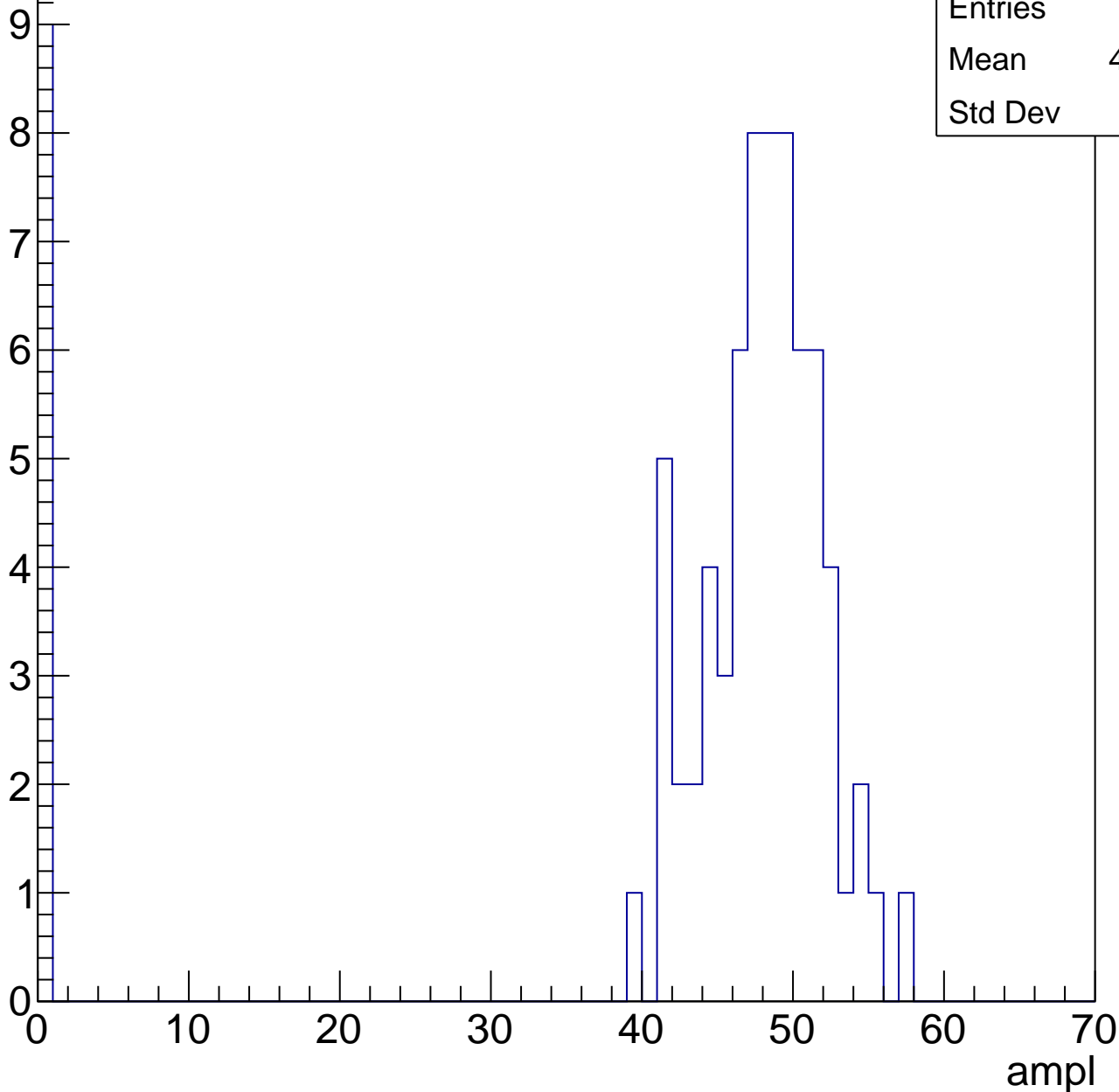


B1L103S, U1-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	42.08
Std Dev	15.7

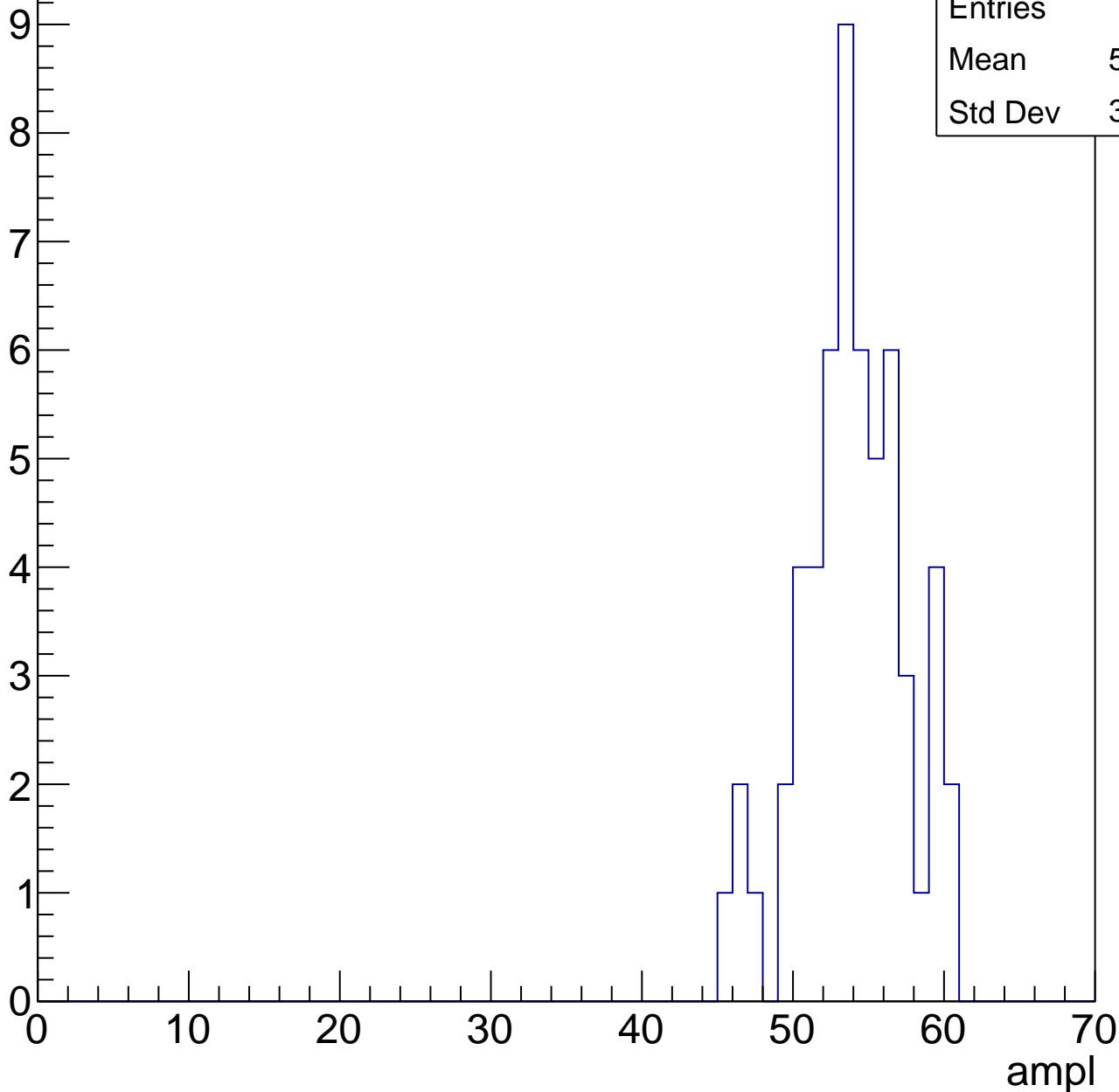


B1L103S, U1-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

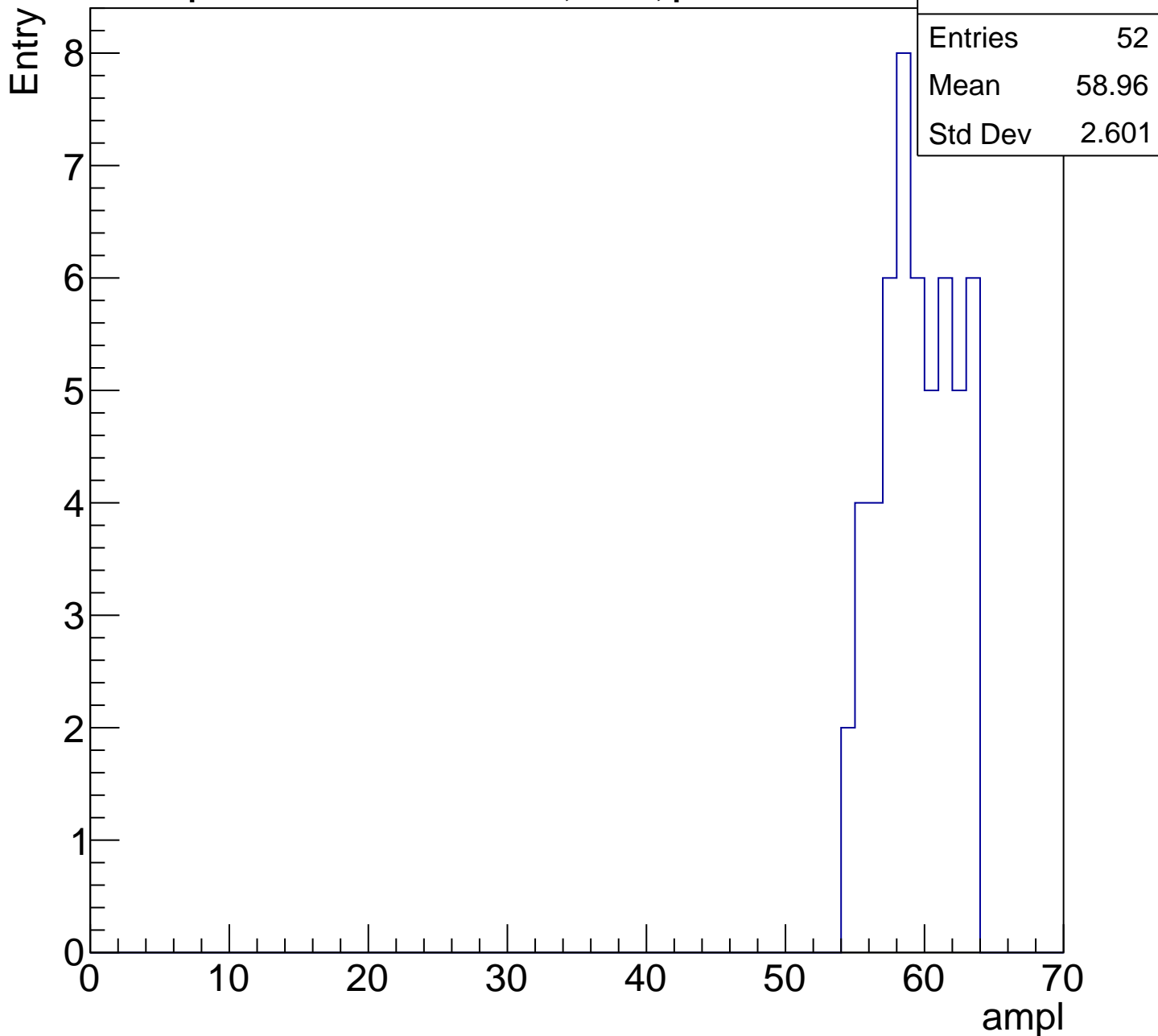
Entry

Entries	56
Mean	53.48
Std Dev	3.459



B1L103S, U1-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

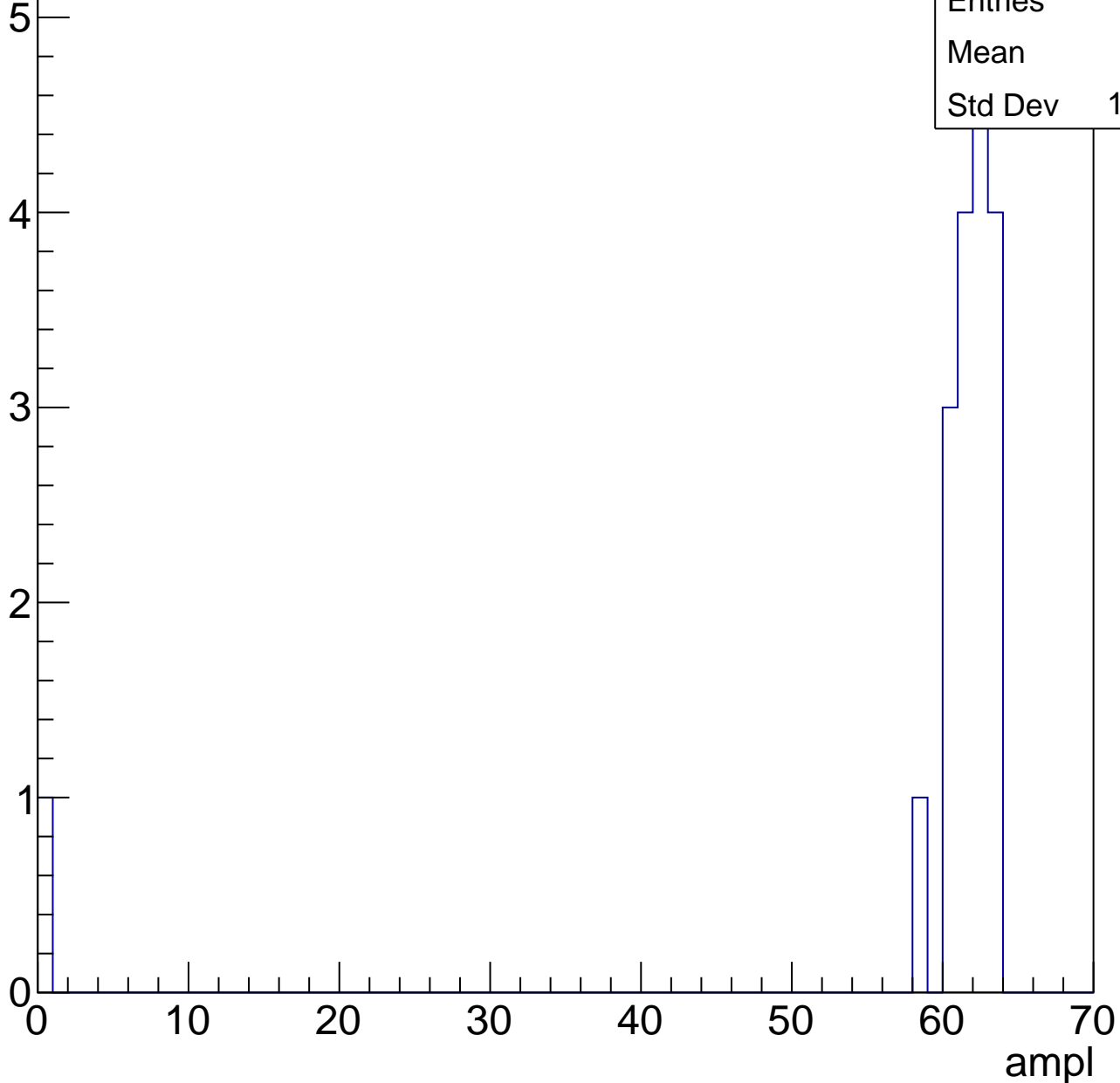


B1L103S, U1-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58
Std Dev	14.13



B1L103S, U1-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U1-ch36, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	15.91
Std Dev	13.56

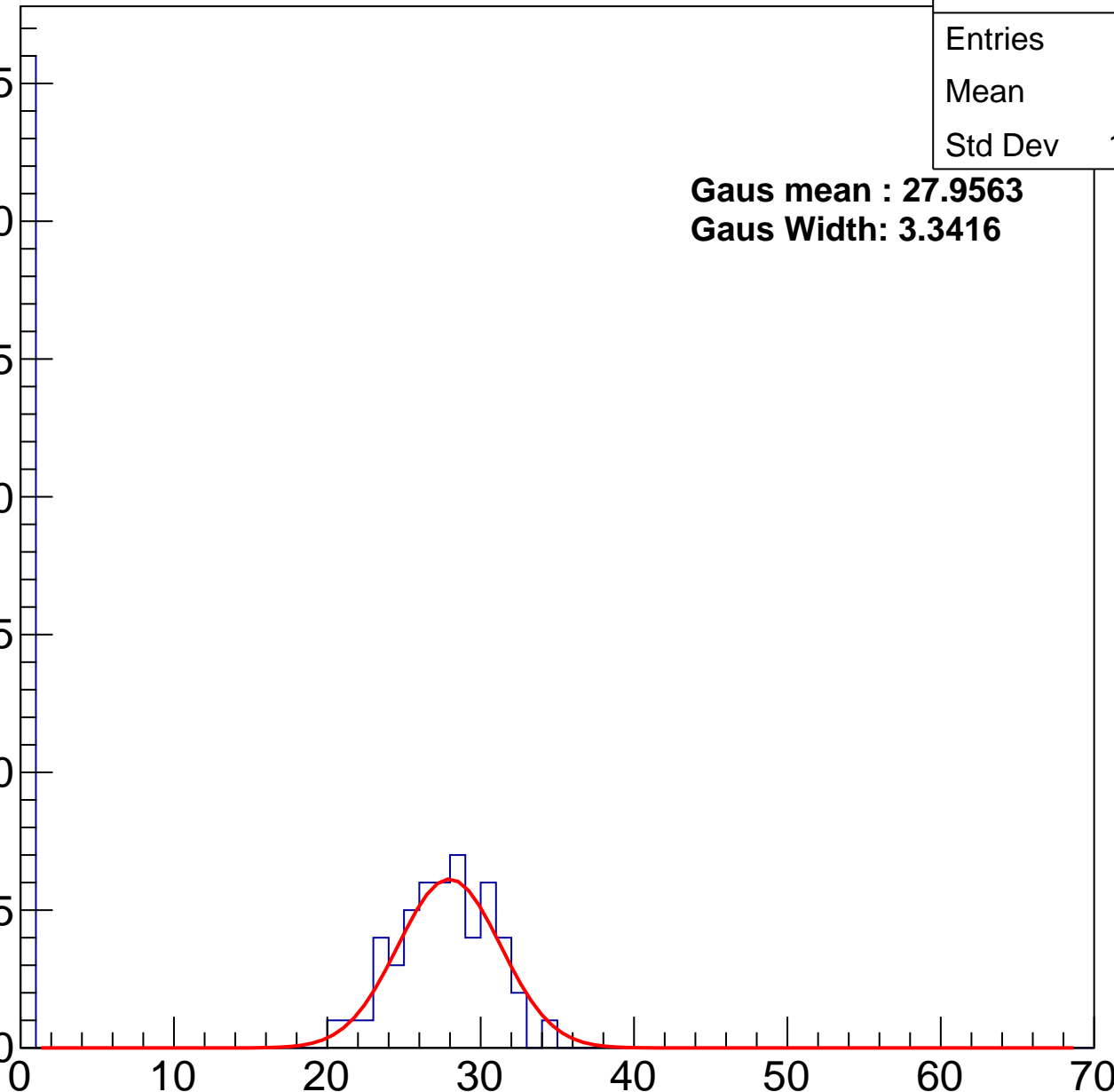
Gaus mean : 27.9563

Gaus Width: 3.3416

Entry

35
30
25
20
15
10
5
0

ampl



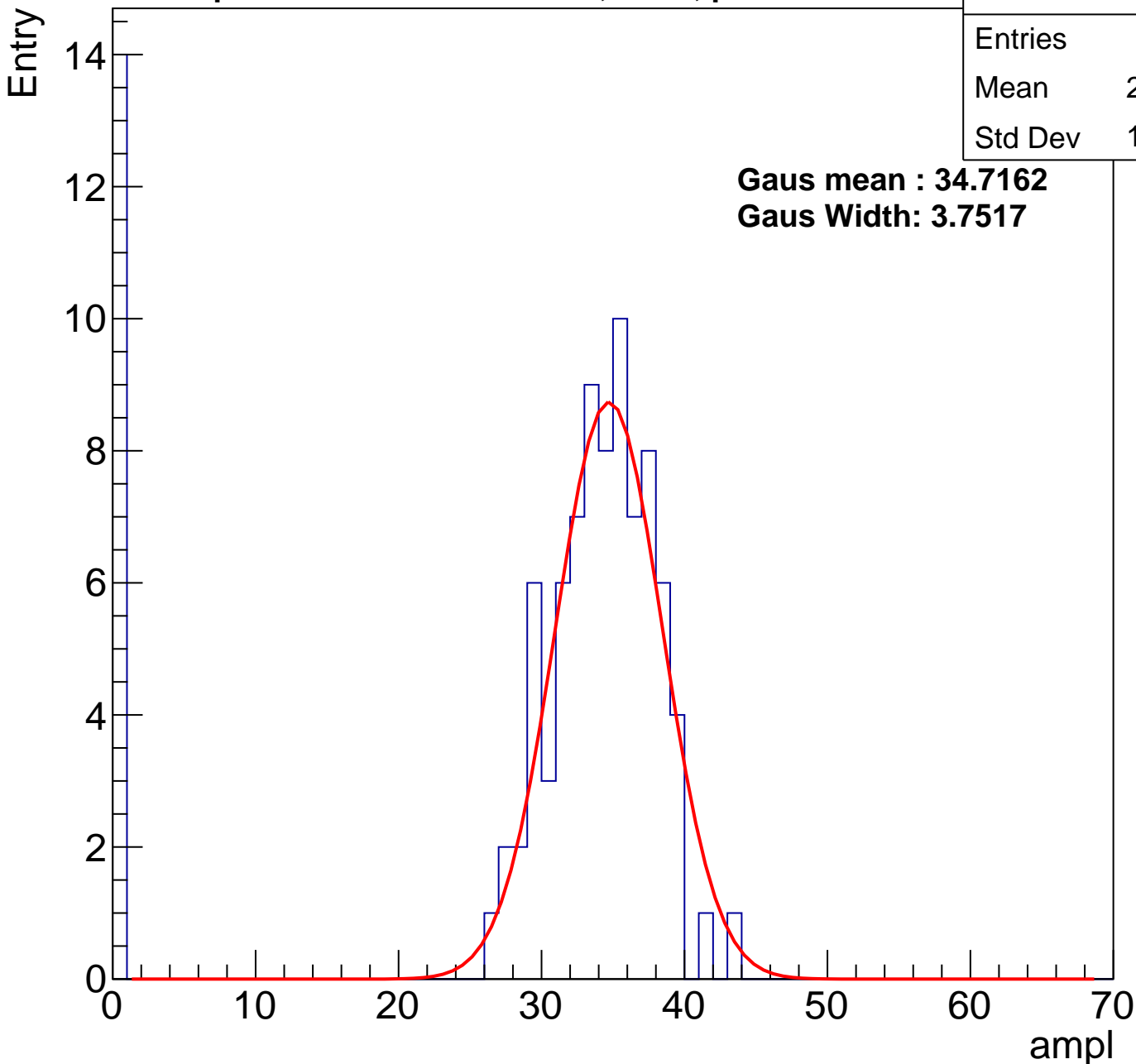
B1L103S, U1-ch36, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	28.89
Std Dev	12.43

Gaus mean : 34.7162

Gaus Width: 3.7517



B1L103S, U1-ch36, adc2

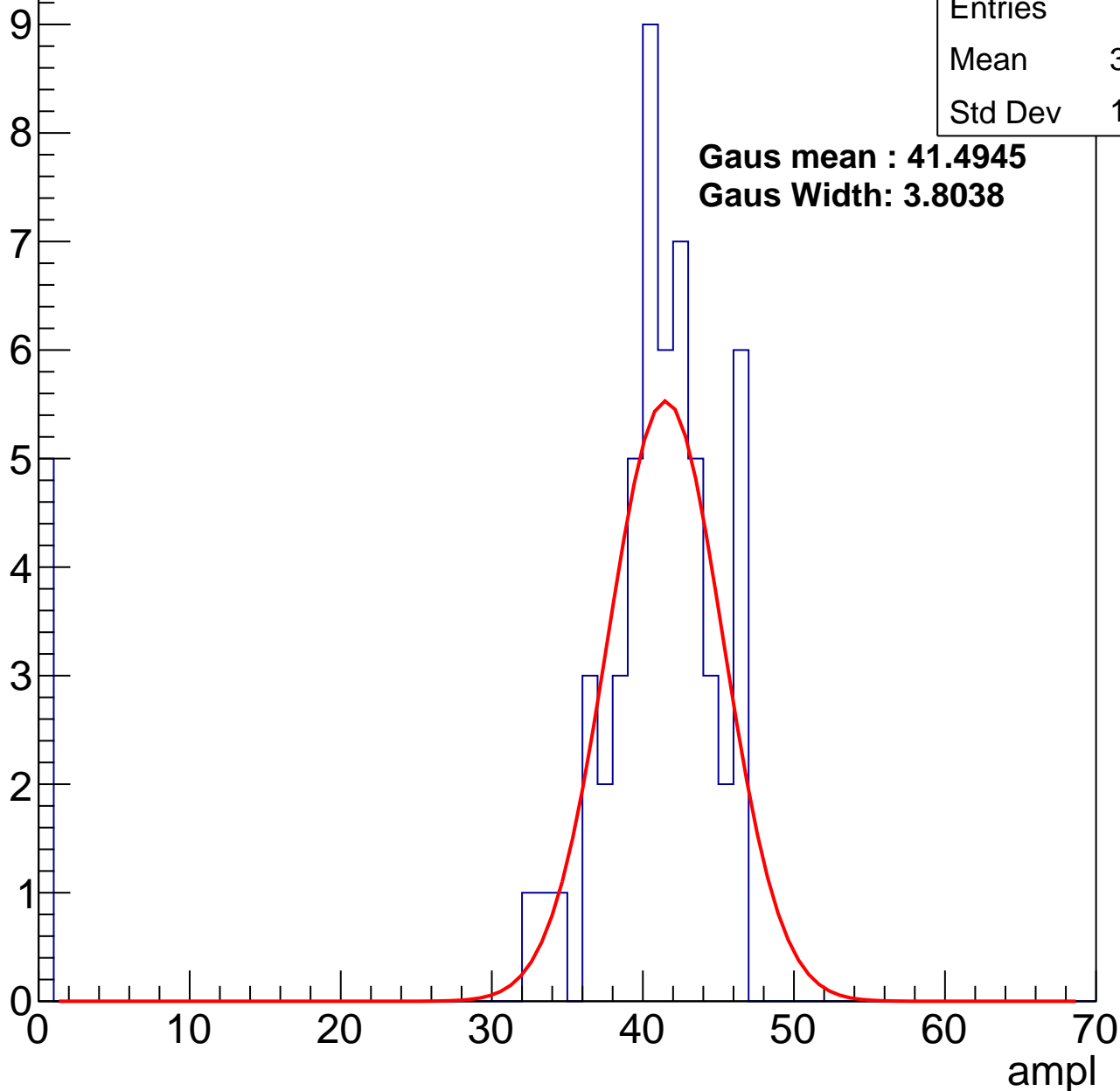
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	37.34
Std Dev	11.79

Gaus mean : 41.4945

Gaus Width: 3.8038

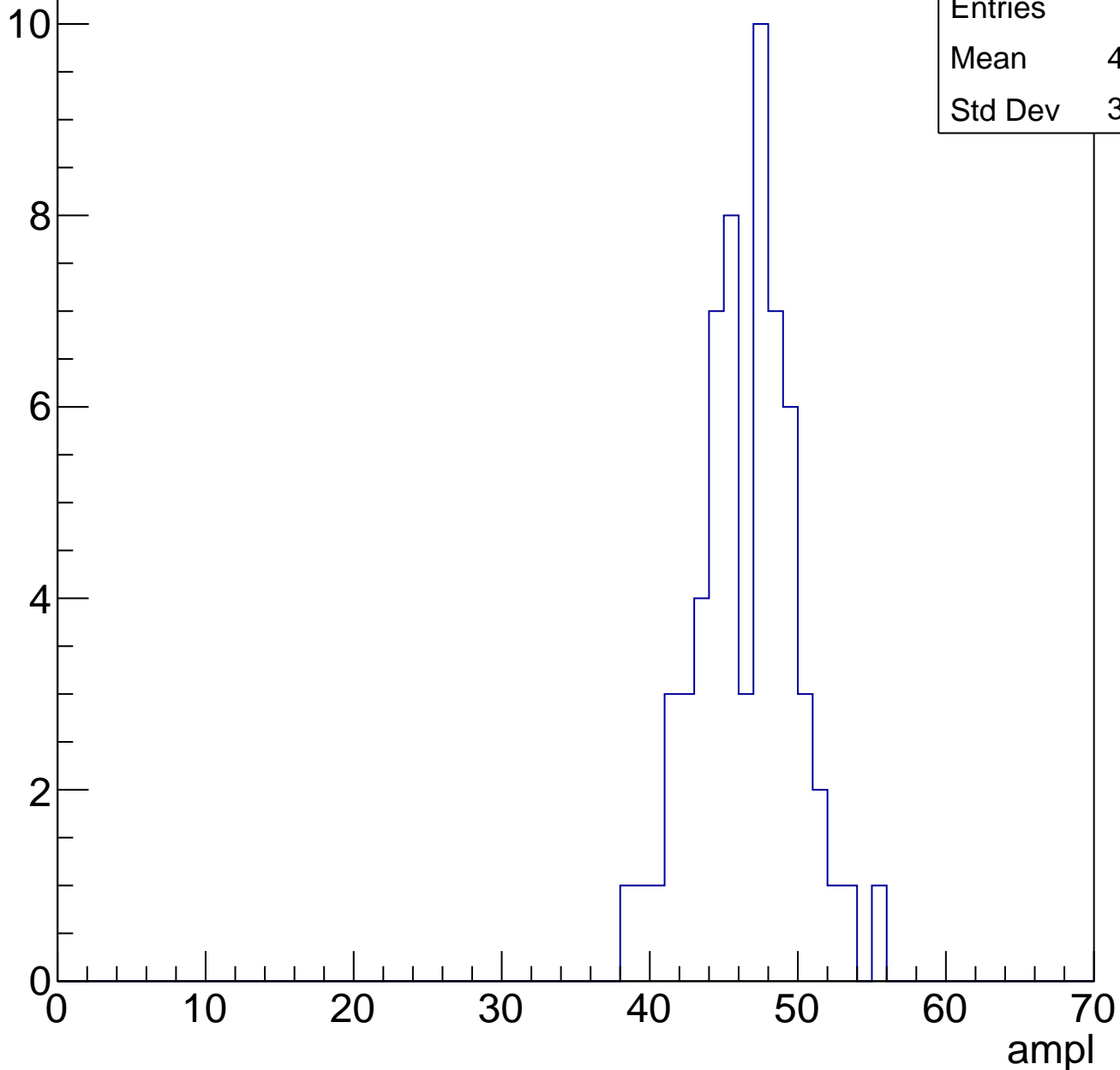


B1L103S, U1-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	46.06
Std Dev	3.359

Entry

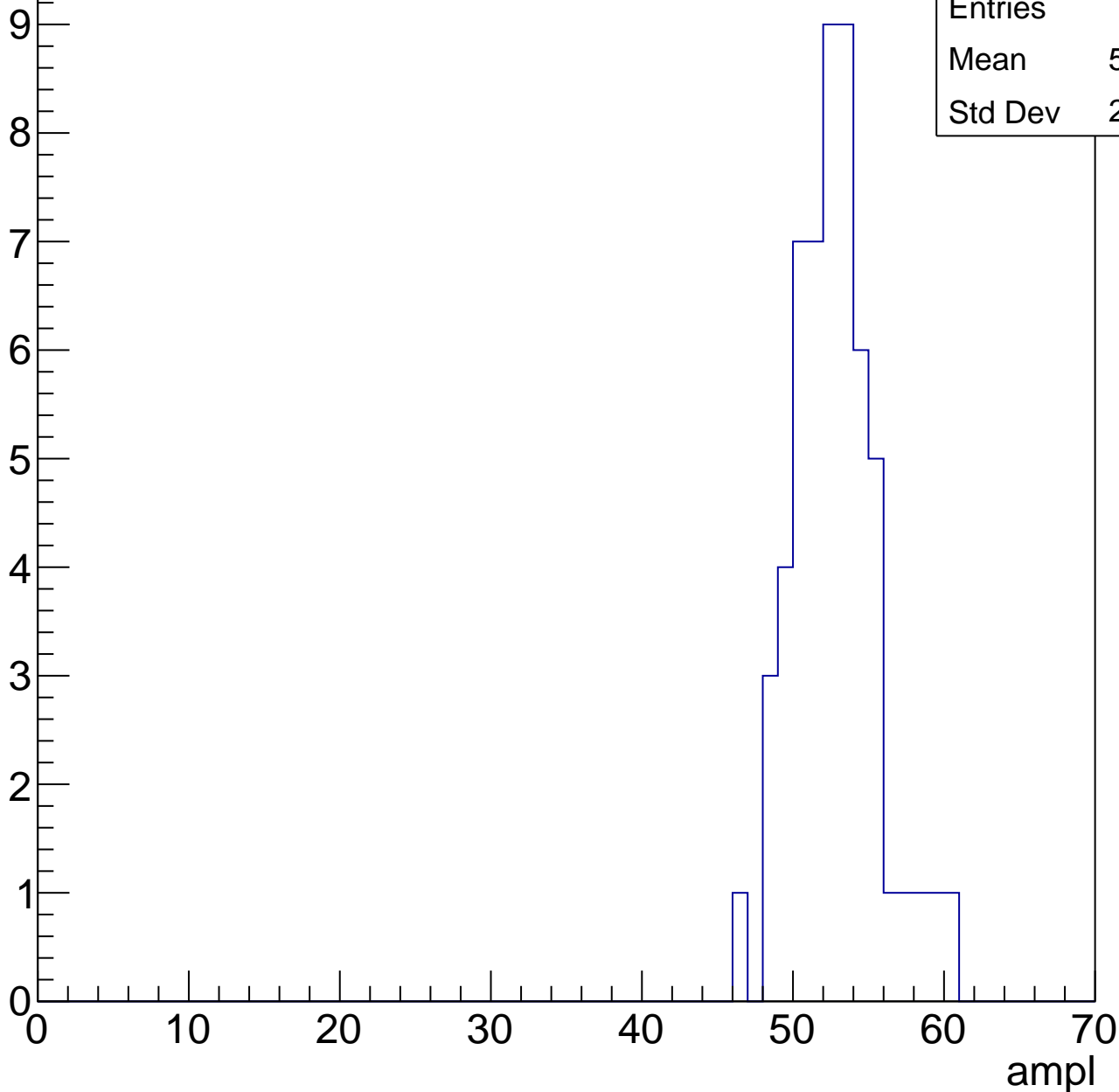


B1L103S, U1-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

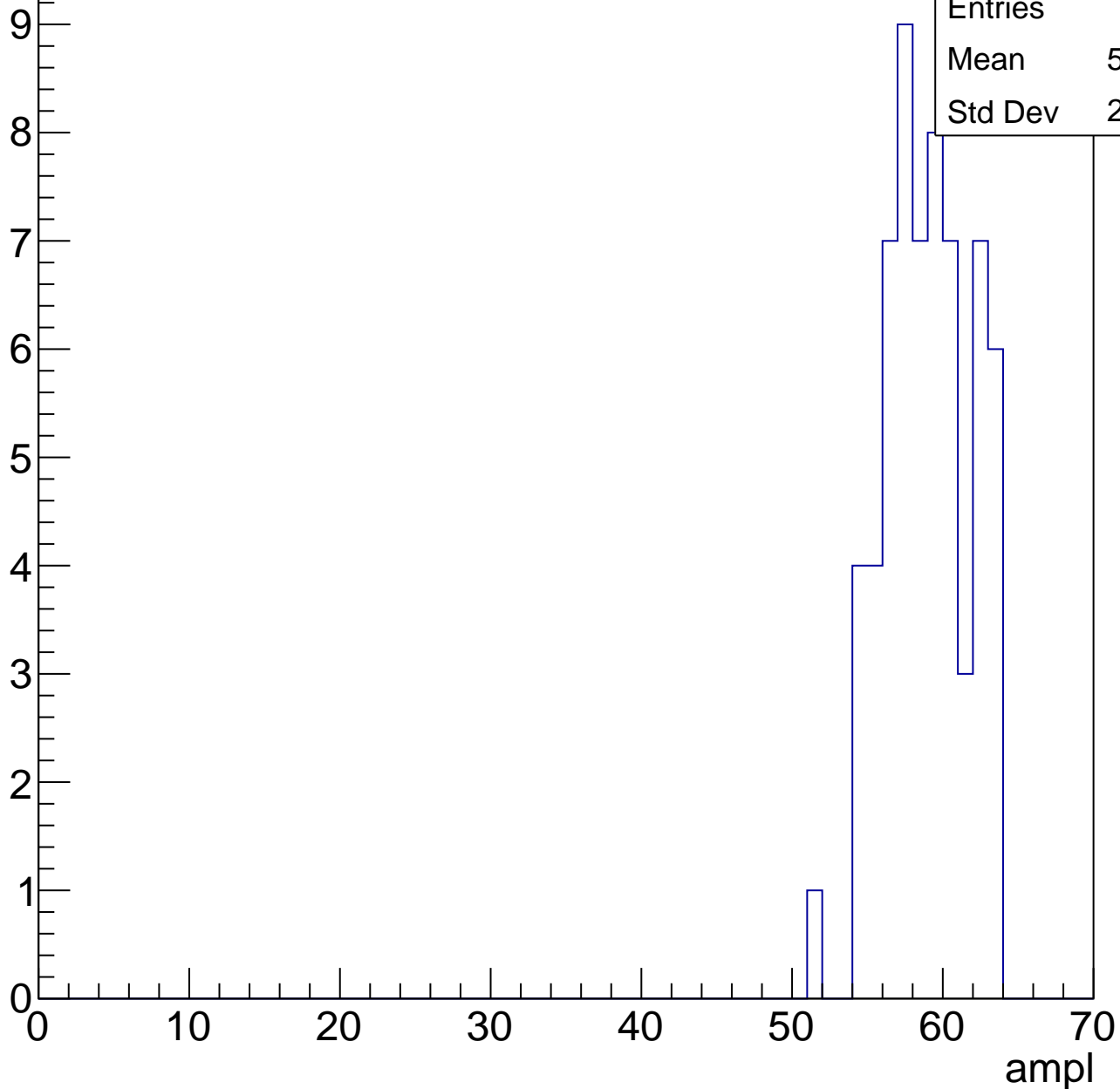
Entries	56
Mean	52.27
Std Dev	2.735



B1L103S, U1-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

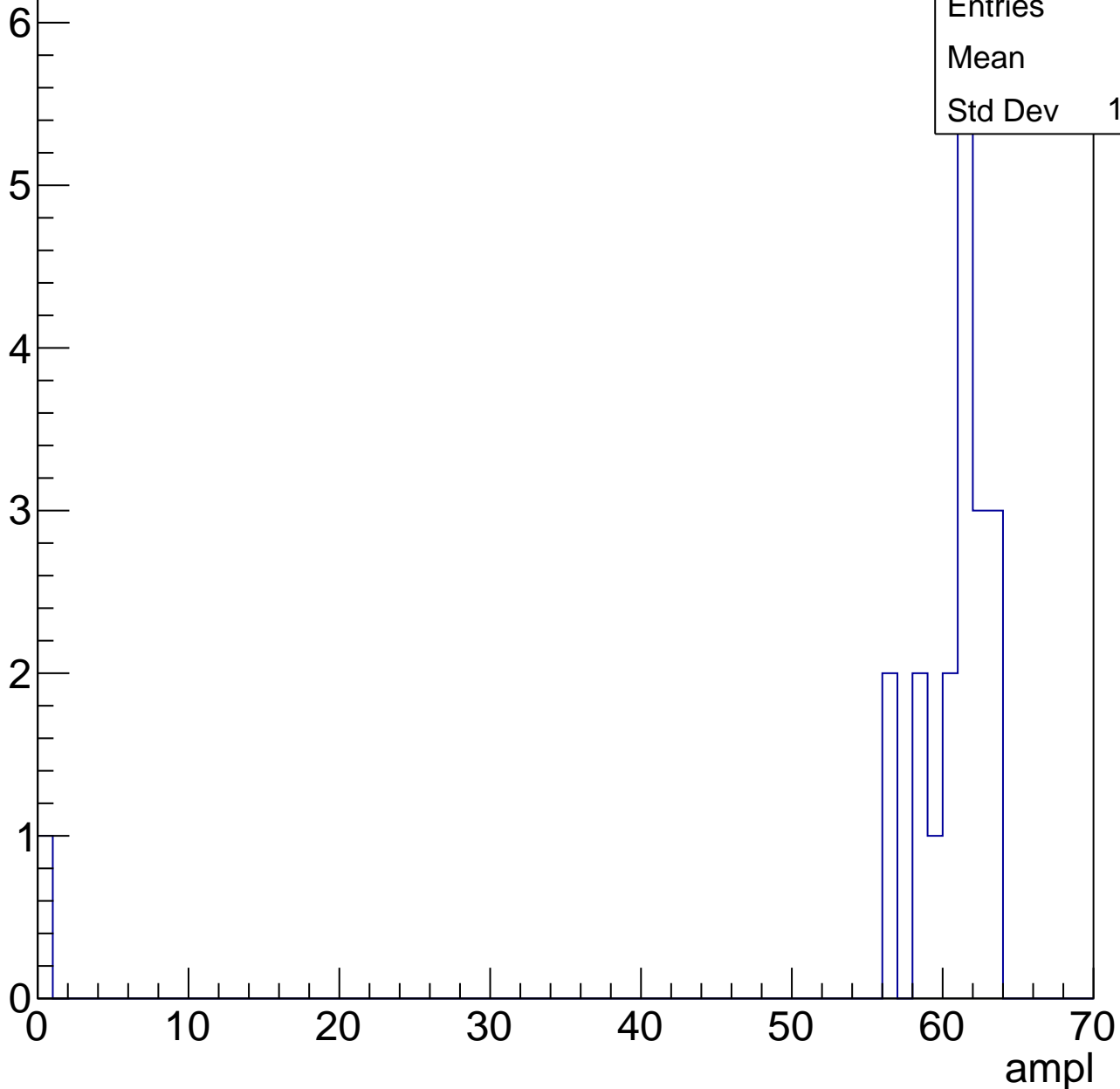


B1L103S, U1-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	57.4
Std Dev	13.32



B1L103S, U1-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



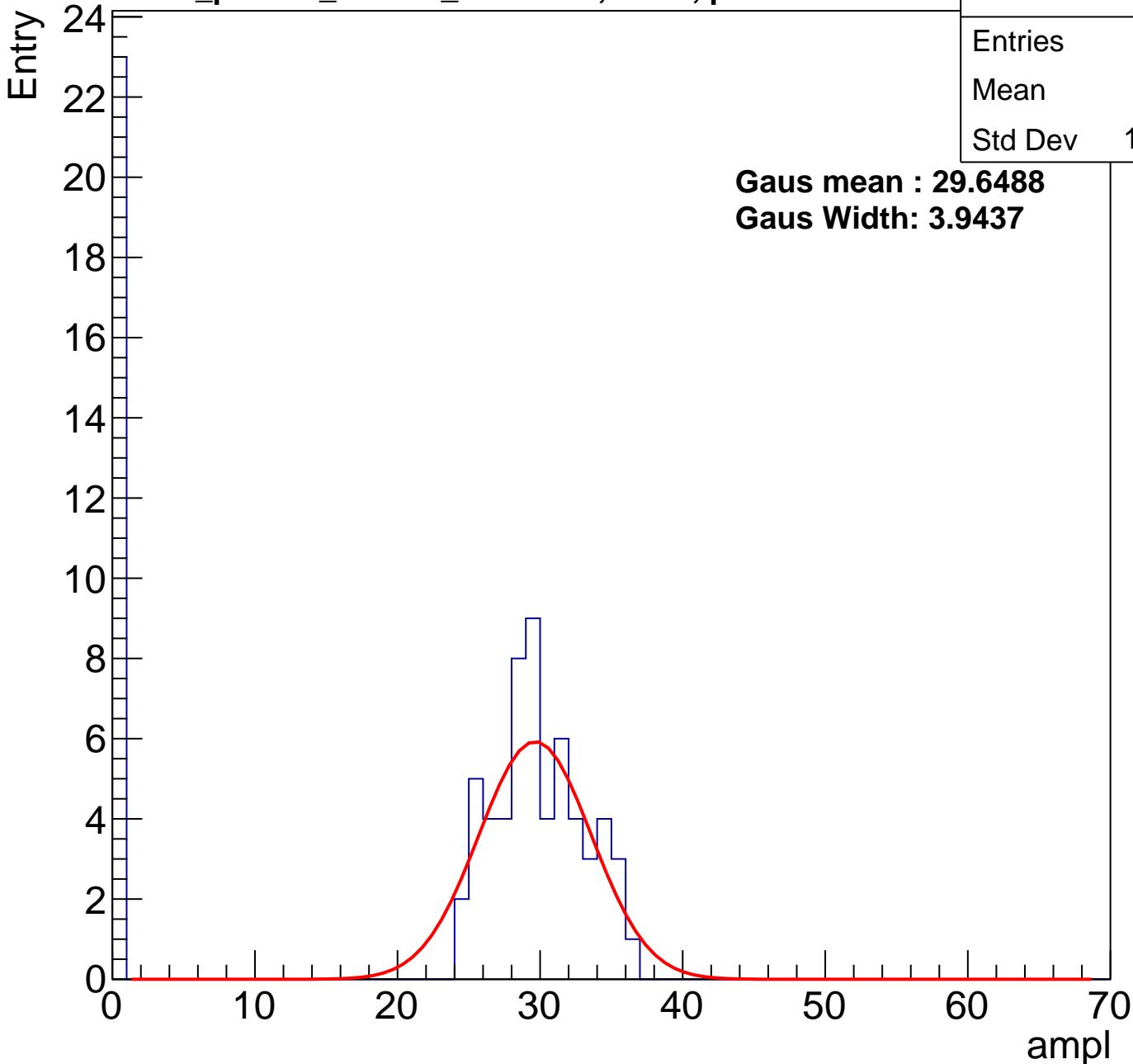
B1L103S, U1-ch37, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21
Std Dev	13.59

Gaus mean : 29.6488

Gaus Width: 3.9437



B1L103S, U1-ch37, adc1

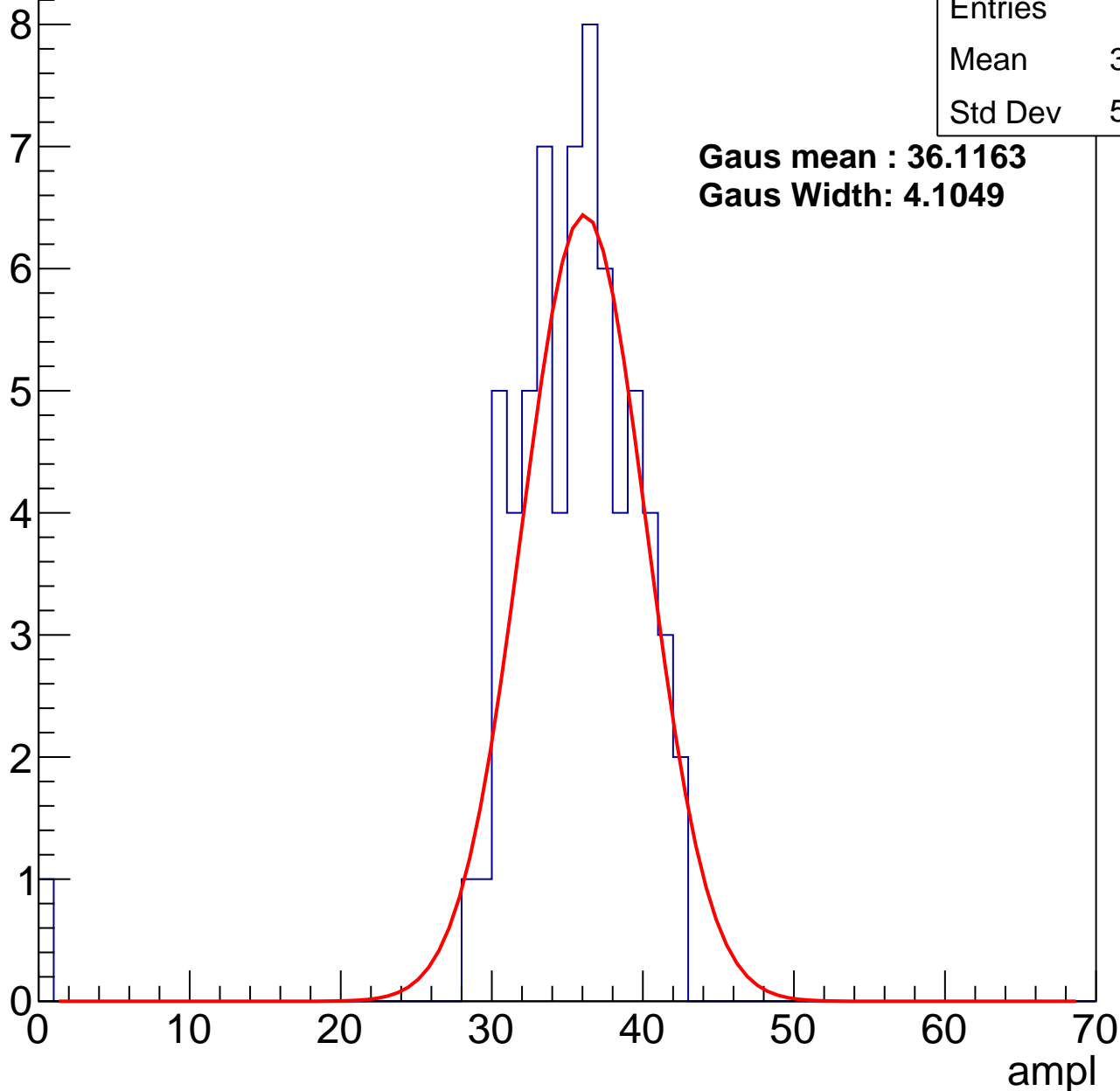
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	34.73
Std Dev	5.498

Gaus mean : 36.1163

Gaus Width: 4.1049



B1L103S, U1-ch37, adc2

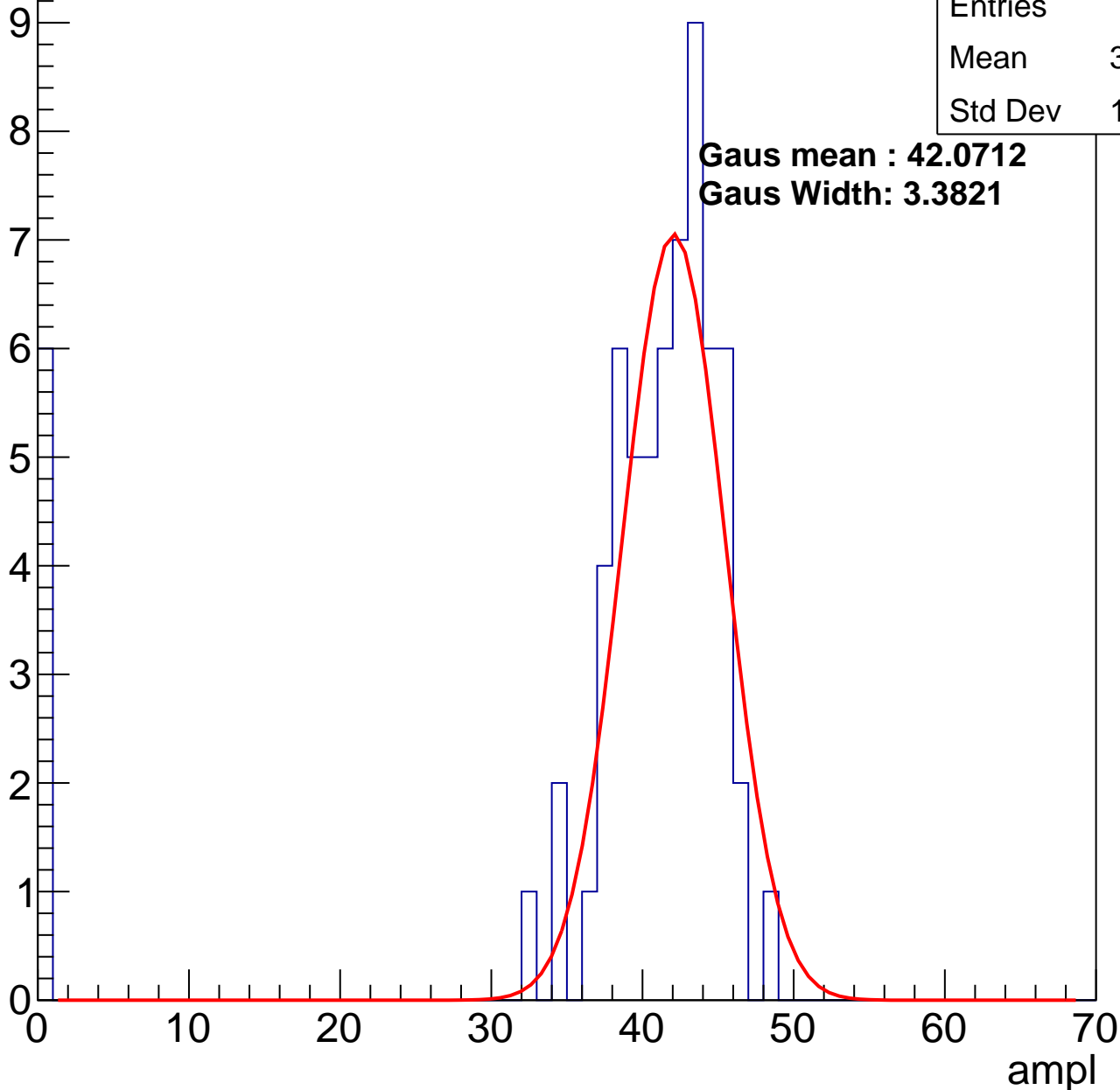
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.43
Std Dev	12.14

Gaus mean : 42.0712

Gaus Width: 3.3821



B1L103S, U1-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	41.9
Std Dev	16.78

Entry

10

8

6

4

2

0

0

10

20

30

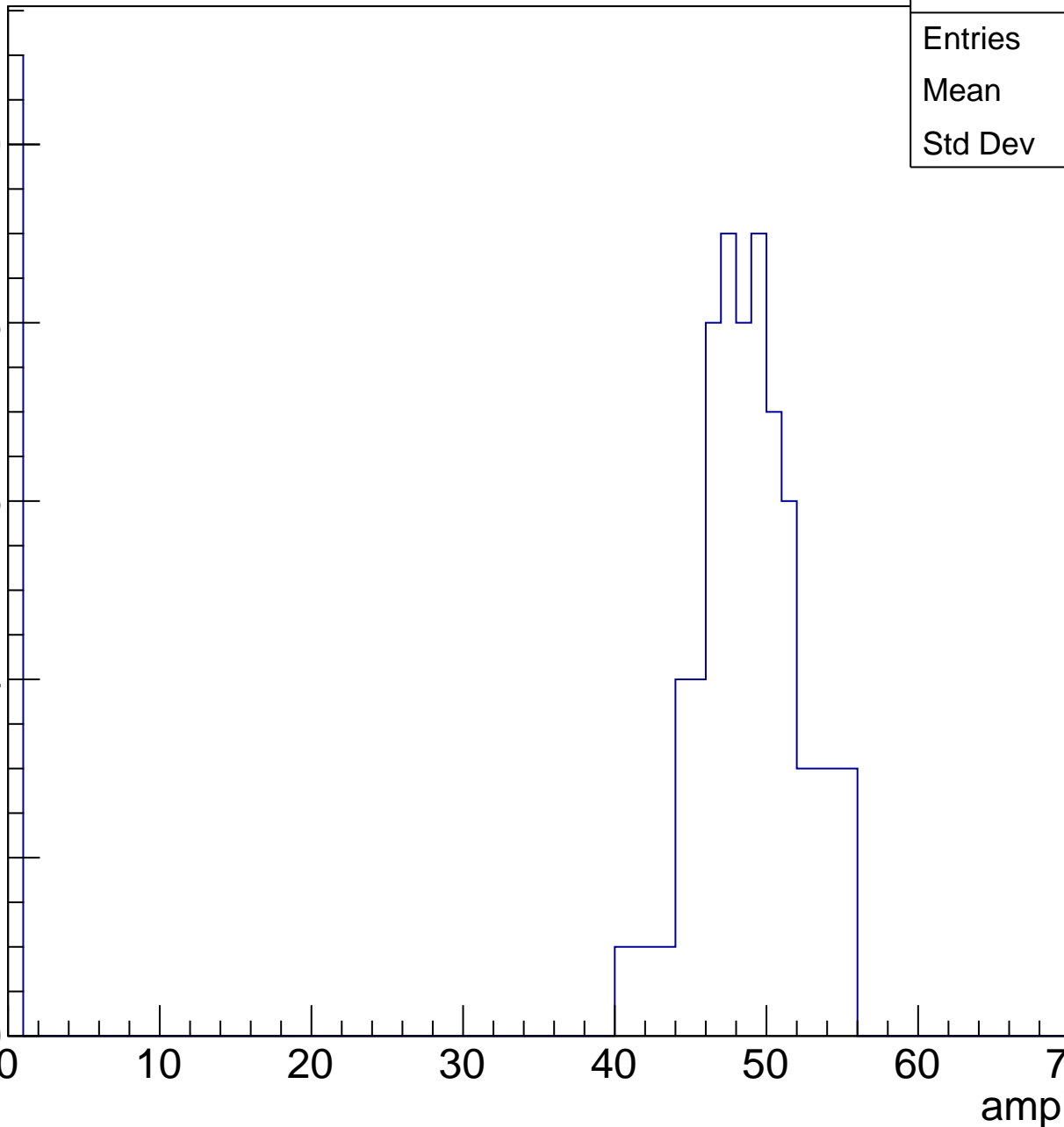
40

50

60

70

ampl

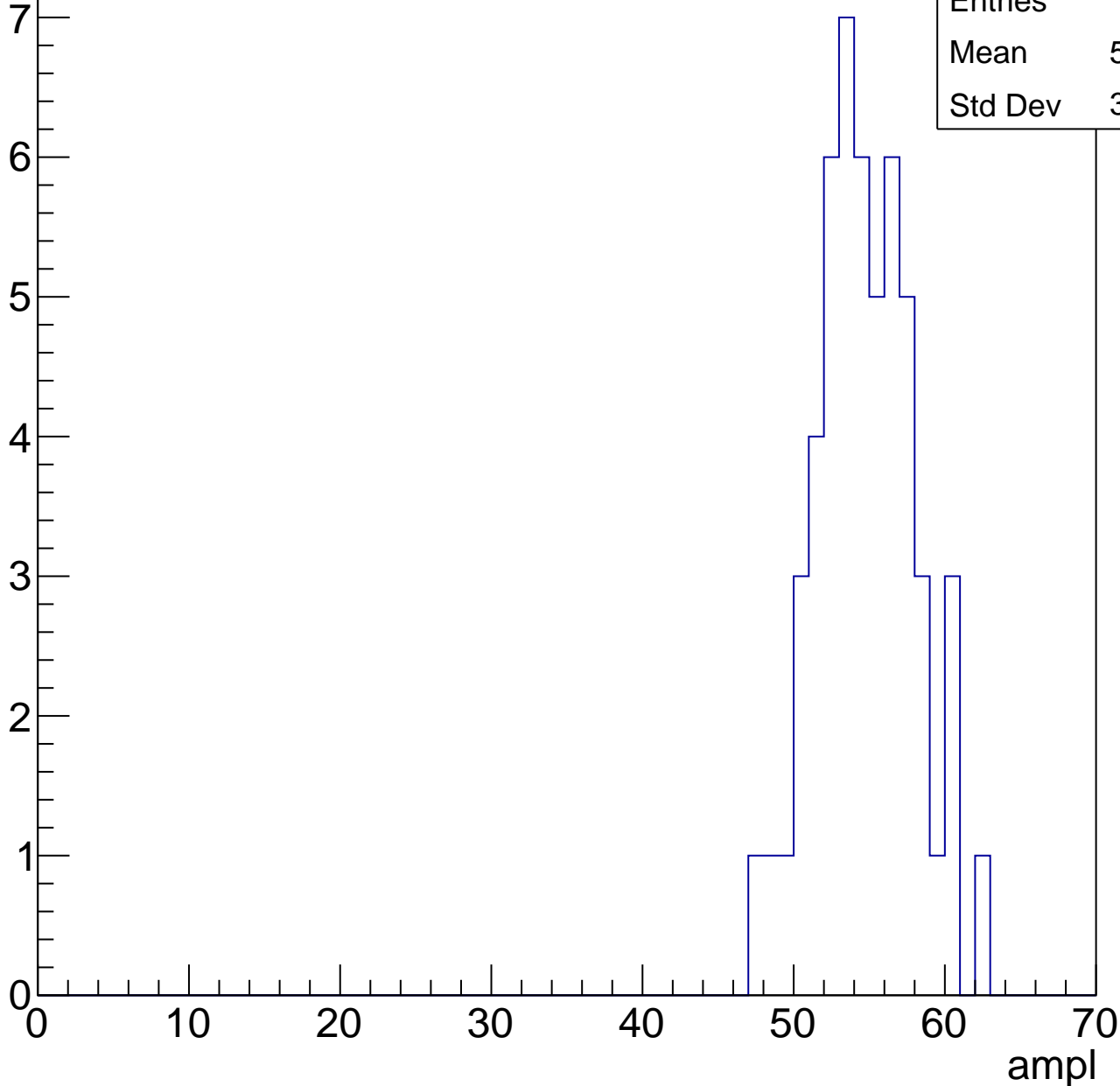


B1L103S, U1-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	54.26
Std Dev	3.193

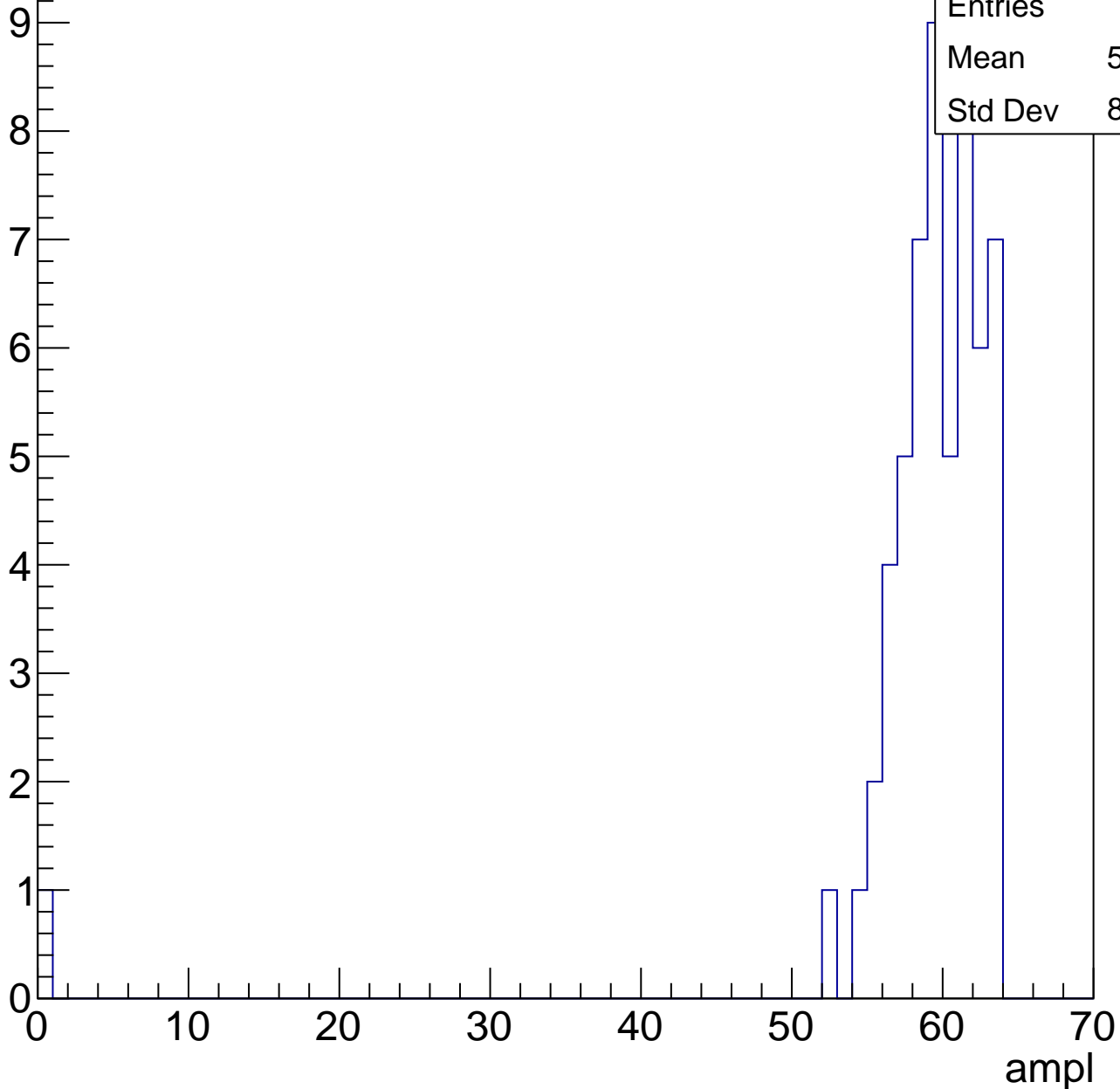


B1L103S, U1-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

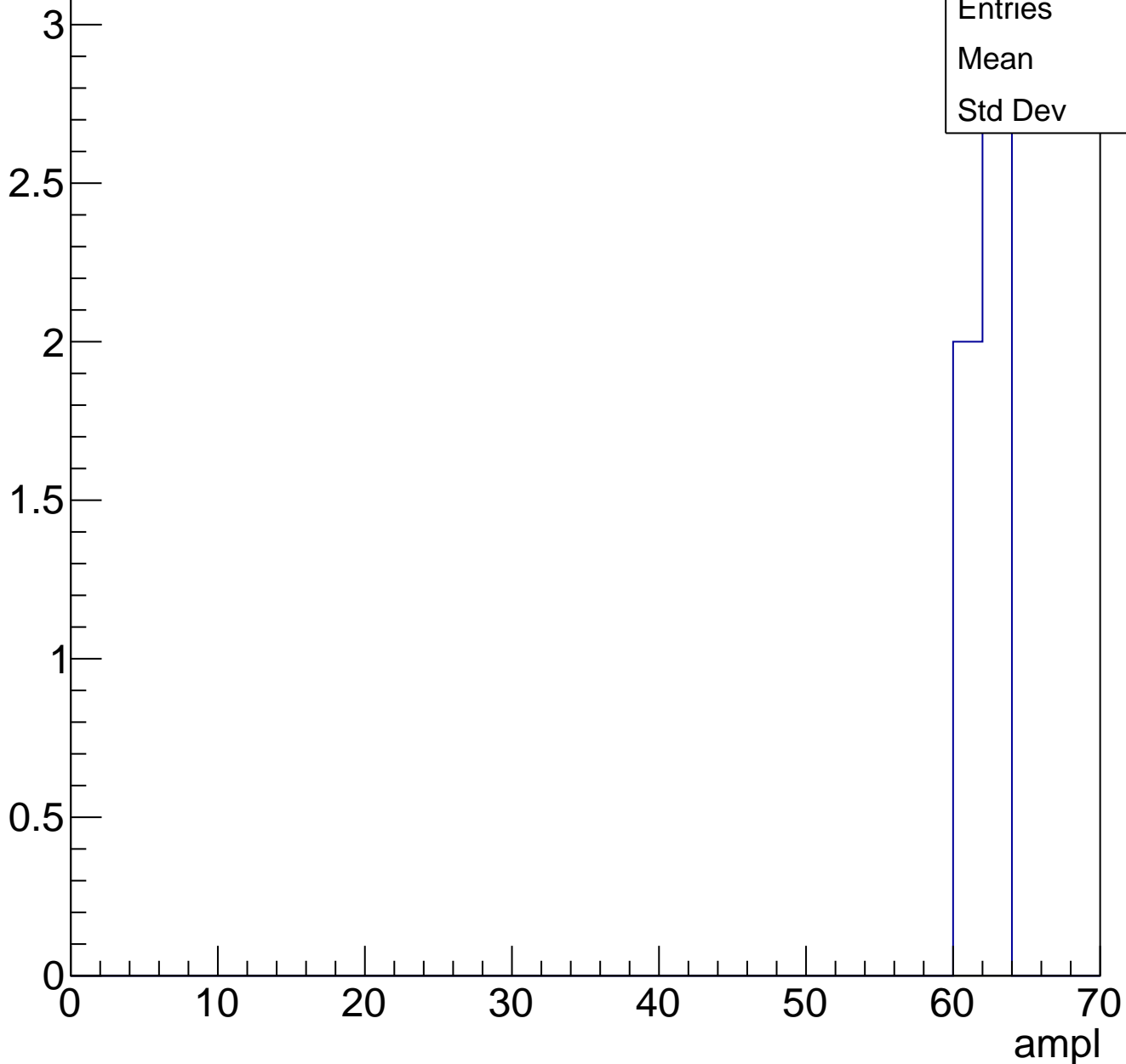
Entries	56
Mean	58.27
Std Dev	8.262



B1L103S, U1-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch38, adc0

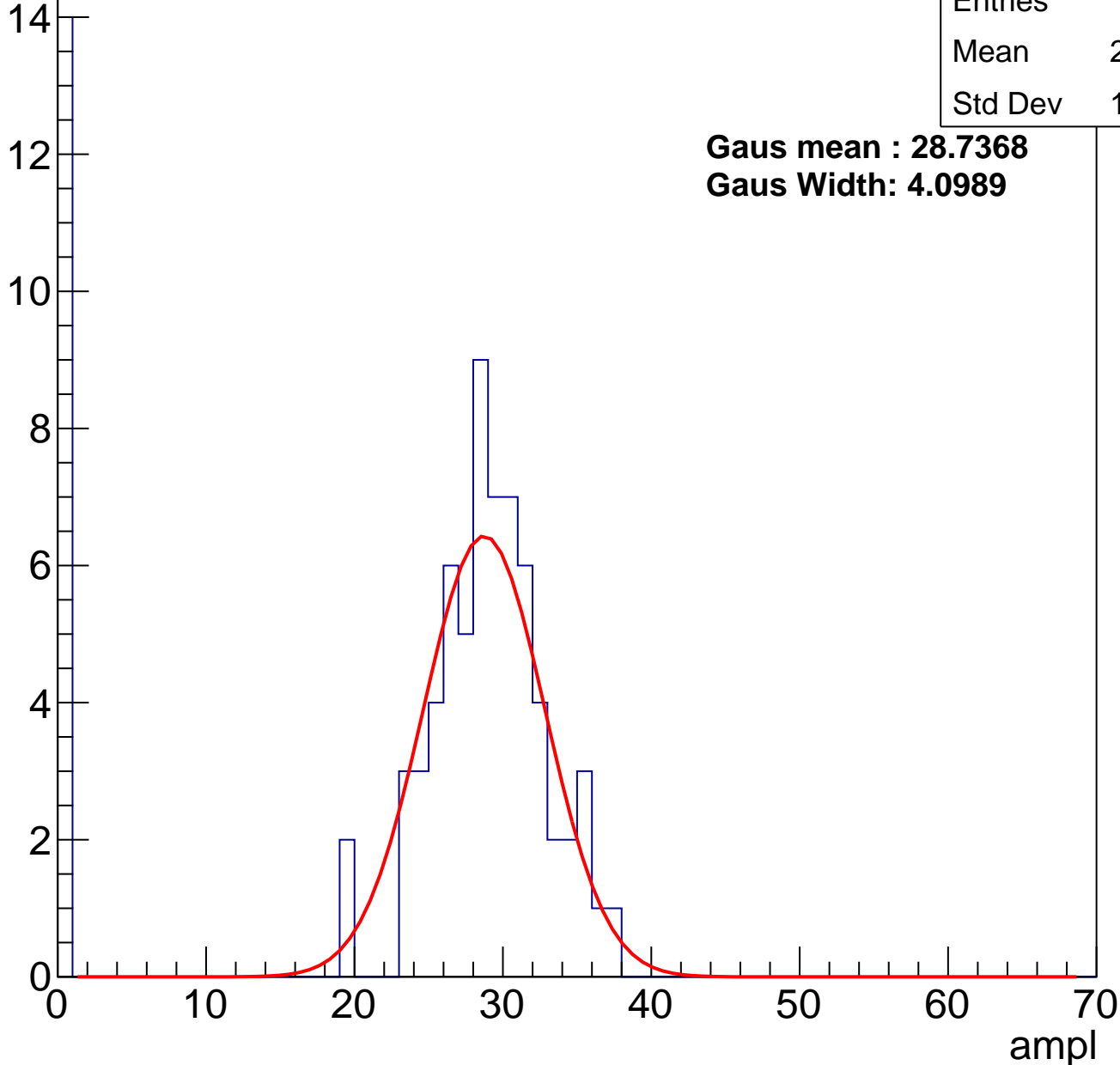
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.56
Std Dev	11.44

Gaus mean : 28.7368

Gaus Width: 4.0989

Entry



B1L103S, U1-ch38, adc1

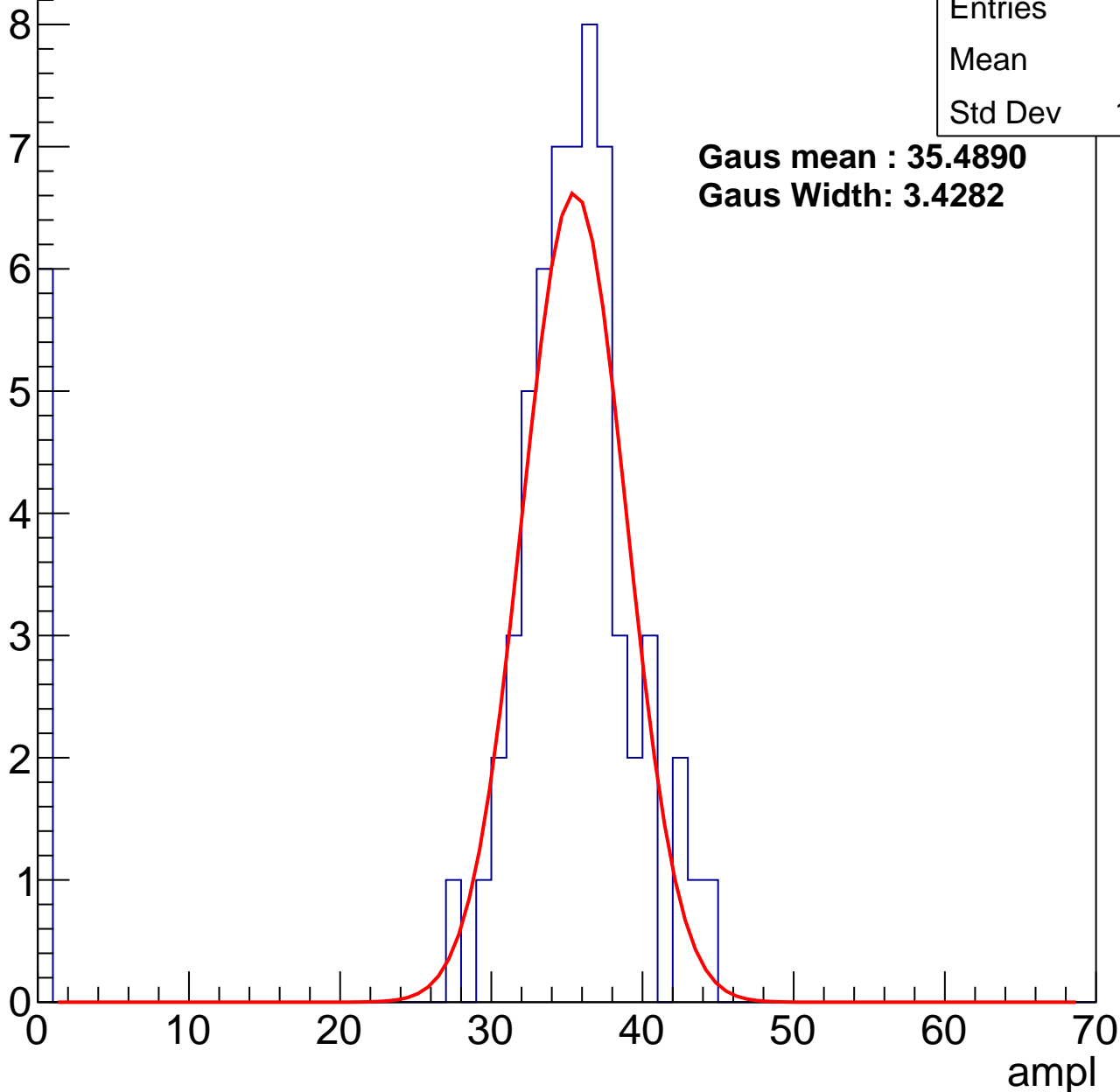
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	32
Std Dev	10.71

Gaus mean : 35.4890

Gaus Width: 3.4282



B1L103S, U1-ch38, adc2

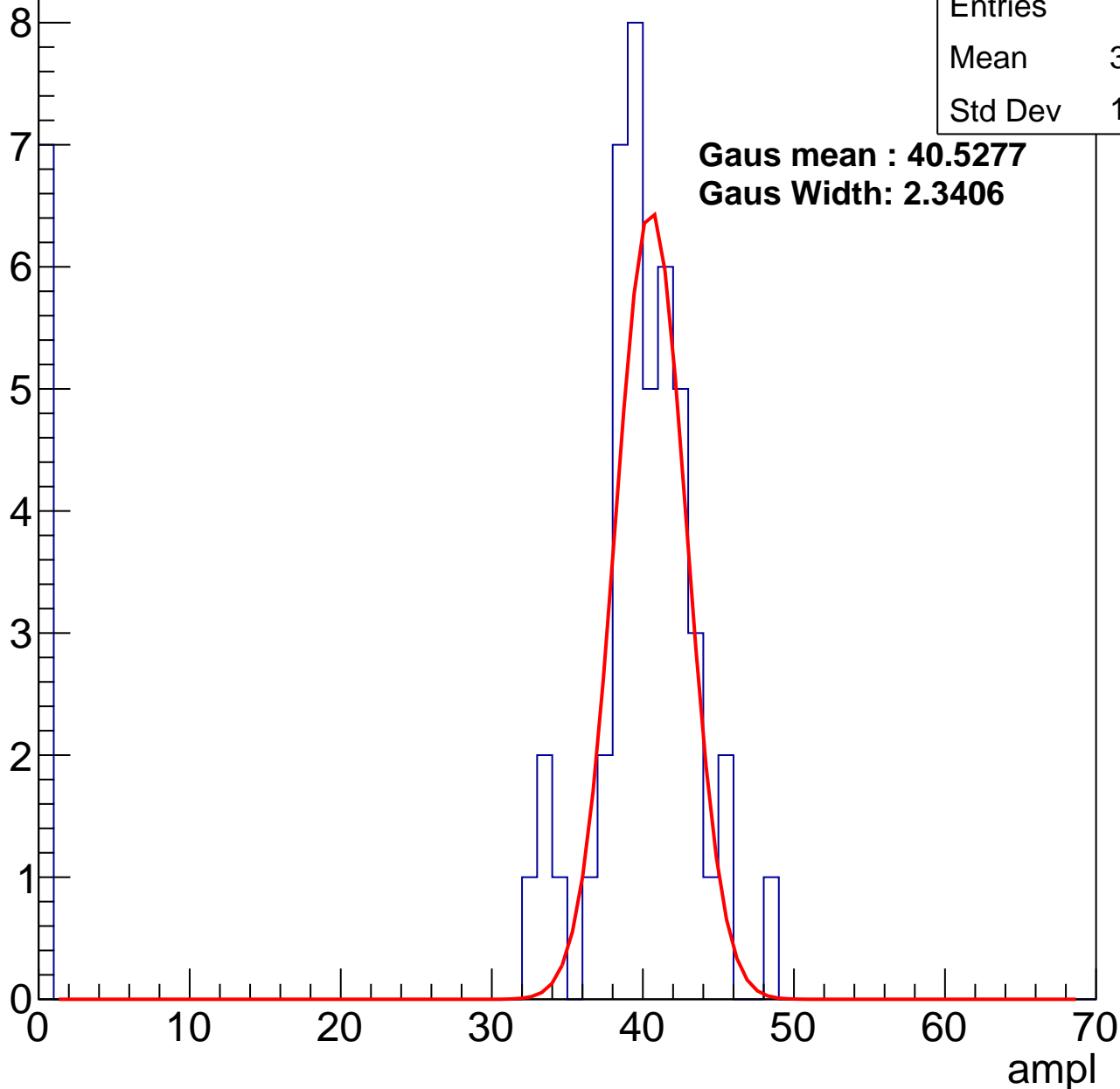
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	34.37
Std Dev	13.87

Gaus mean : 40.5277

Gaus Width: 2.3406



B1L103S, U1-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	45.66
Std Dev	8.057

Entry

10

8

6

4

2

0

0

10

20

30

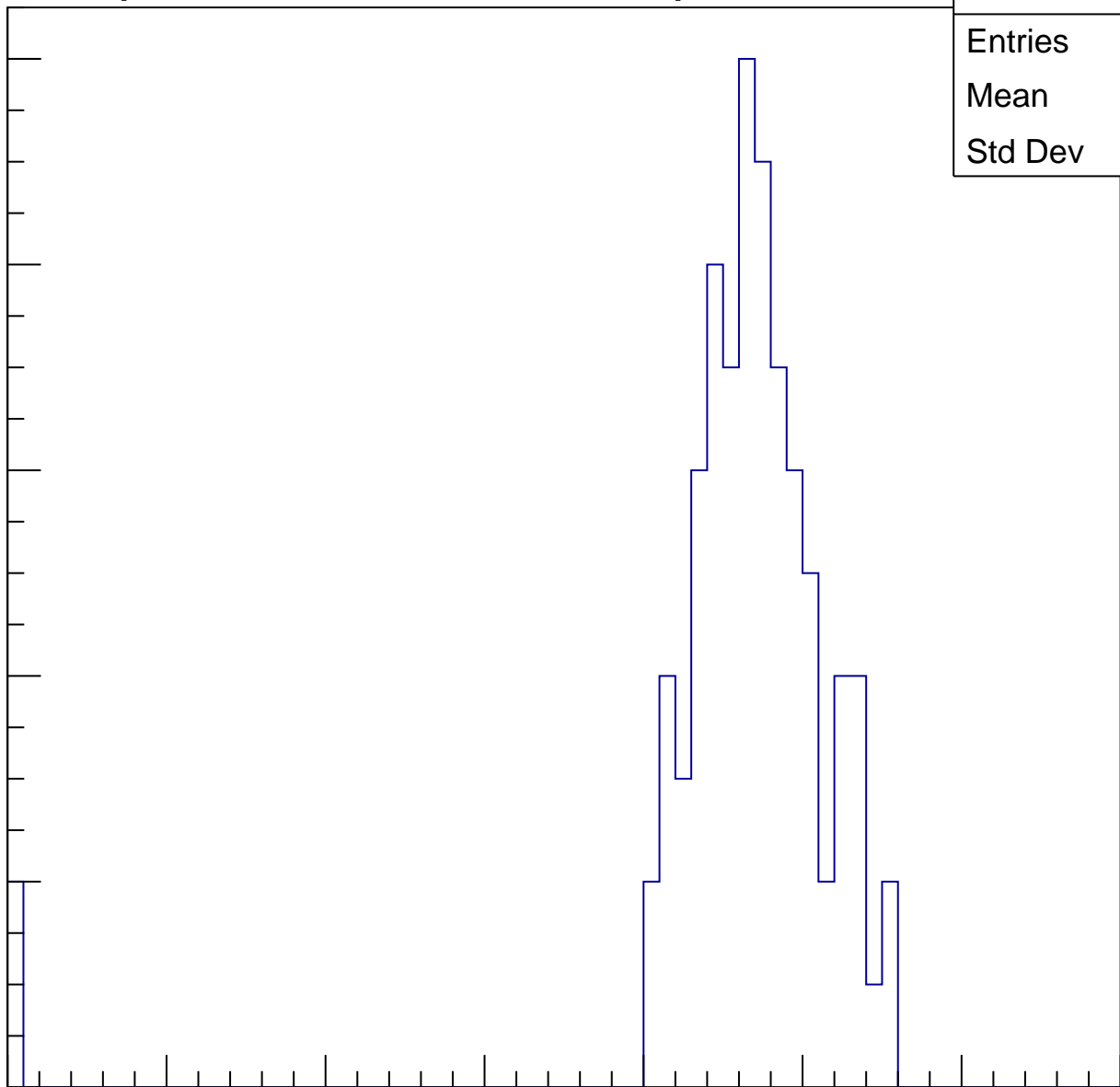
40

50

60

70

ampl

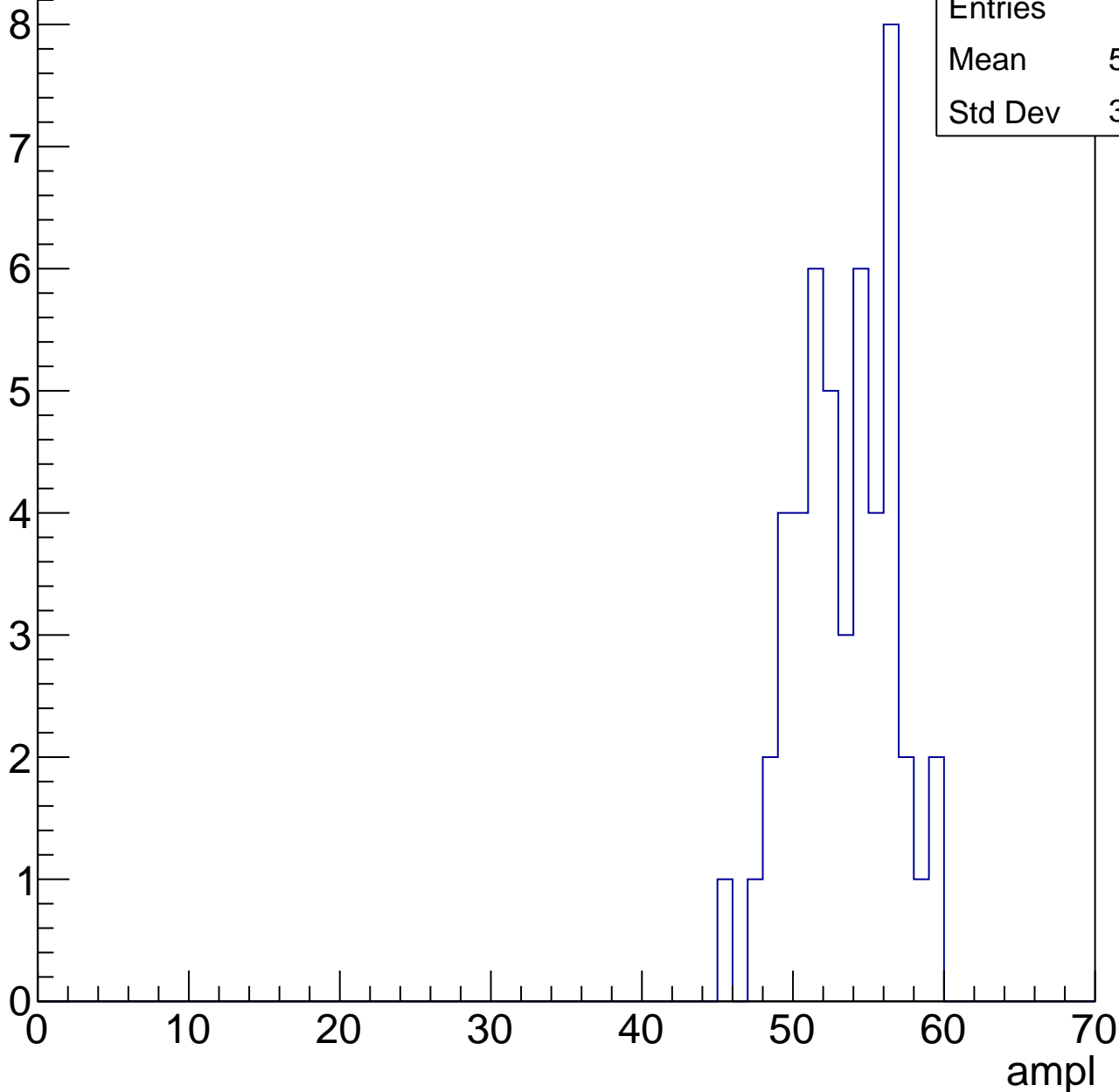


B1L103S, U1-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.88
Std Dev	3.198

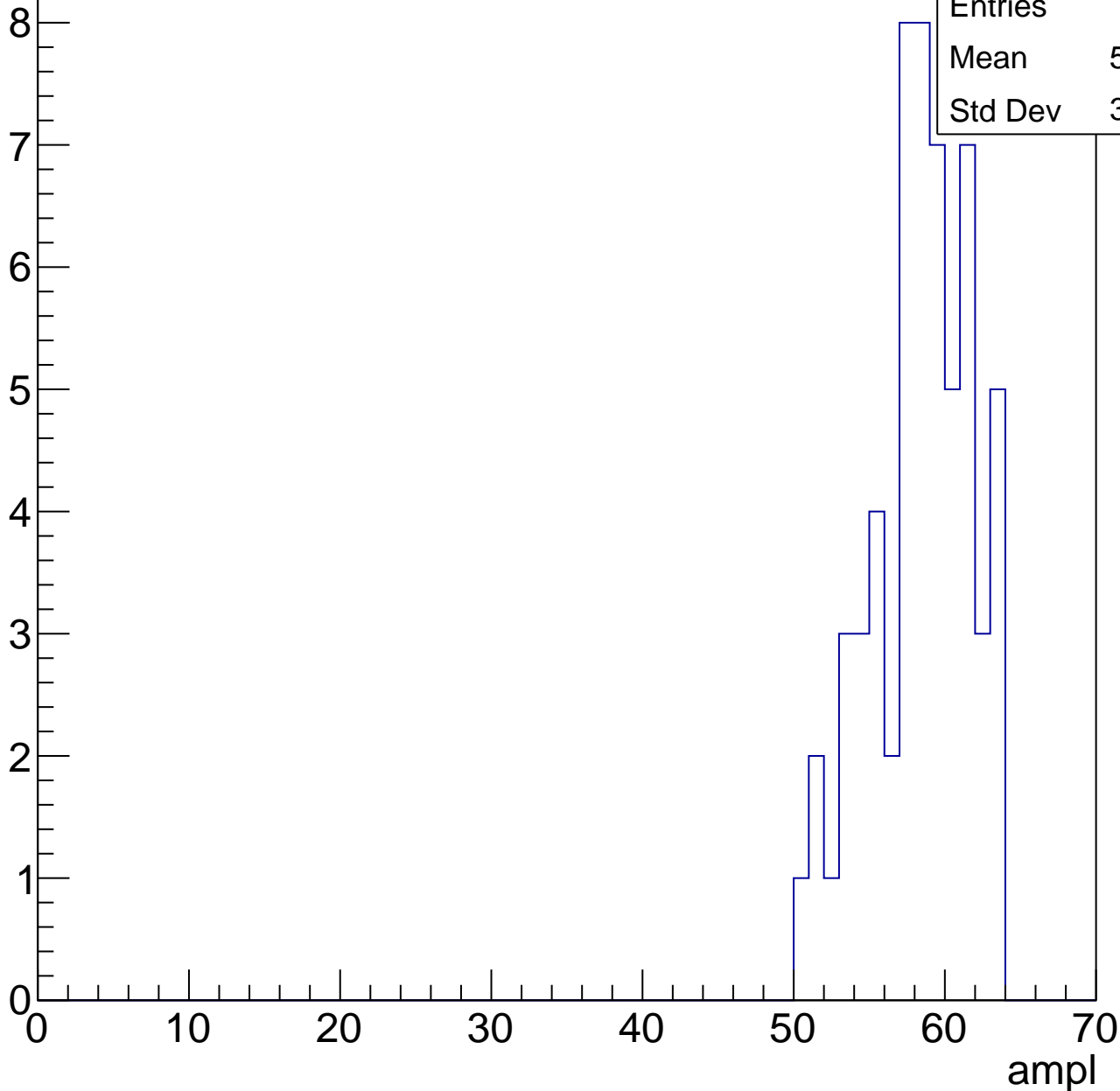


B1L103S, U1-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.93
Std Dev	3.283

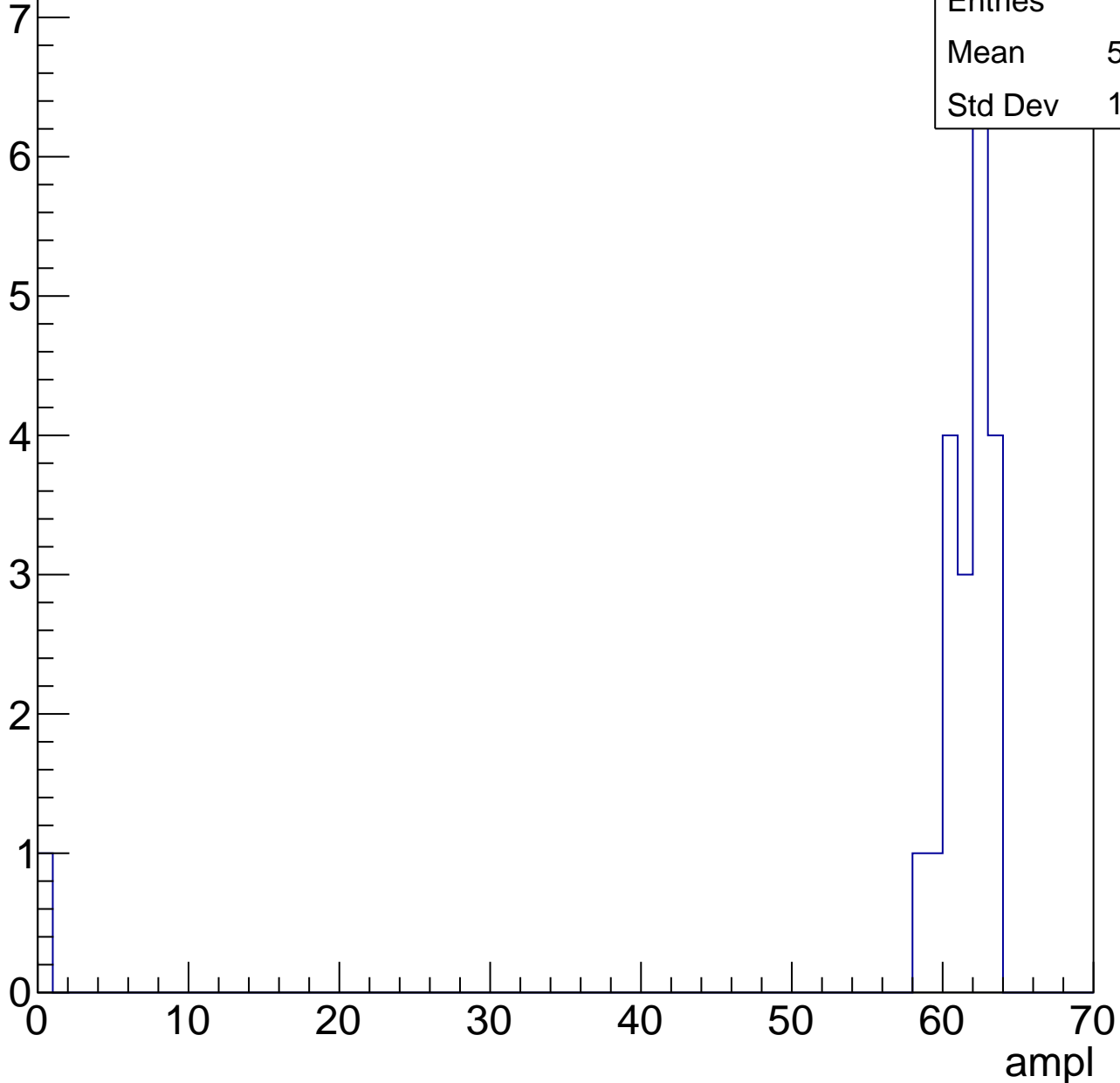


B1L103S, U1-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

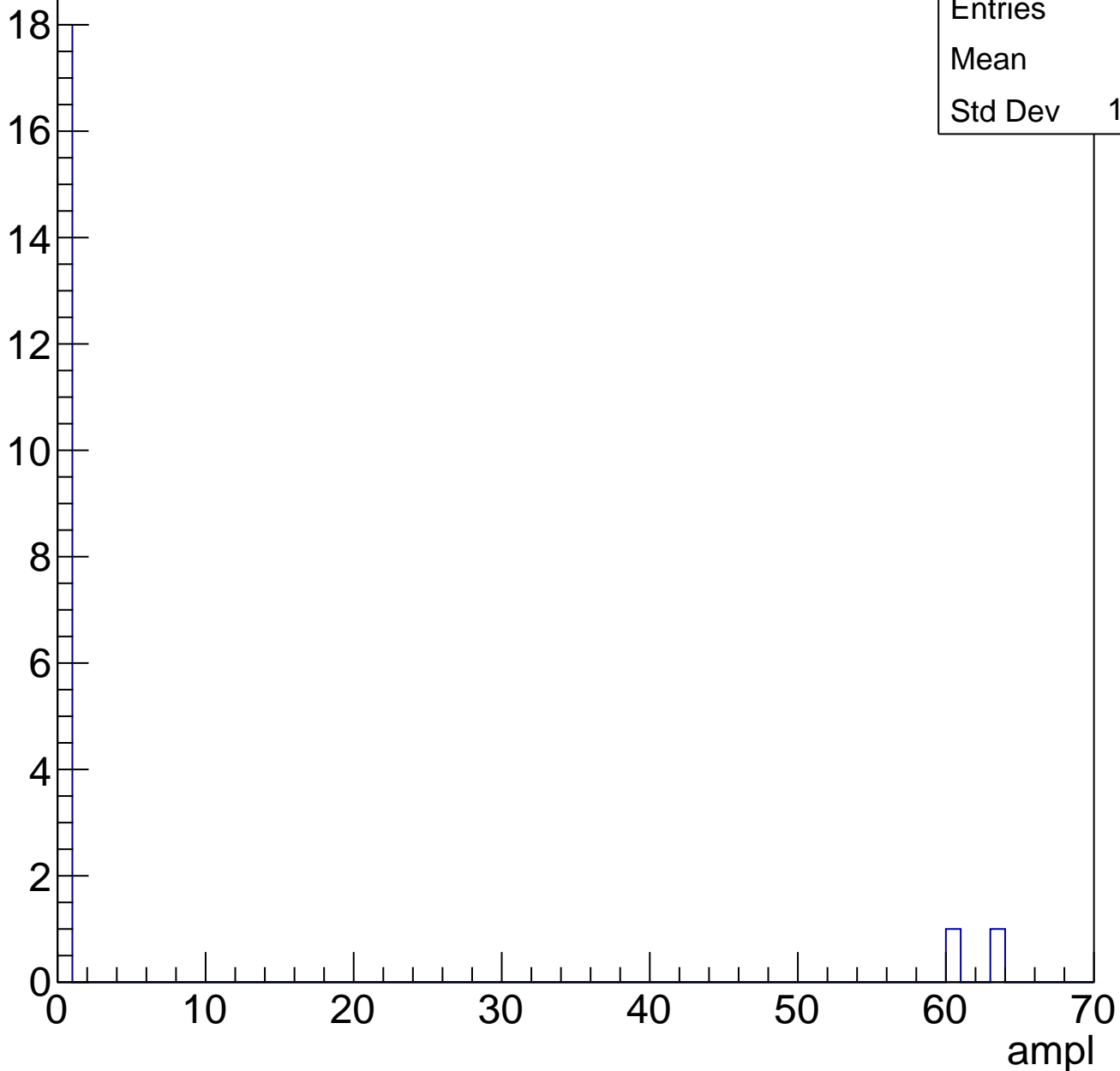
Entries	21
Mean	58.38
Std Dev	13.12



B1L103S, U1-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	6.15
Std Dev	18.46

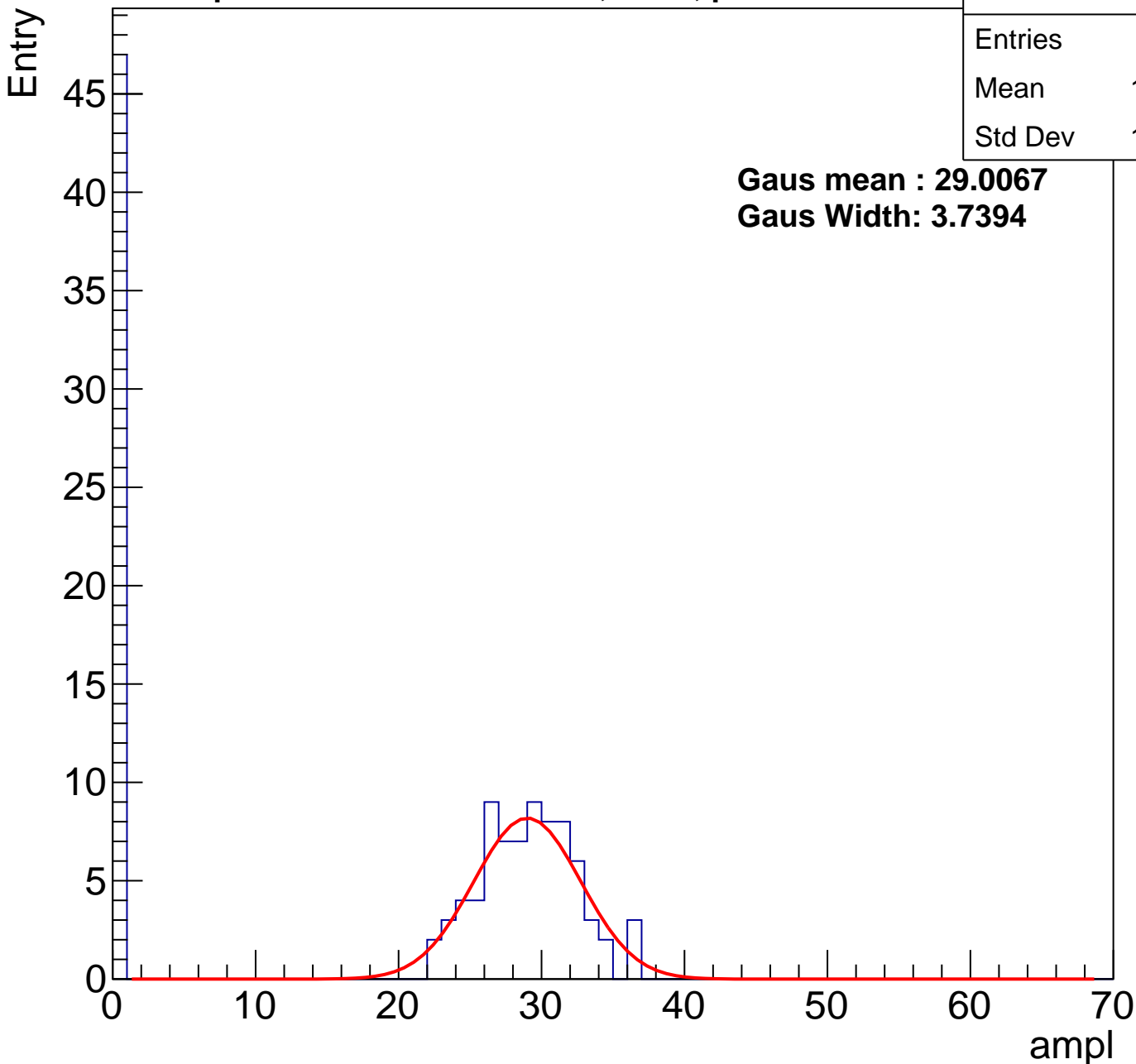
B1L103S, U1-ch39, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	122
Mean	17.57
Std Dev	14.15

Gaus mean : 29.0067

Gaus Width: 3.7394



B1L103S, U1-ch39, adc1

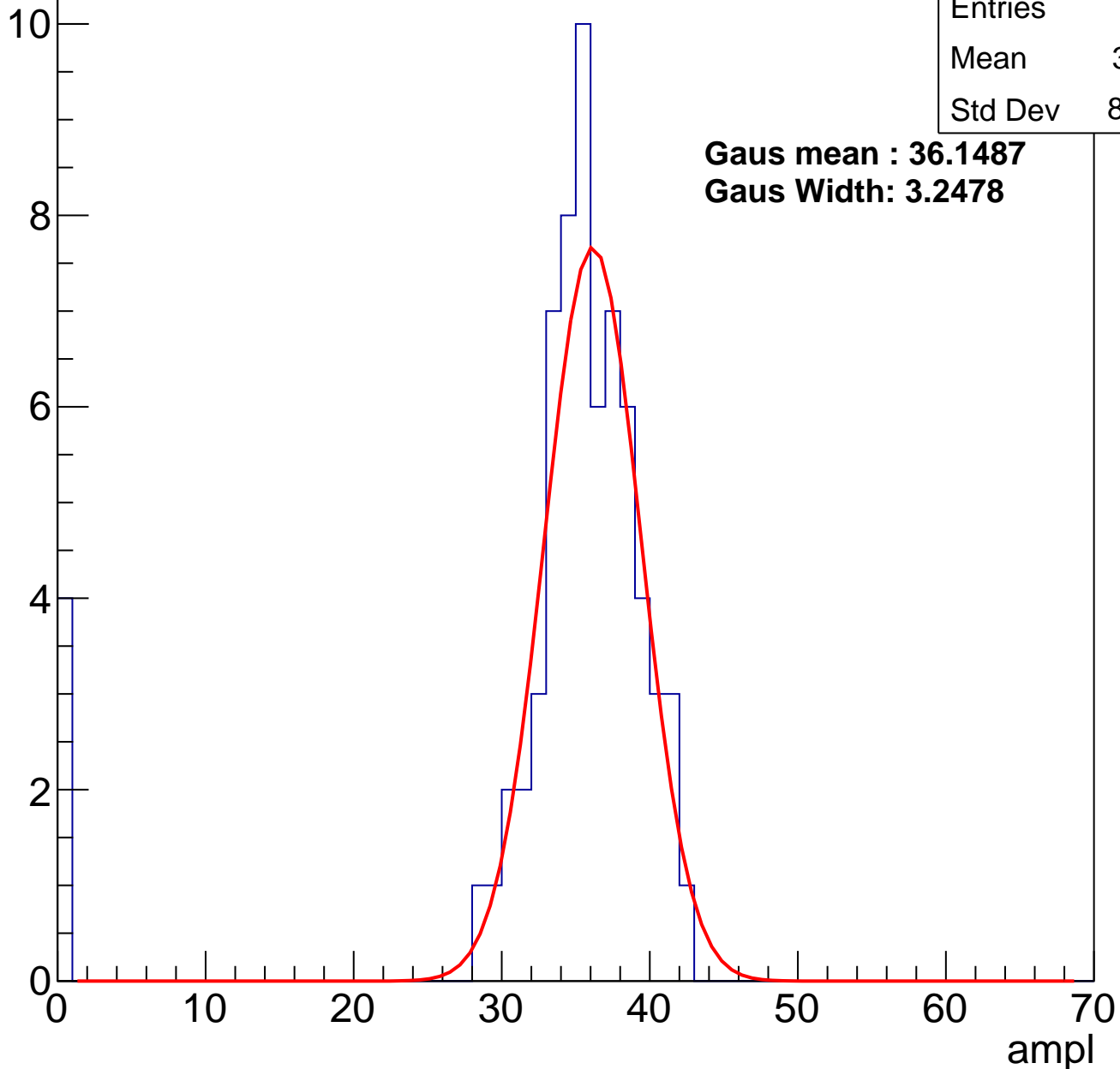
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	33.41
Std Dev	8.864

Gaus mean : 36.1487

Gaus Width: 3.2478

Entry



B1L103S, U1-ch39, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	34.97
Std Dev	15.47

Gaus mean : 42.5449

Gaus Width: 3.9026

Entry

10

8

6

4

2

0

0

10

20

30

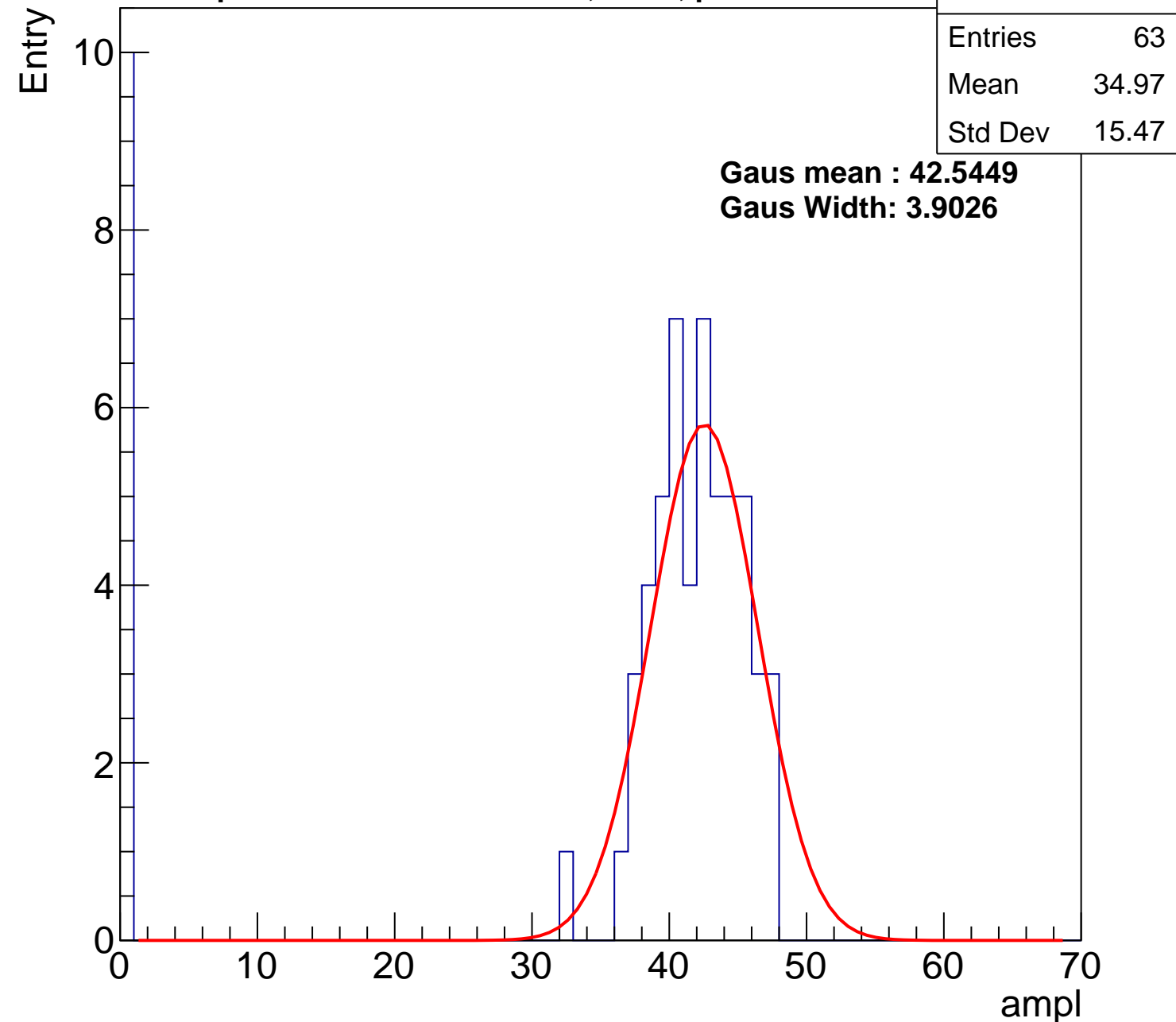
40

50

60

70

ampl

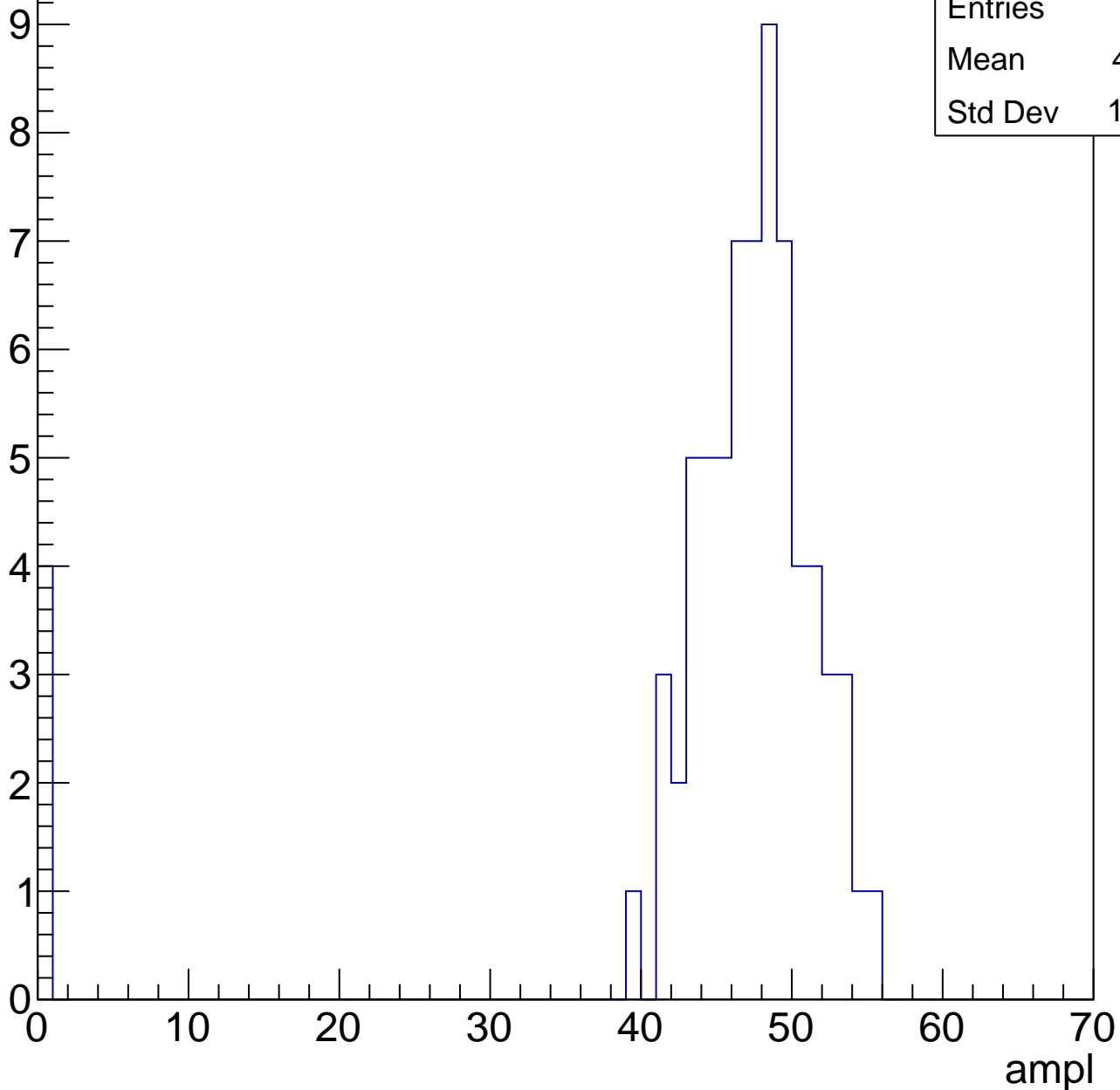


B1L103S, U1-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	44.51
Std Dev	11.38

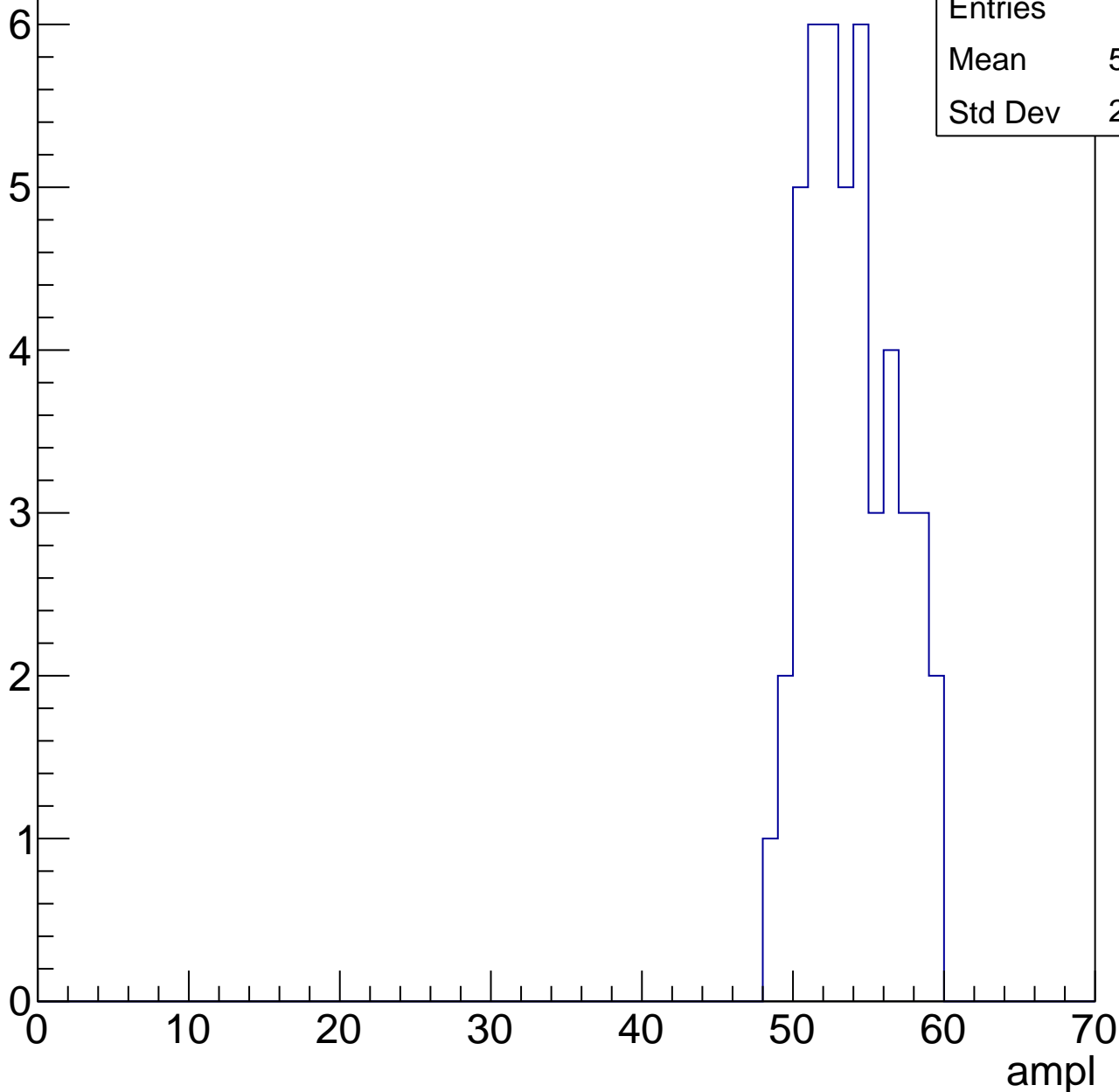


B1L103S, U1-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	53.37
Std Dev	2.854

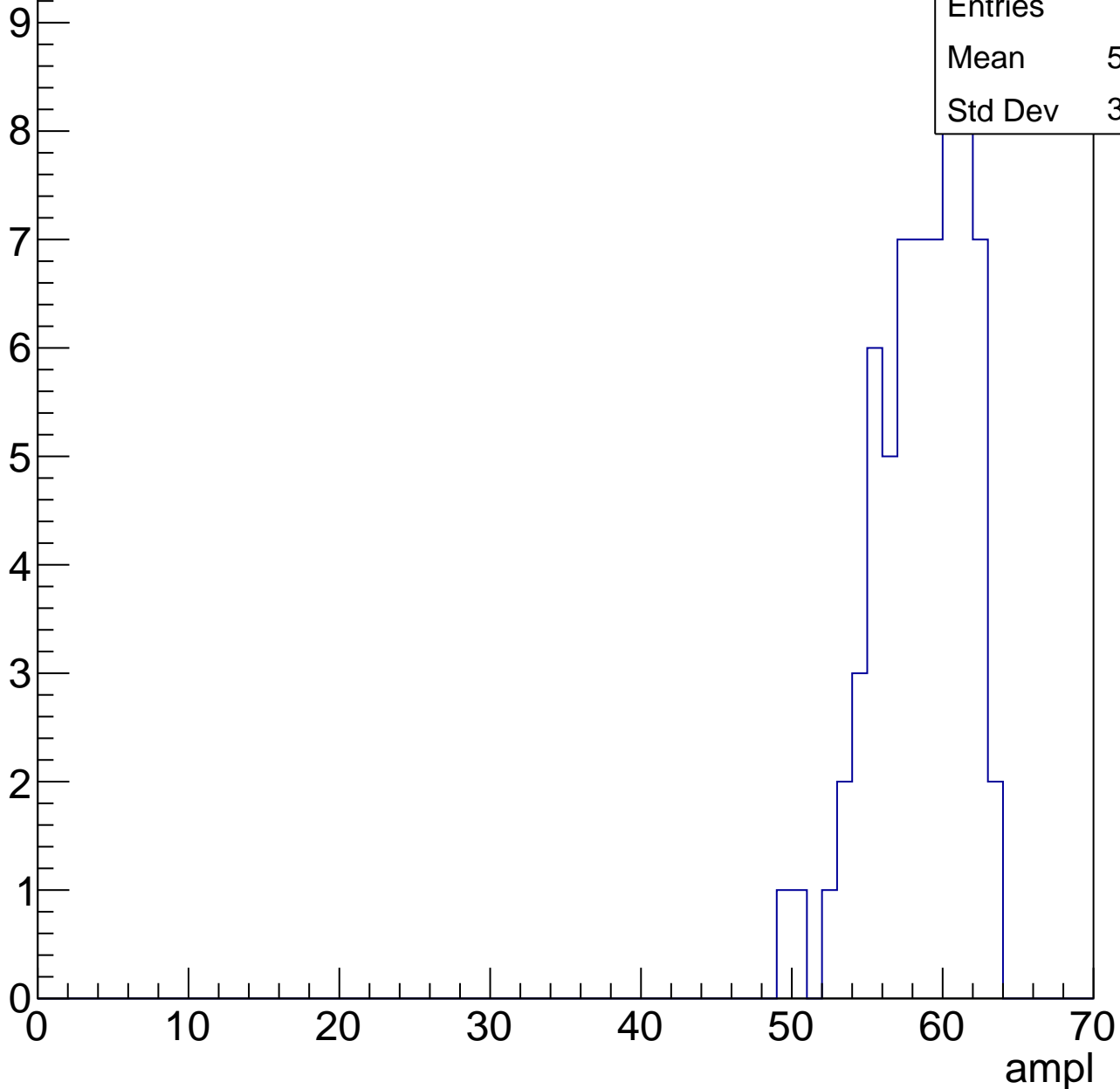


B1L103S, U1-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	58.12
Std Dev	3.117

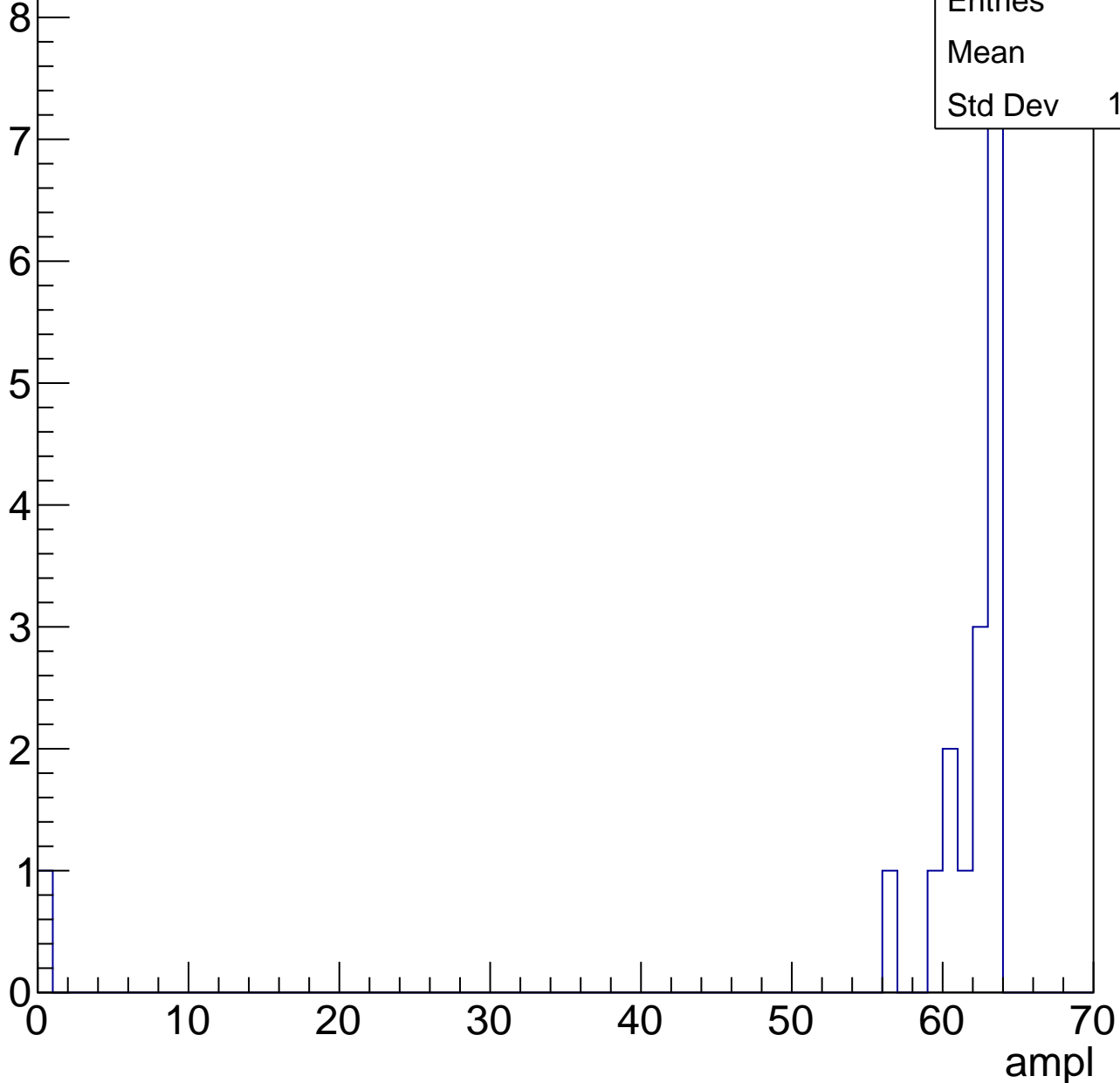


B1L103S, U1-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	58
Std Dev	14.62



B1L103S, U1-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch40, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	18.34
Std Dev	15.32

Gaus mean : 27.4490

Gaus Width: 3.8673

Entry

25

20

15

10

5

0

0

10

20

30

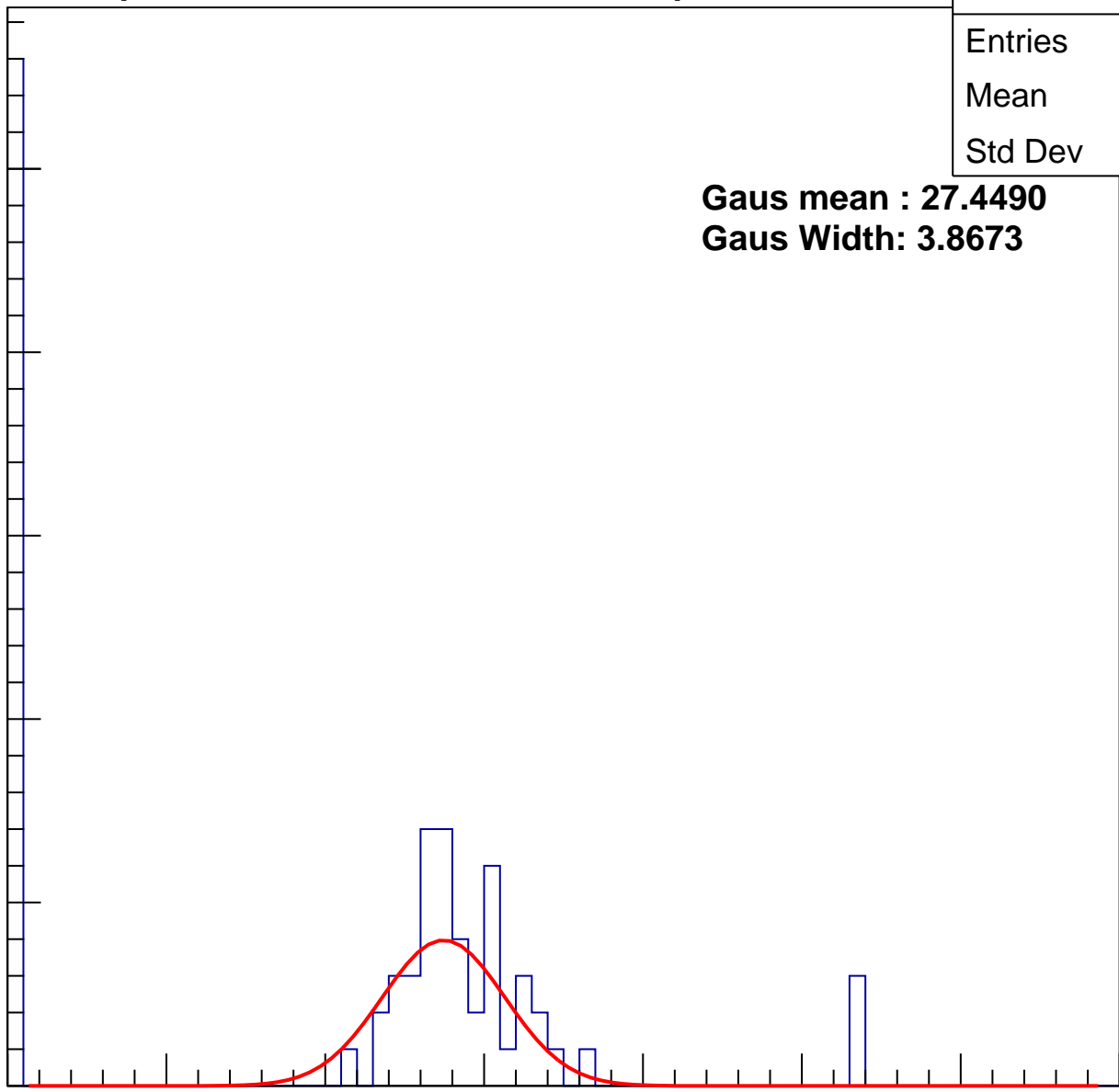
40

50

60

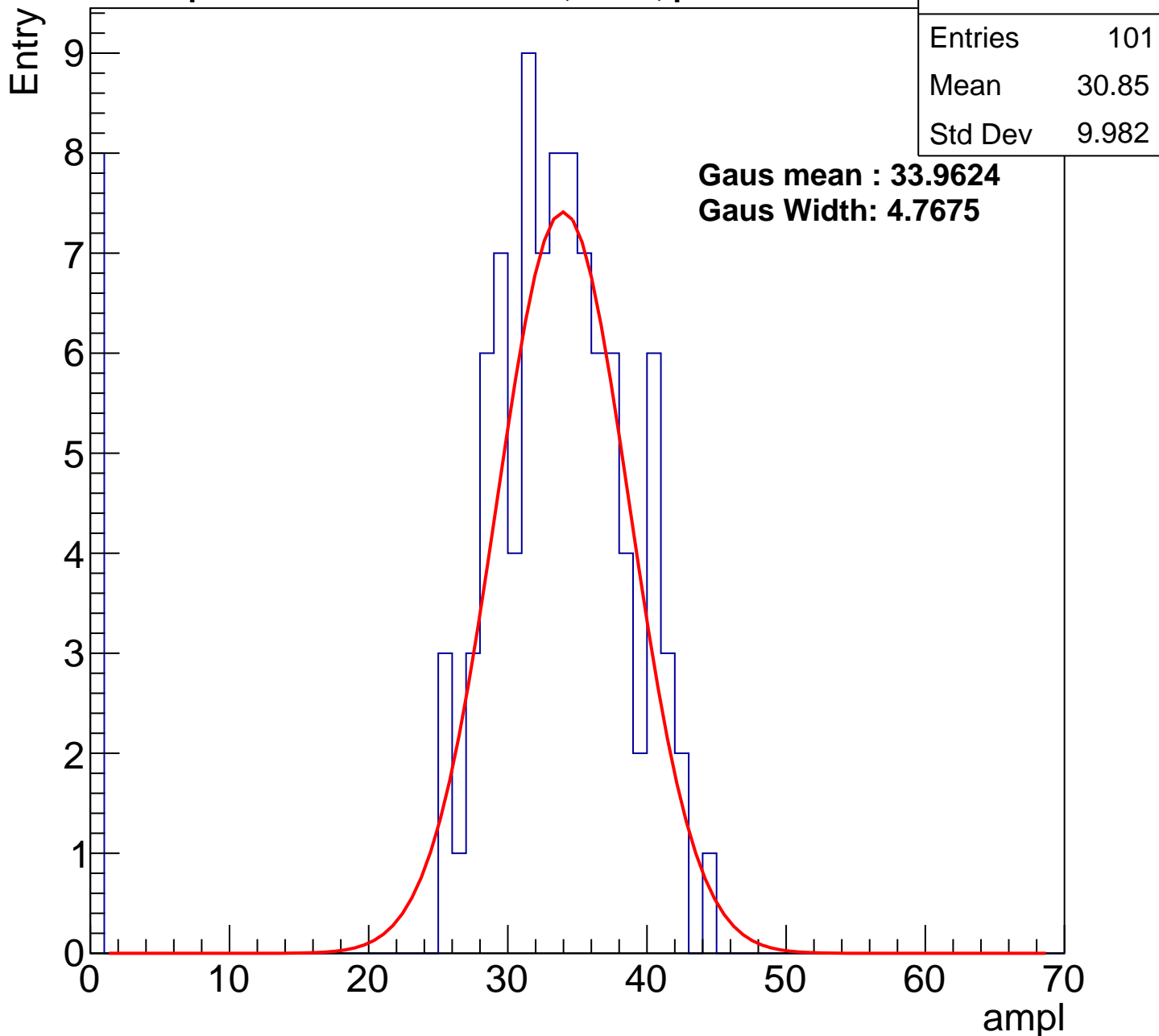
70

ampl



B1L103S, U1-ch40, adc1

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch40, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	60
Mean	31.25
Std Dev	16.65

Gaus mean : 40.2152

Gaus Width: 3.3978

Entry

12

10

8

6

4

2

0

0

10

20

30

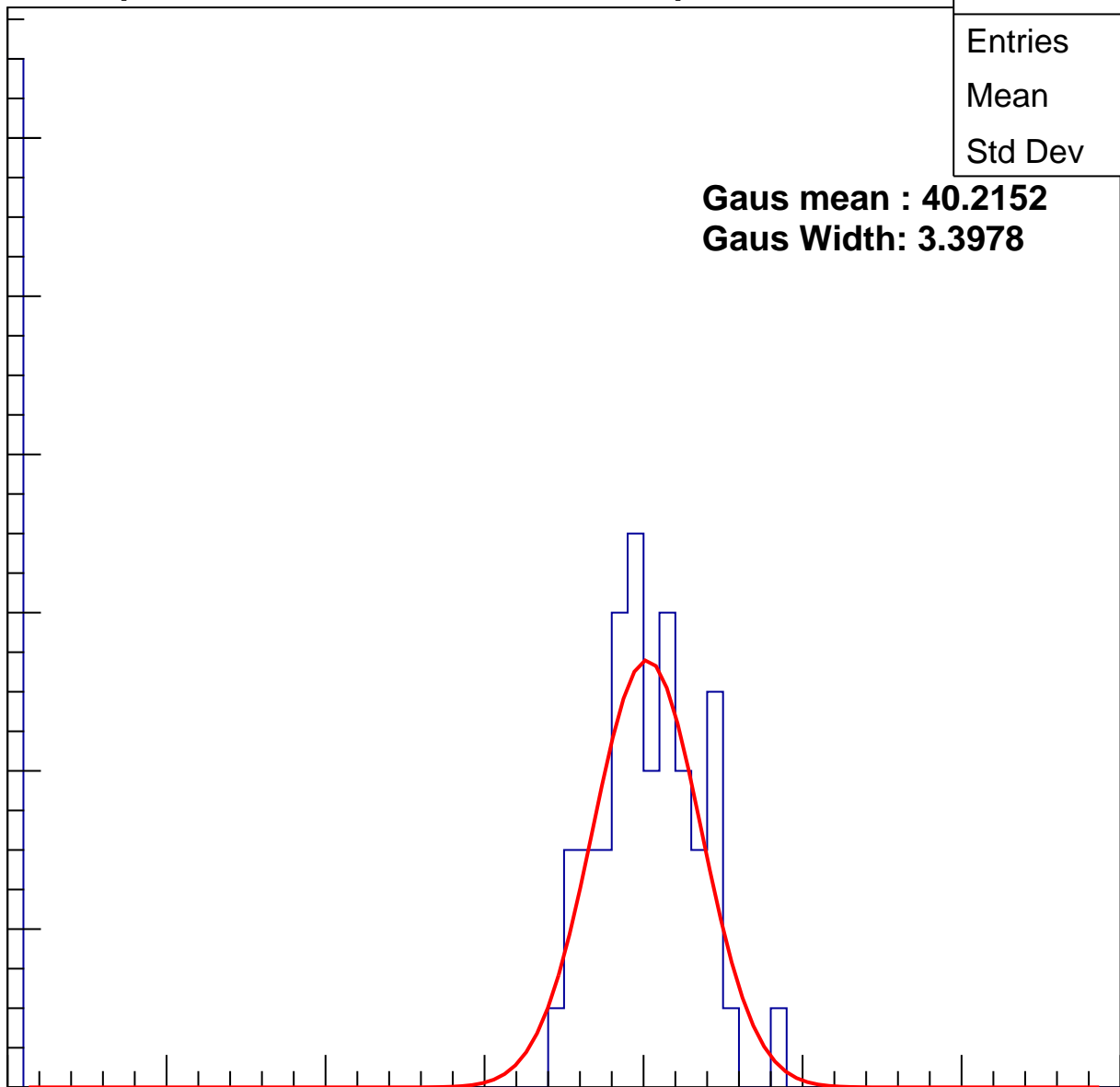
40

50

60

70

ampl

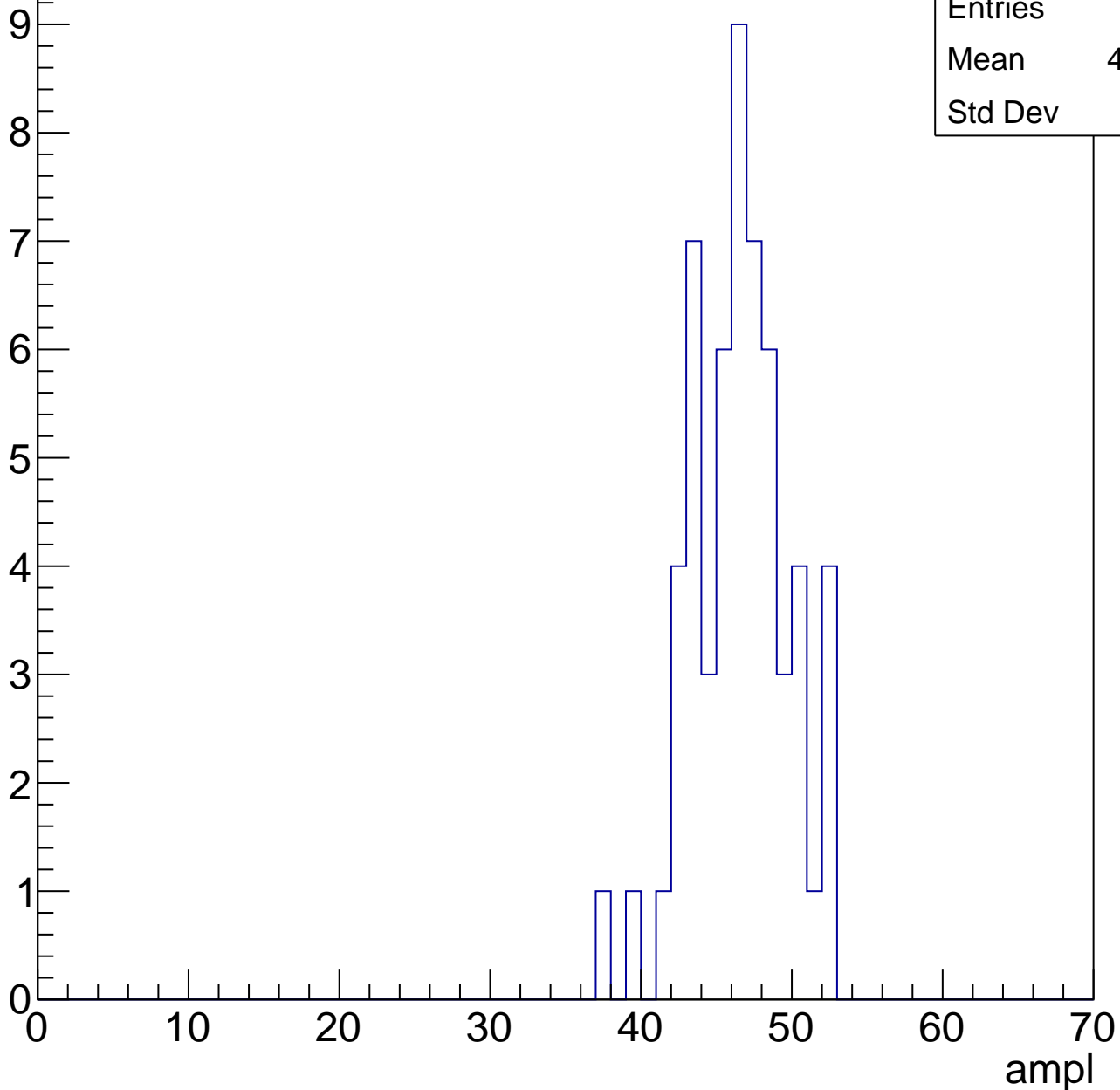


B1L103S, U1-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	46.05
Std Dev	3.22

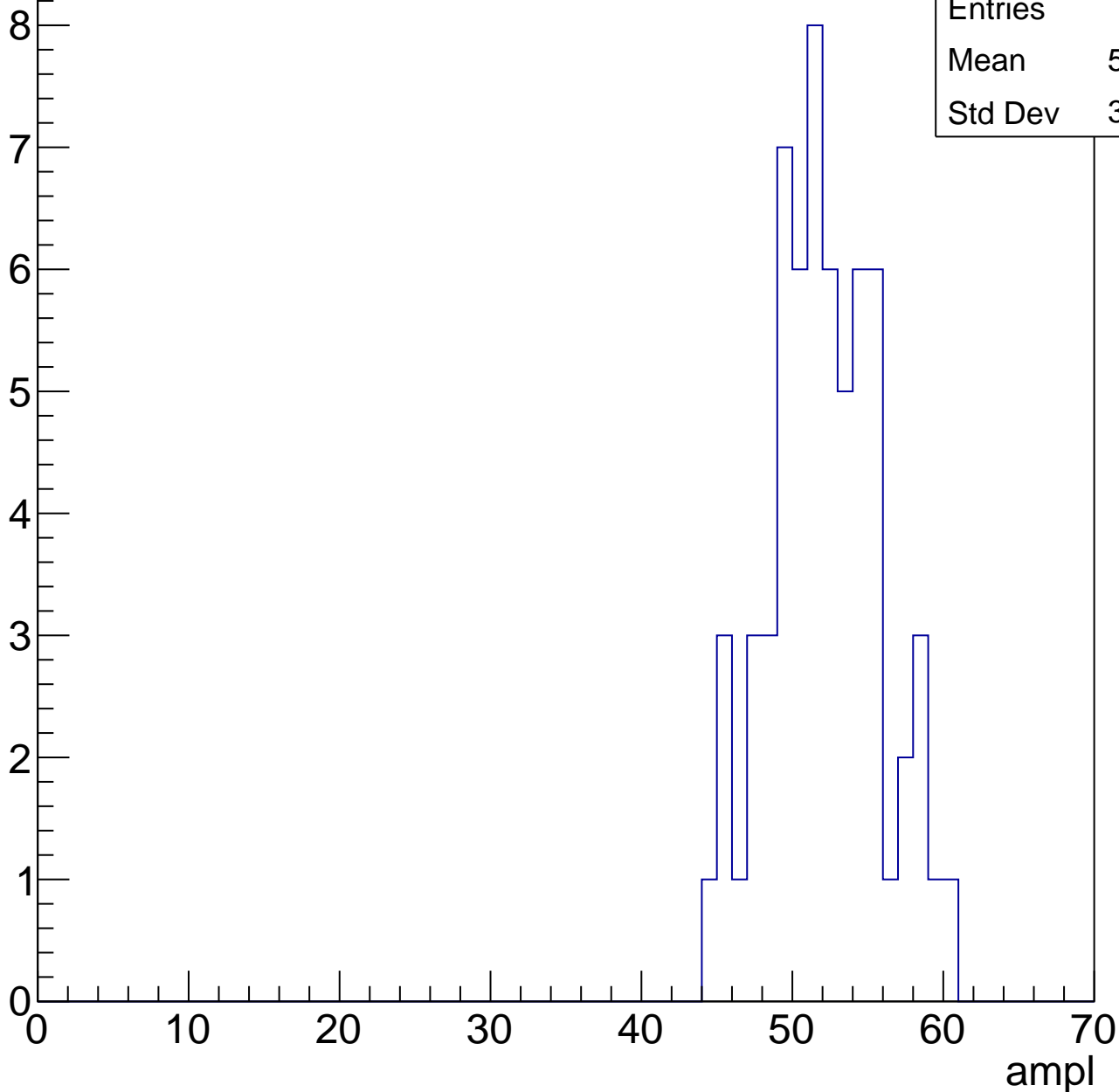


B1L103S, U1-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

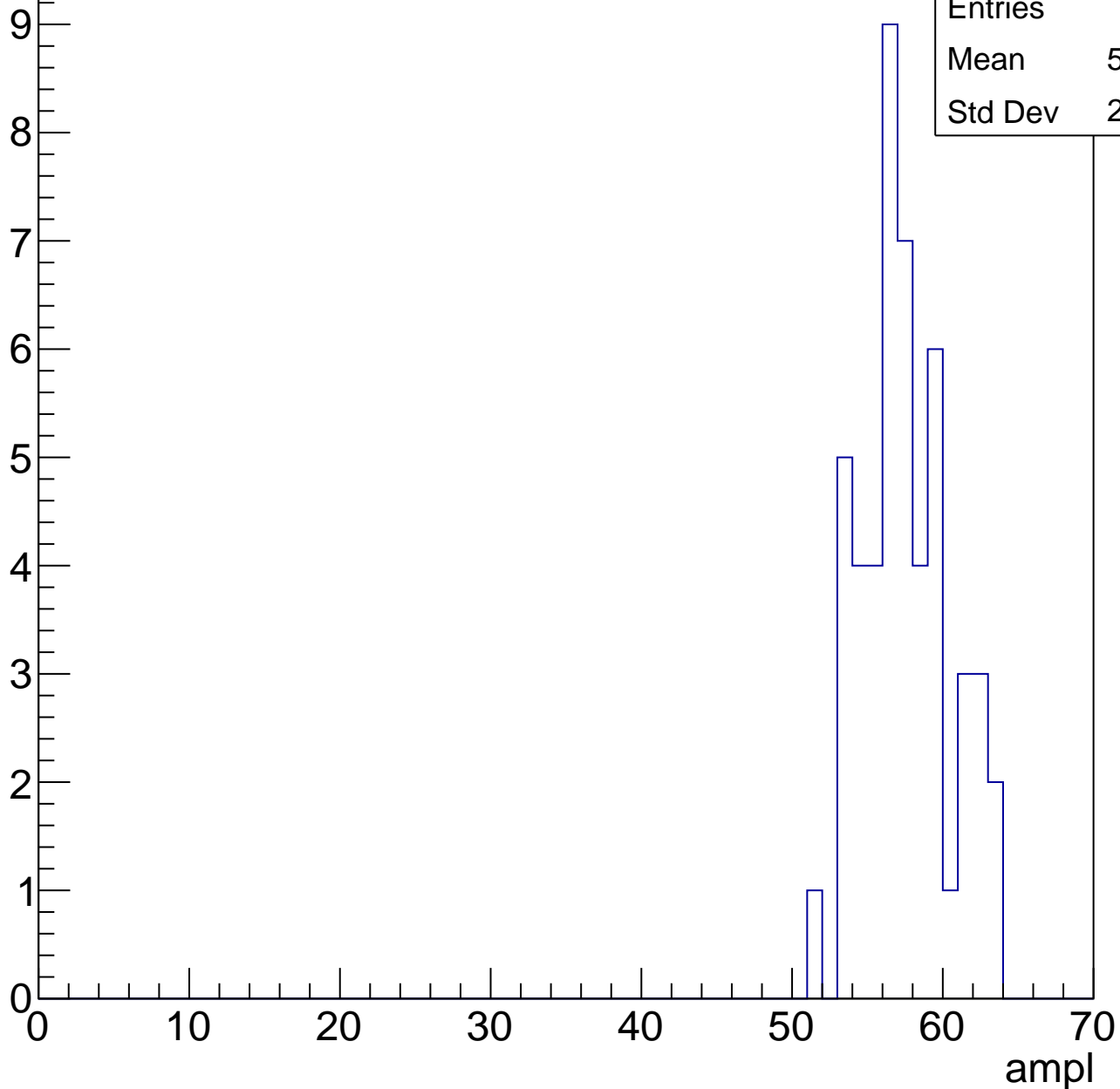
Entries	63
Mean	51.67
Std Dev	3.625



B1L103S, U1-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



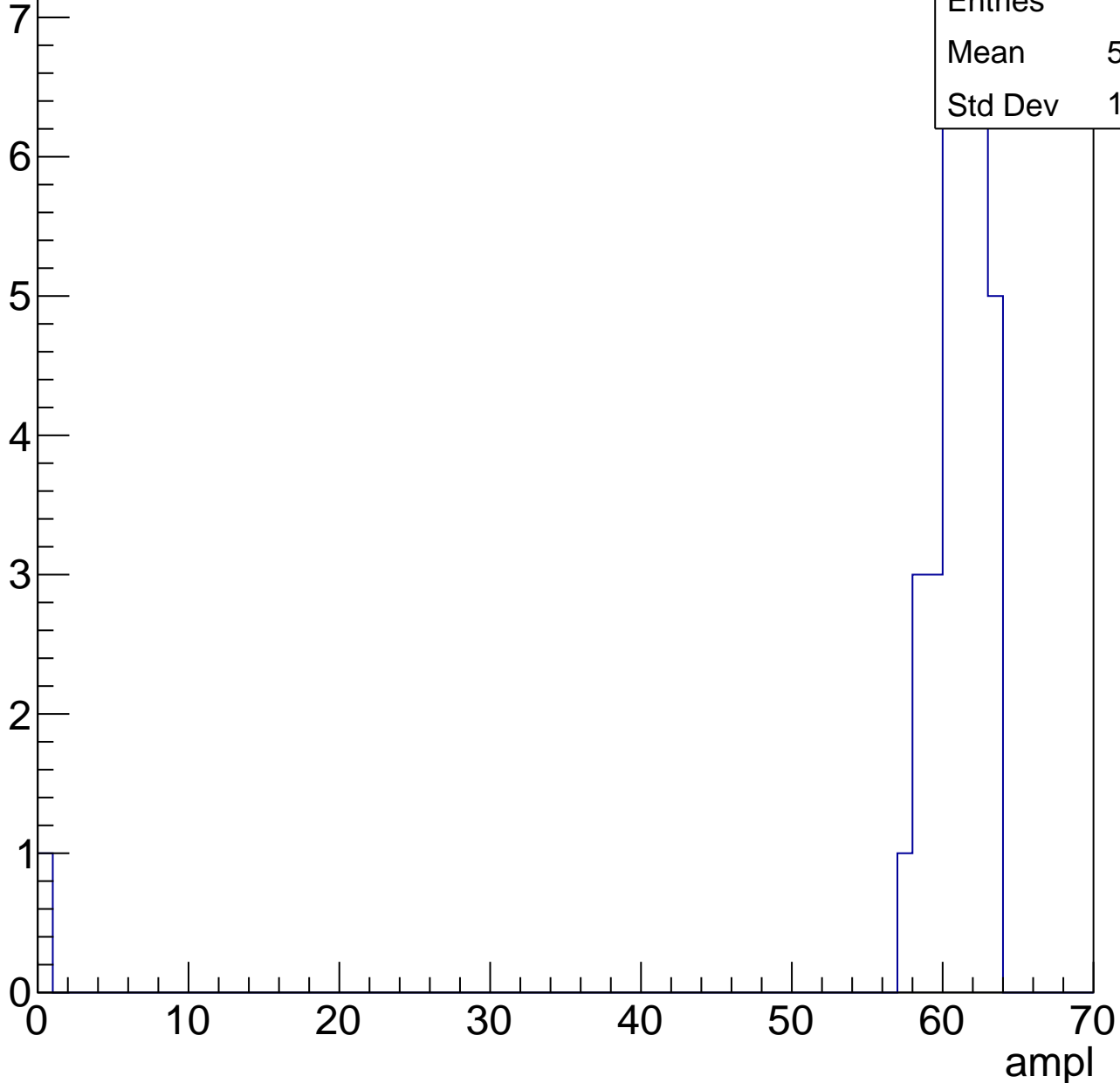
Entries	49
Mean	57.06
Std Dev	2.888

B1L103S, U1-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	58.94
Std Dev	10.38

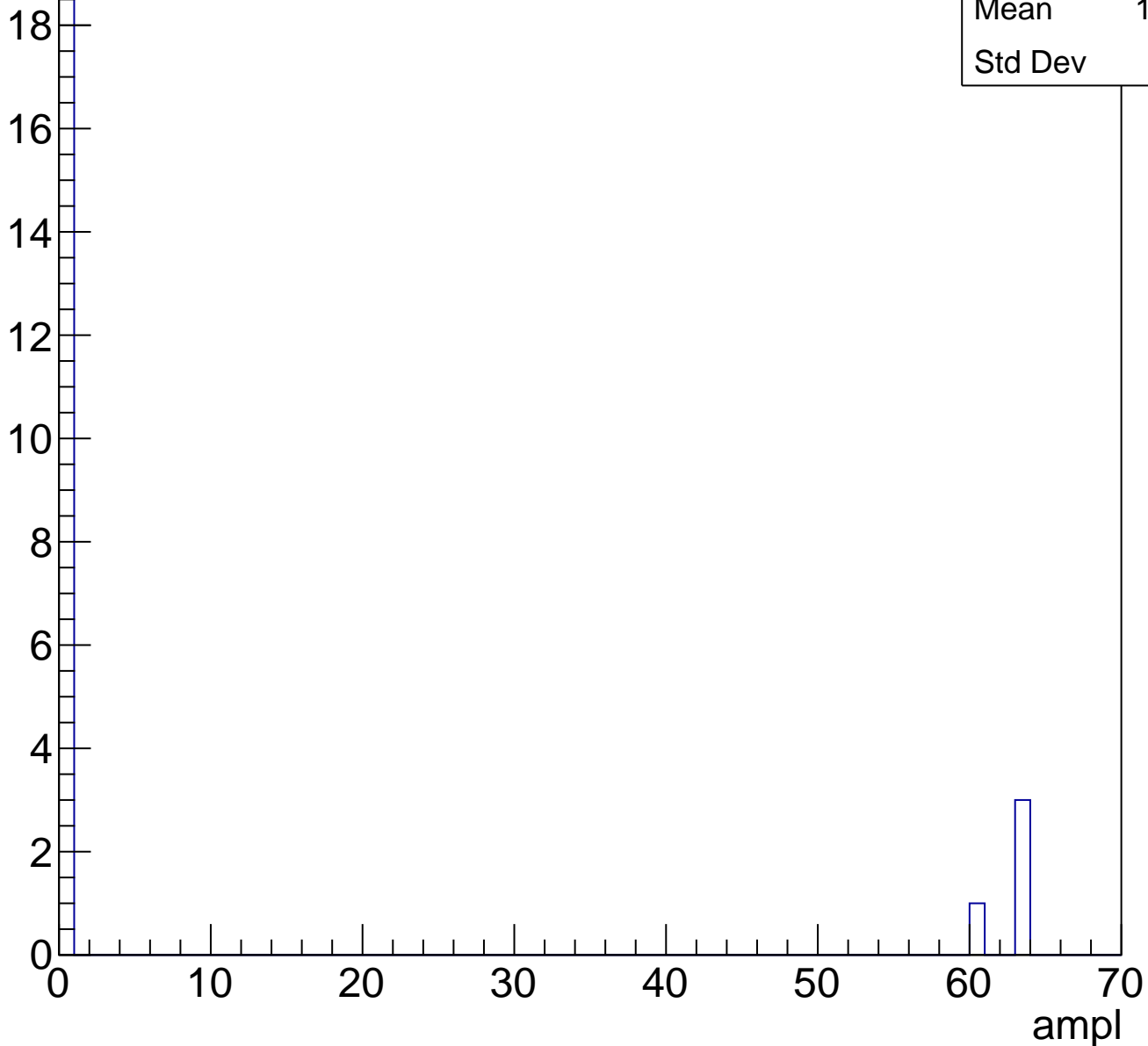


B1L103S, U1-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.83
Std Dev	23.6

Entry



B1L103S, U1-ch41, adc0

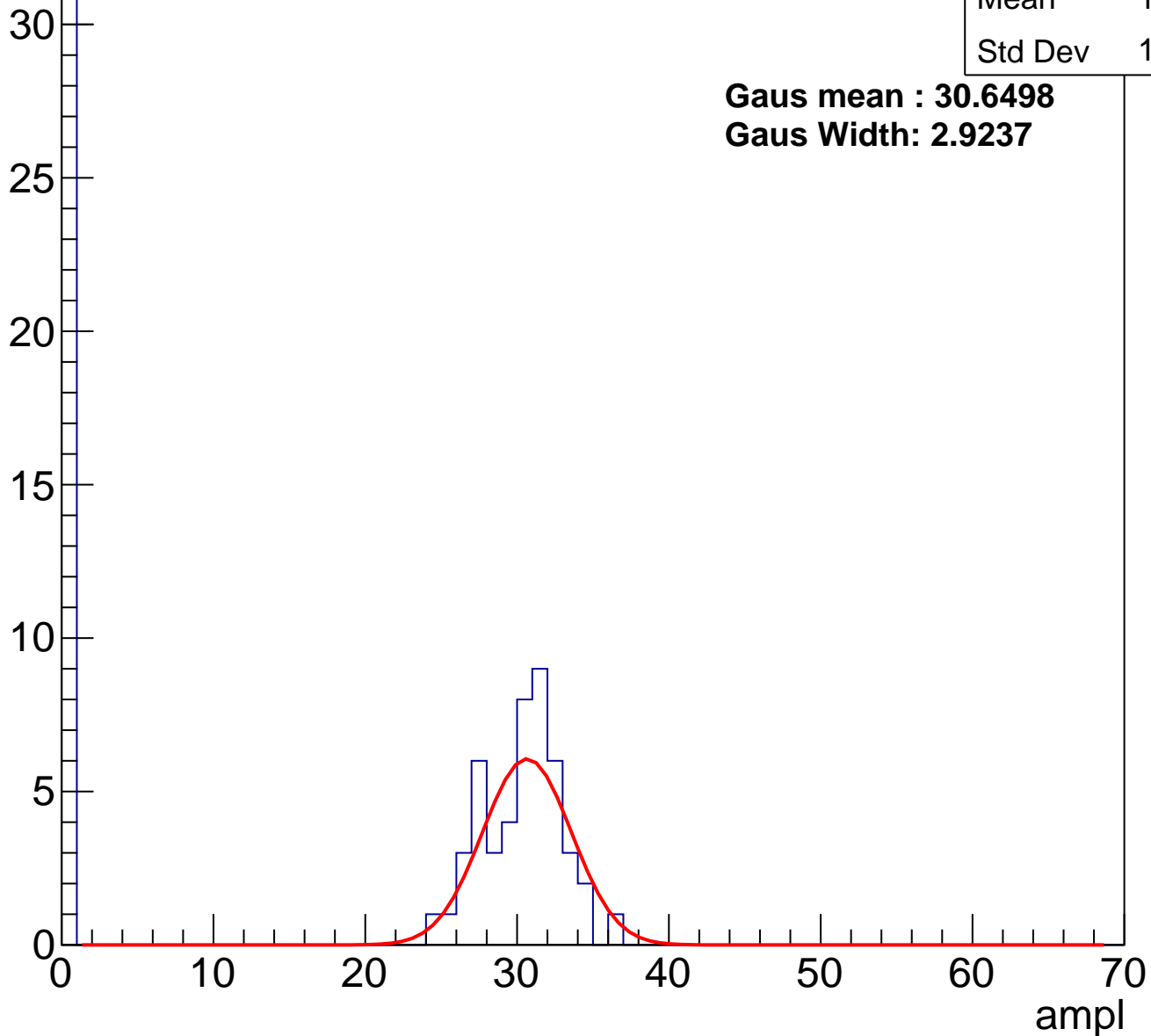
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	17.76
Std Dev	14.79

Gaus mean : 30.6498

Gaus Width: 2.9237

Entry



B1L103S, U1-ch41, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	30.37
Std Dev	13.16

Gaus mean : 36.6644

Gaus Width: 3.0917

Entry

12

10

8

6

4

2

0

0

10

20

30

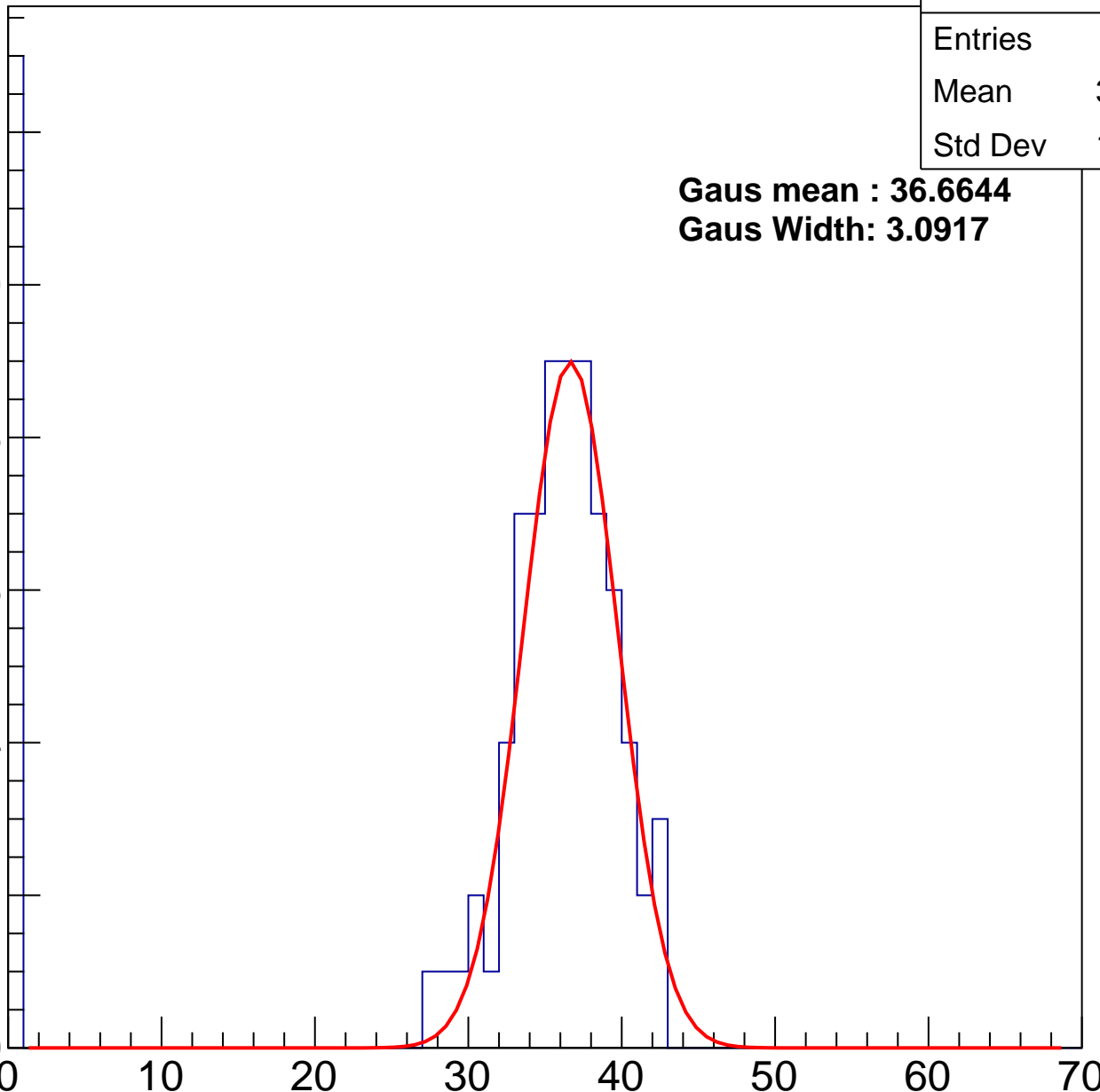
40

50

60

70

ampl



B1L103S, U1-ch41, adc2

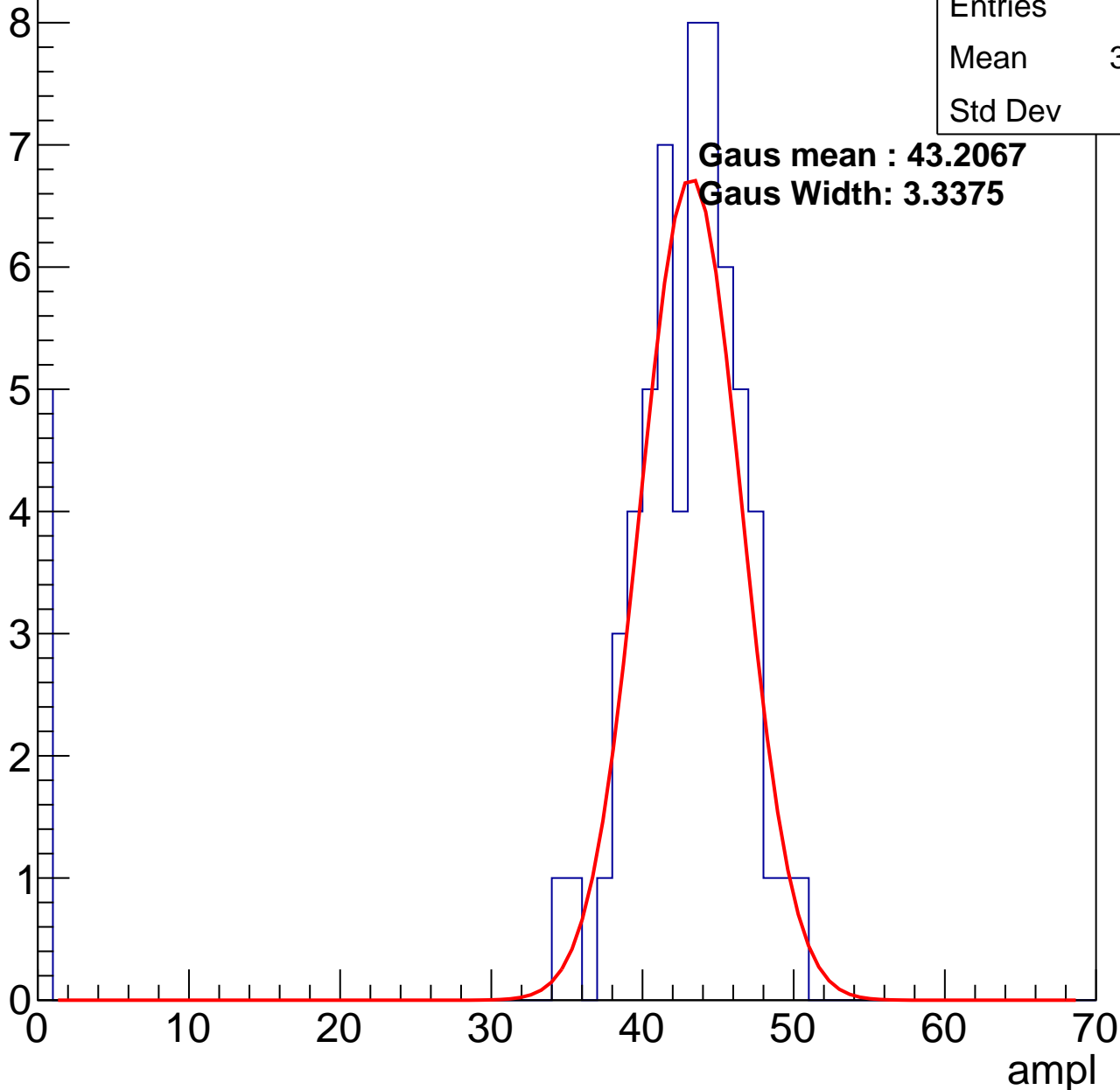
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.42
Std Dev	11.8

Gaus mean : 43.2067

Gaus Width: 3.3375

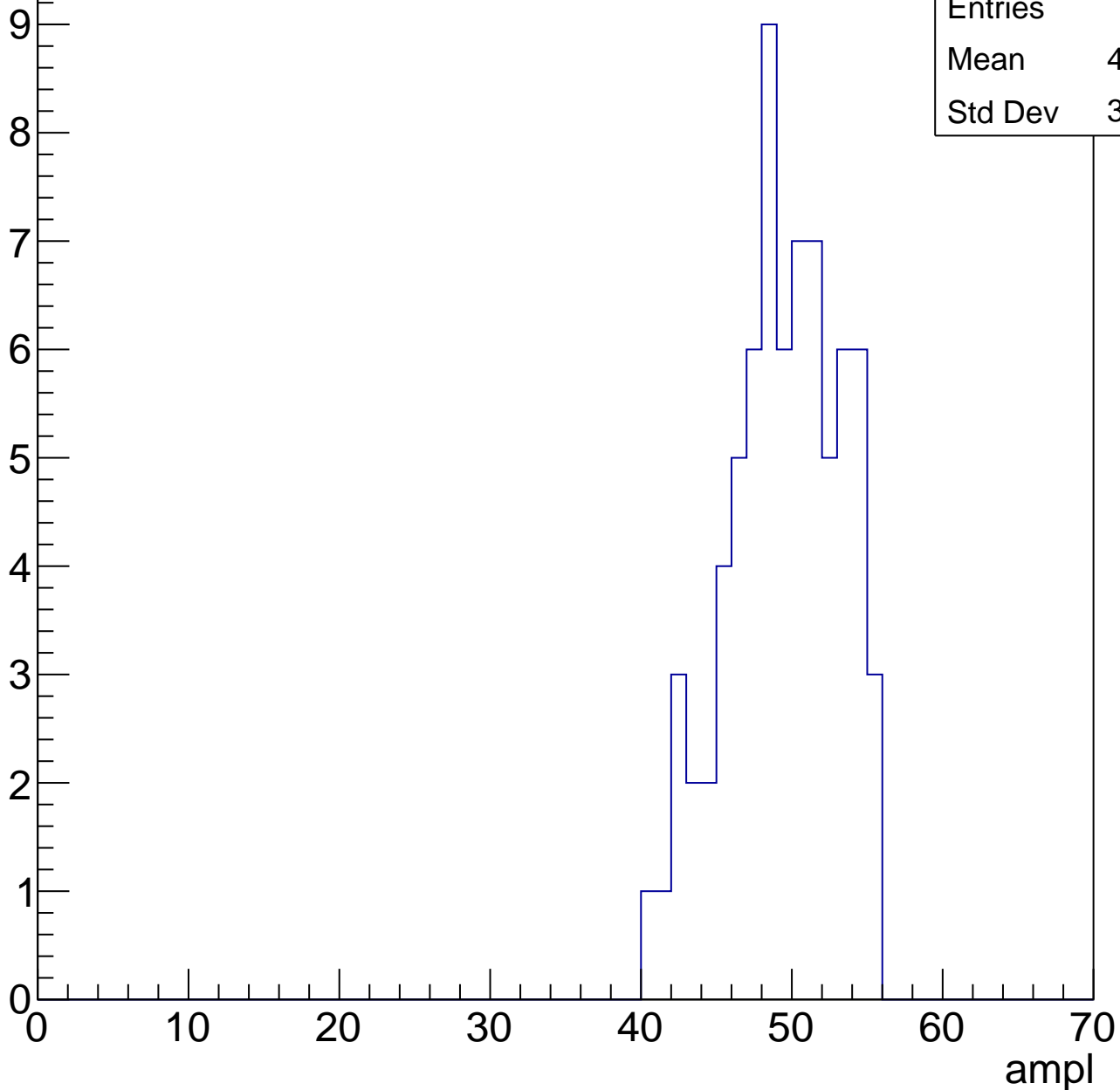


B1L103S, U1-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

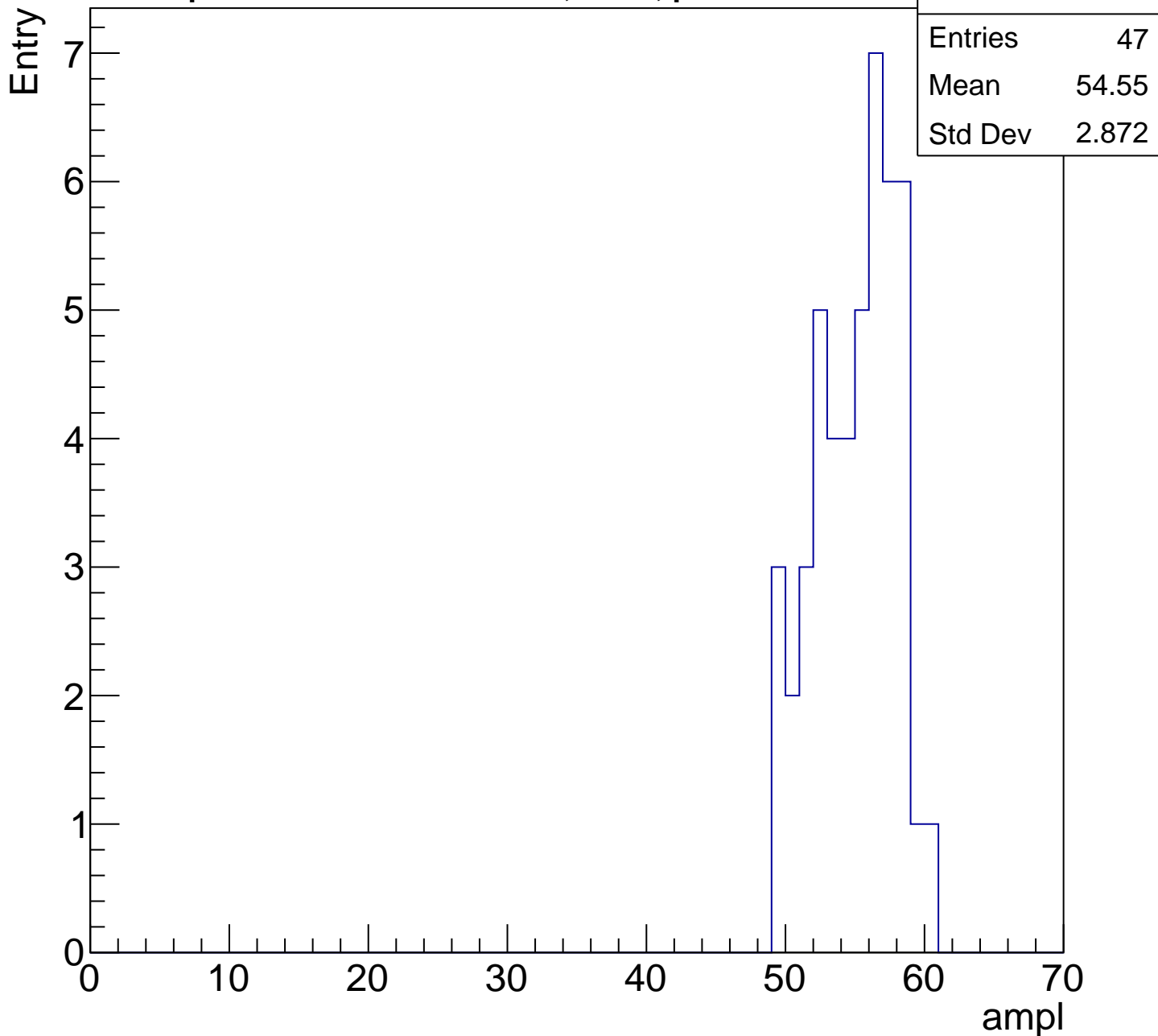
Entry

Entries	73
Mean	48.95
Std Dev	3.682



B1L103S, U1-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch41, adc5

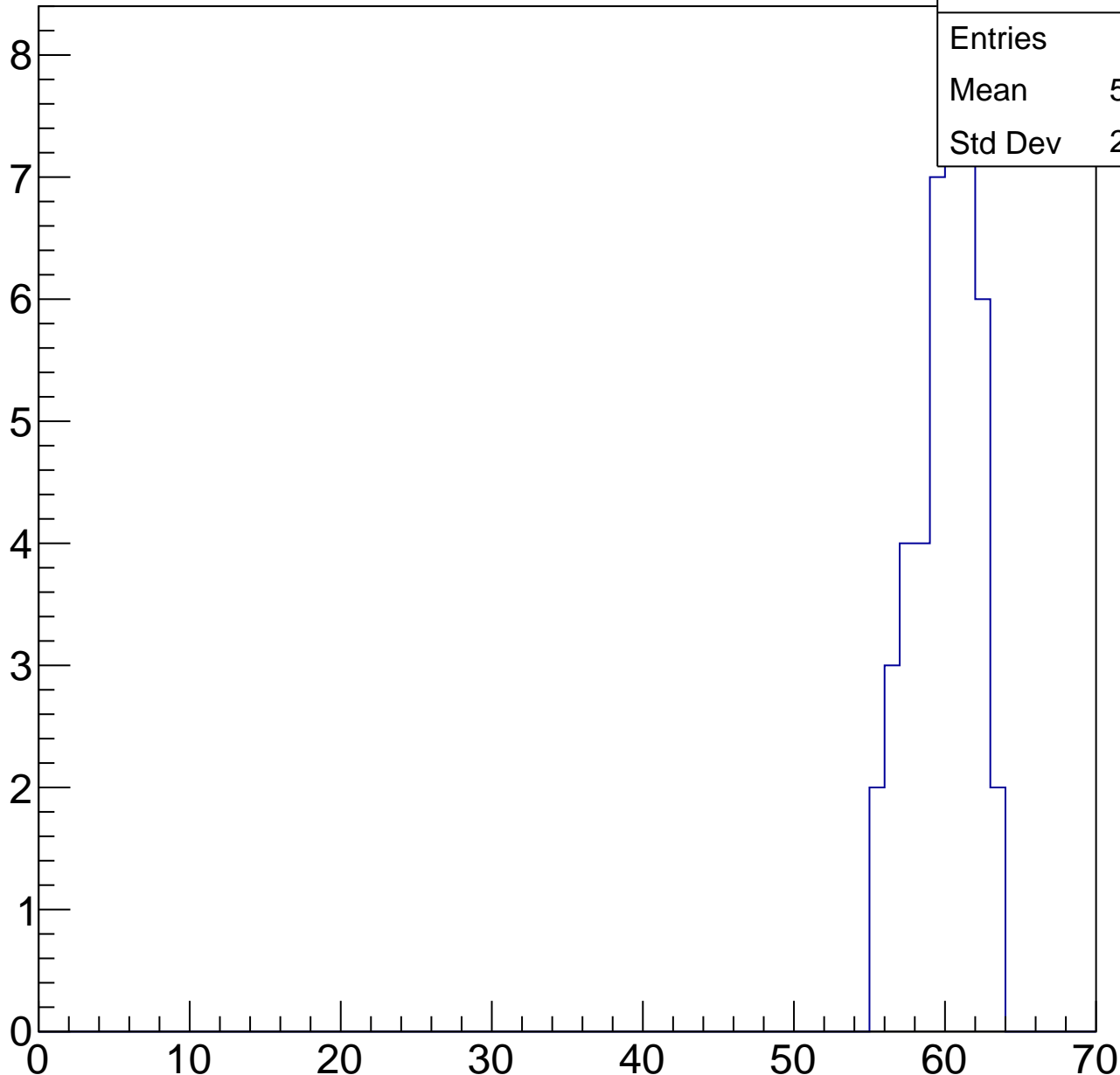
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	59.48
Std Dev	2.105

ampl

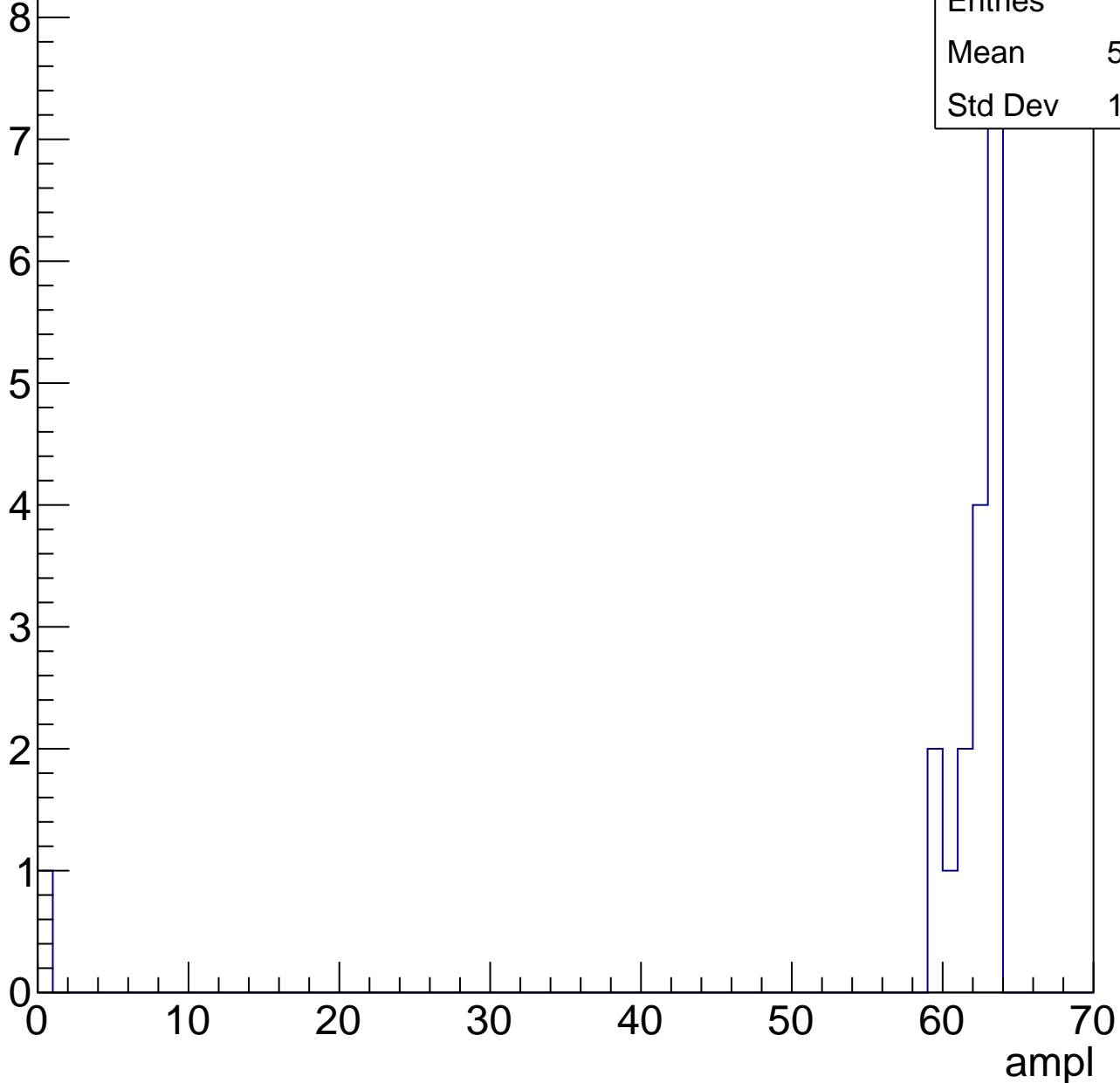


B1L103S, U1-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58.44
Std Dev	14.24



B1L103S, U1-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

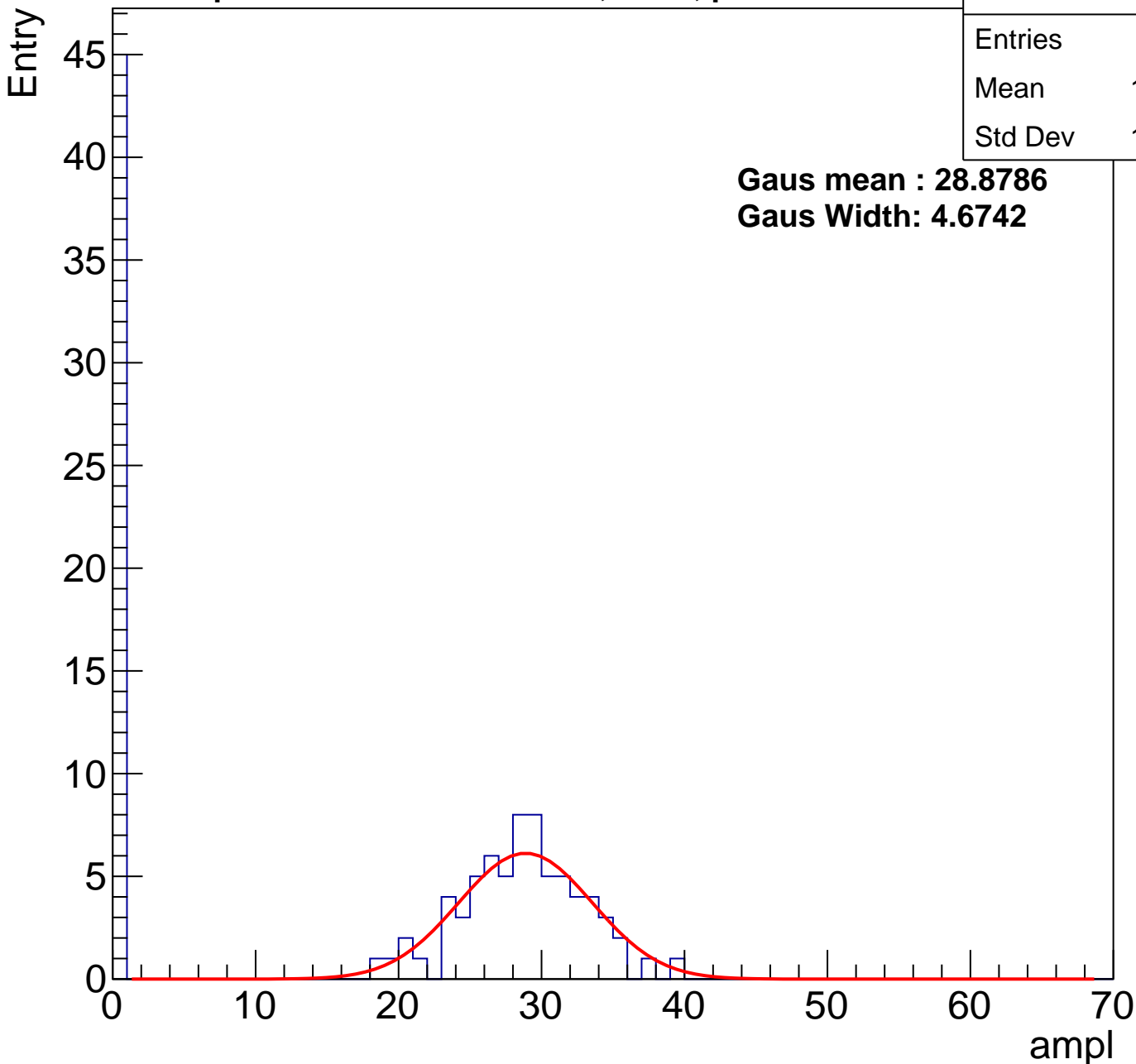
B1L103S, U1-ch42, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	114
Mean	17.08
Std Dev	14.18

Gaus mean : 28.8786

Gaus Width: 4.6742



B1L103S, U1-ch42, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	27.98
Std Dev	13.69

Gaus mean : 34.8412

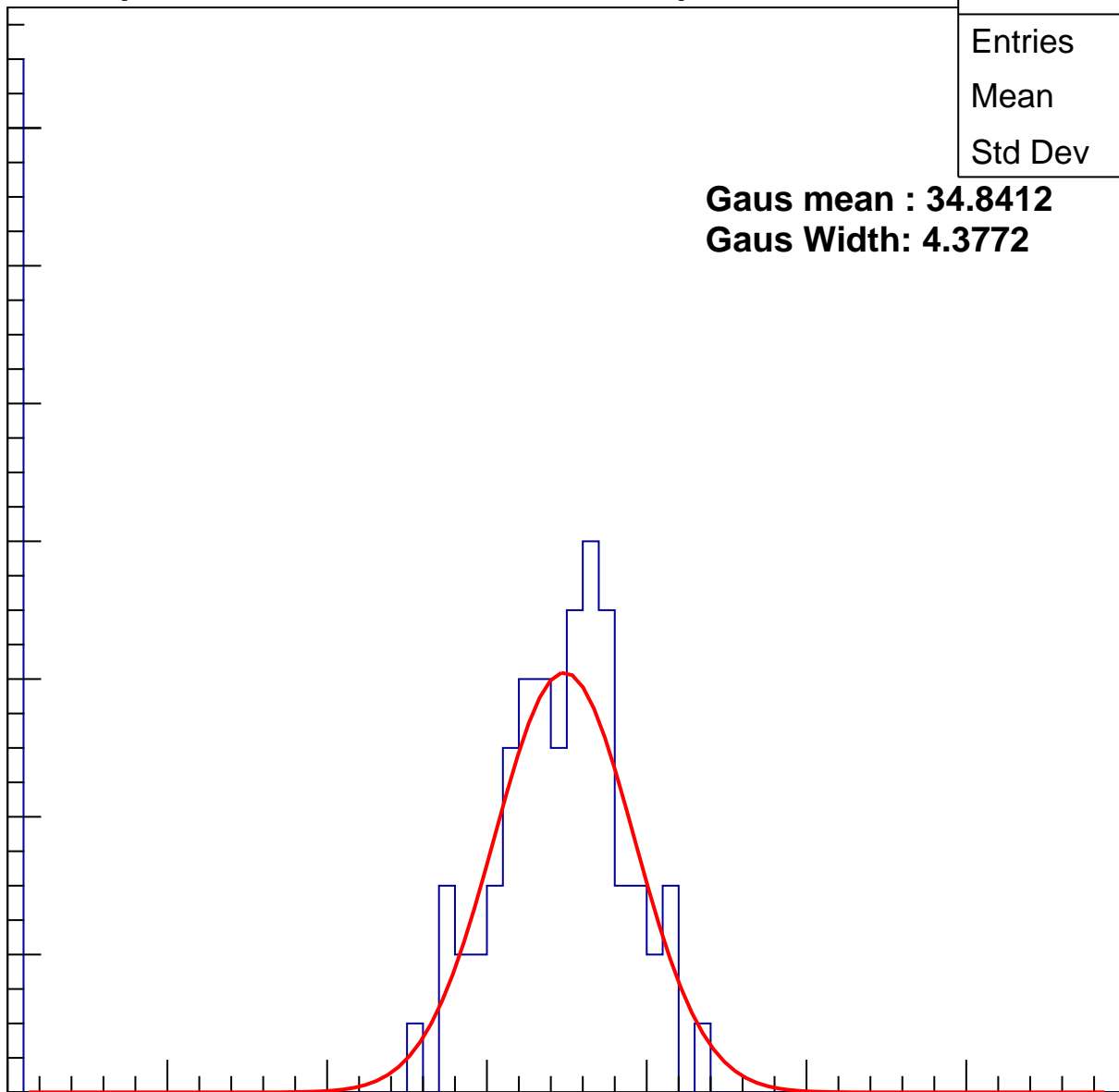
Gaus Width: 4.3772

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch42, adc2

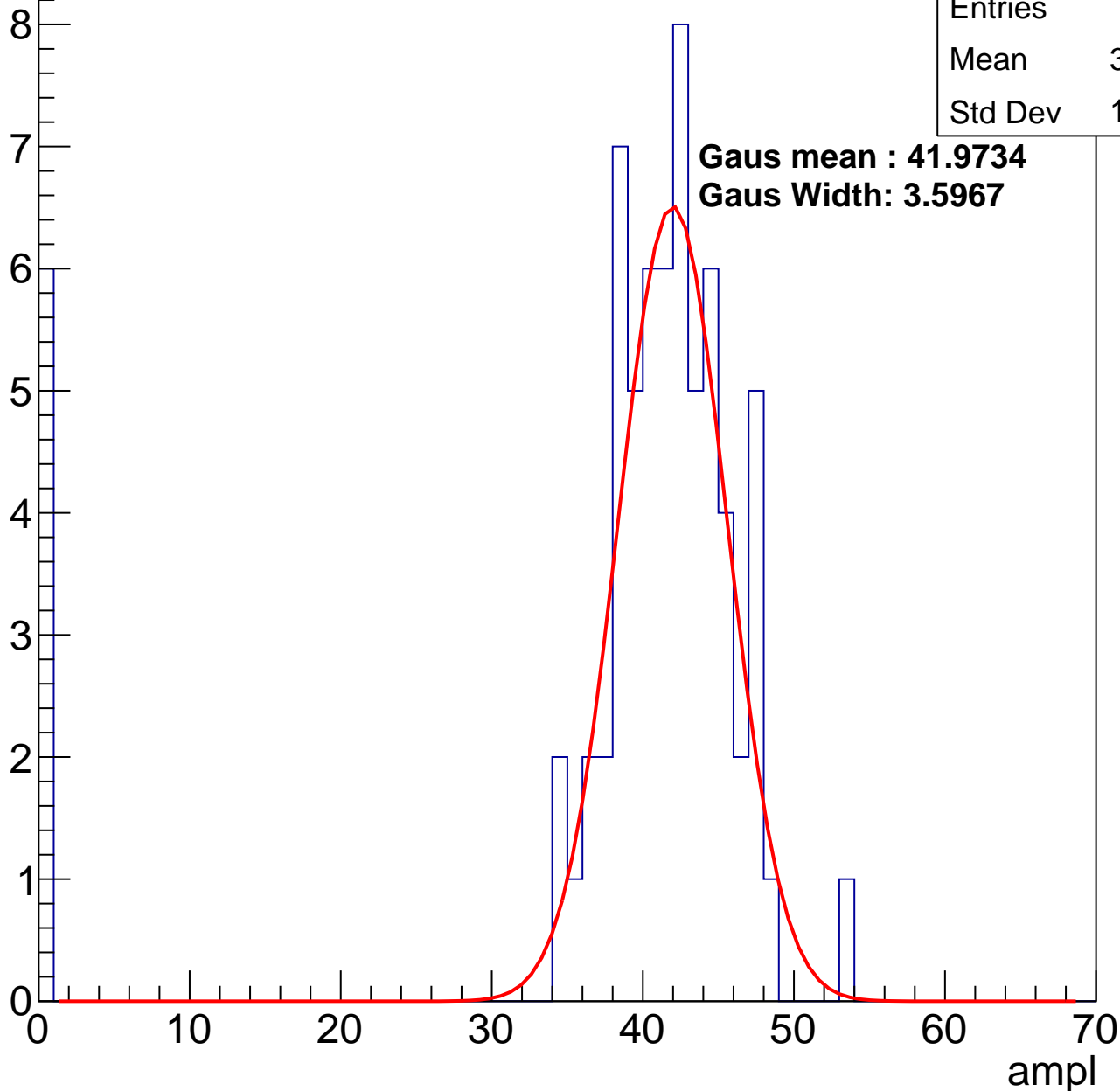
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.96
Std Dev	12.23

Gaus mean : 41.9734

Gaus Width: 3.5967

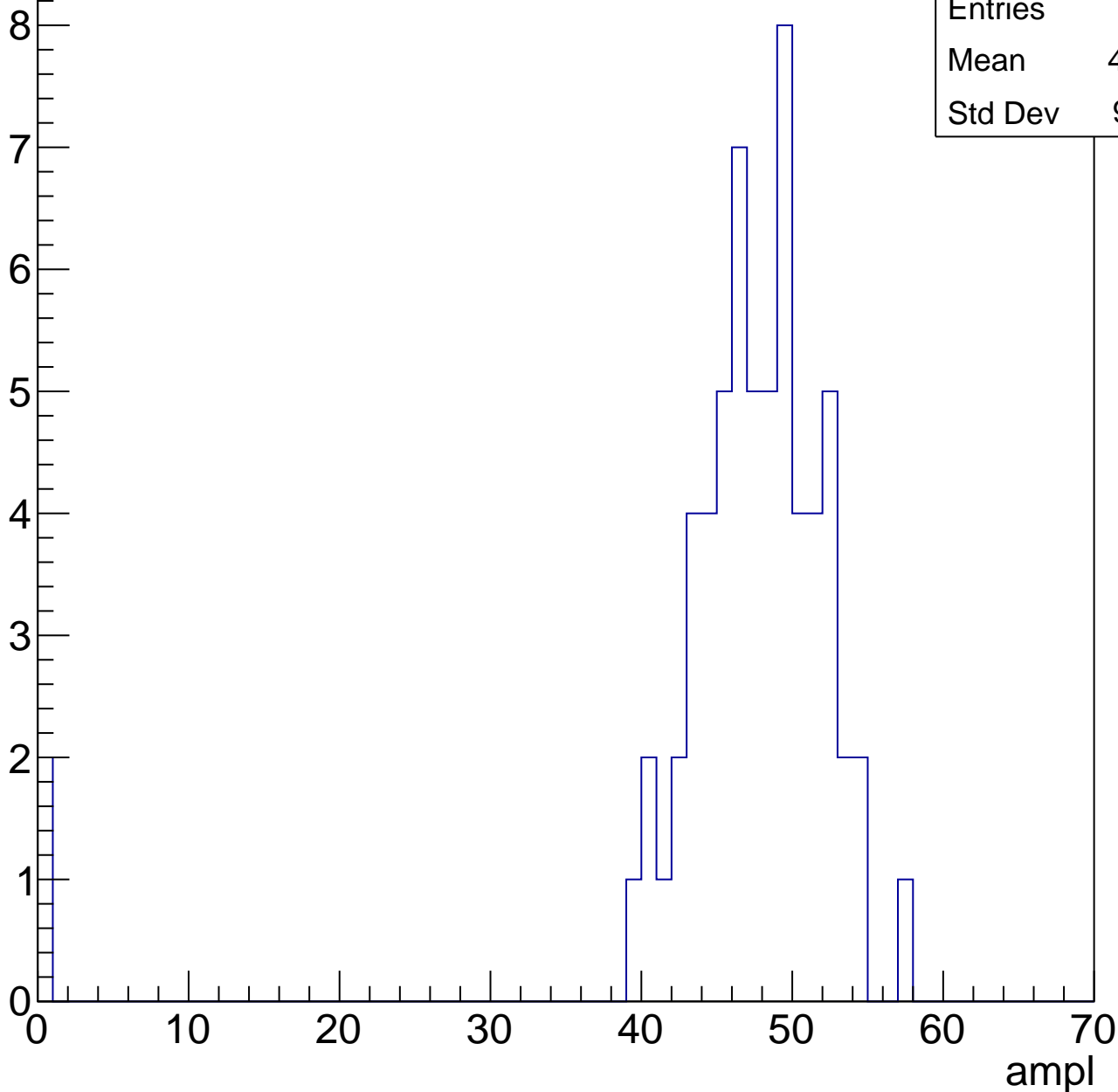


B1L103S, U1-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	45.95
Std Dev	9.061

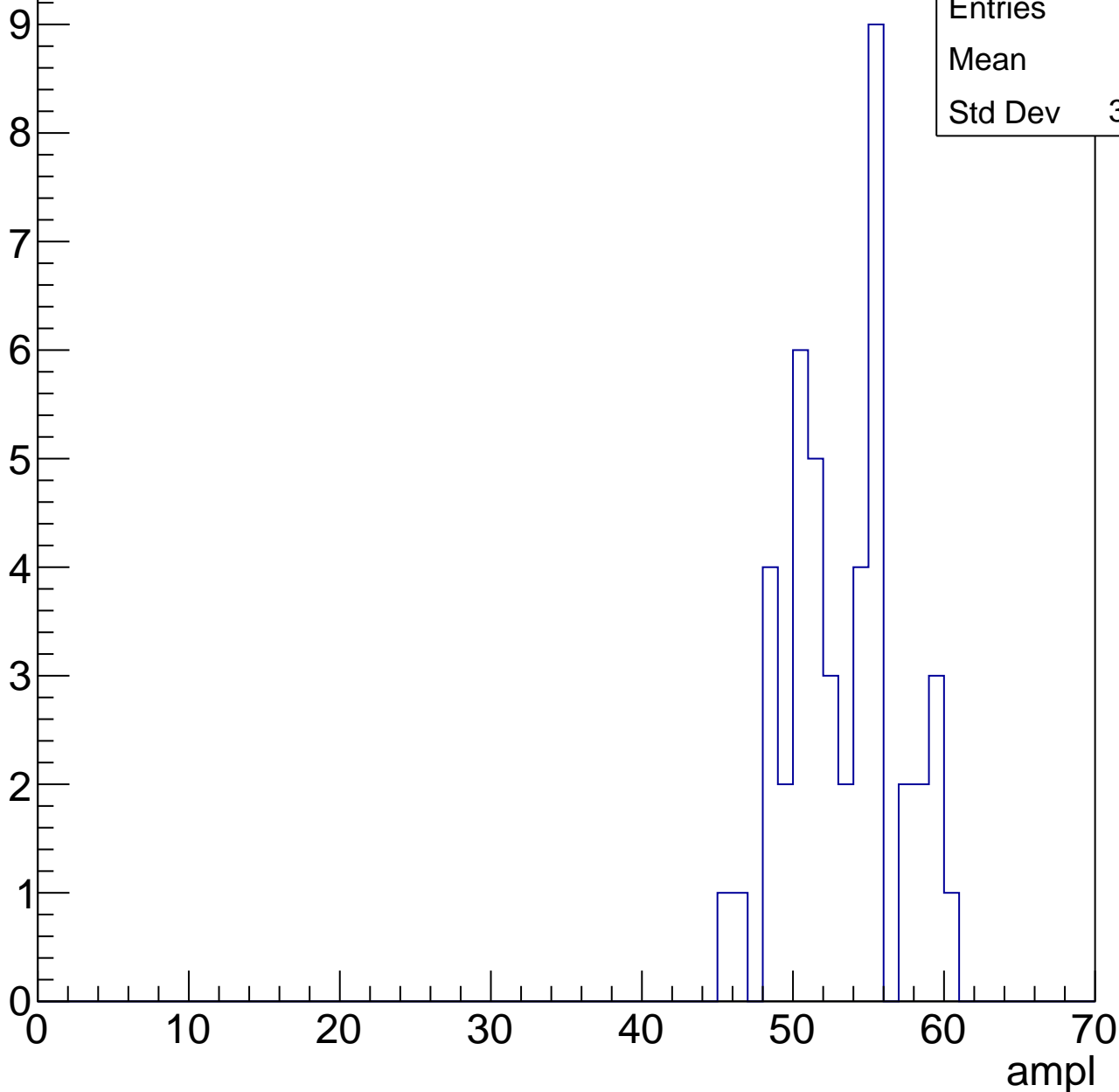


B1L103S, U1-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	52.8
Std Dev	3.649

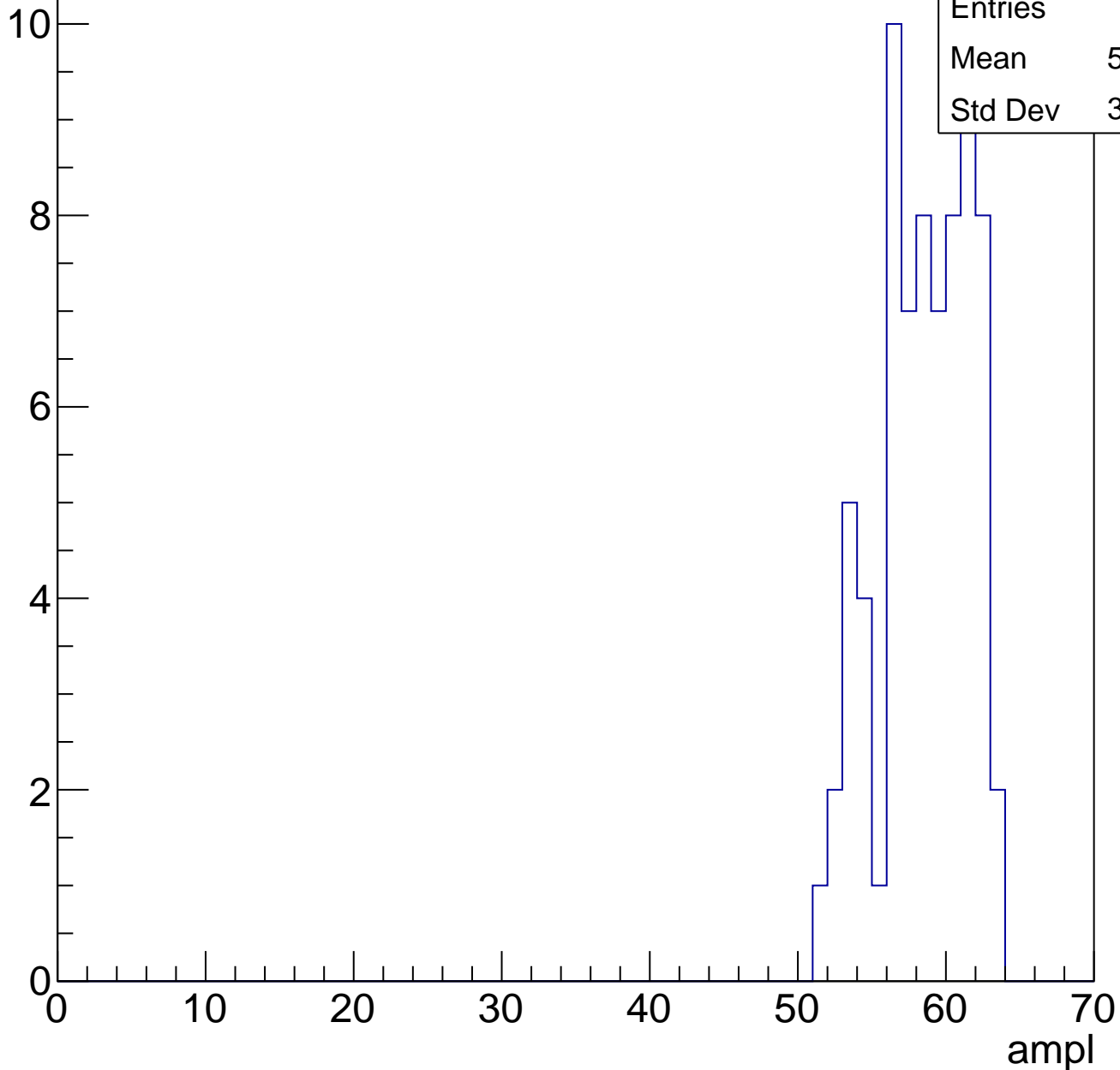


B1L103S, U1-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	58.07
Std Dev	3.036

Entry

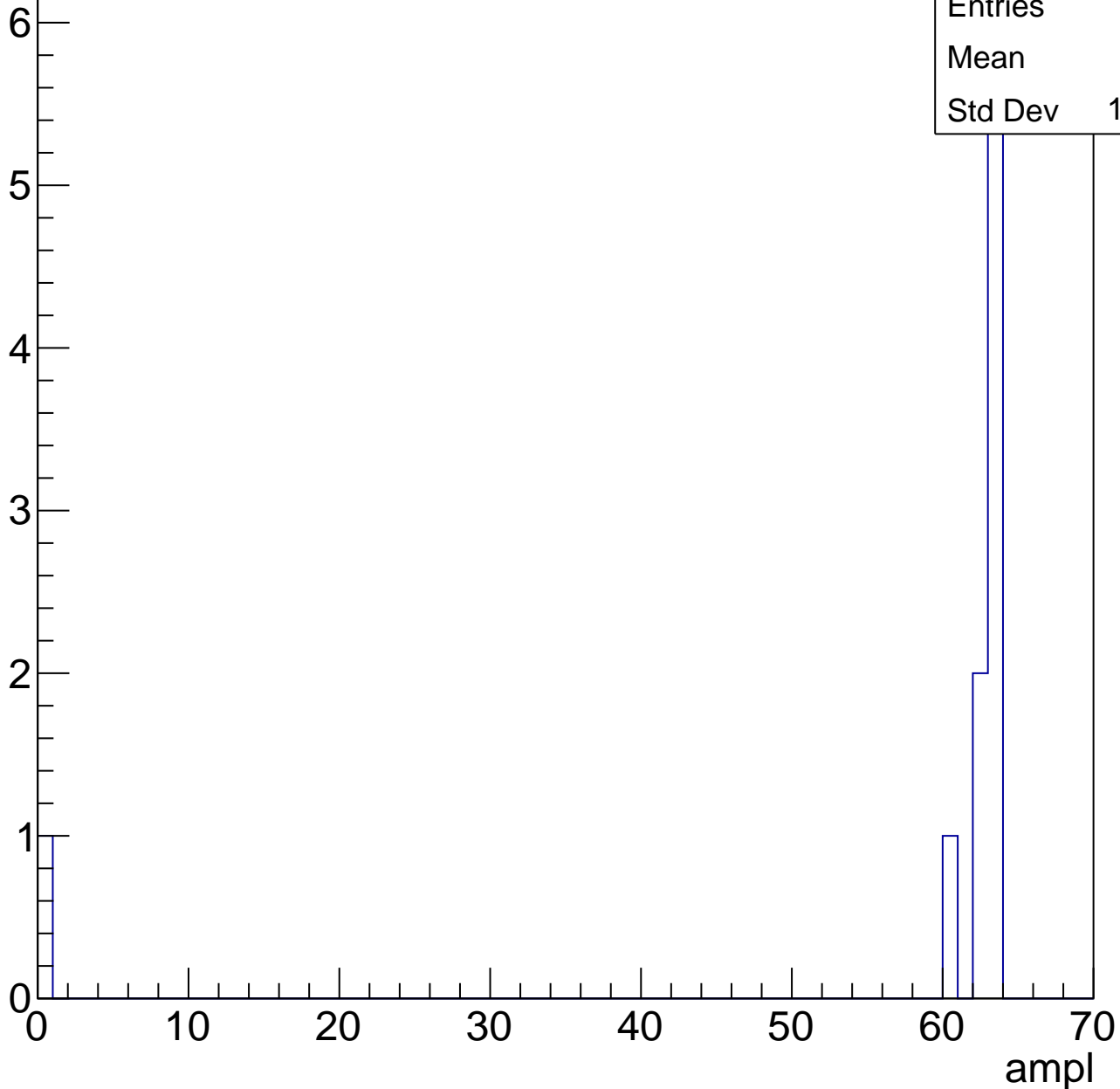


B1L103S, U1-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	56.2
Std Dev	18.76



B1L103S, U1-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



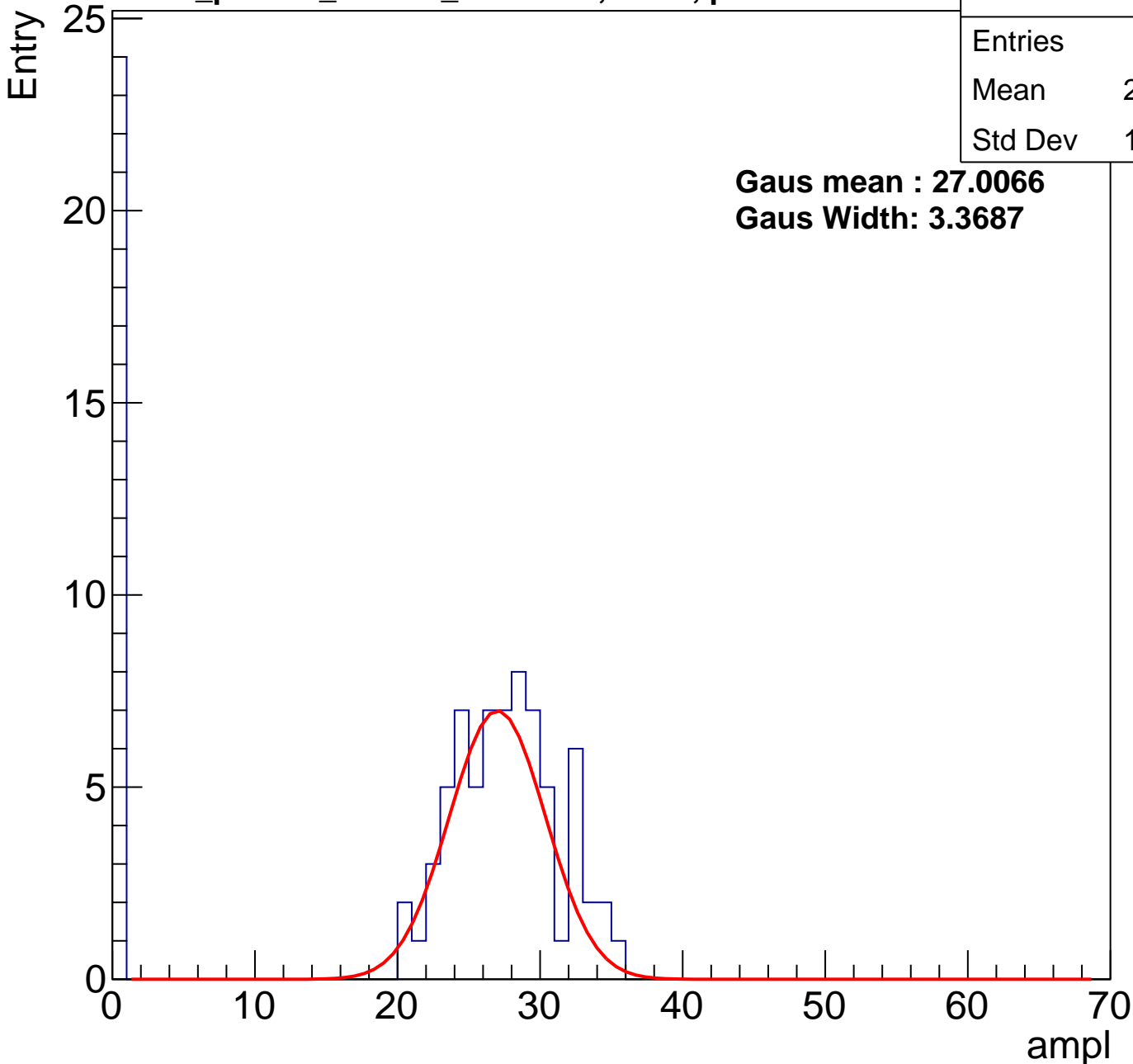
B1L103S, U1-ch43, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	20.16
Std Dev	12.27

Gaus mean : 27.0066

Gaus Width: 3.3687



B1L103S, U1-ch43, adc1

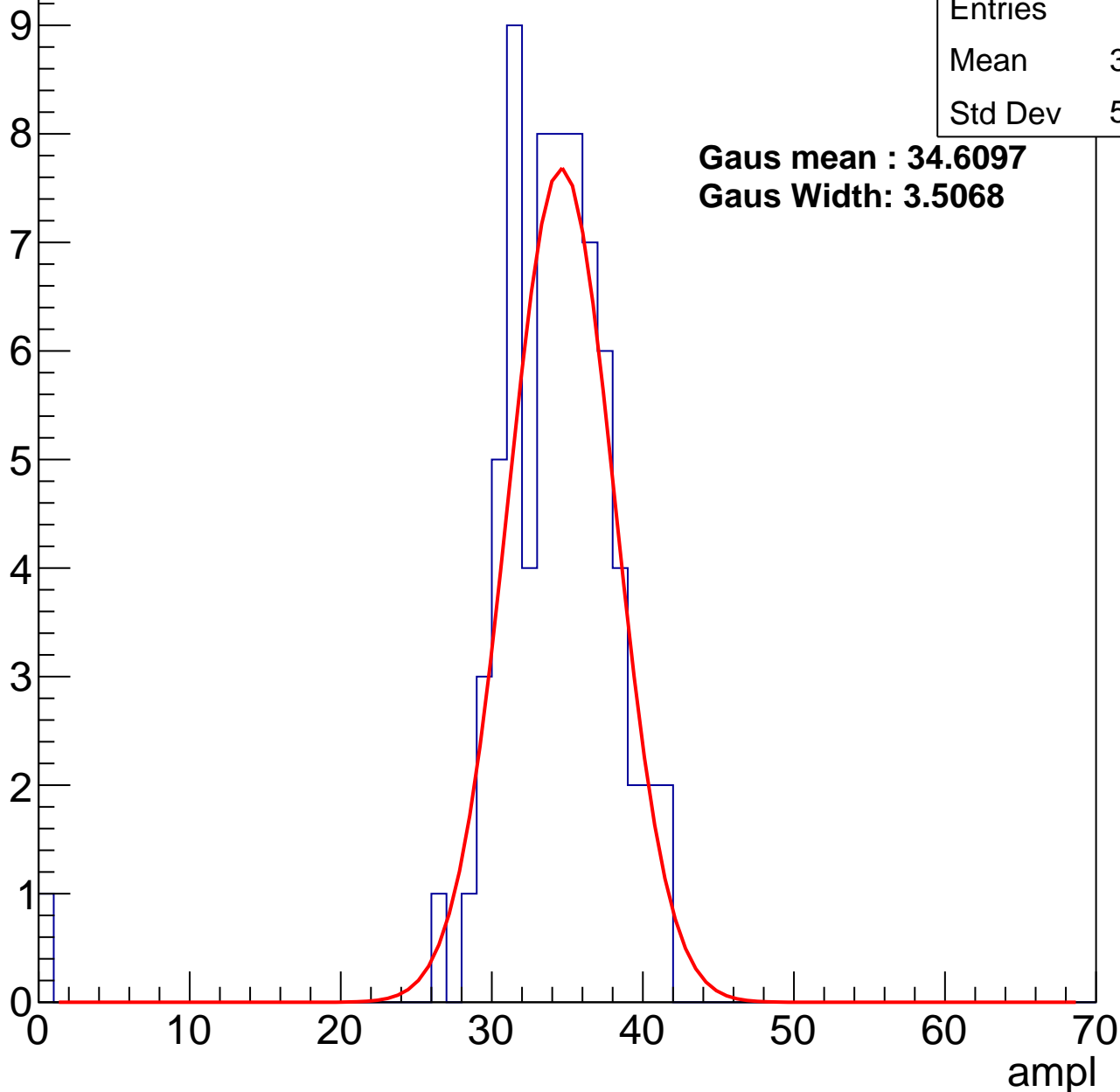
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.52
Std Dev	5.137

Gaus mean : 34.6097

Gaus Width: 3.5068



B1L103S, U1-ch43, adc2

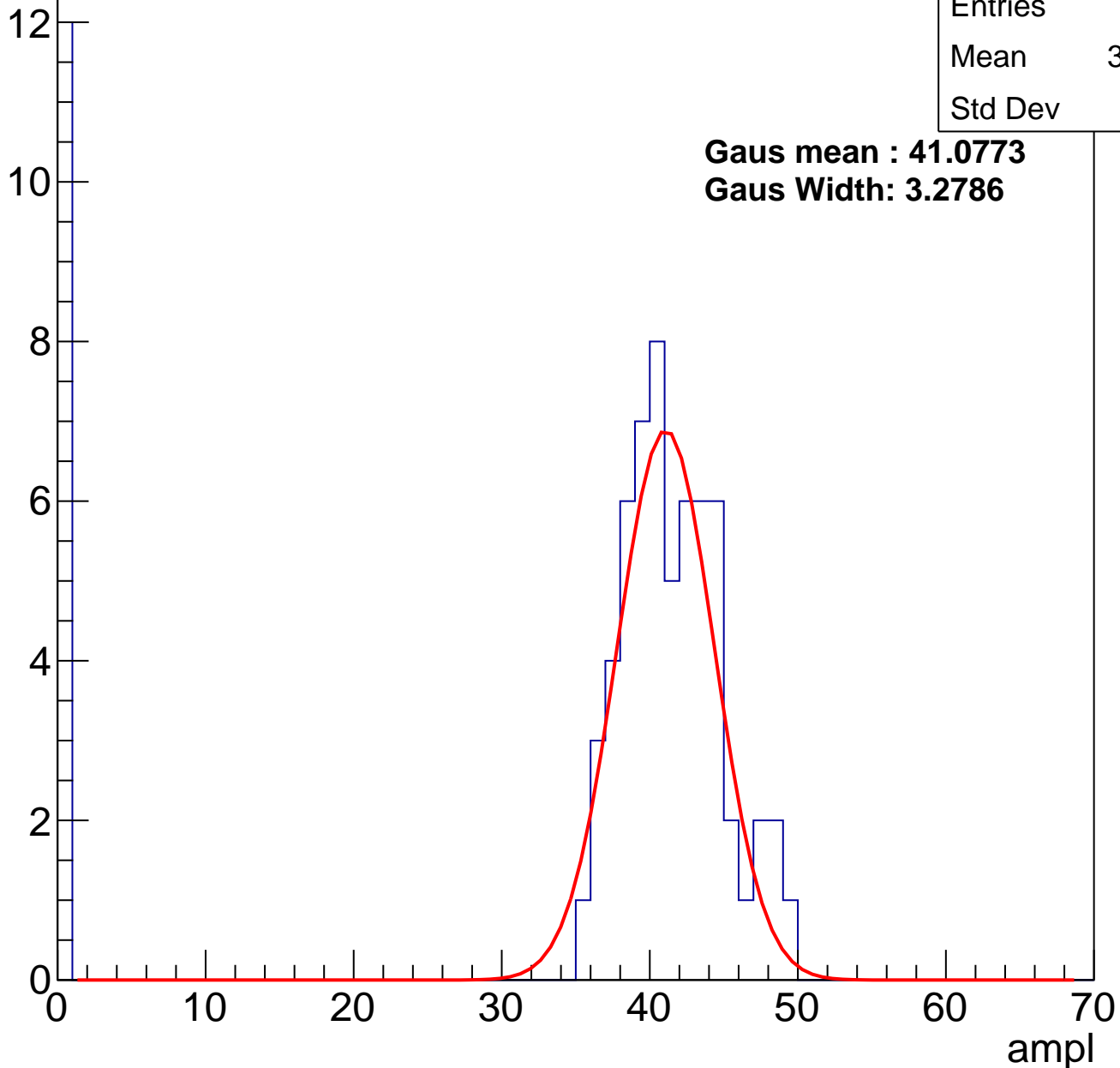
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.25
Std Dev	15.6

Gaus mean : 41.0773

Gaus Width: 3.2786

Entry

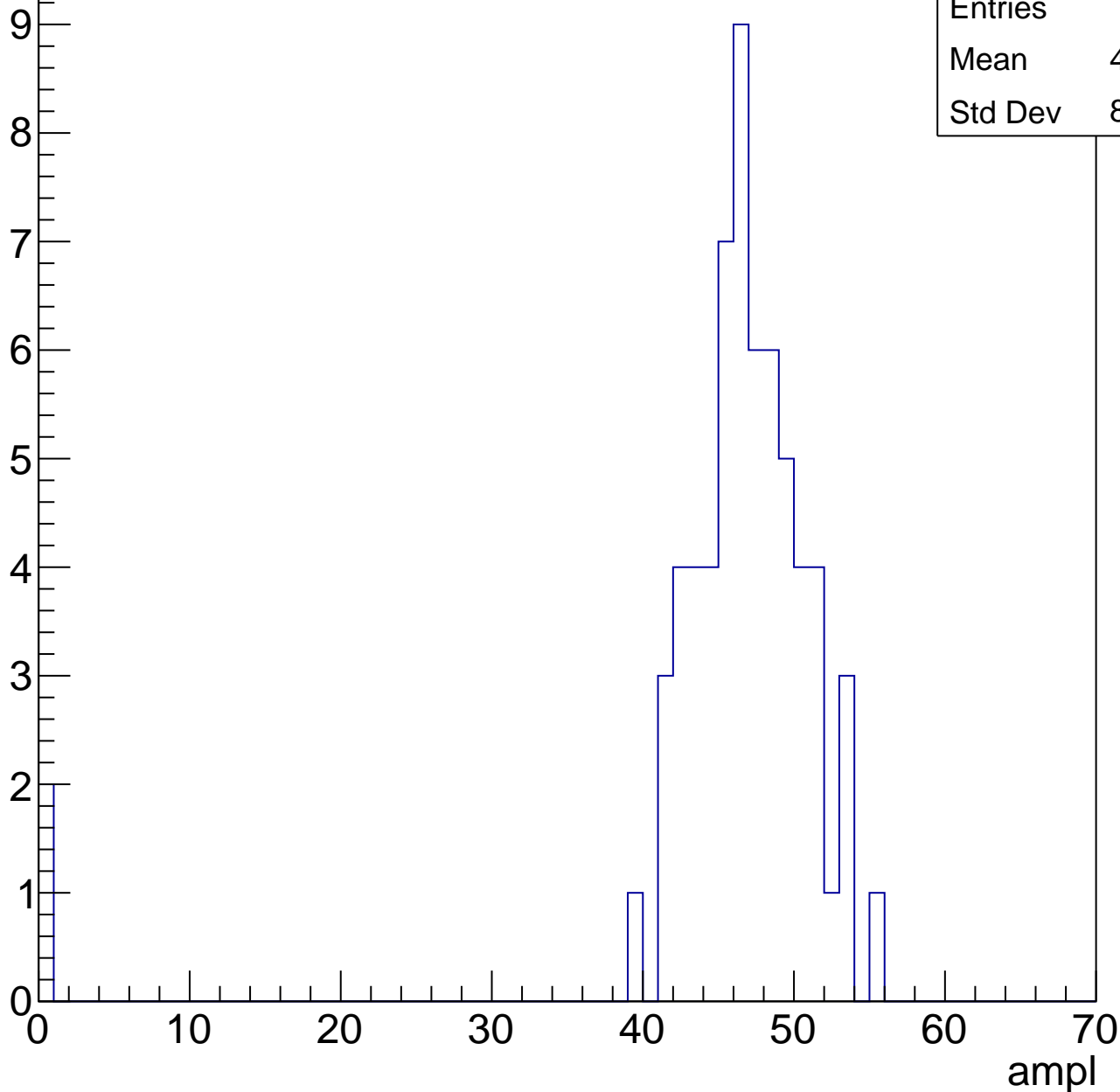


B1L103S, U1-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	45.19
Std Dev	8.785

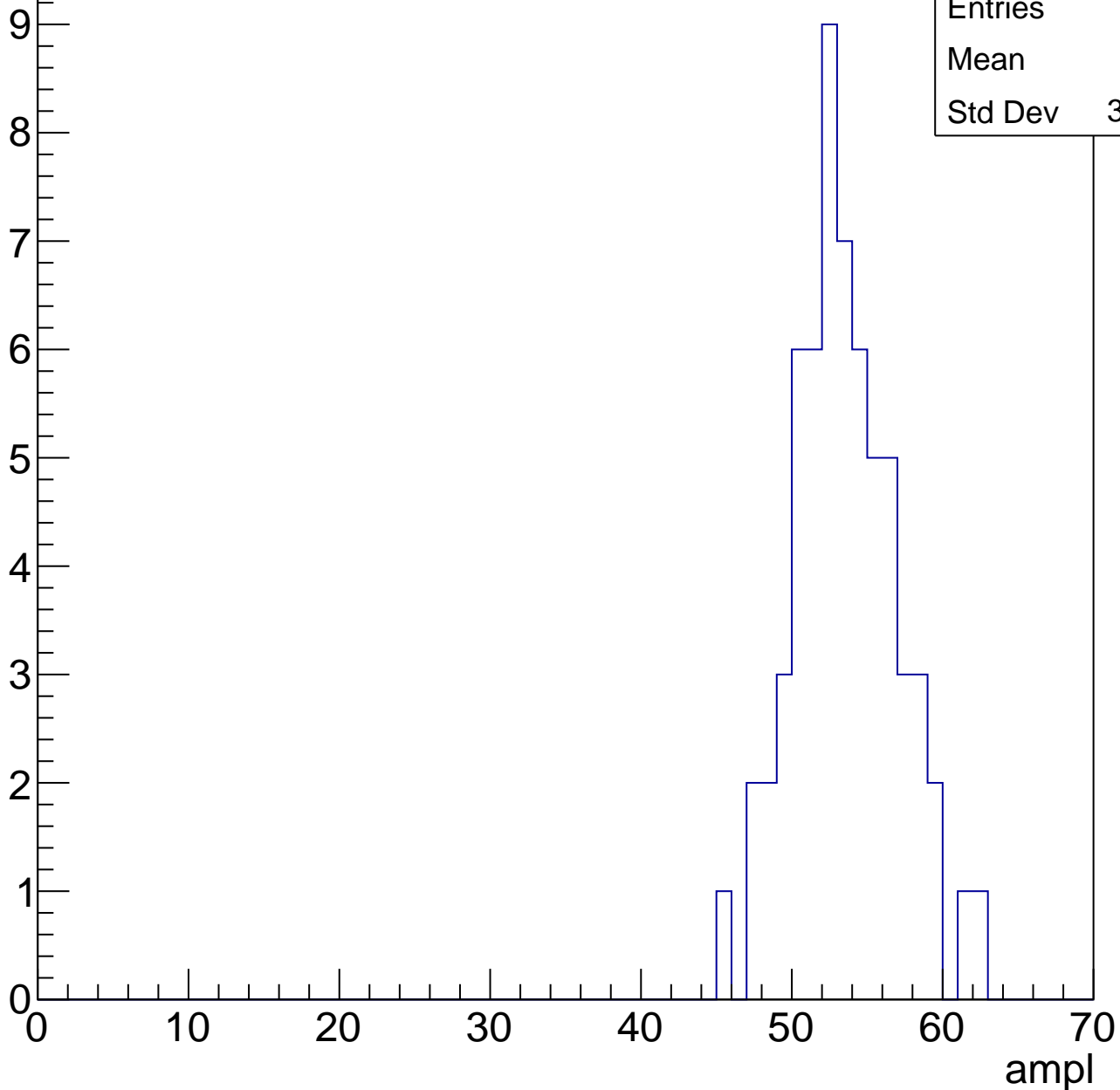


B1L103S, U1-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

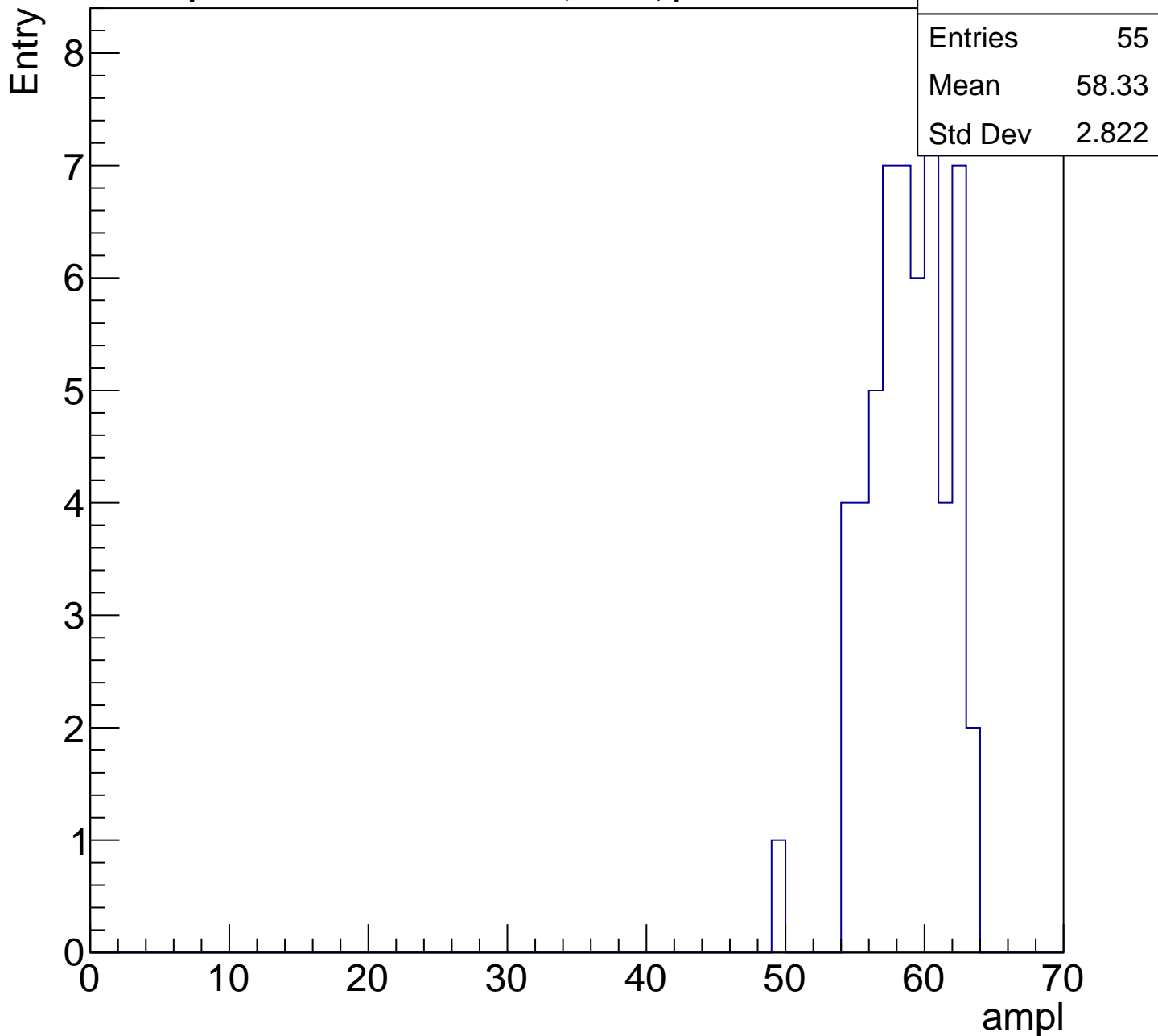
Entry

Entries	62
Mean	53.1
Std Dev	3.435



B1L103S, U1-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

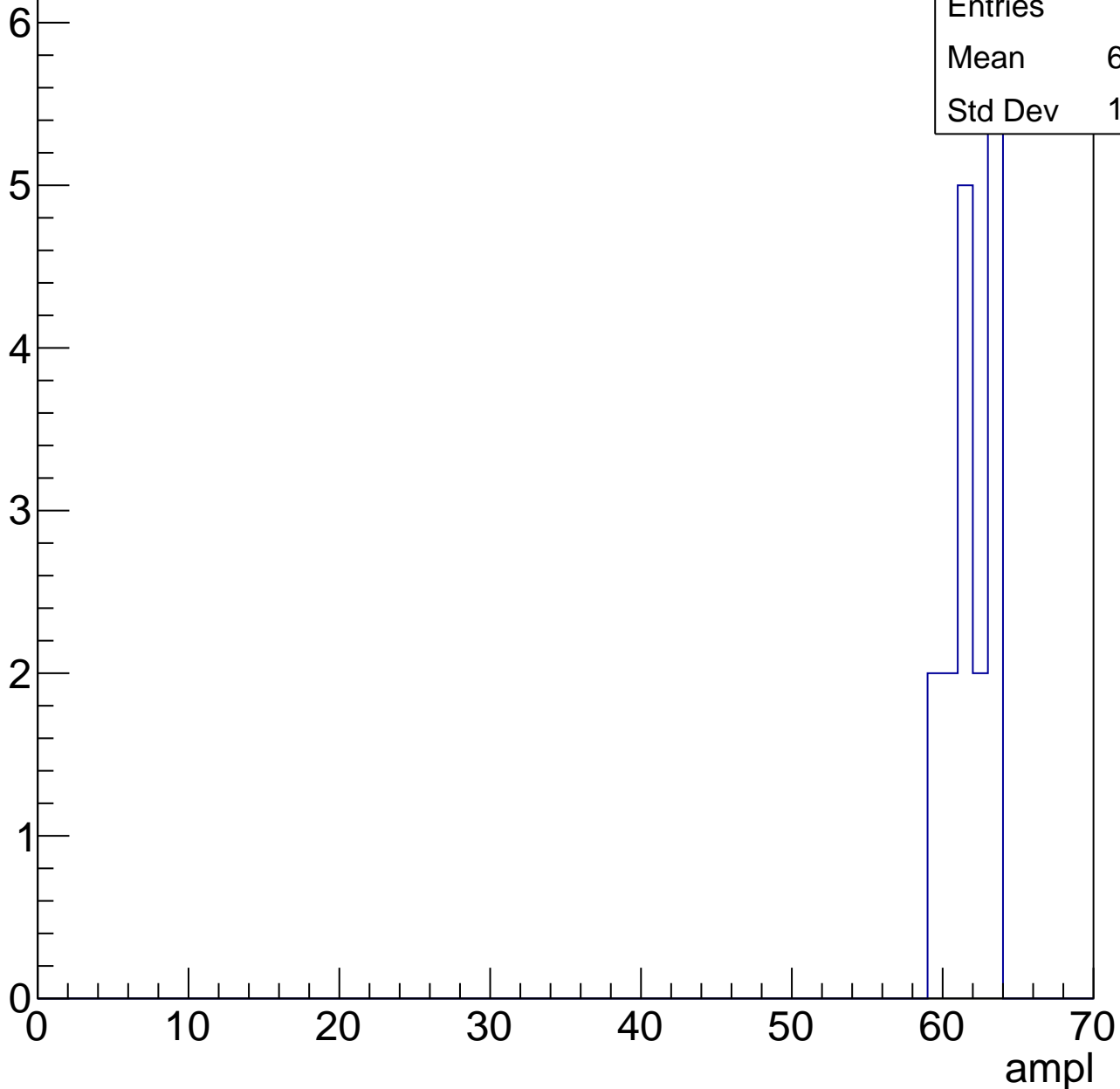


B1L103S, U1-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

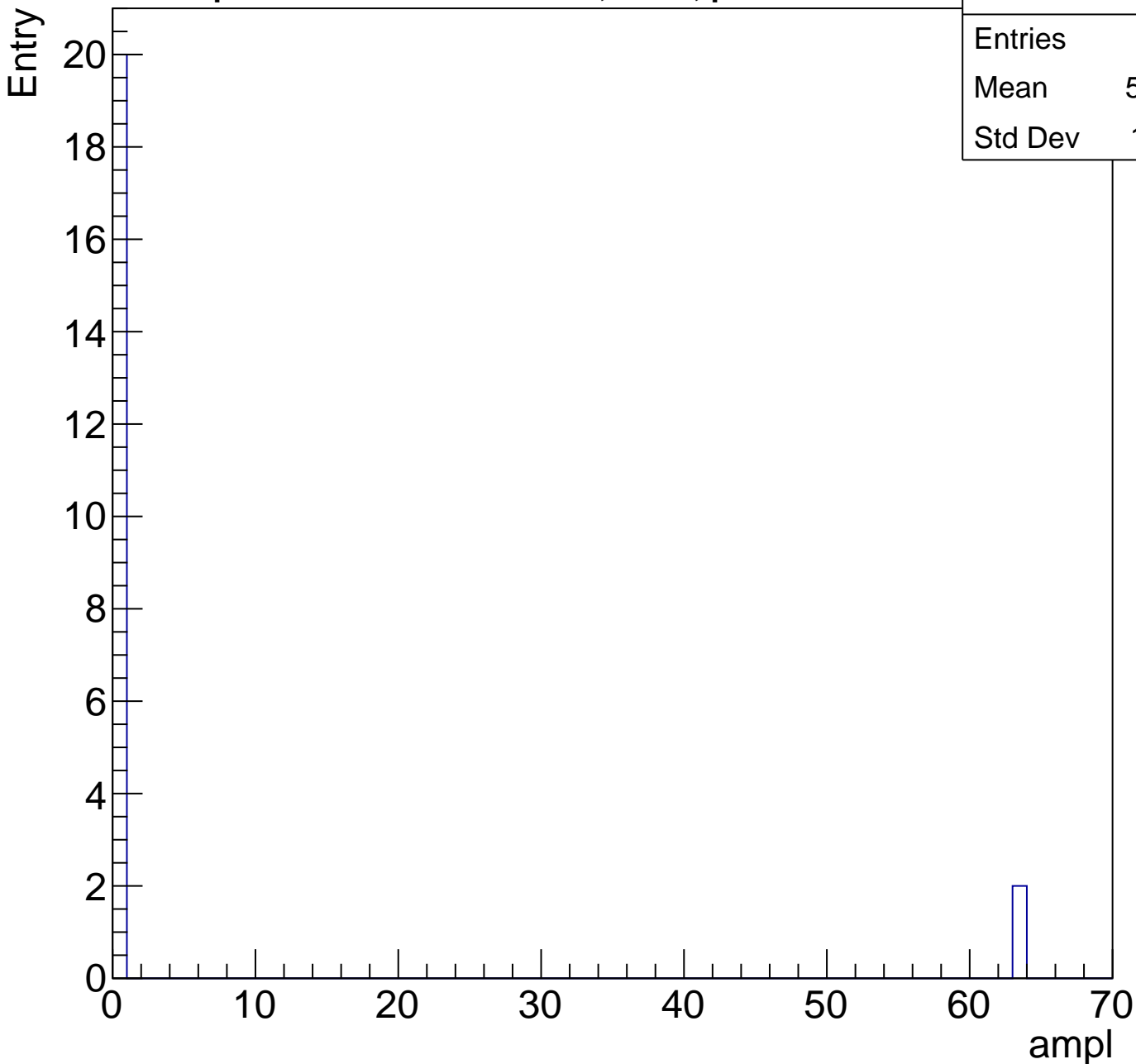
Entries	17
Mean	61.47
Std Dev	1.377



B1L103S, U1-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	5.727
Std Dev	18.11



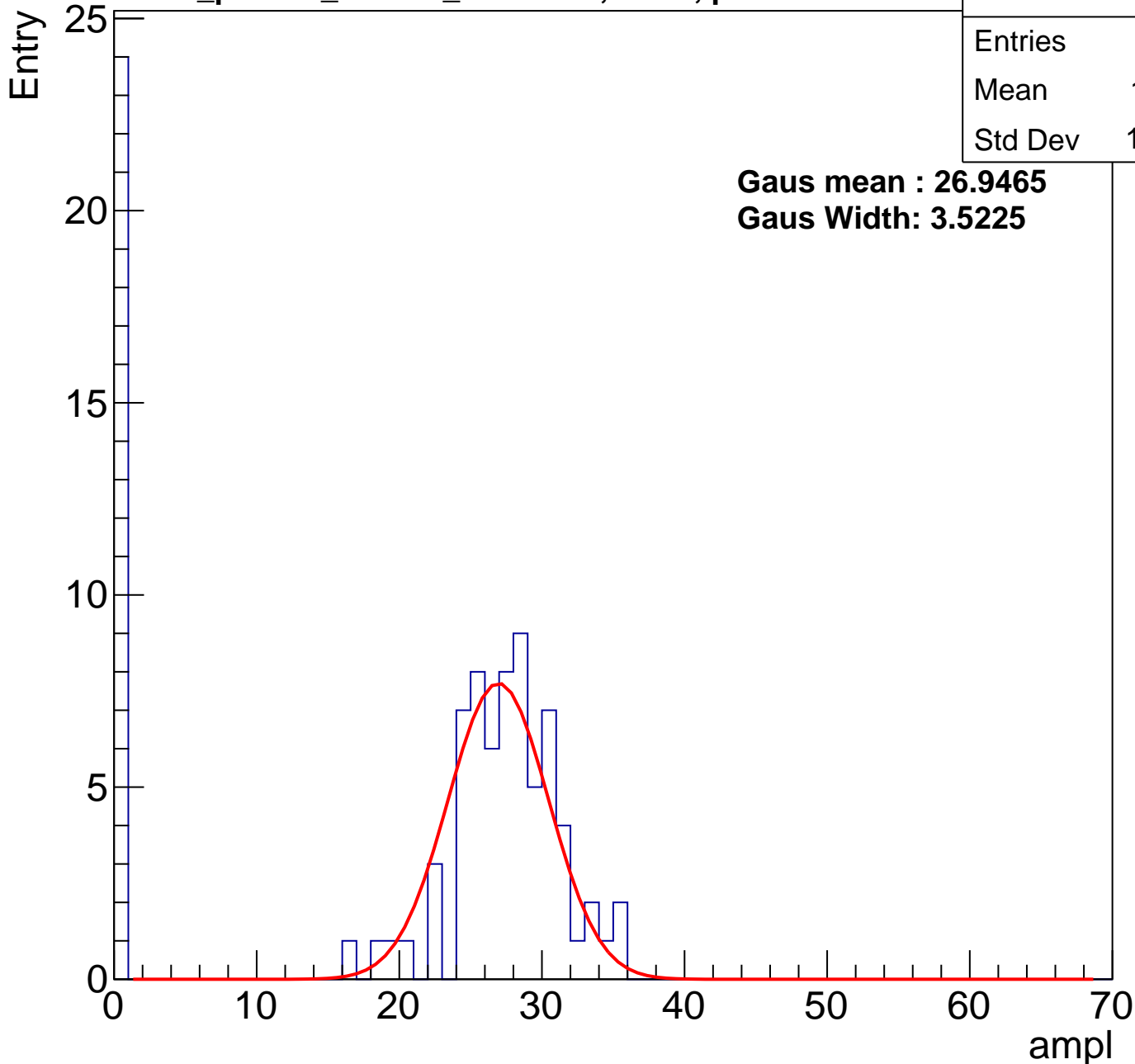
B1L103S, U1-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	19.91
Std Dev	12.33

Gaus mean : 26.9465

Gaus Width: 3.5225



B1L103S, U1-ch44, adc1

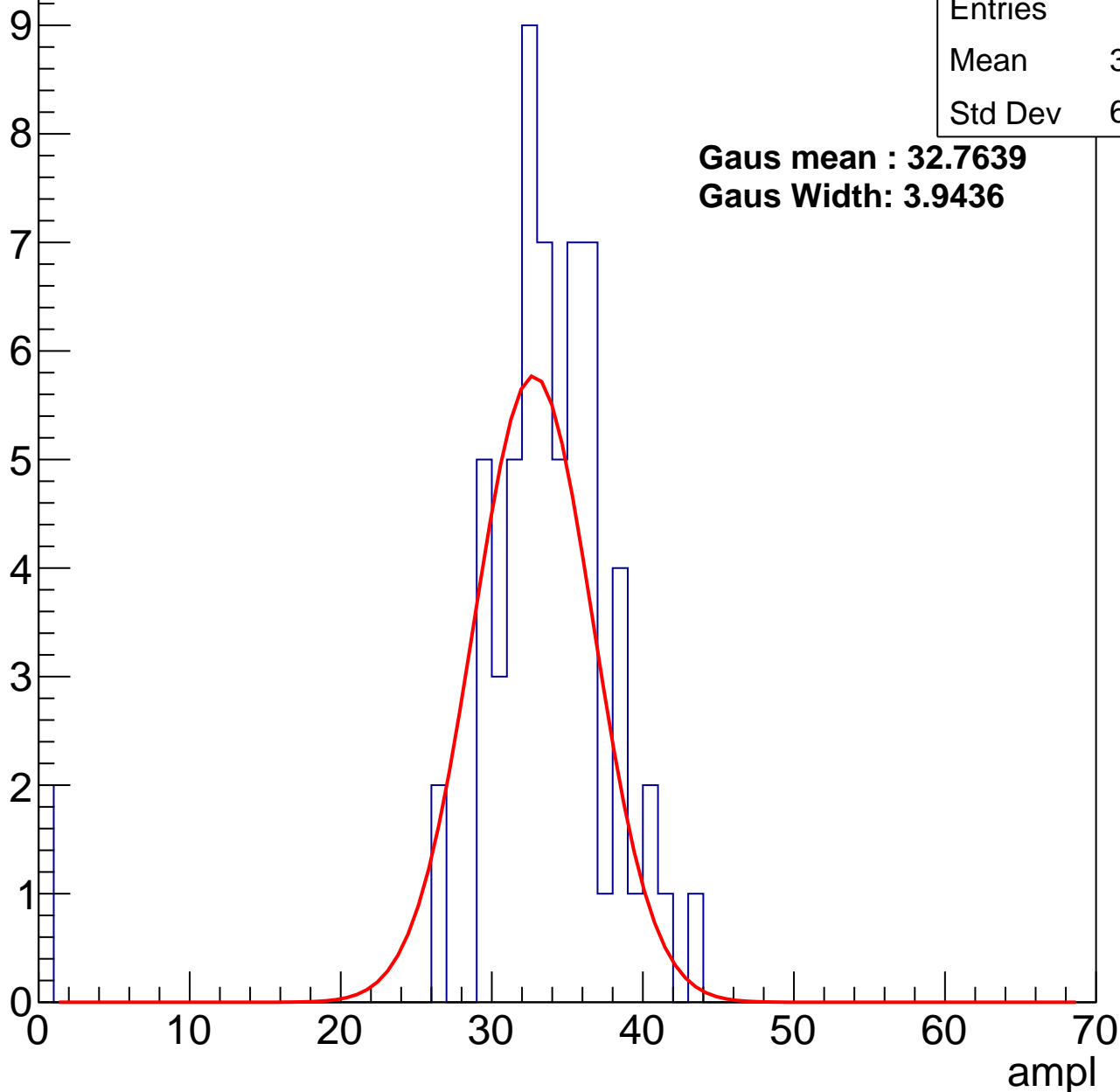
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.58
Std Dev	6.852

Gaus mean : 32.7639

Gaus Width: 3.9436



B1L103S, U1-ch44, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	34.57
Std Dev	14.1

Gaus mean : 40.9893

Gaus Width: 3.7503

Entry

10

8

6

4

2

0

0

10

20

30

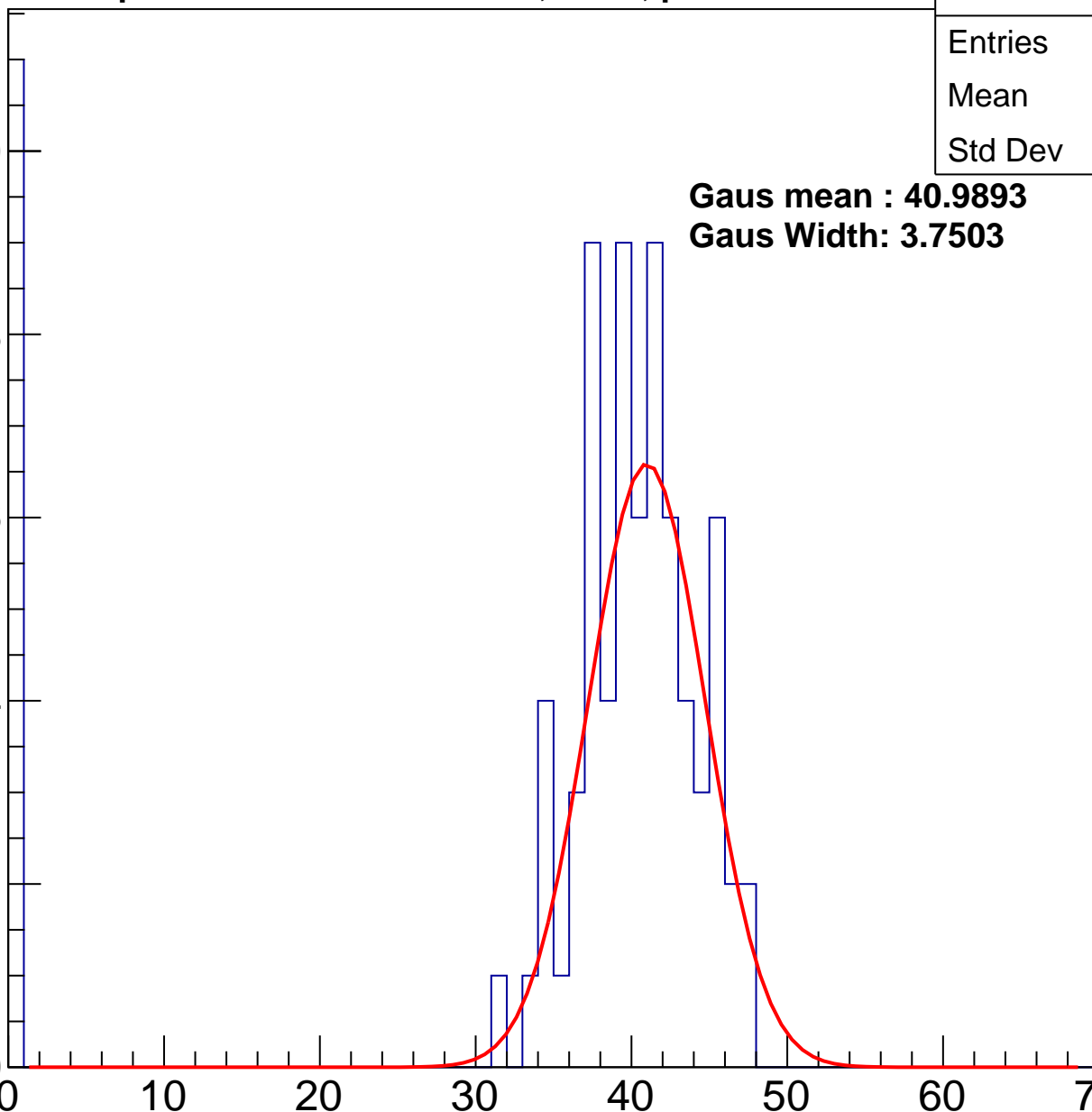
40

50

60

70

ampl

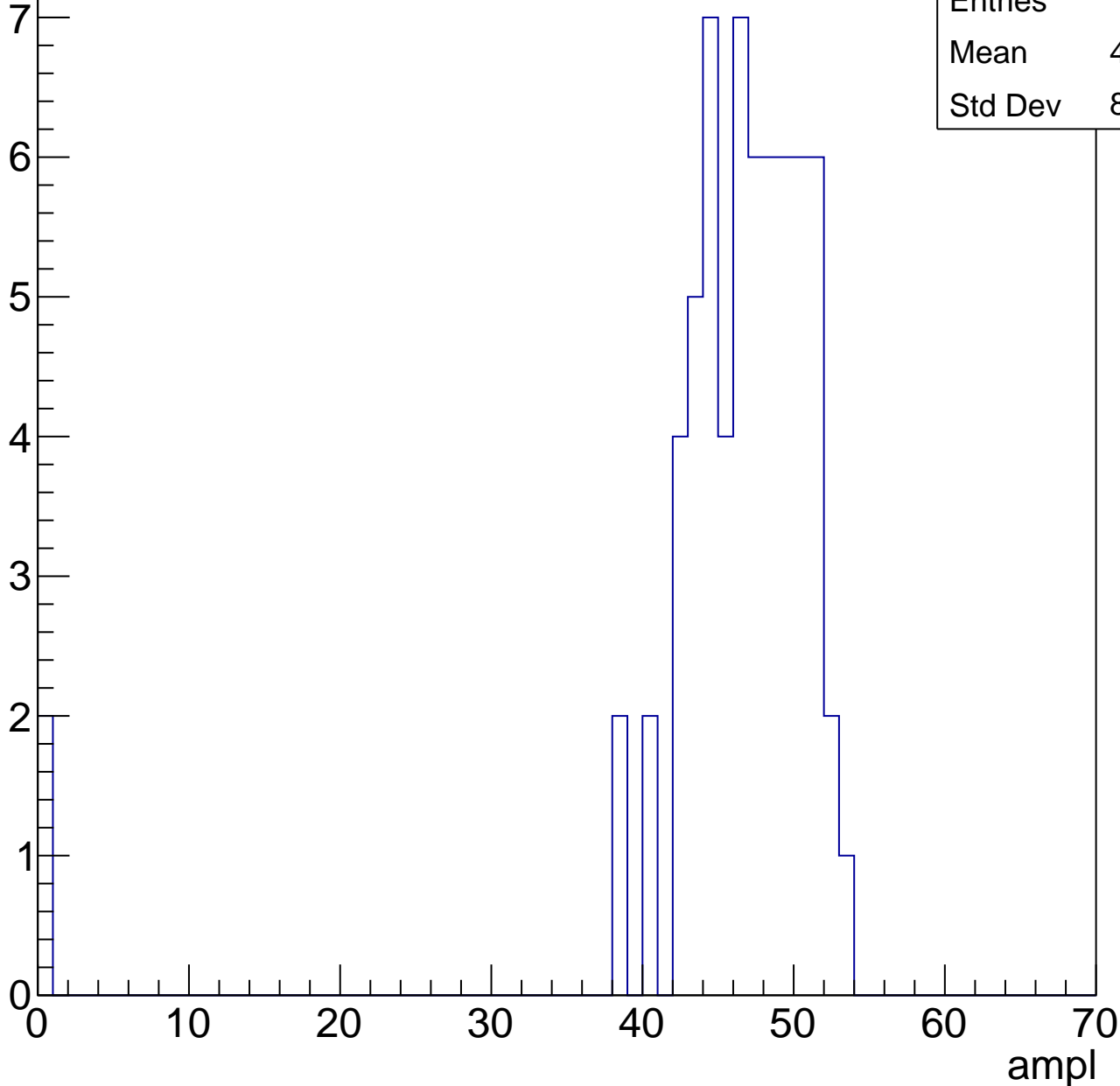


B1L103S, U1-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	45.09
Std Dev	8.677

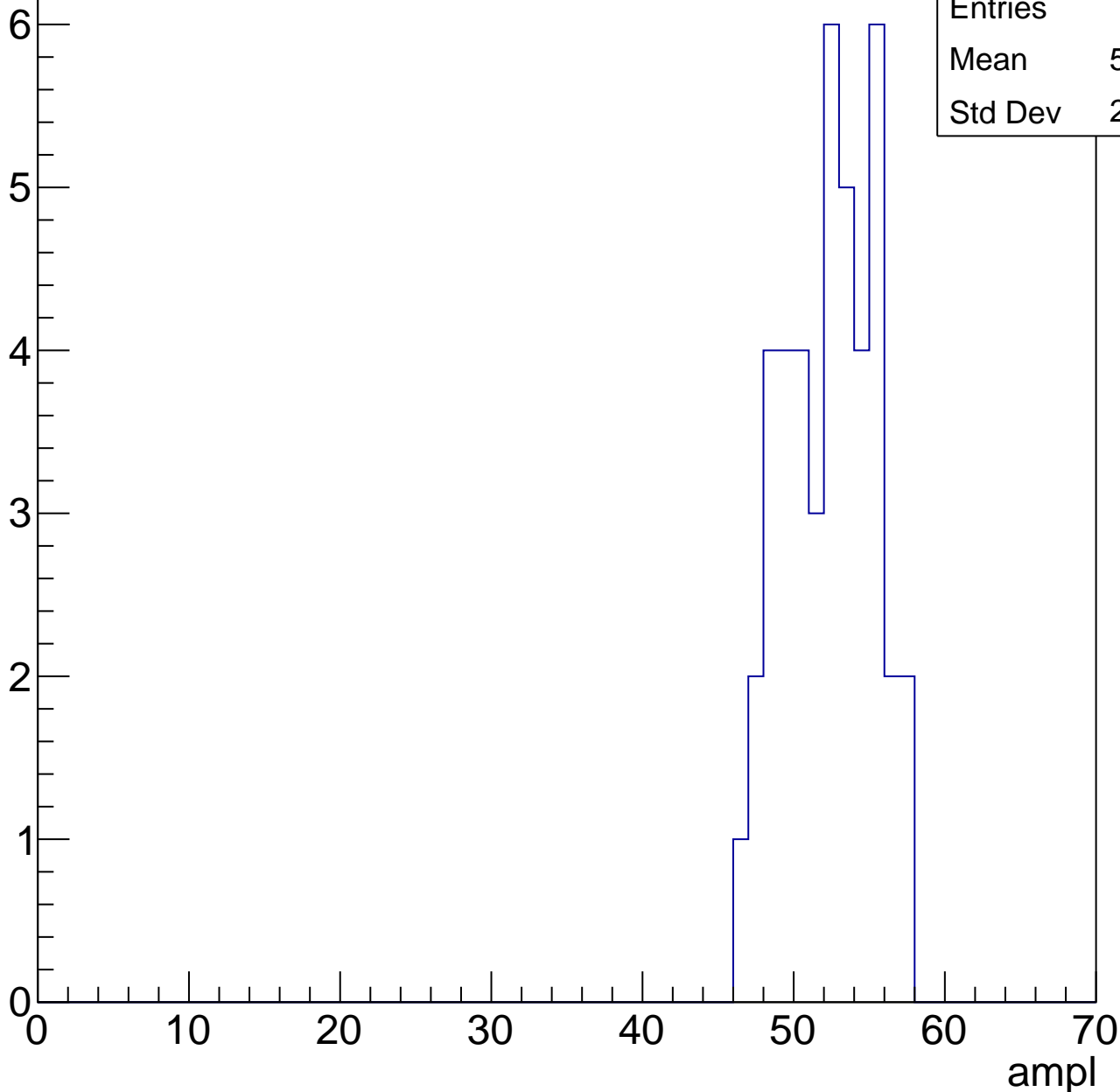


B1L103S, U1-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	51.86
Std Dev	2.898

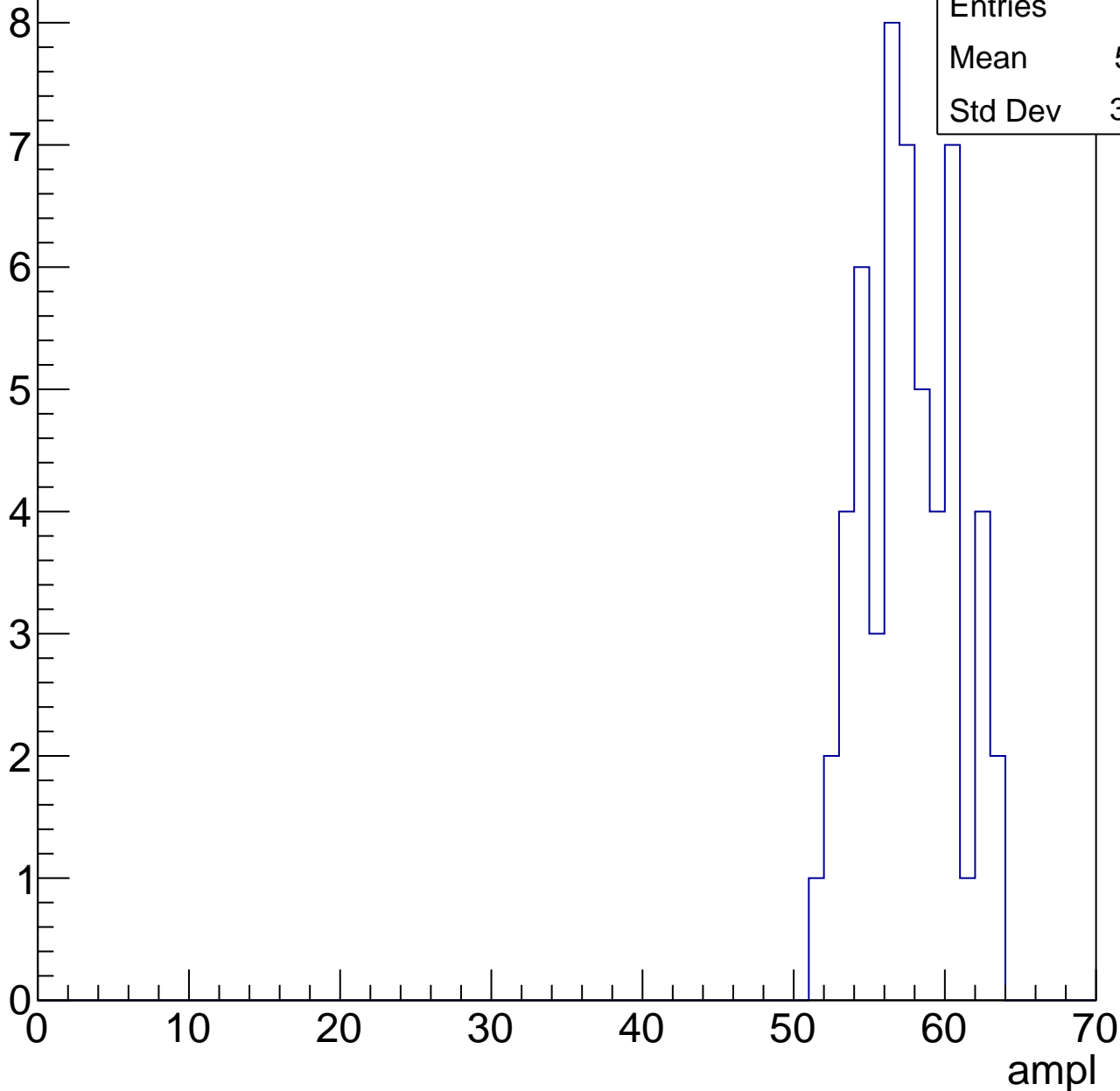


B1L103S, U1-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.11
Std Dev	3.029

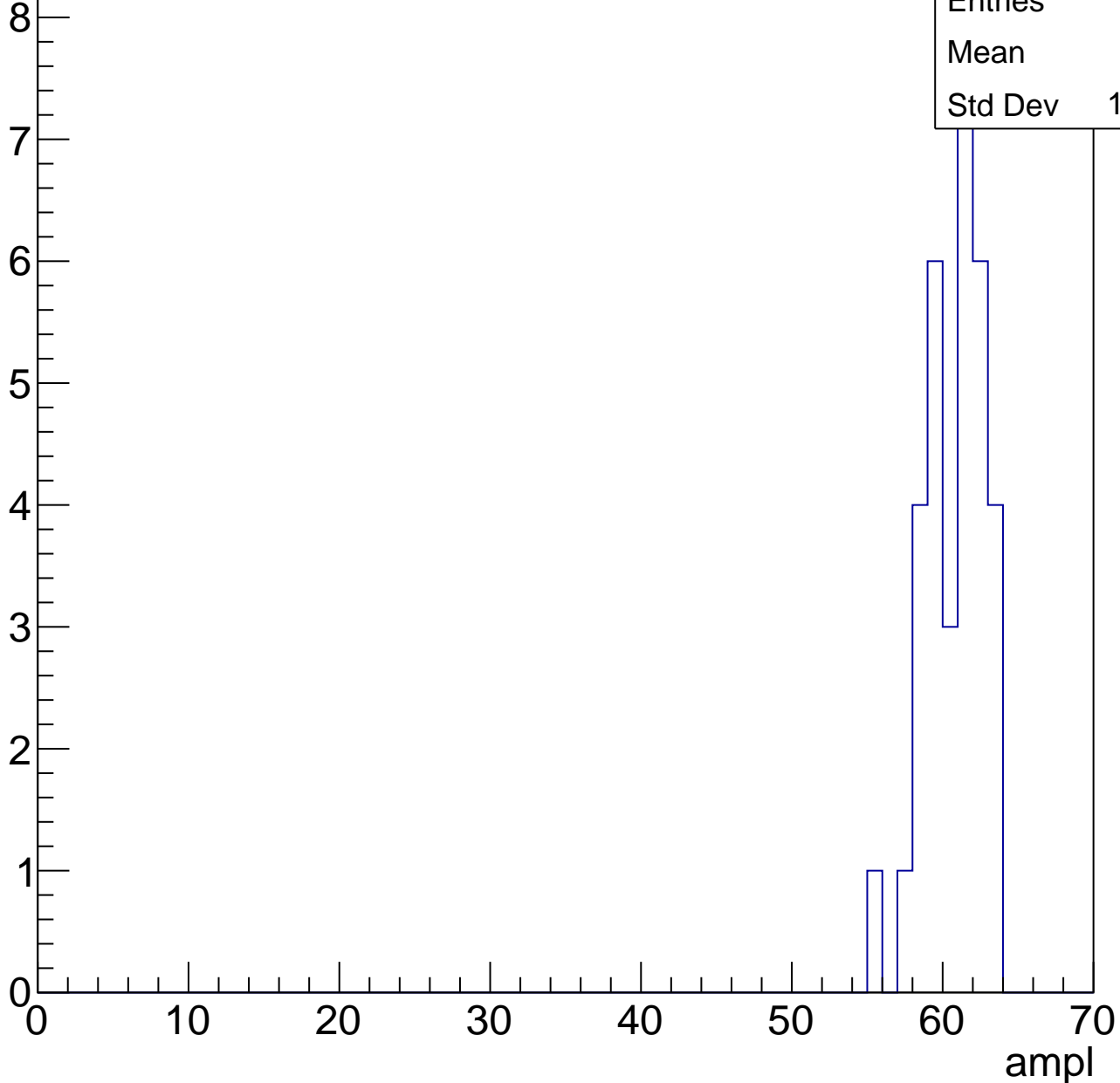


B1L103S, U1-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	60.3
Std Dev	1.915



B1L103S, U1-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	26
Mean	14.27
Std Dev	26.07

ampl

0 10 20 30 40 50 60 70

B1L103S, U1-ch45, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.91
Std Dev	10.08

Gaus mean : 28.0679

Gaus Width: 3.1447

Entry

10

8

6

4

2

0

0

10

20

30

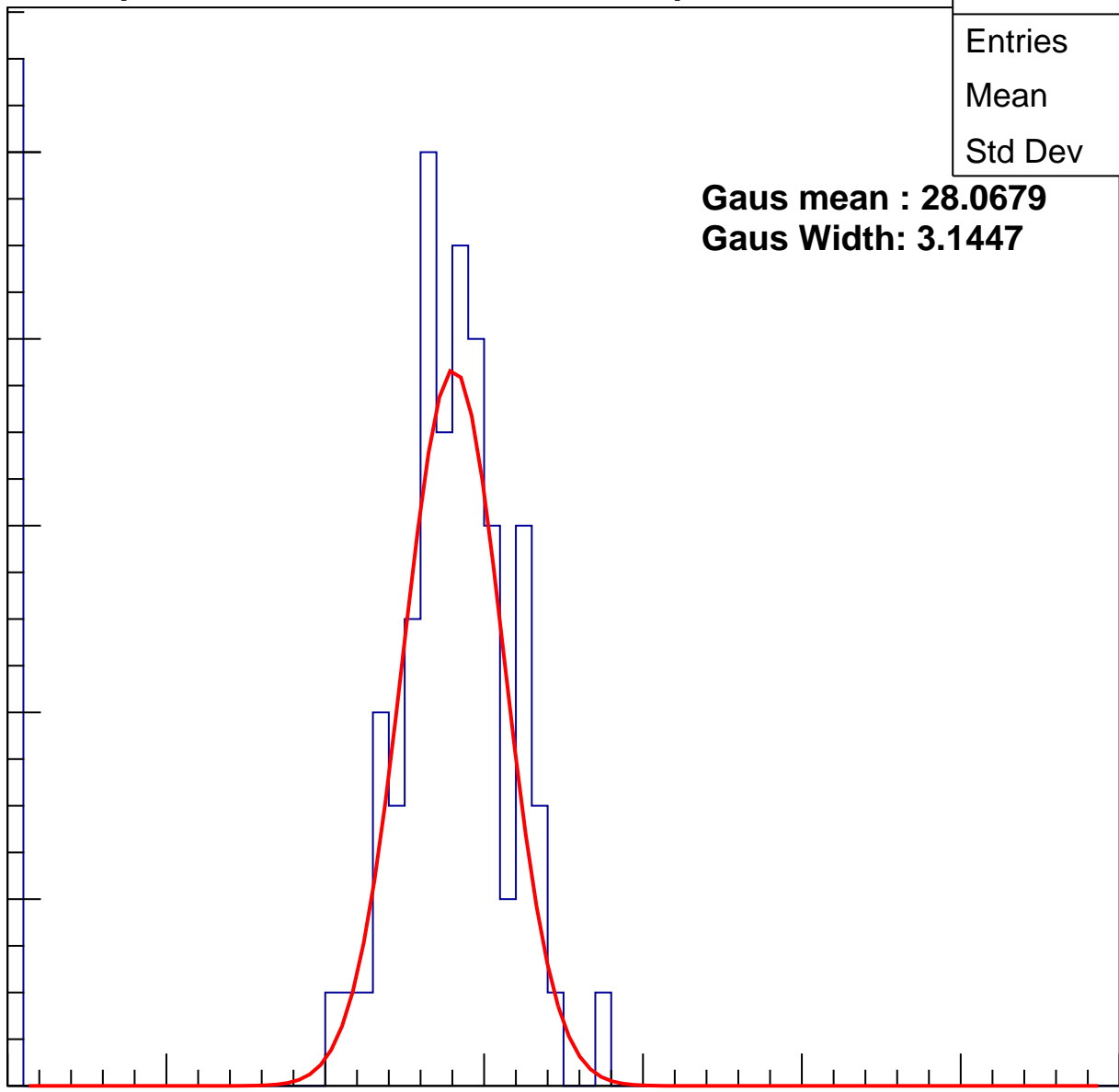
40

50

60

70

ampl



B1L103S, U1-ch45, adc1

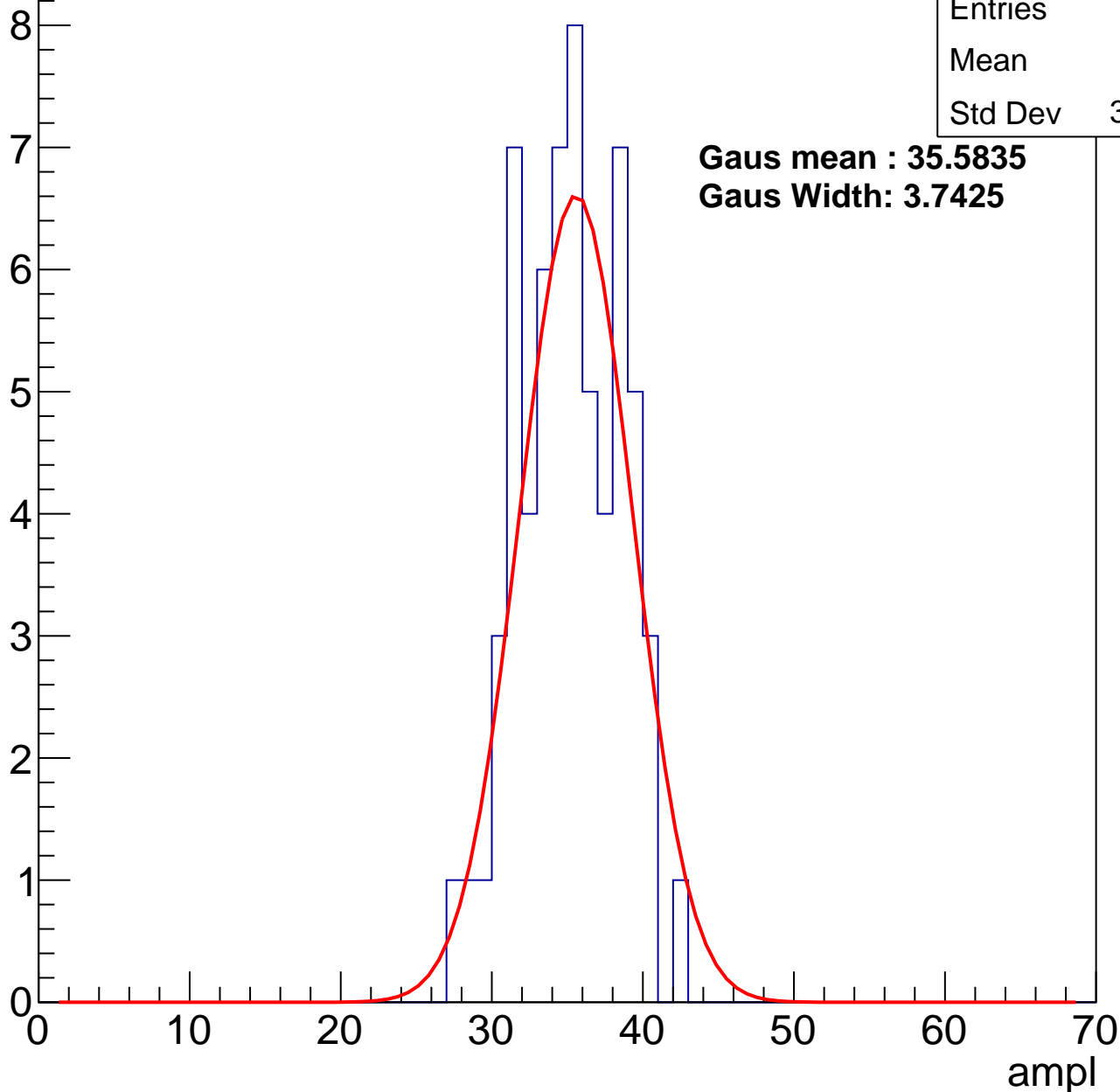
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.7
Std Dev	3.298

Gaus mean : 35.5835

Gaus Width: 3.7425



B1L103S, U1-ch45, adc2

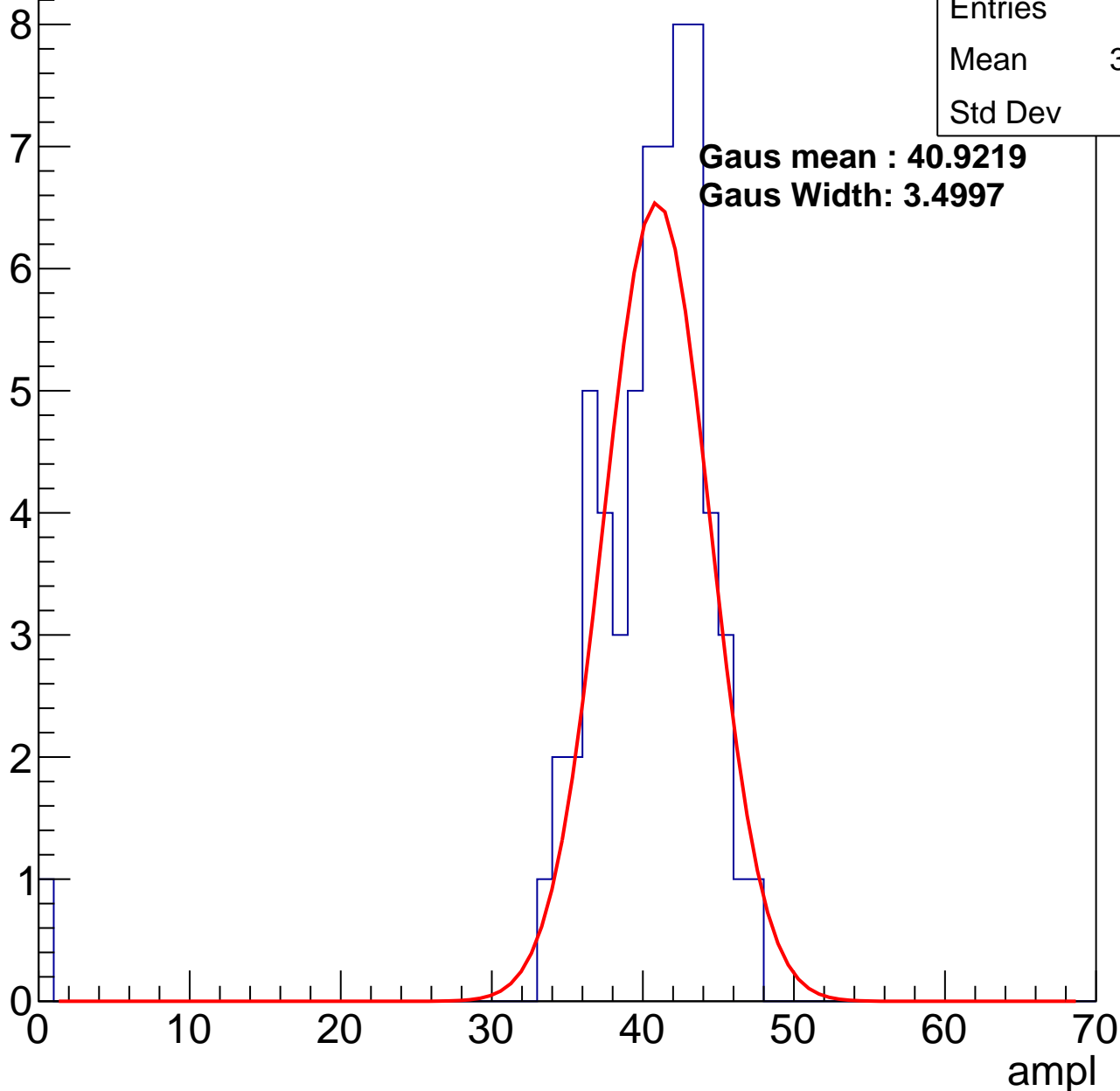
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.66
Std Dev	6

Gaus mean : 40.9219

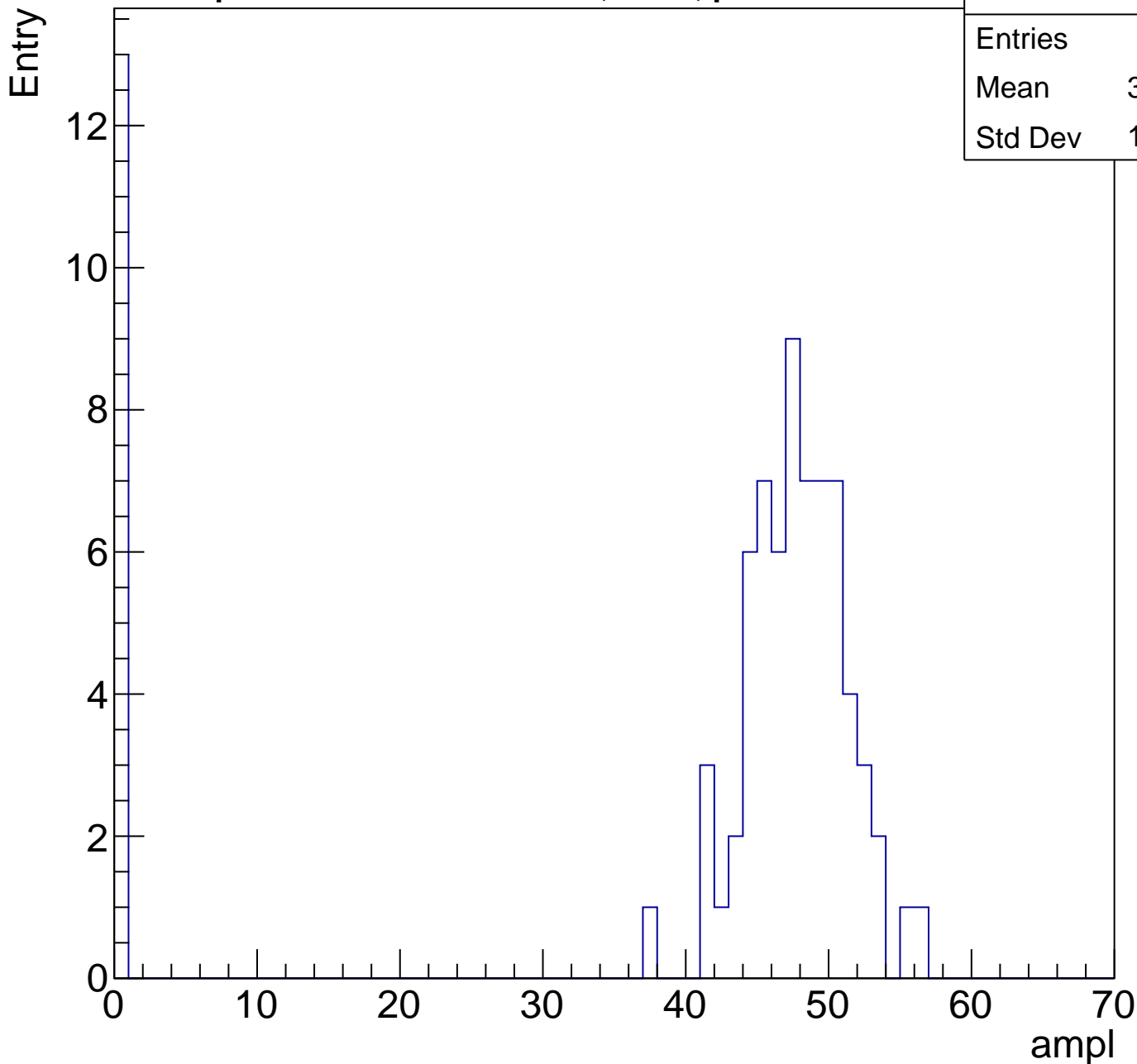
Gaus Width: 3.4997



B1L103S, U1-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	39.65
Std Dev	17.75

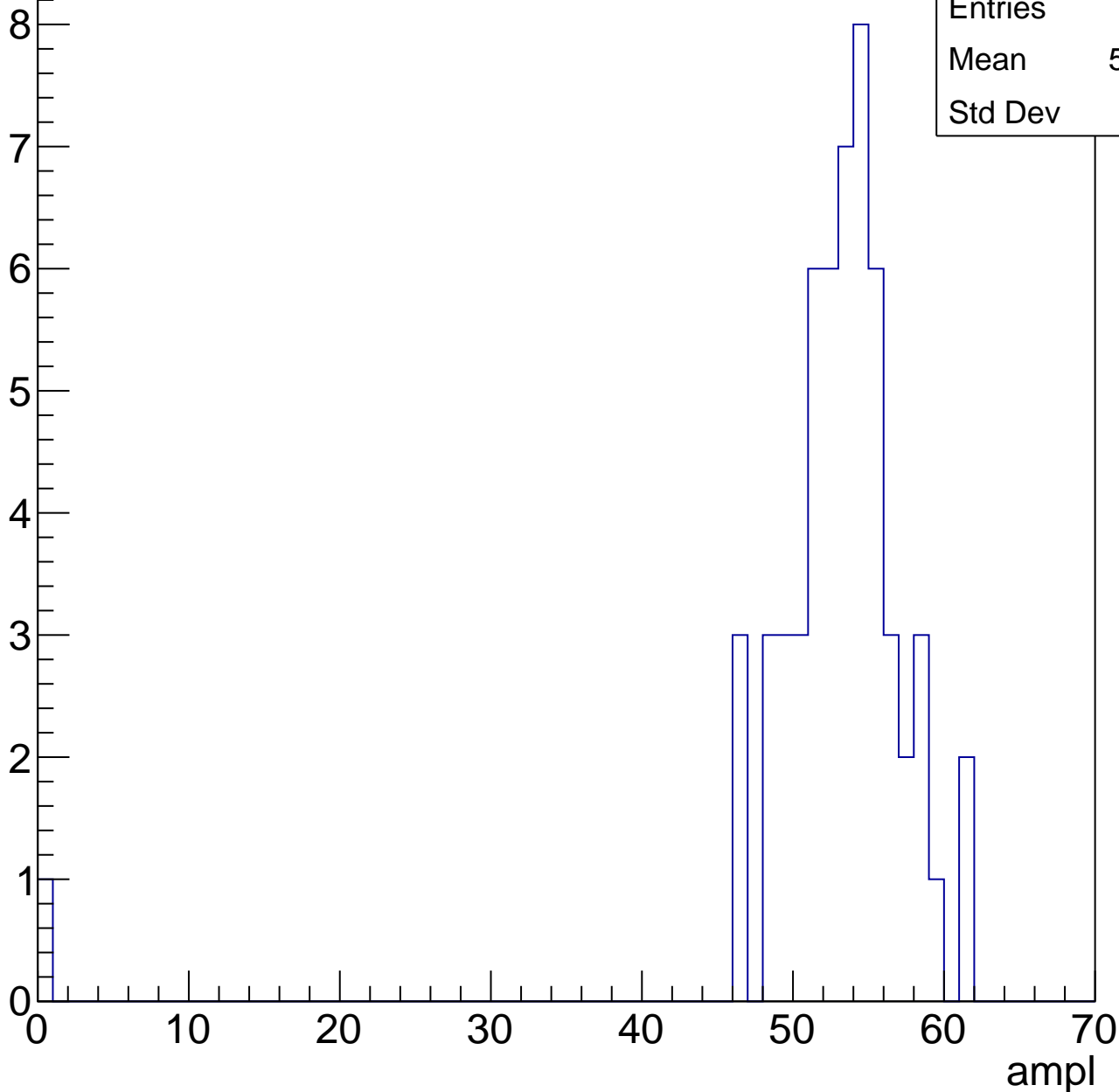


B1L103S, U1-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

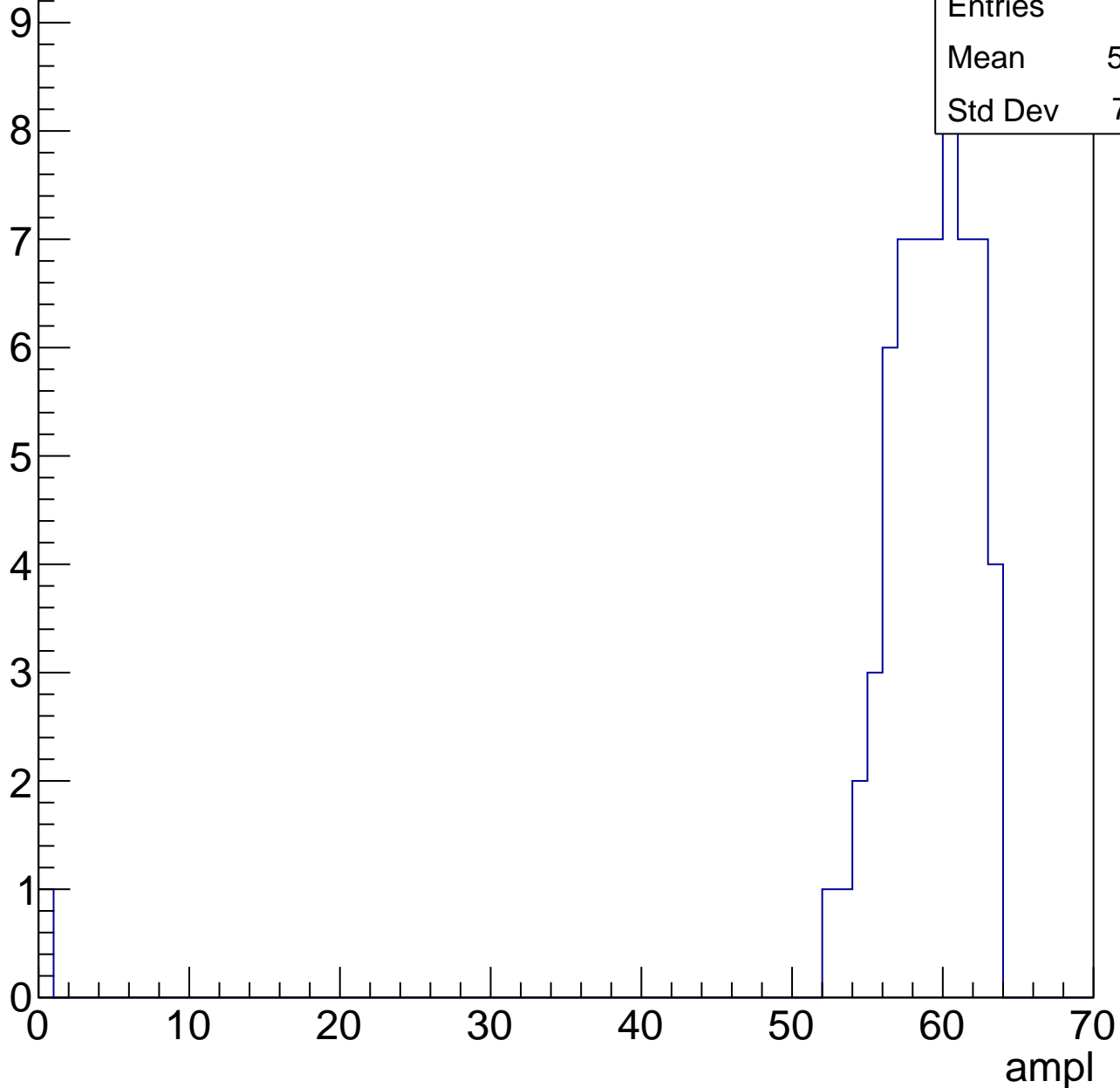
Entries	57
Mean	52.05
Std Dev	7.74



B1L103S, U1-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

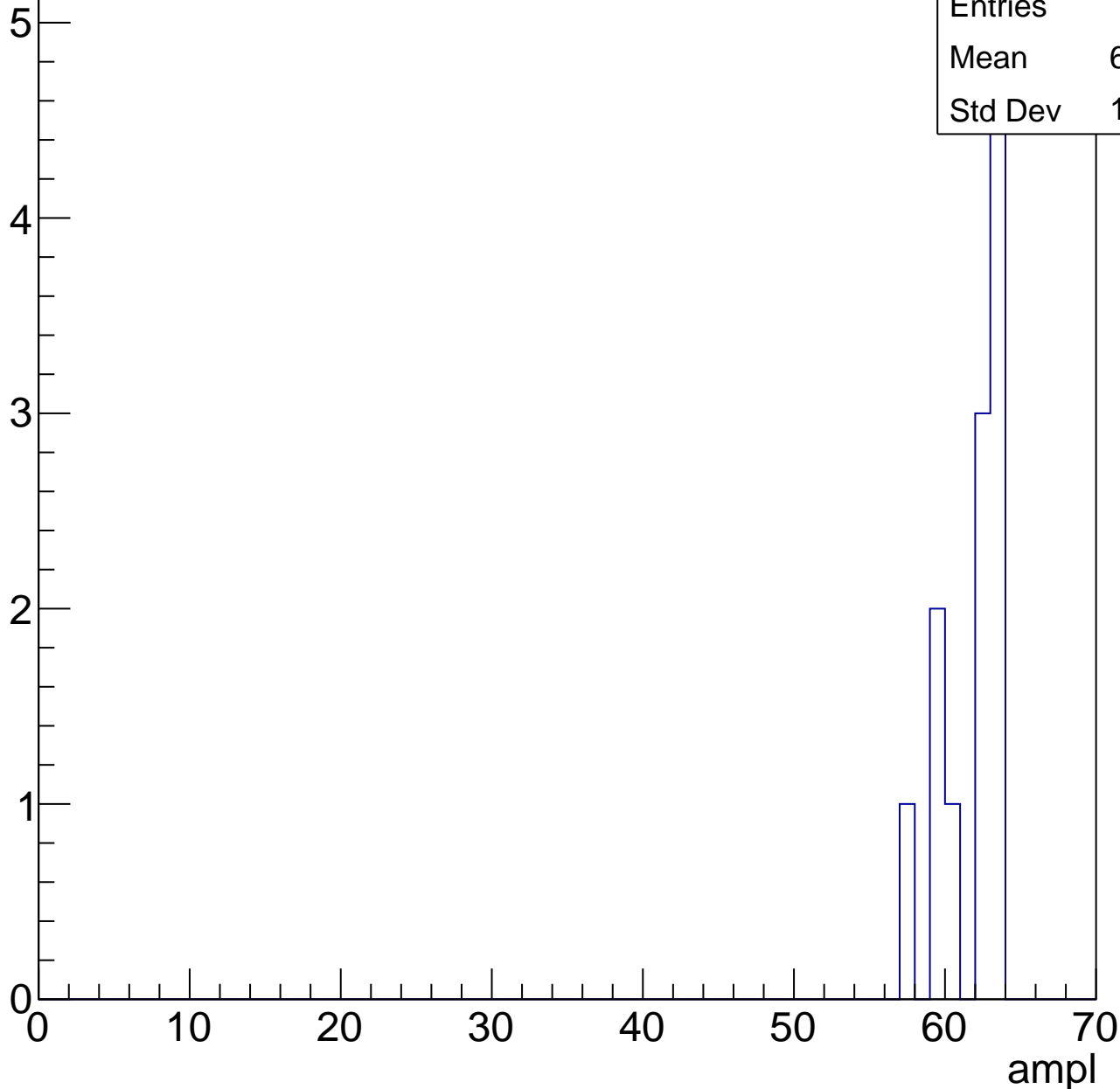


B1L103S, U1-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.33
Std Dev	1.972



B1L103S, U1-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



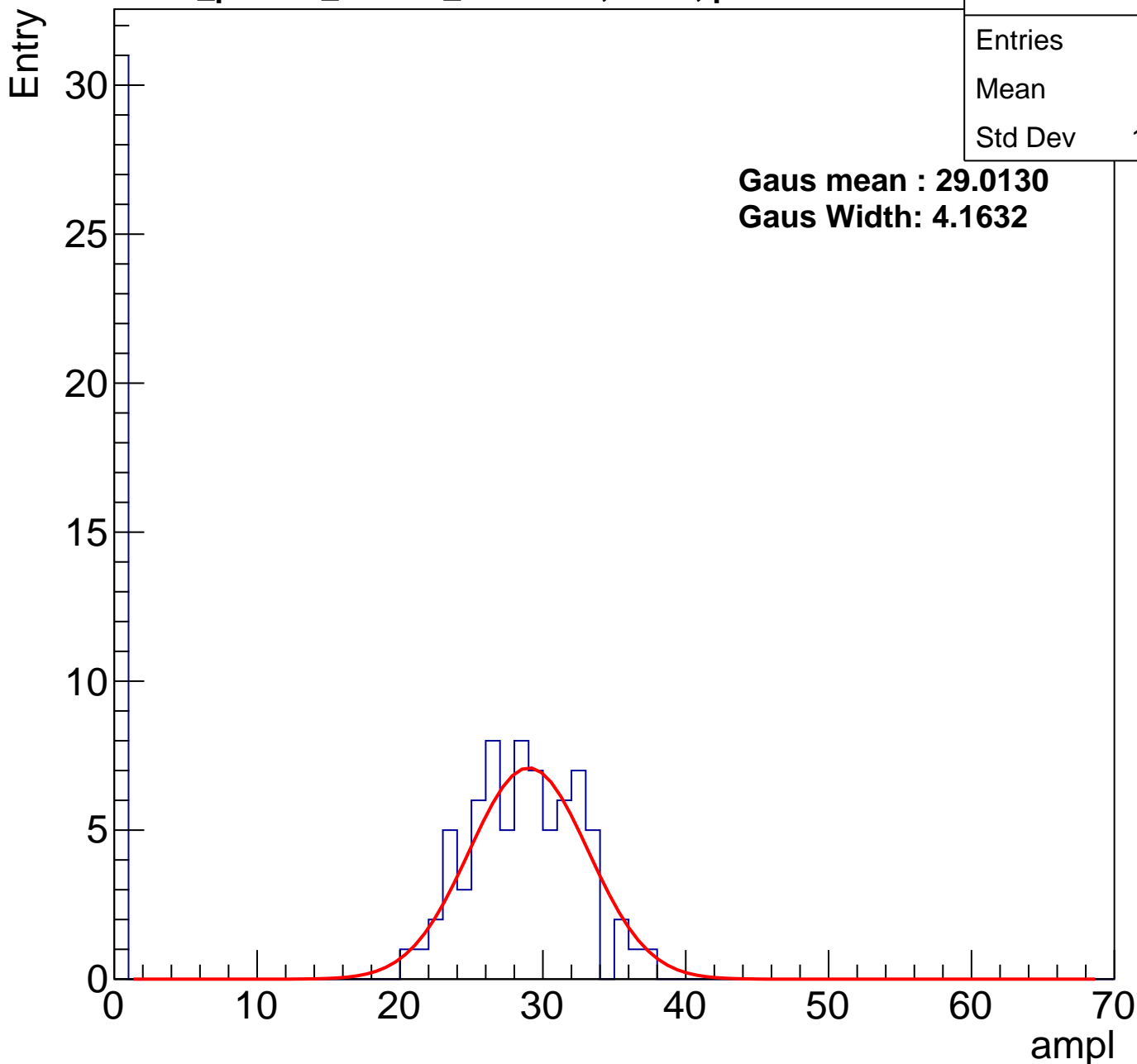
B1L103S, U1-ch46, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	19.81
Std Dev	13.28

Gaus mean : 29.0130

Gaus Width: 4.1632



B1L103S, U1-ch46, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	29.43
Std Dev	13.09

Gaus mean : 35.5313

Gaus Width: 4.0456

Entry

10

8

6

4

2

0

0

10

20

30

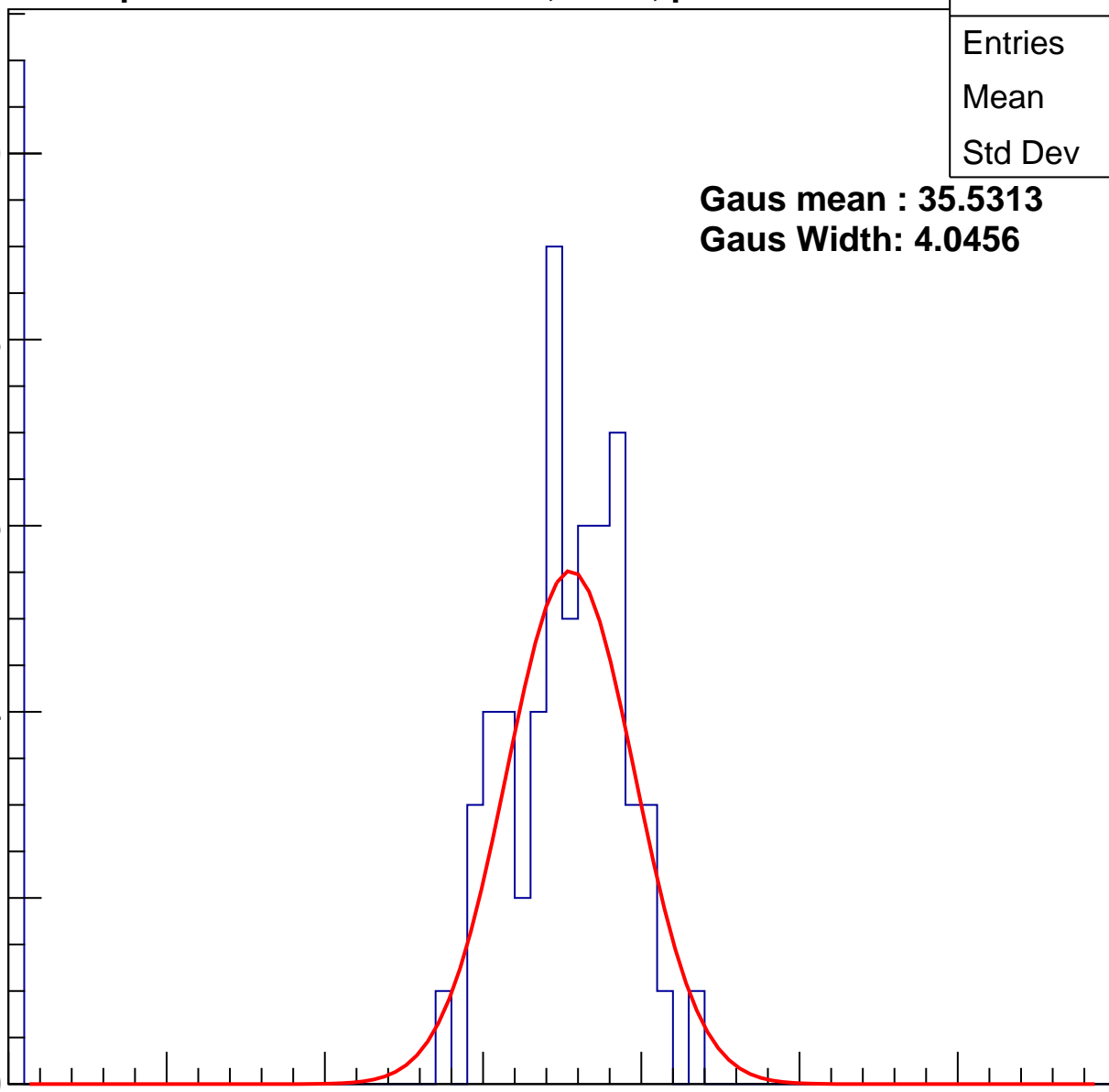
40

50

60

70

ampl



B1L103S, U1-ch46, adc2

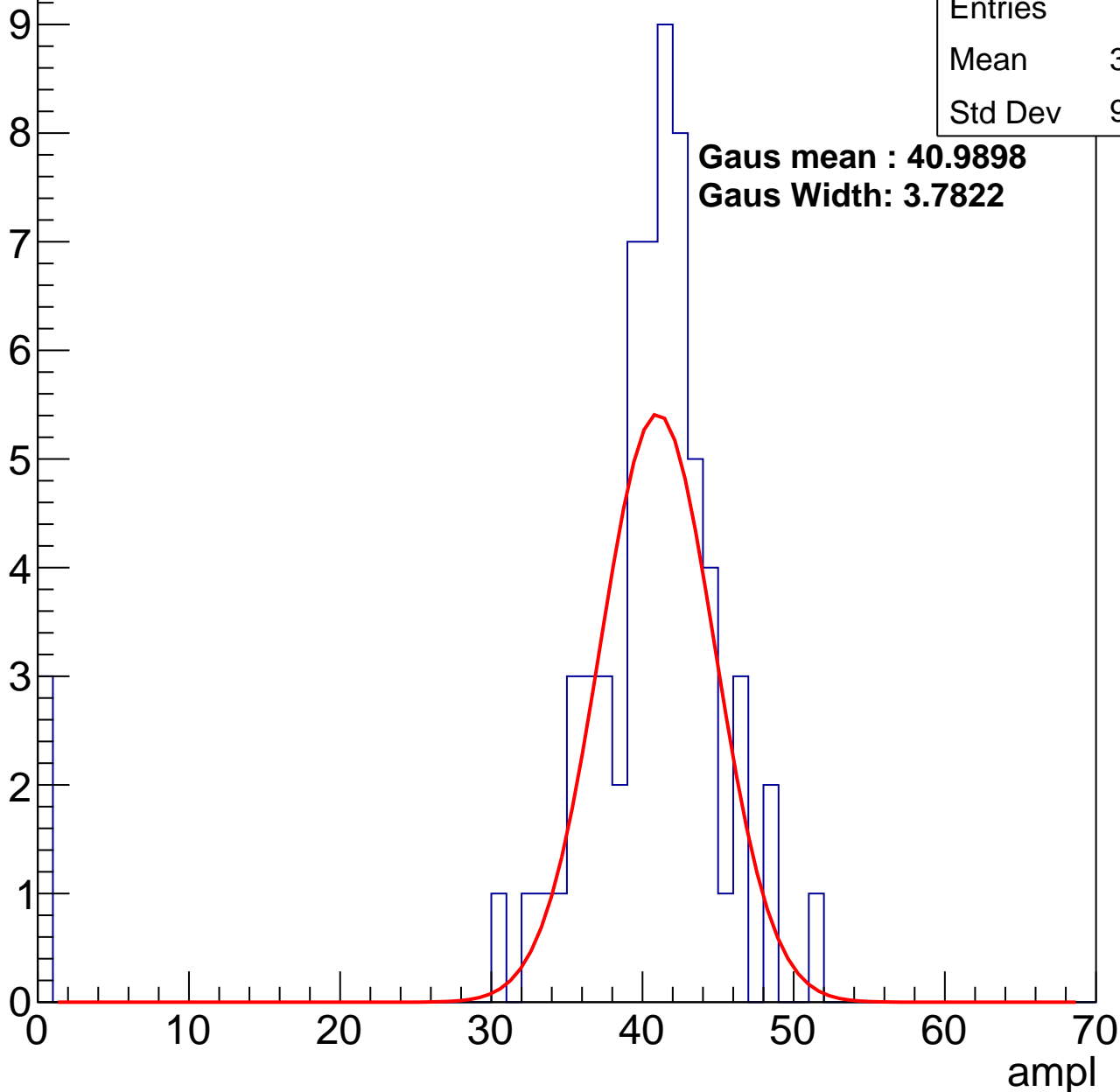
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.58
Std Dev	9.295

Gaus mean : 40.9898

Gaus Width: 3.7822

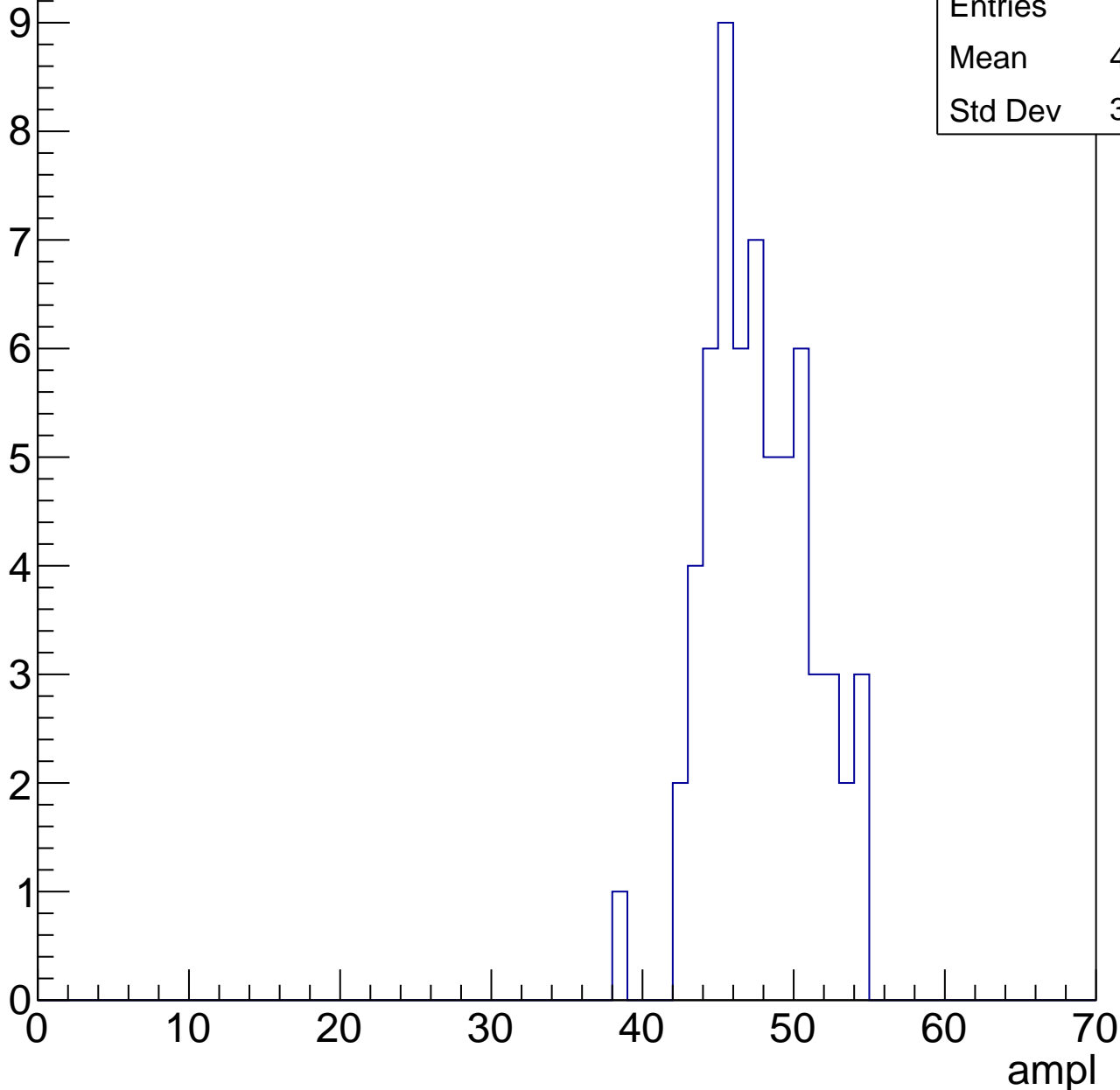


B1L103S, U1-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.26
Std Dev	3.379

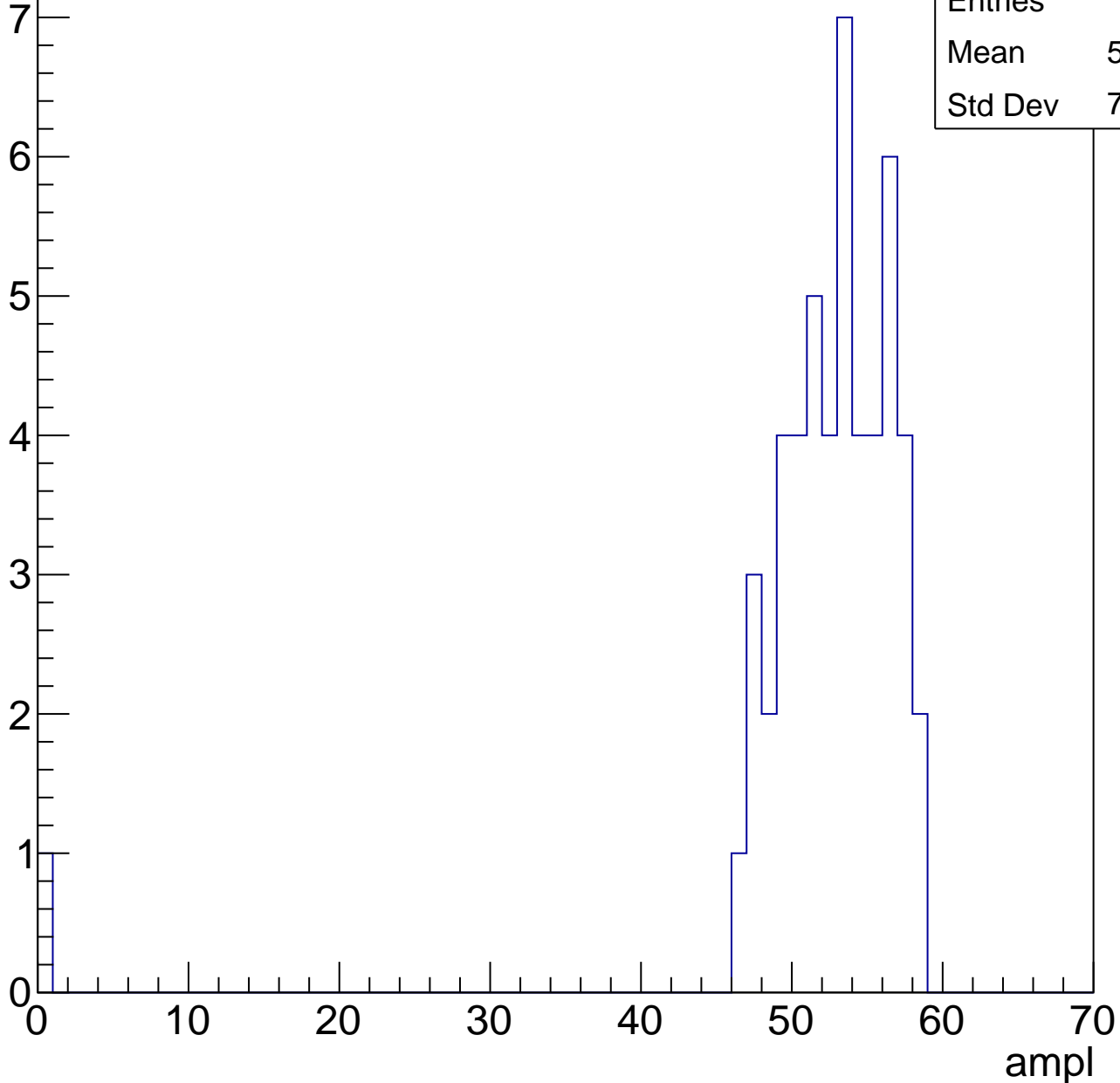


B1L103S, U1-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.55
Std Dev	7.947

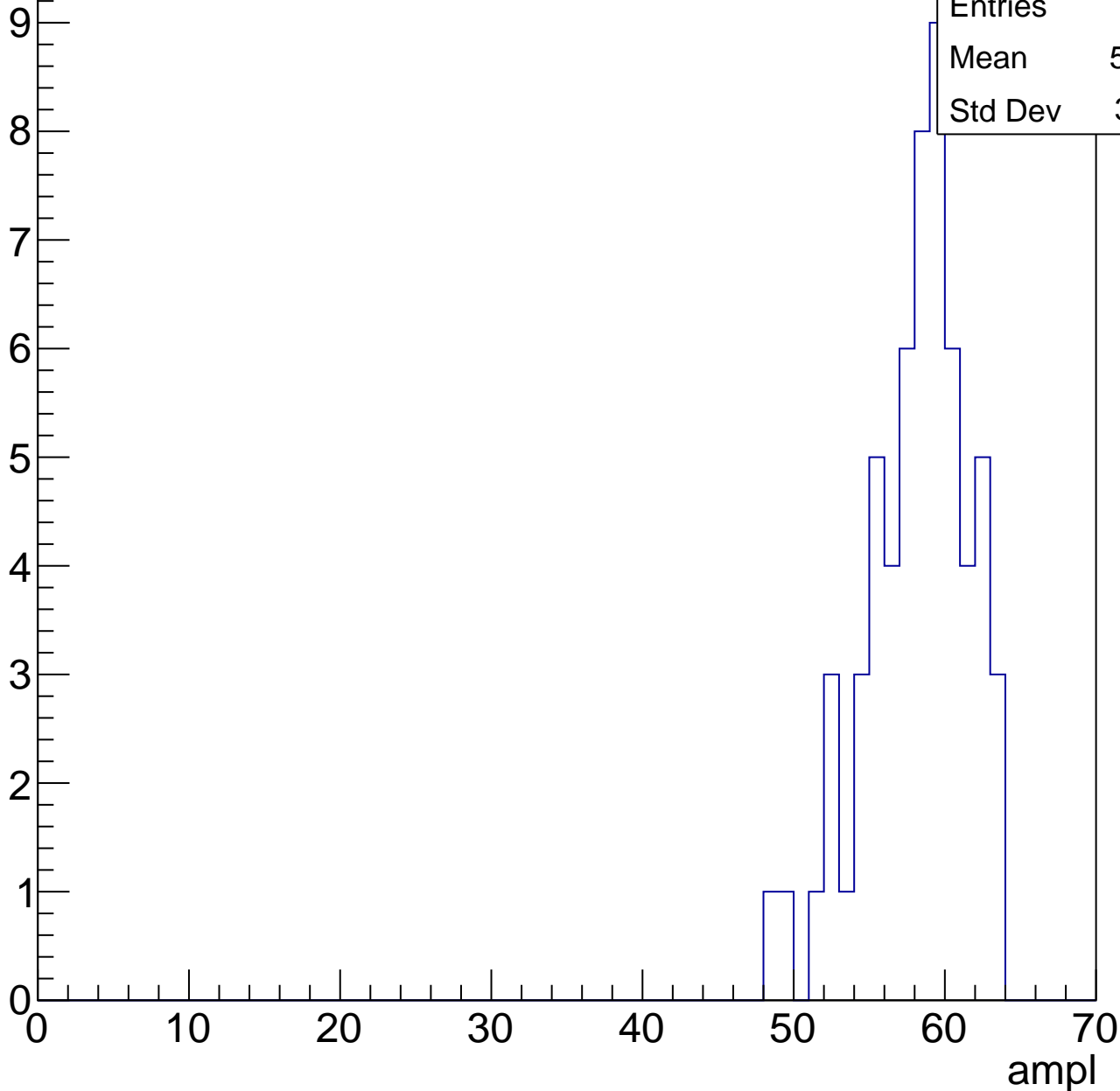


B1L103S, U1-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.63
Std Dev	3.401

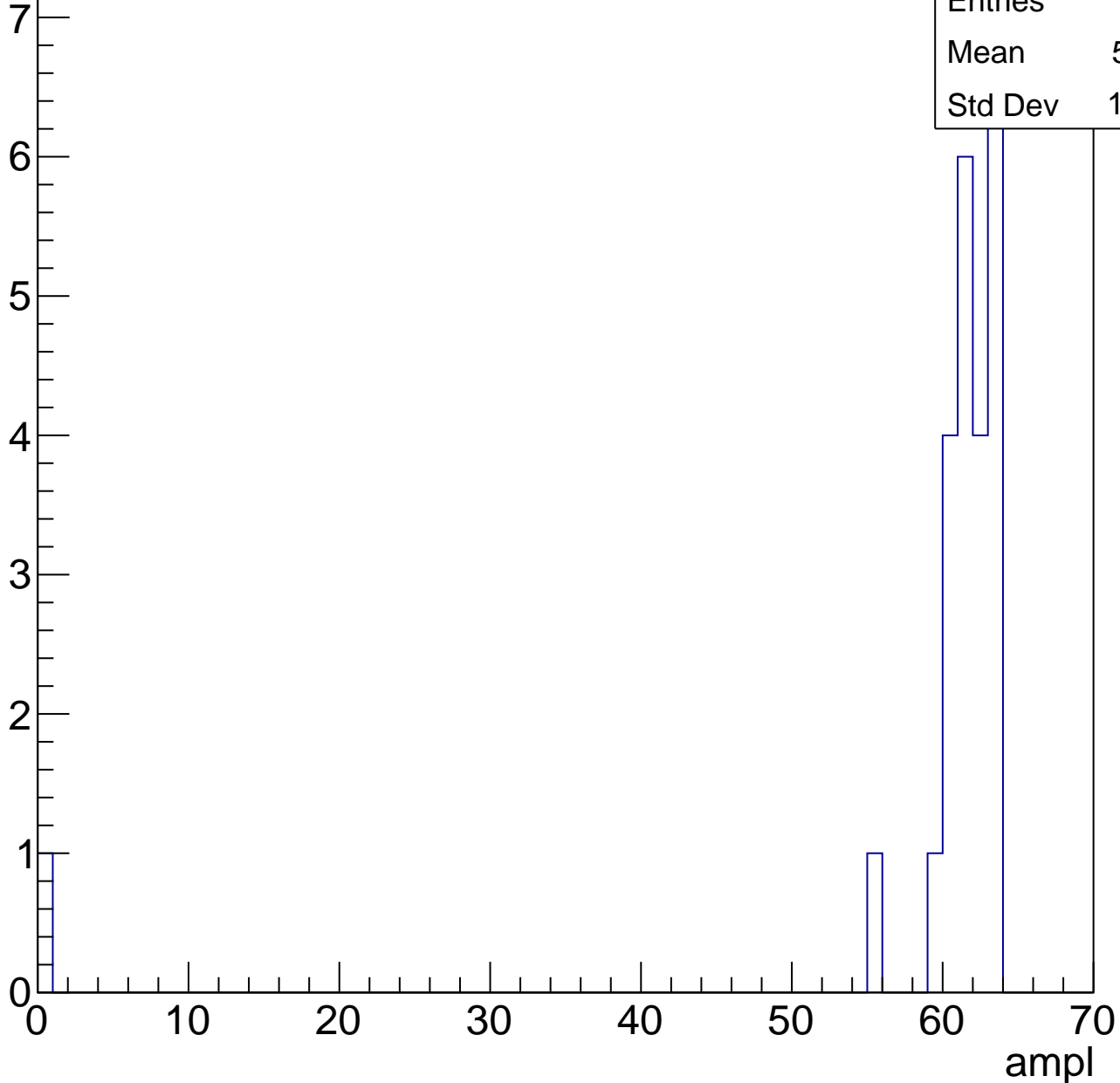


B1L103S, U1-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.71
Std Dev	12.37



B1L103S, U1-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U1-ch47, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	134
Mean	15.81
Std Dev	13.86

Gaus mean : 27.8959

Gaus Width: 3.6854

Entry

50

40

30

20

10

0

0

10

20

30

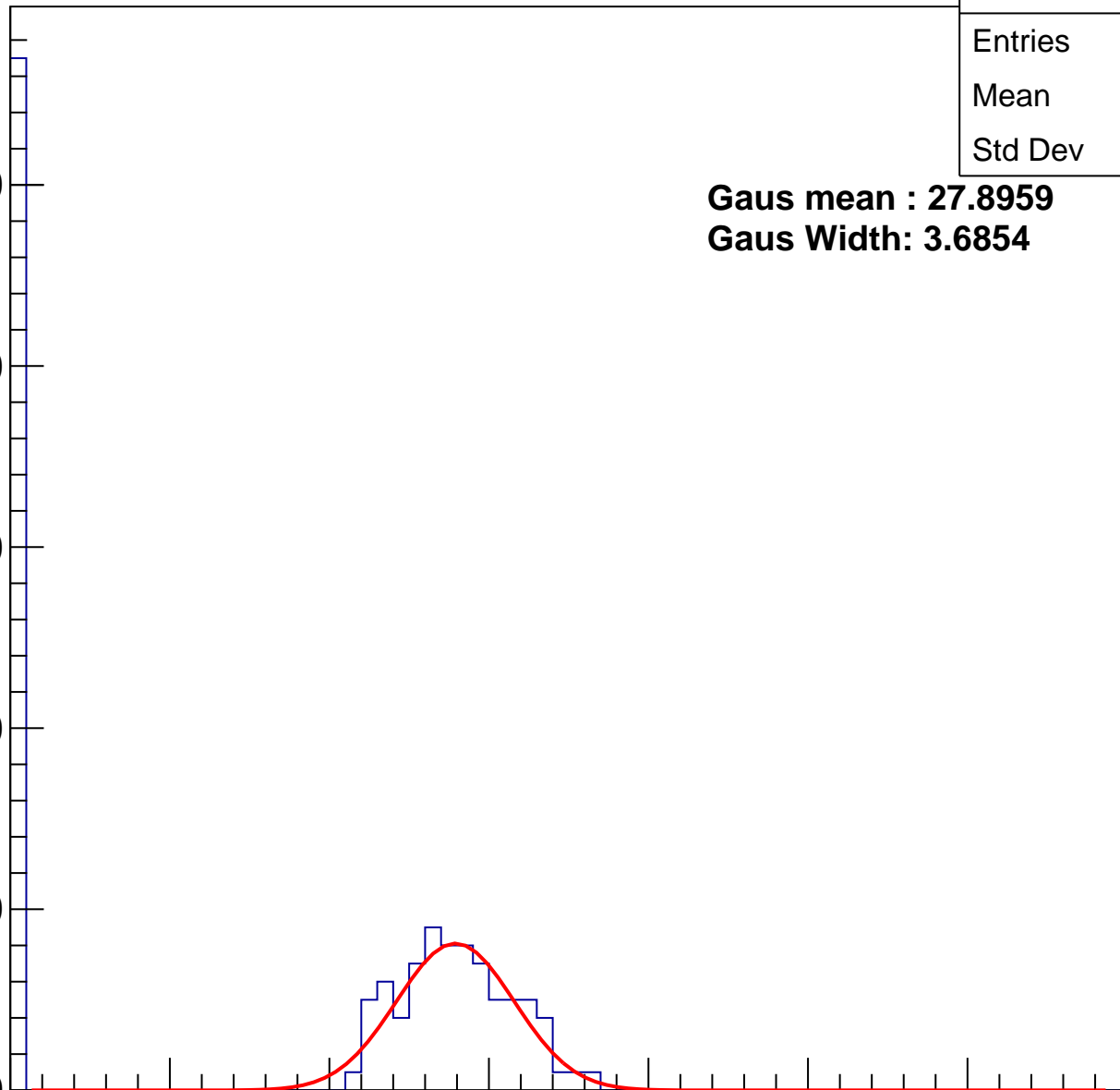
40

50

60

70

ampl



B1L103S, U1-ch47, adc1

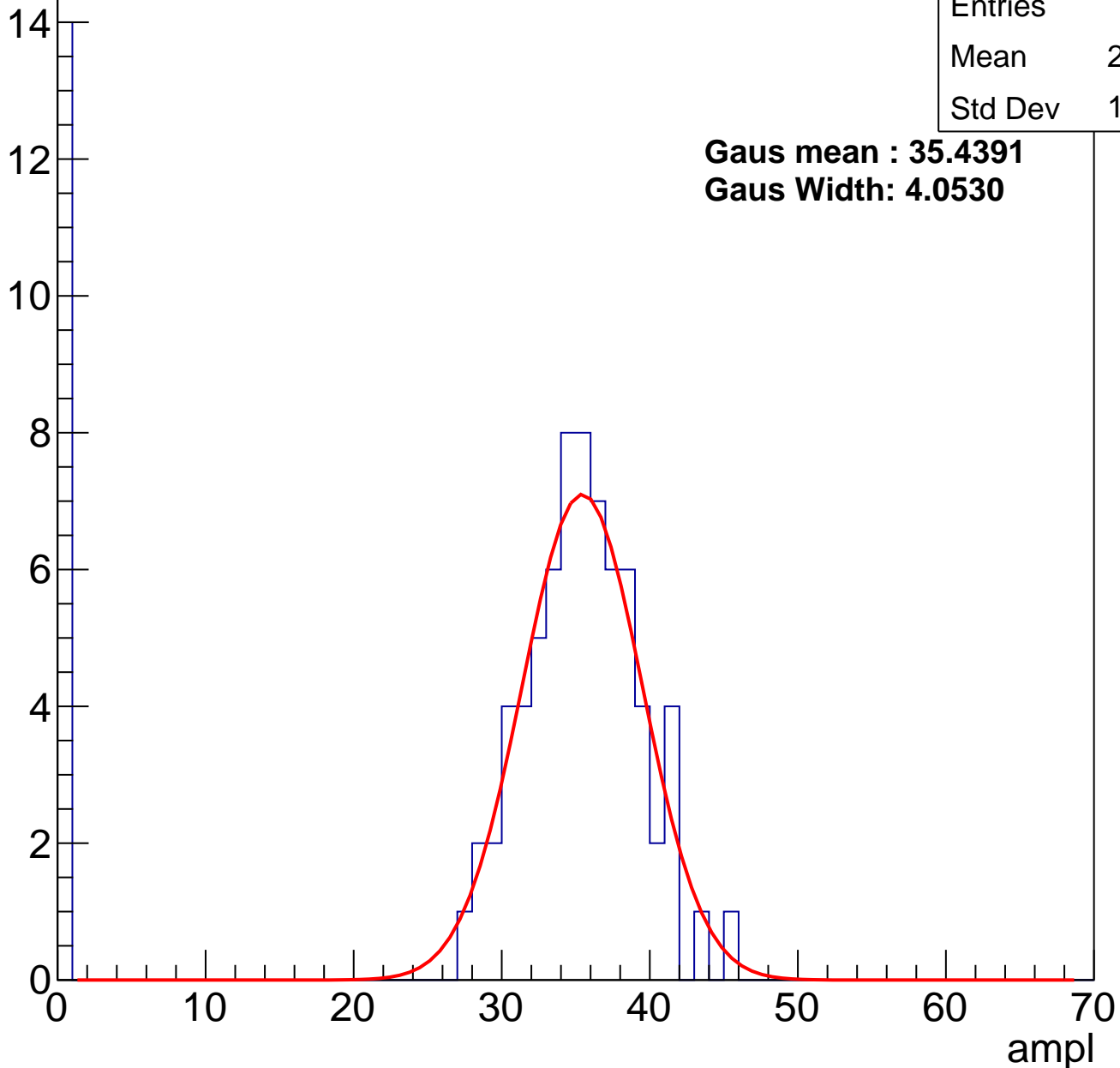
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	29.24
Std Dev	13.42

Gaus mean : 35.4391

Gaus Width: 4.0530

Entry



B1L103S, U1-ch47, adc2

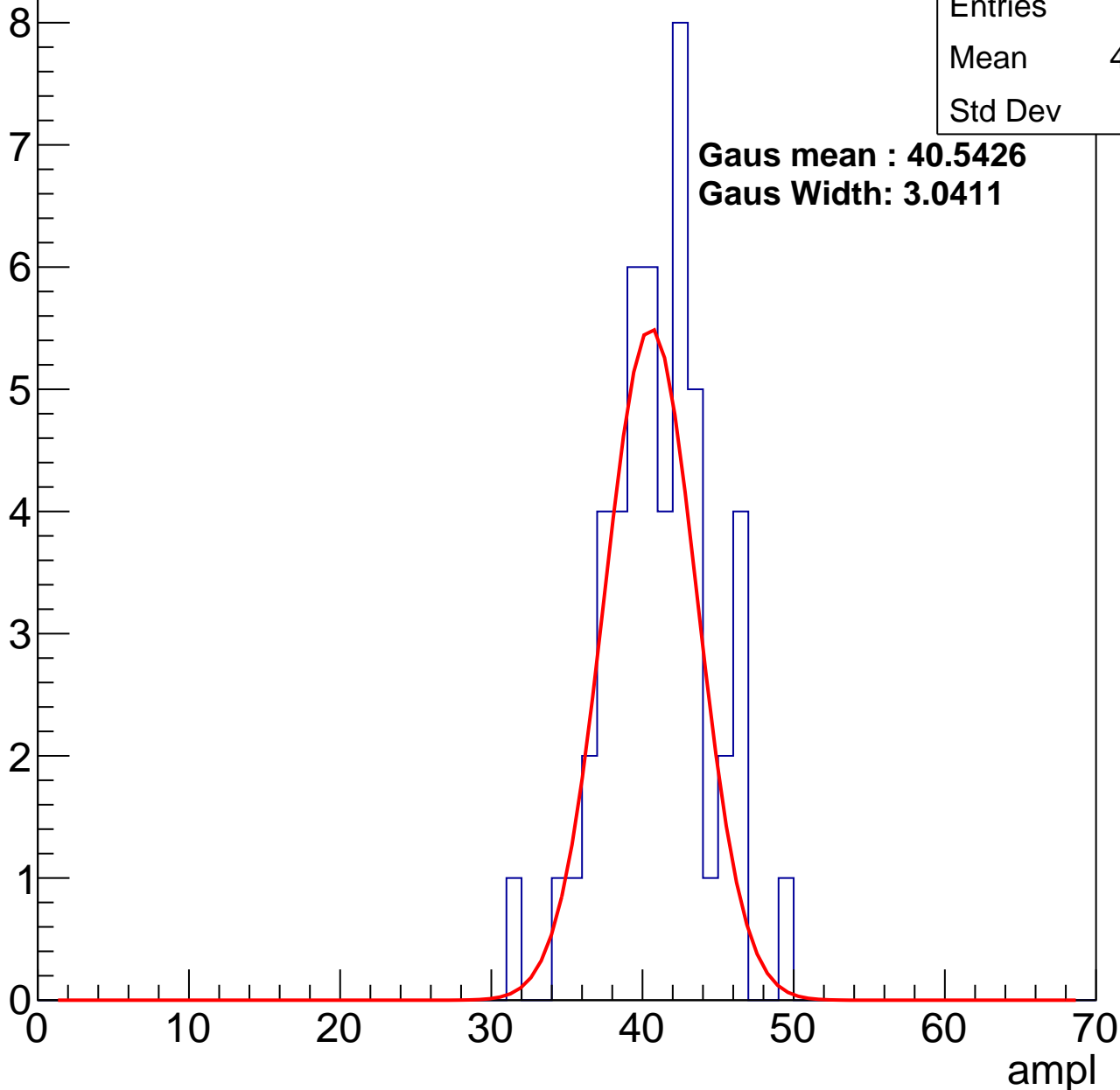
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	40.56
Std Dev	3.43

Gaus mean : 40.5426

Gaus Width: 3.0411

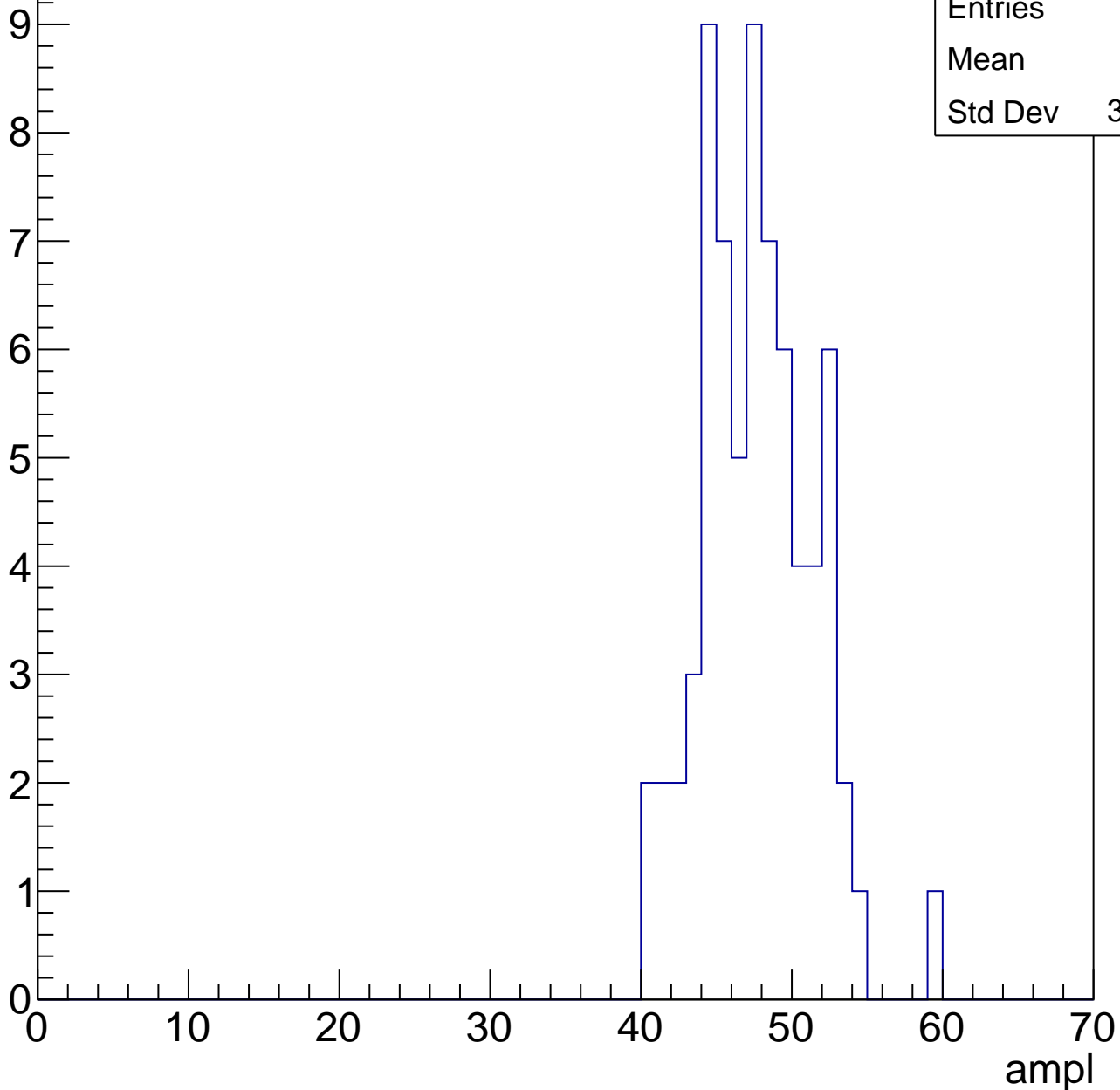


B1L103S, U1-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	47.2
Std Dev	3.636

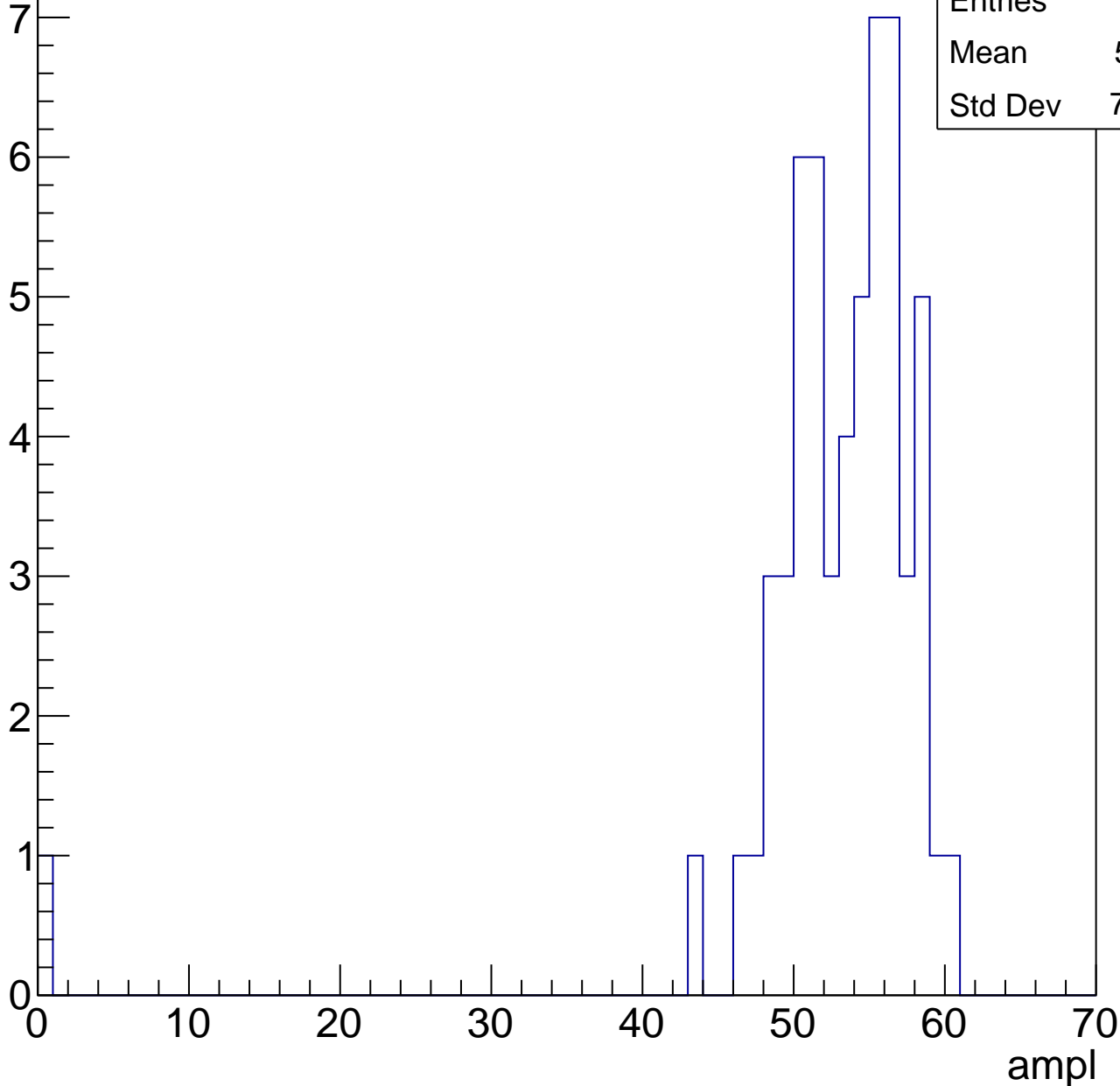


B1L103S, U1-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.21
Std Dev	7.785

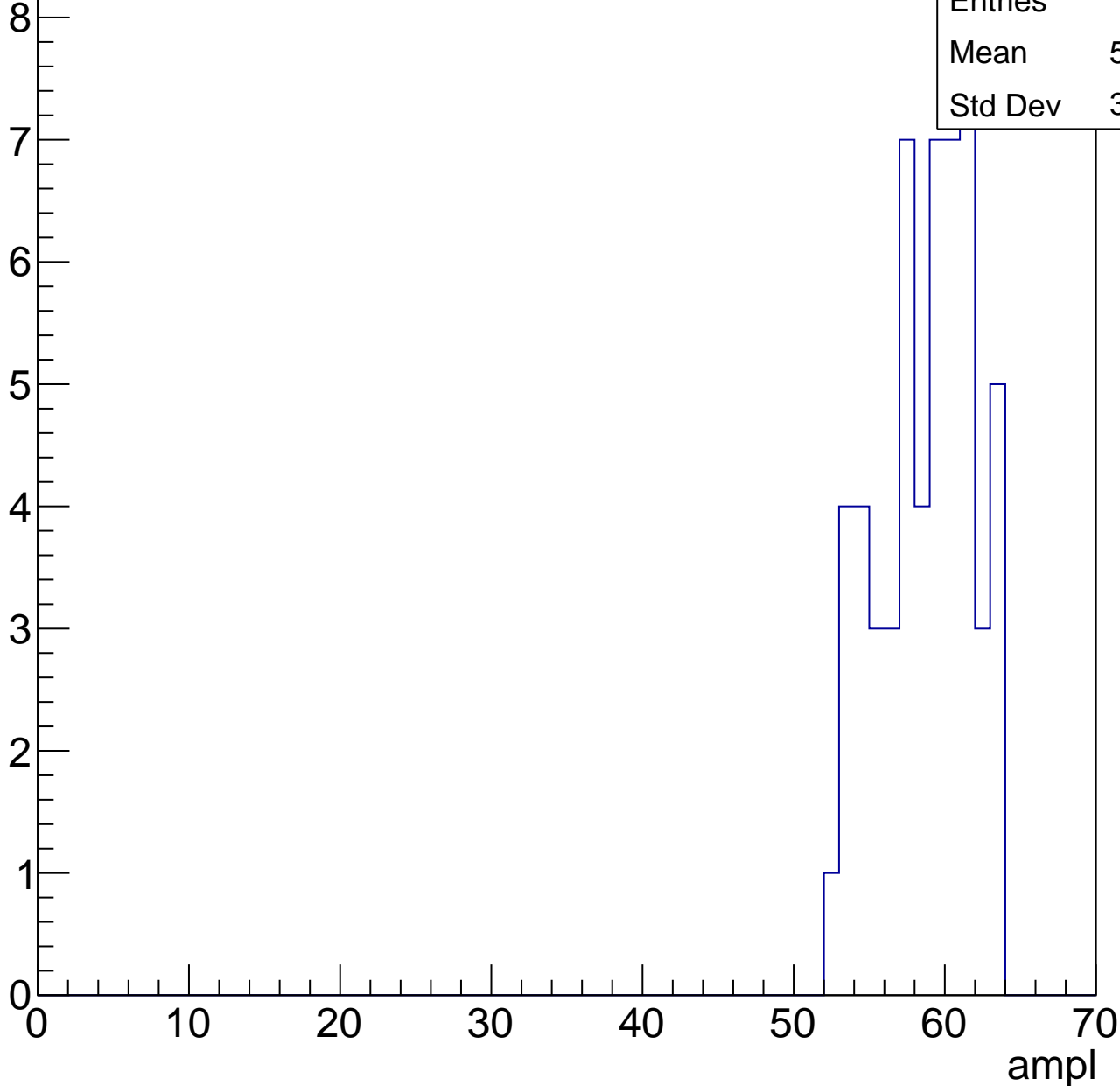


B1L103S, U1-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.32
Std Dev	3.048

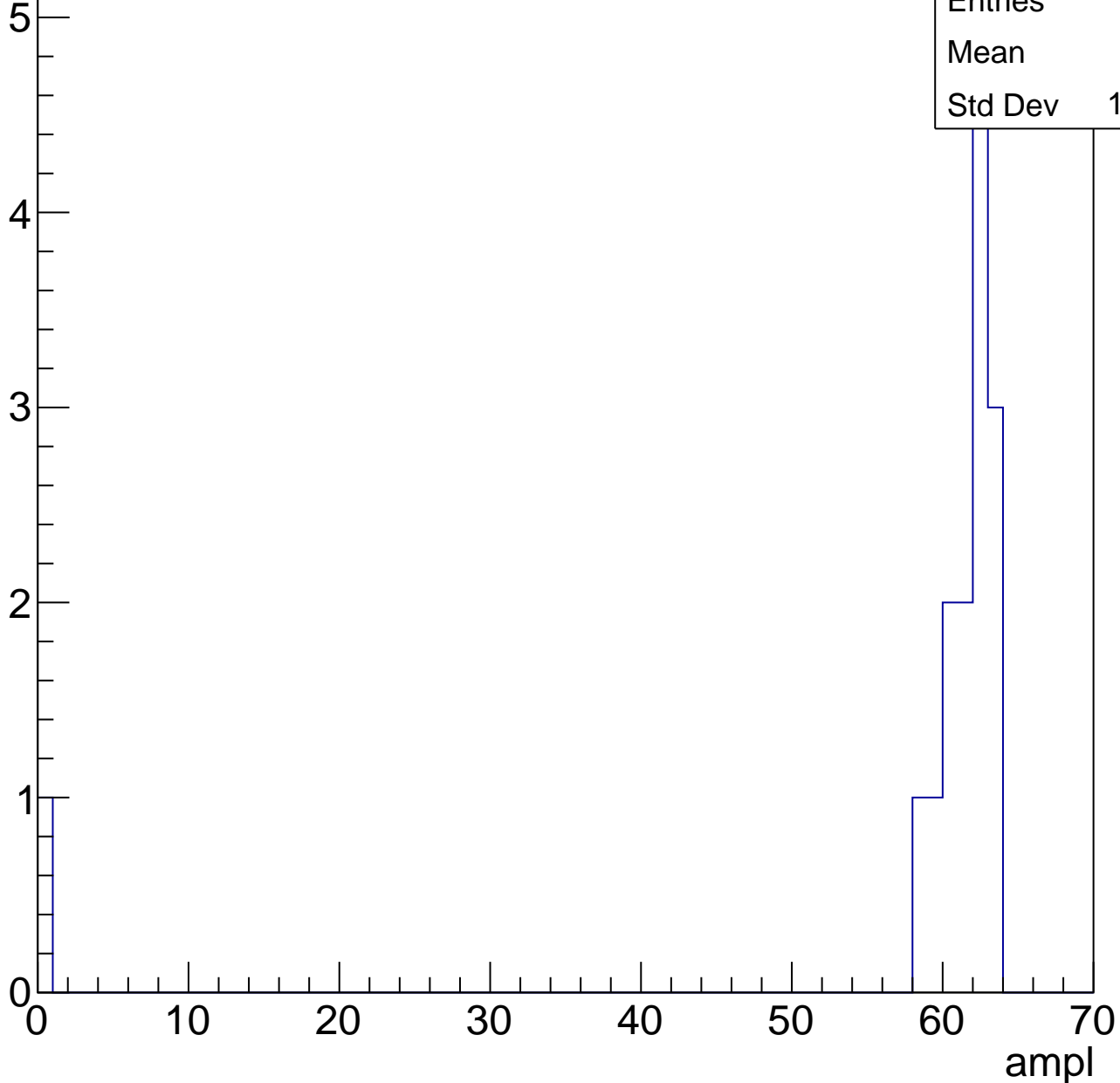


B1L103S, U1-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.2
Std Dev	15.35



B1L103S, U1-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	23
Mean	10.87
Std Dev	23.69

ampl

0 10 20 30 40 50 60 70

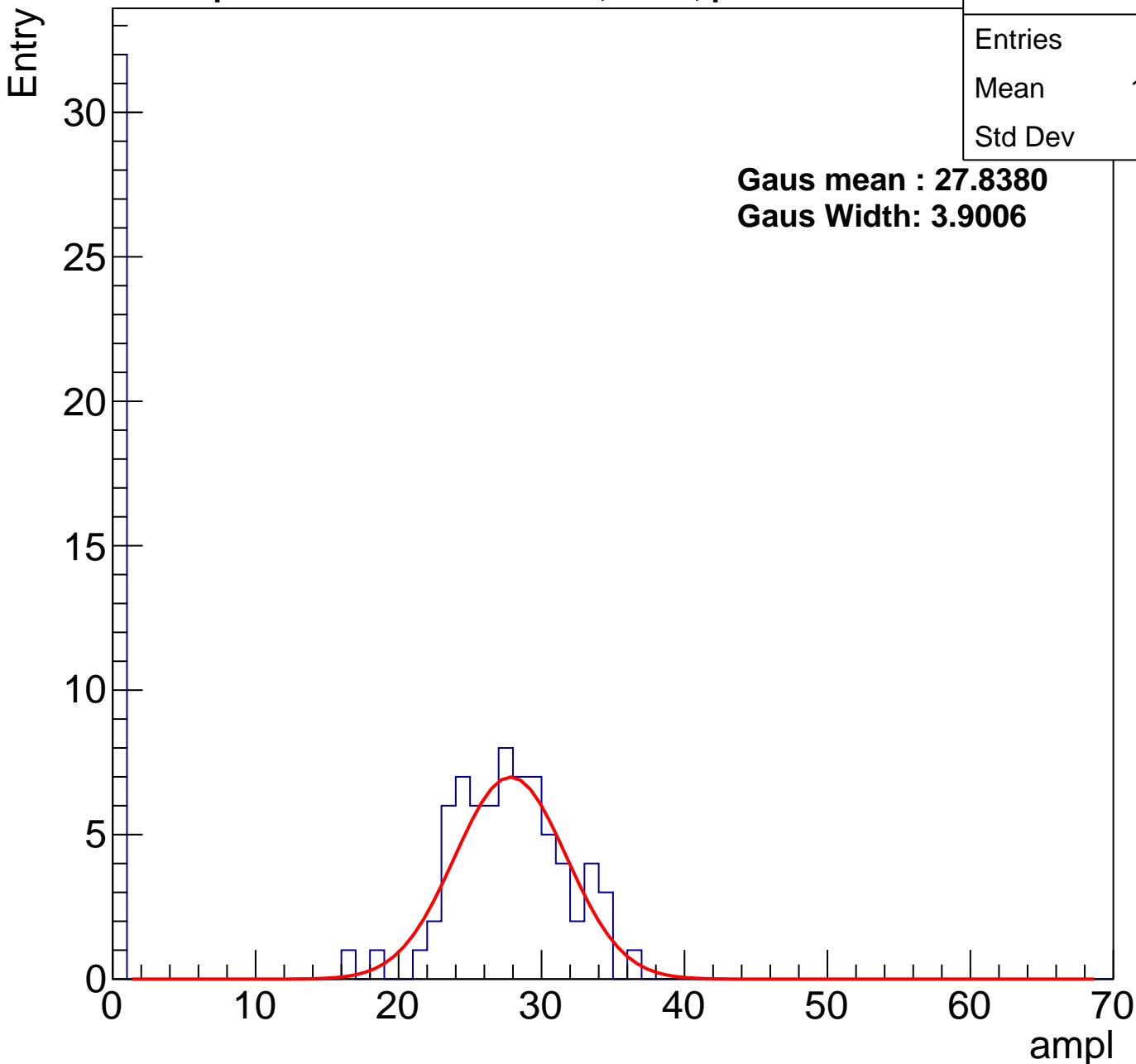
B1L103S, U1-ch48, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	18.78
Std Dev	13

Gaus mean : 27.8380

Gaus Width: 3.9006



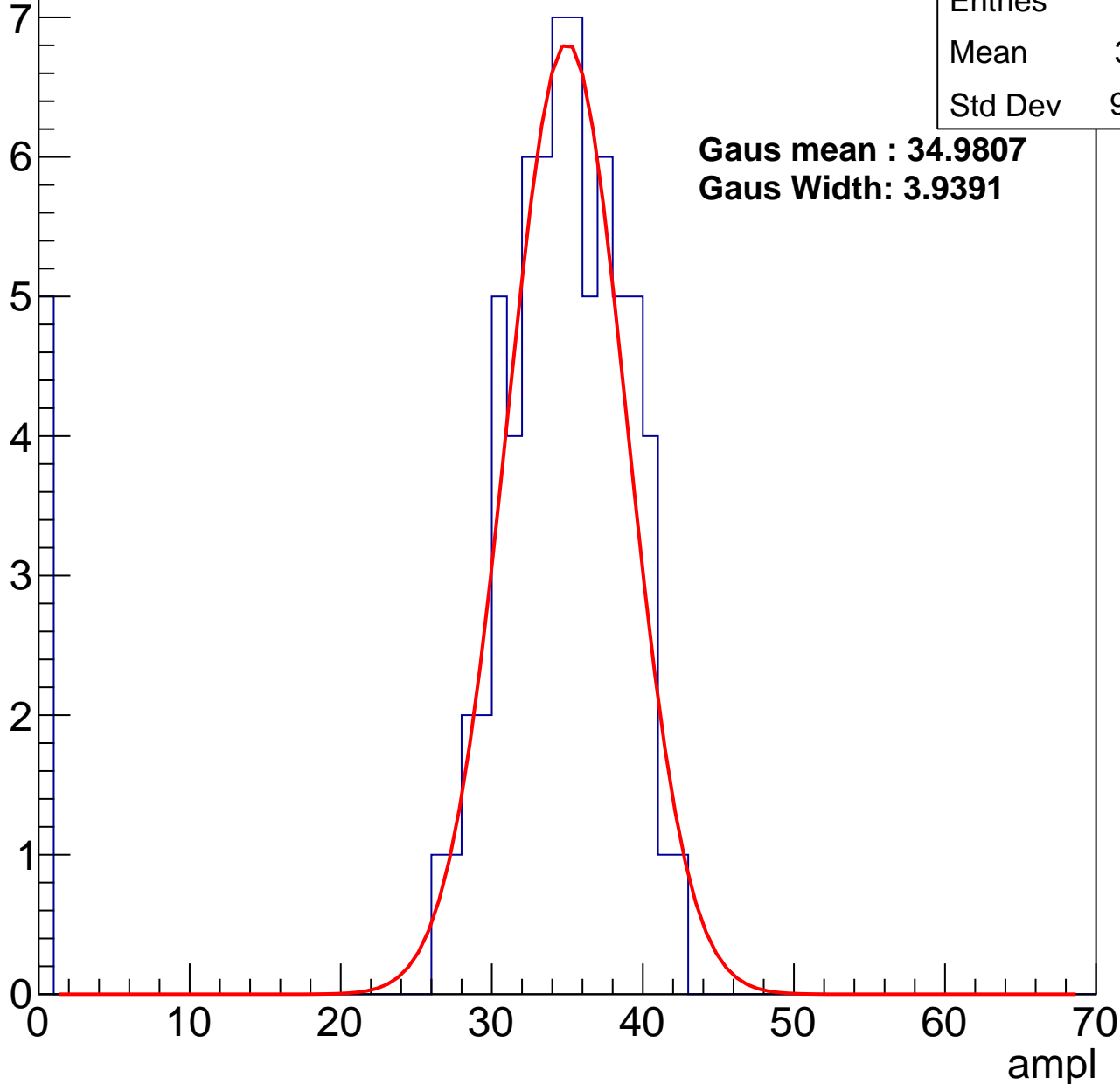
B1L103S, U1-ch48, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.11
Std Dev	9.395

Gaus mean : 34.9807
Gaus Width: 3.9391



B1L103S, U1-ch48, adc2

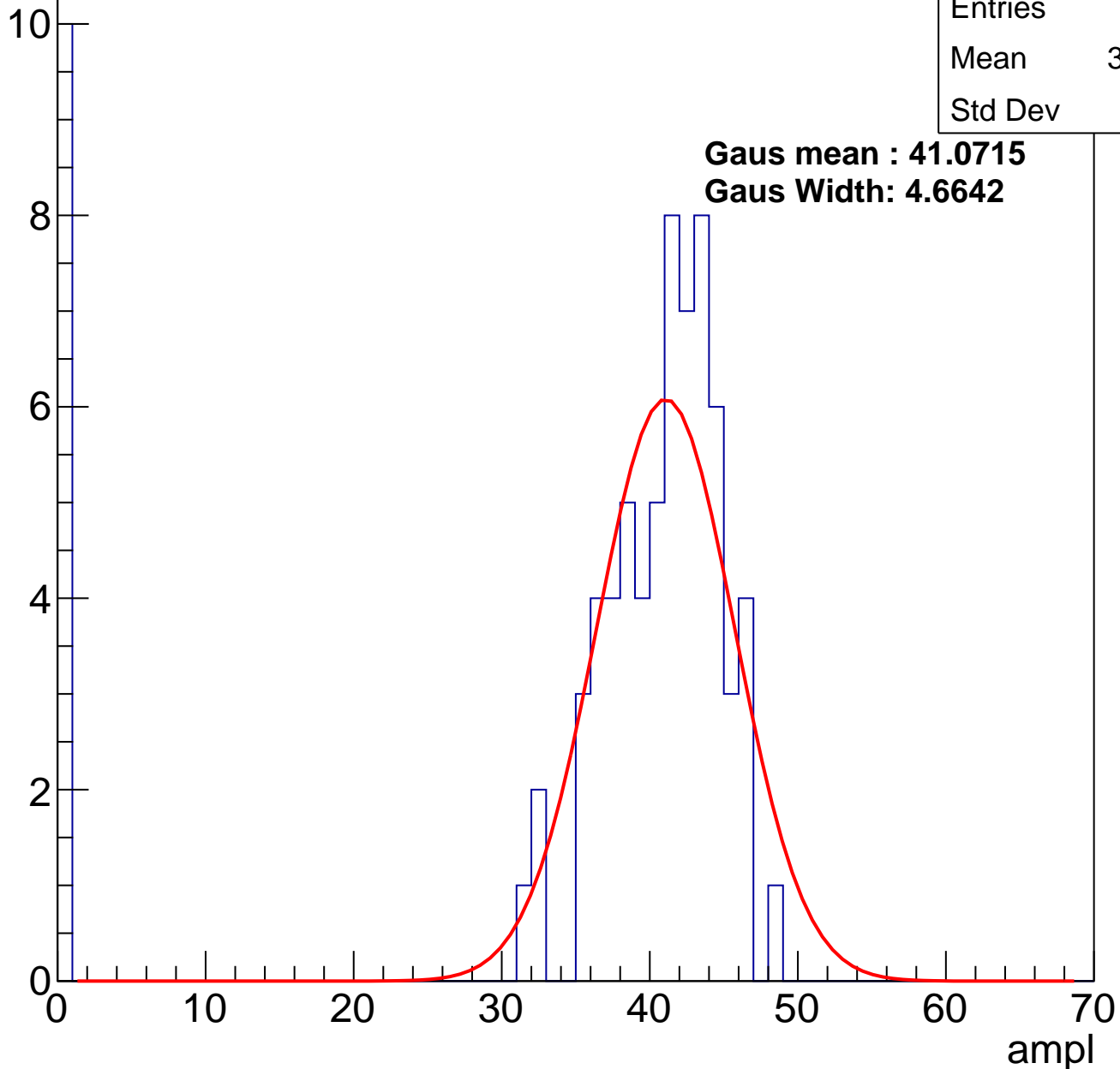
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	35.13
Std Dev	14.2

Gaus mean : 41.0715

Gaus Width: 4.6642

Entry

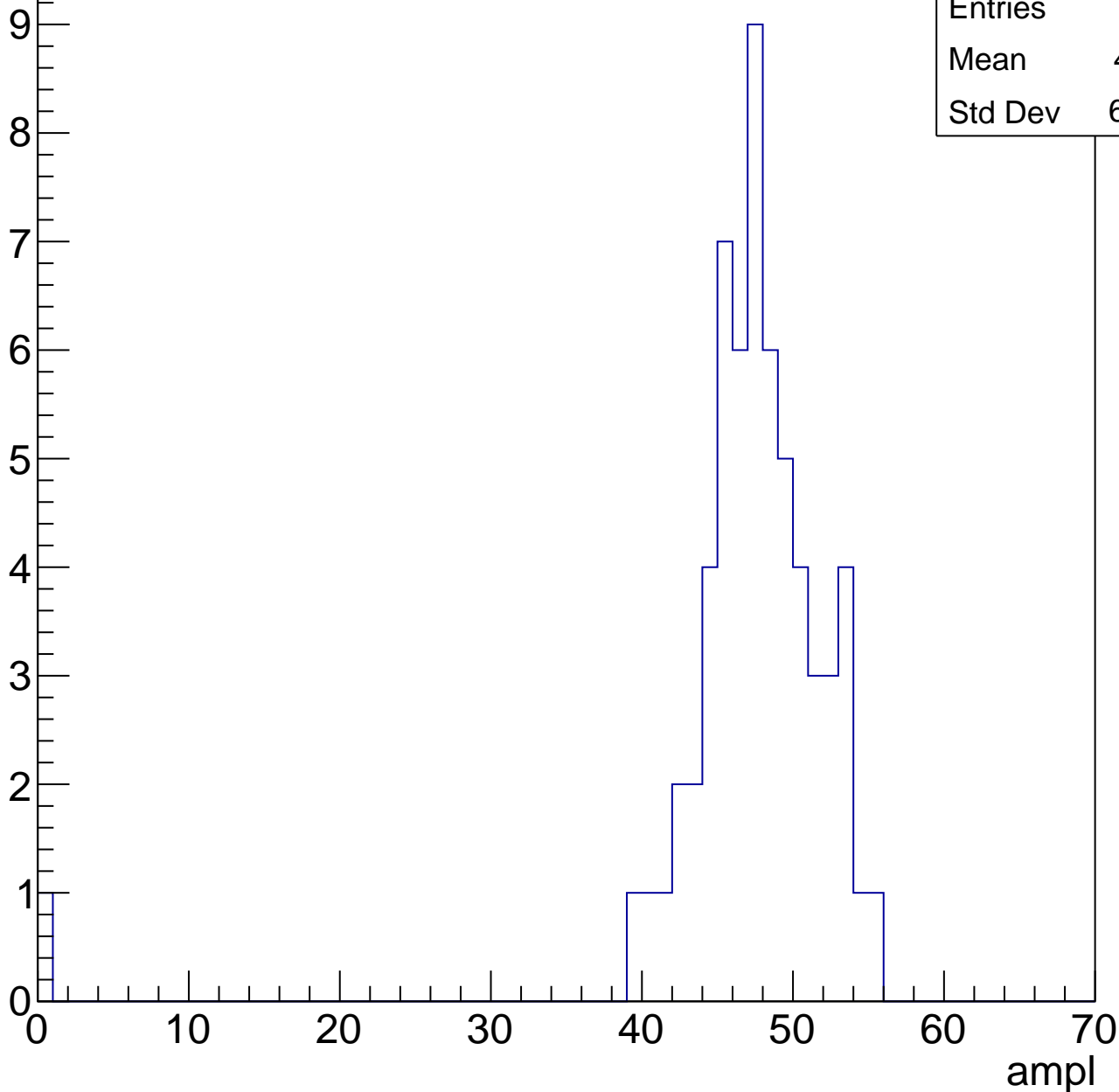


B1L103S, U1-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	46.61
Std Dev	6.945

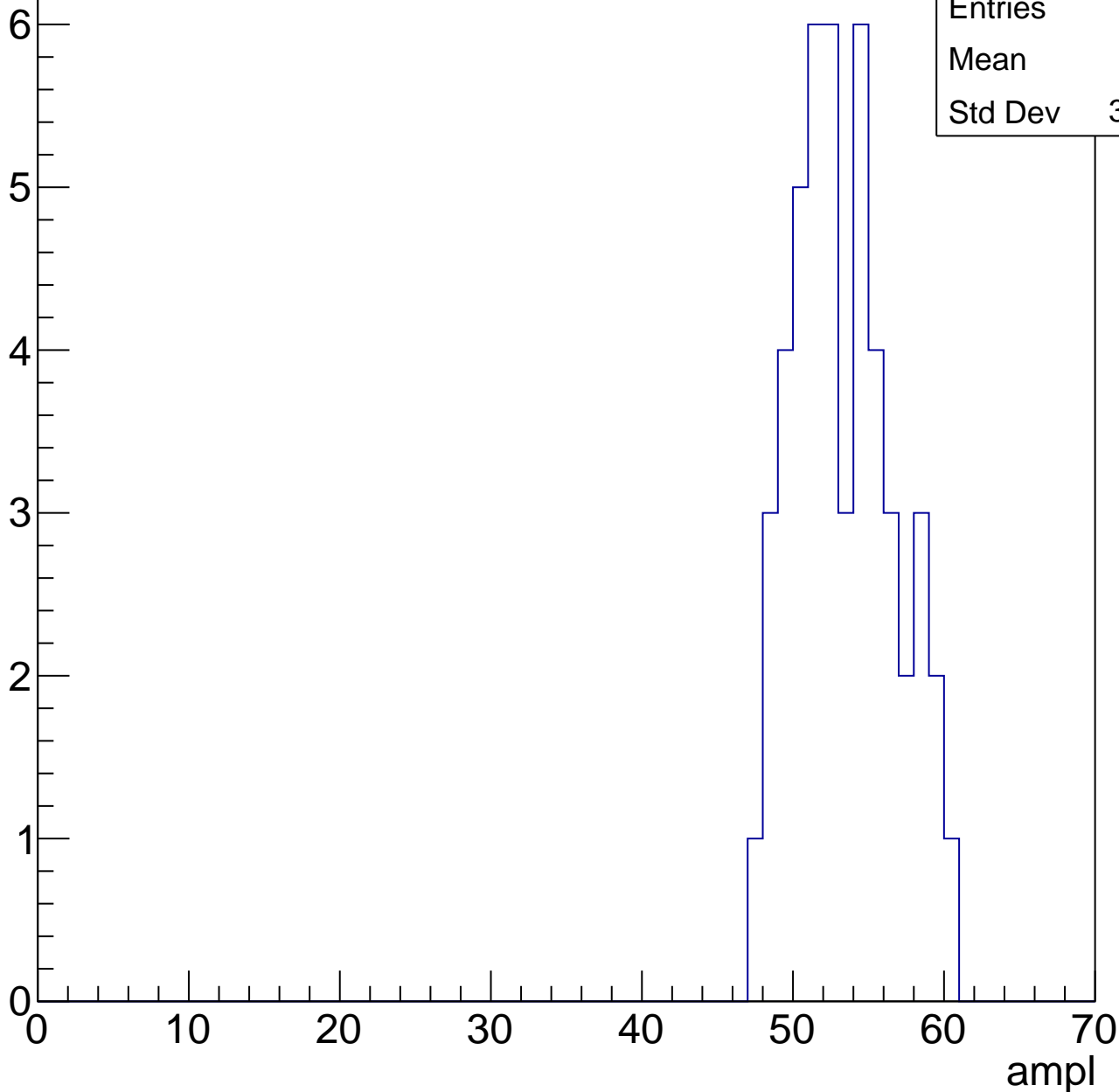


B1L103S, U1-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

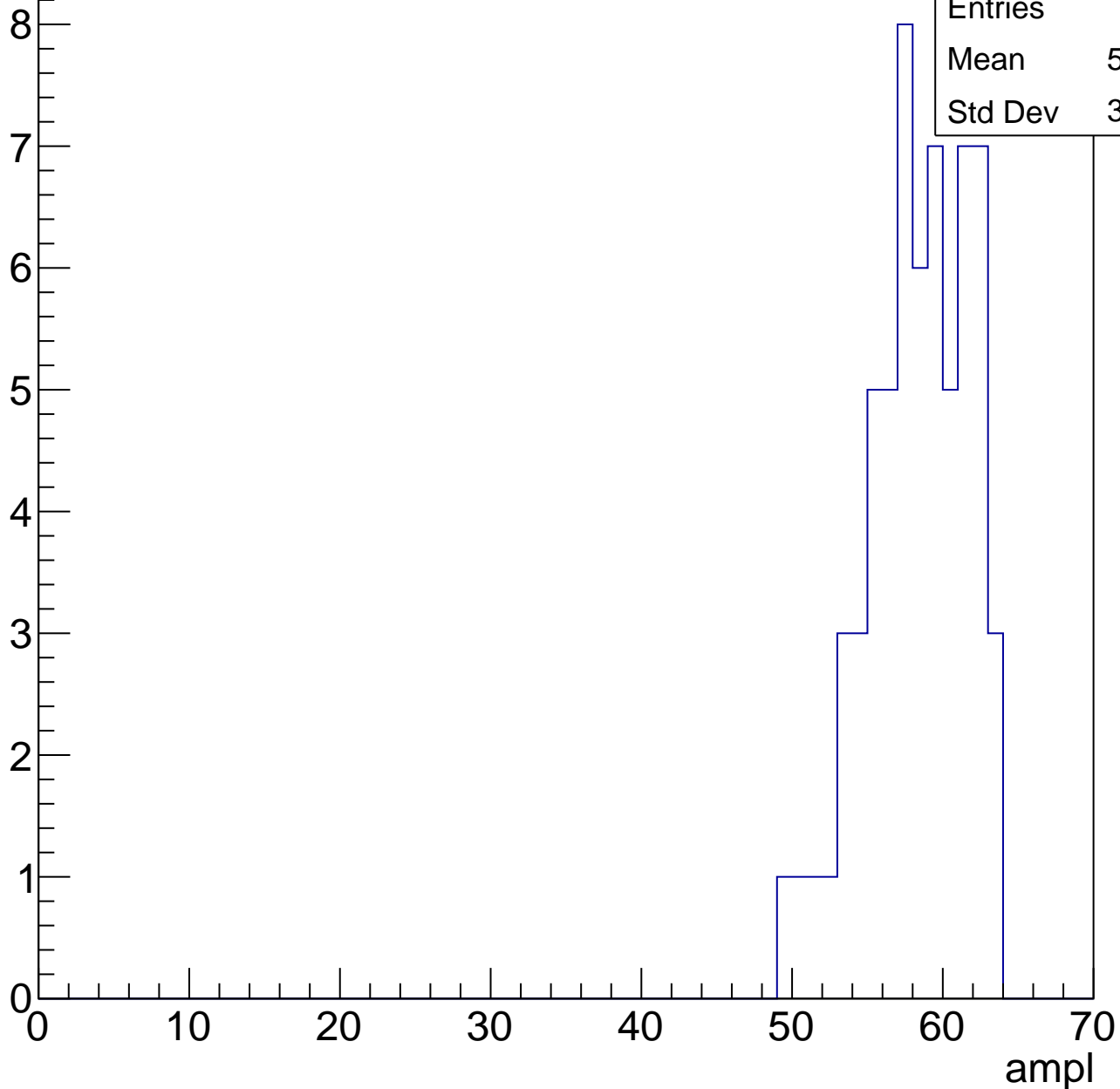
Entries	49
Mean	52.9
Std Dev	3.278



B1L103S, U1-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

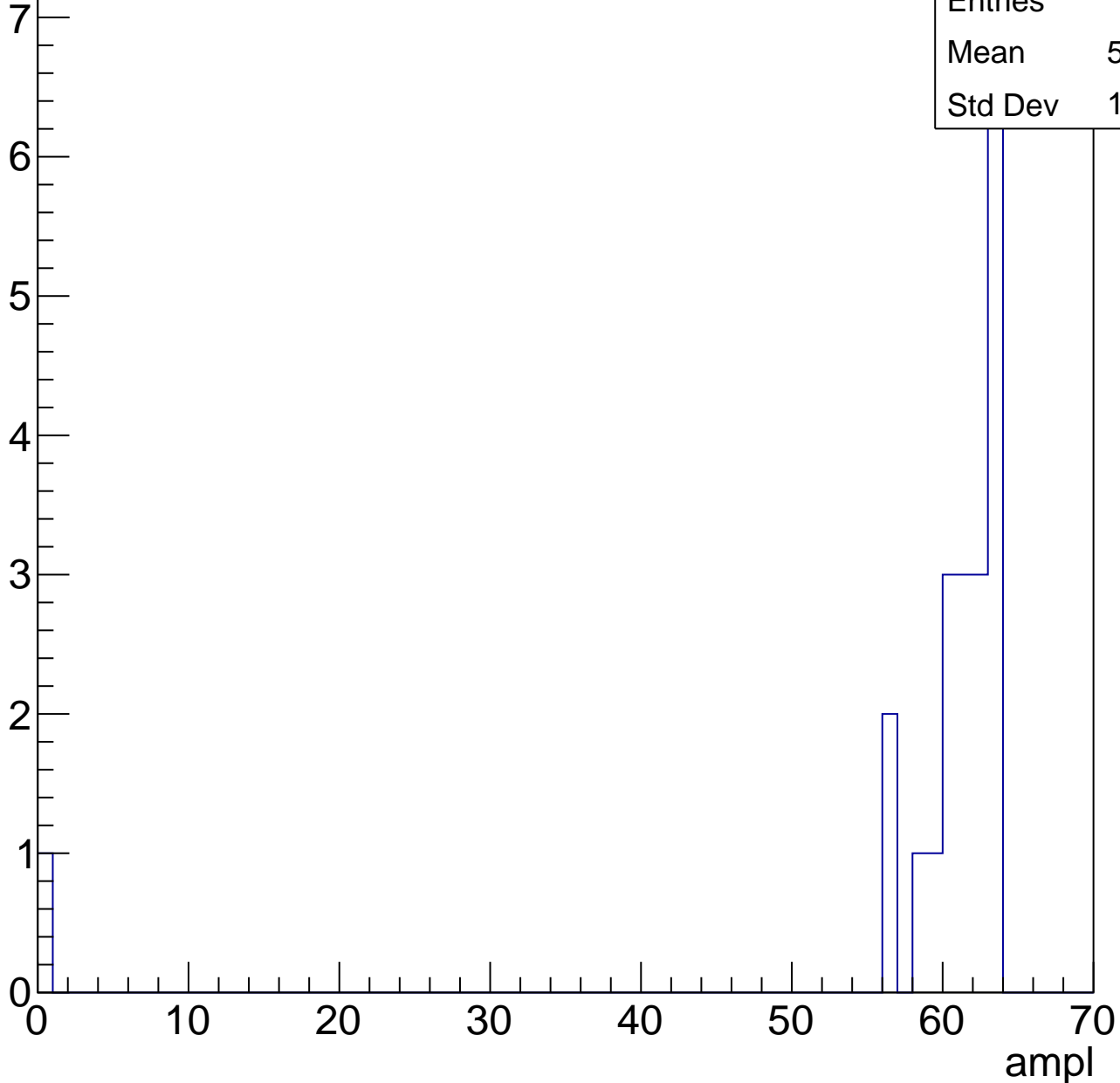


B1L103S, U1-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.05
Std Dev	13.16

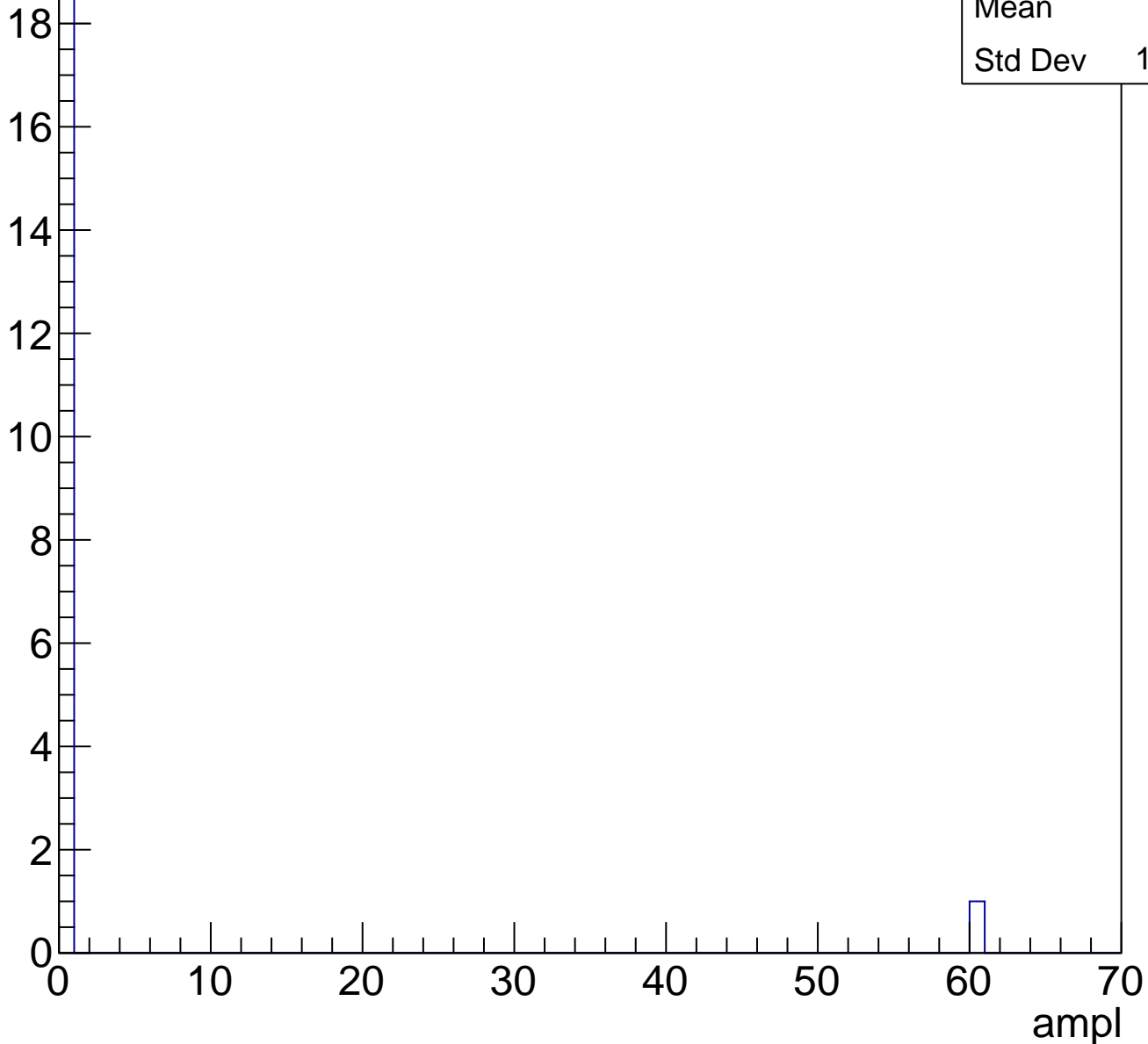


B1L103S, U1-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U1-ch49, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	18.61
Std Dev	13.71

Gaus mean : 28.8530

Gaus Width: 3.8236

Entry

25

20

15

10

5

0

0

10

20

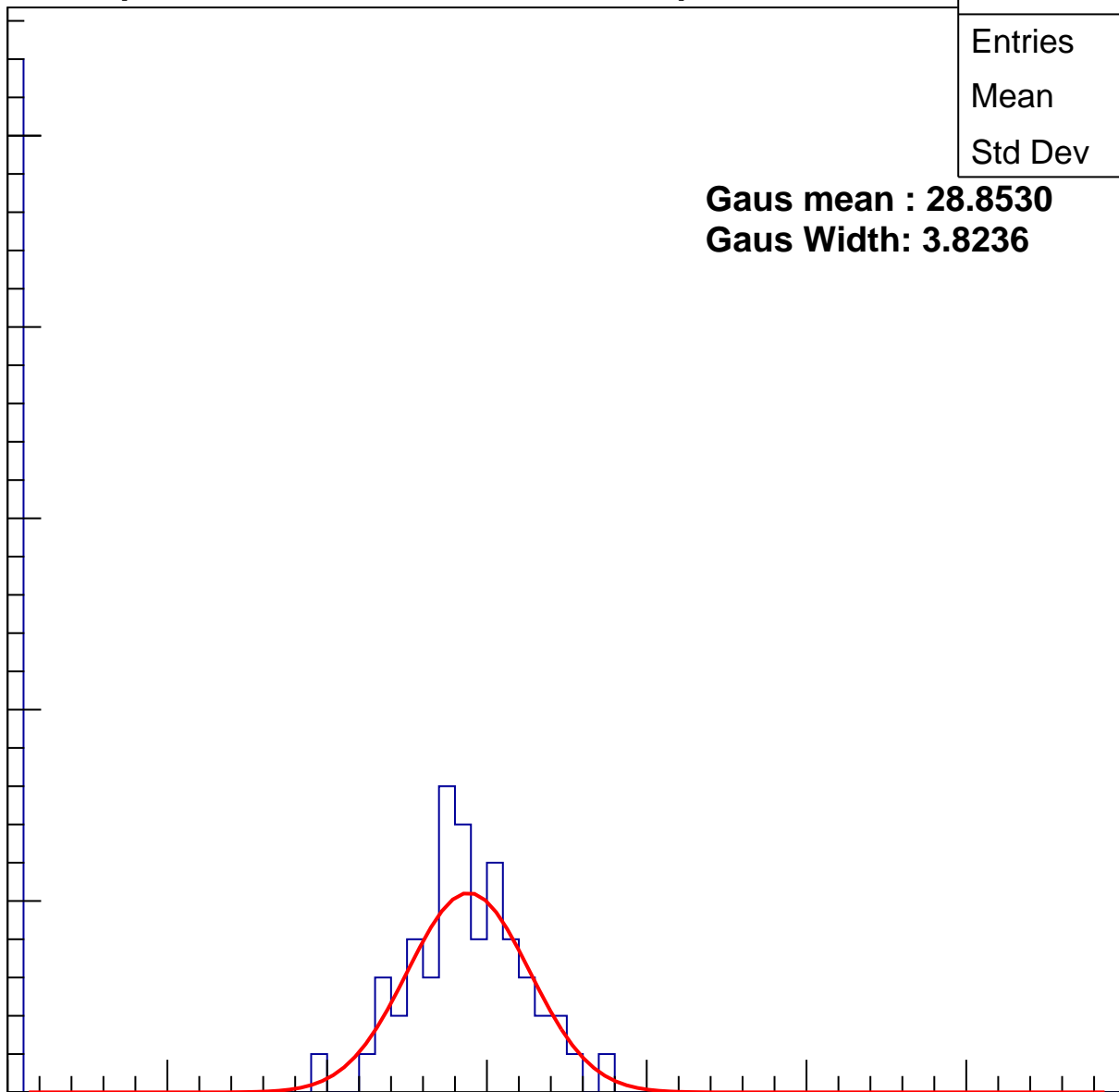
30

50

60

70

ampl



B1L103S, U1-ch49, adc1

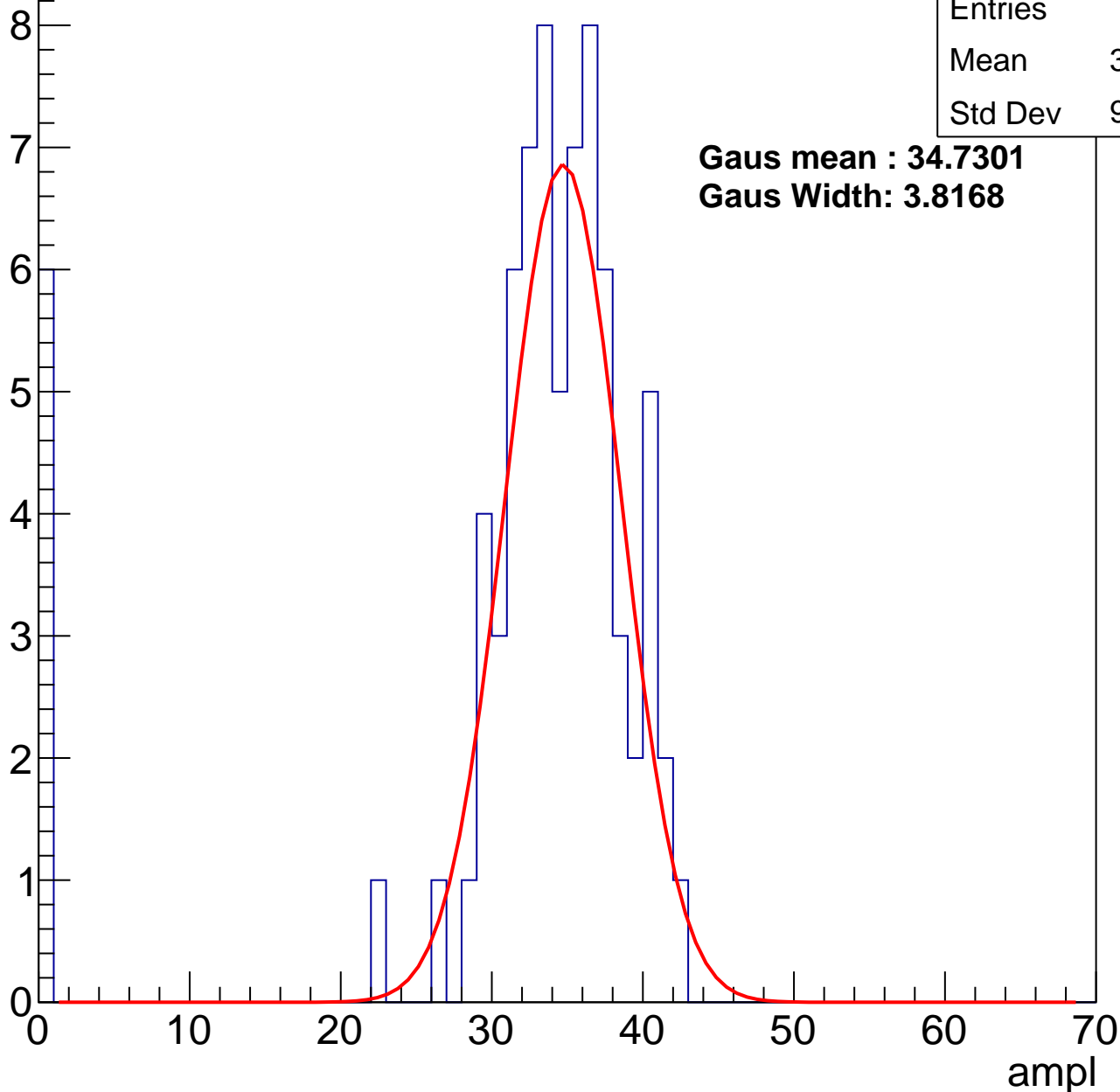
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	31.54
Std Dev	9.928

Gaus mean : 34.7301

Gaus Width: 3.8168



B1L103S, U1-ch49, adc2

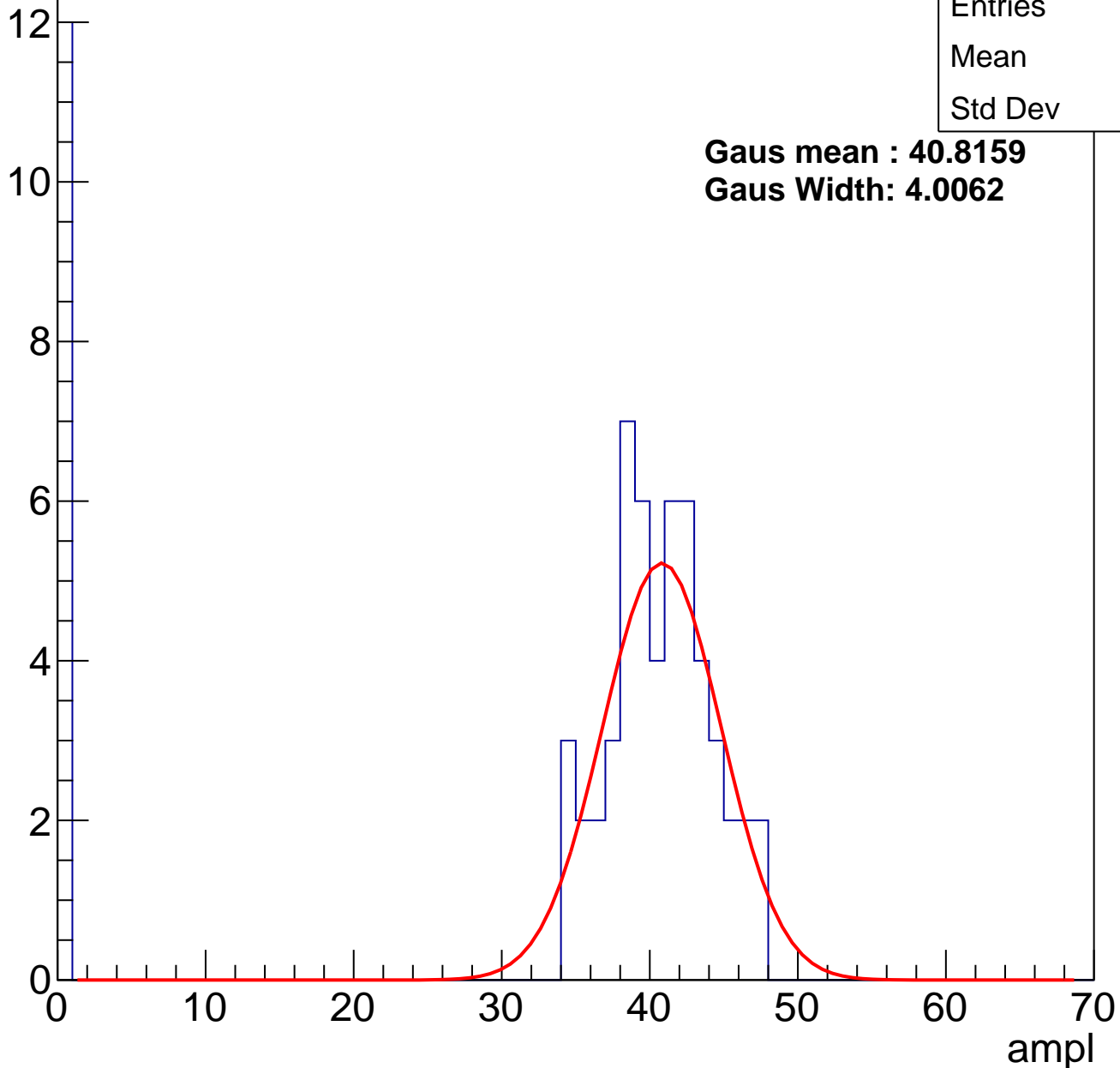
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	32.7
Std Dev	16

Gaus mean : 40.8159

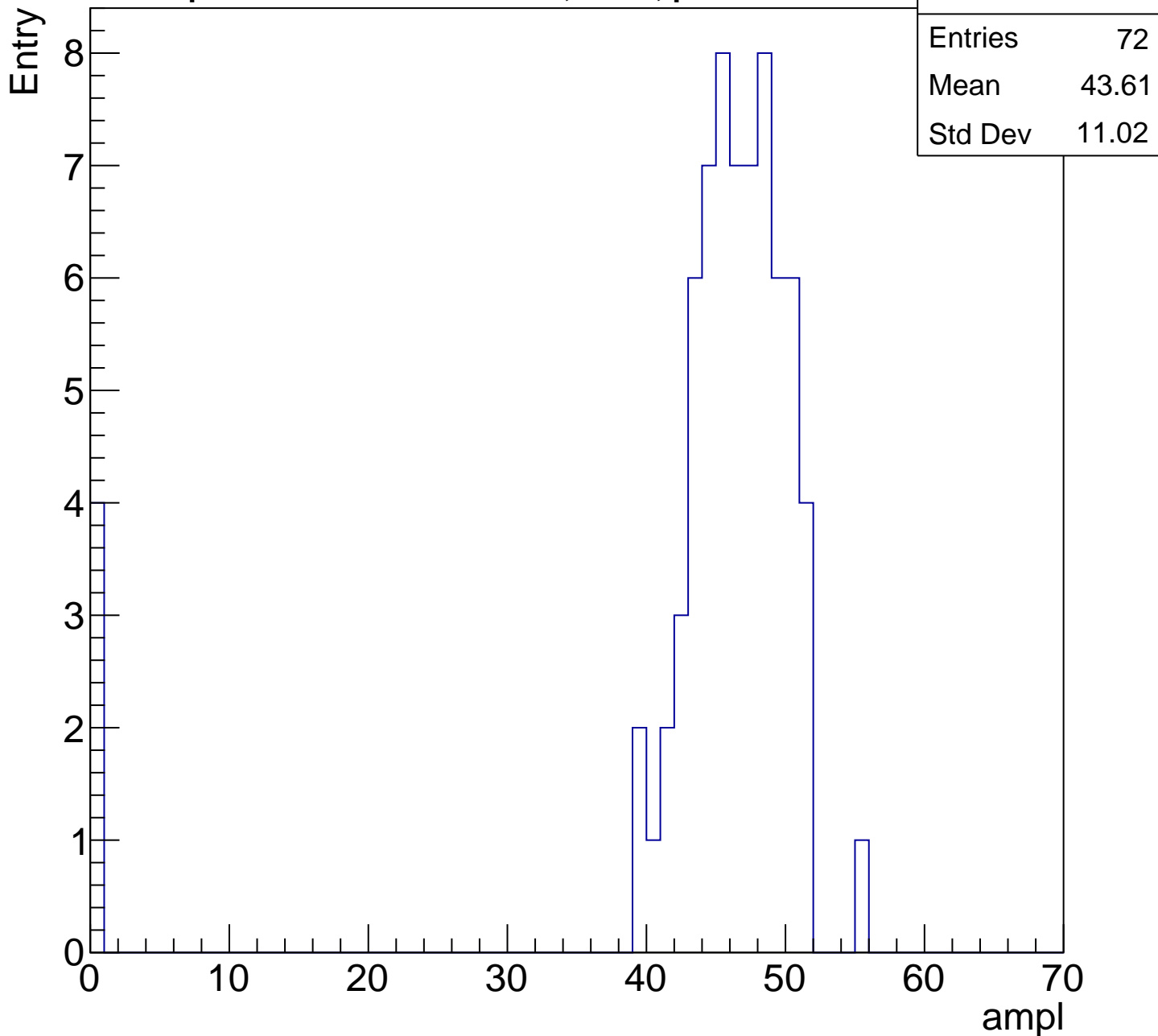
Gaus Width: 4.0062

Entry



B1L103S, U1-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

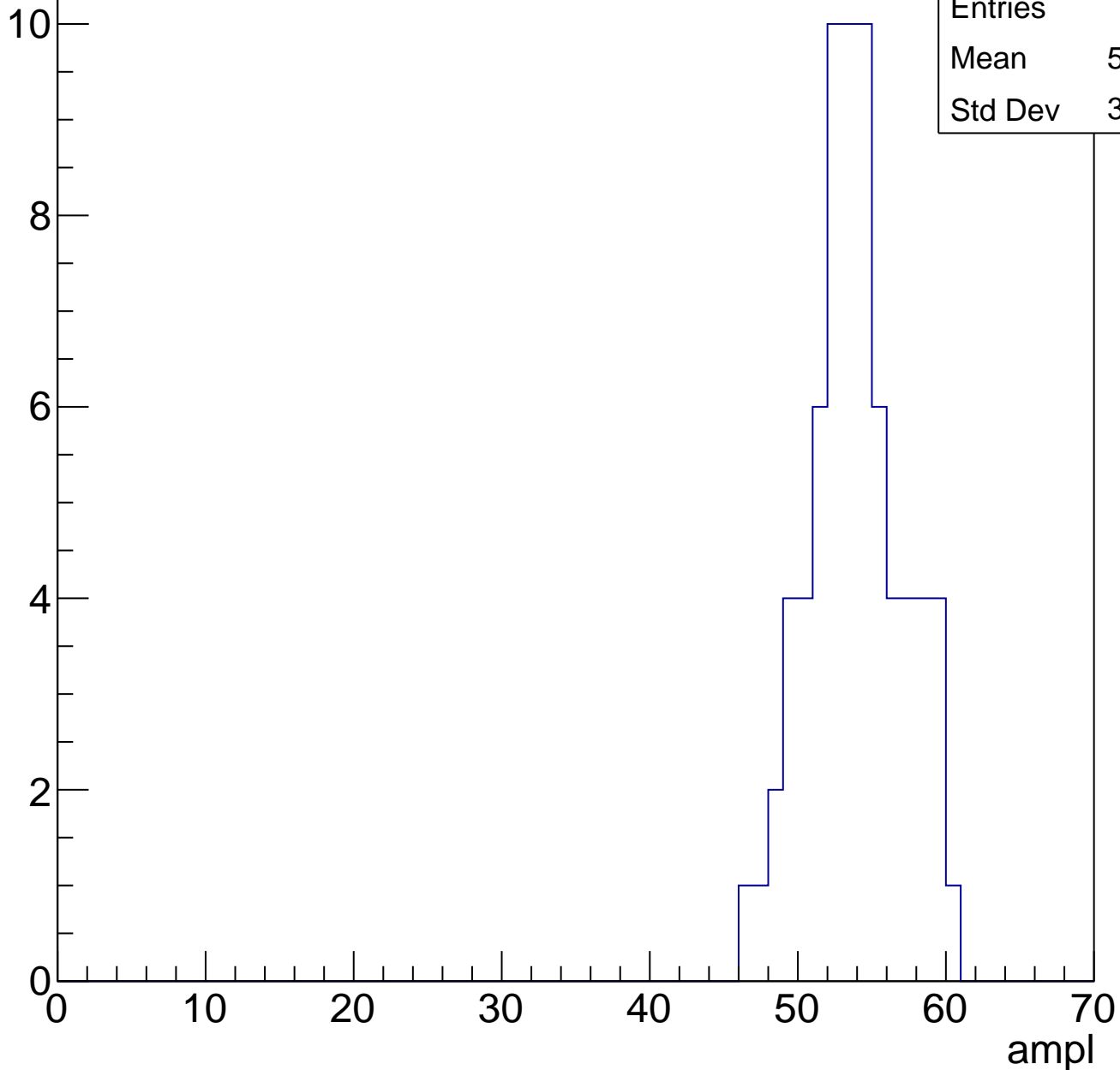


B1L103S, U1-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	53.39
Std Dev	3.106

Entry

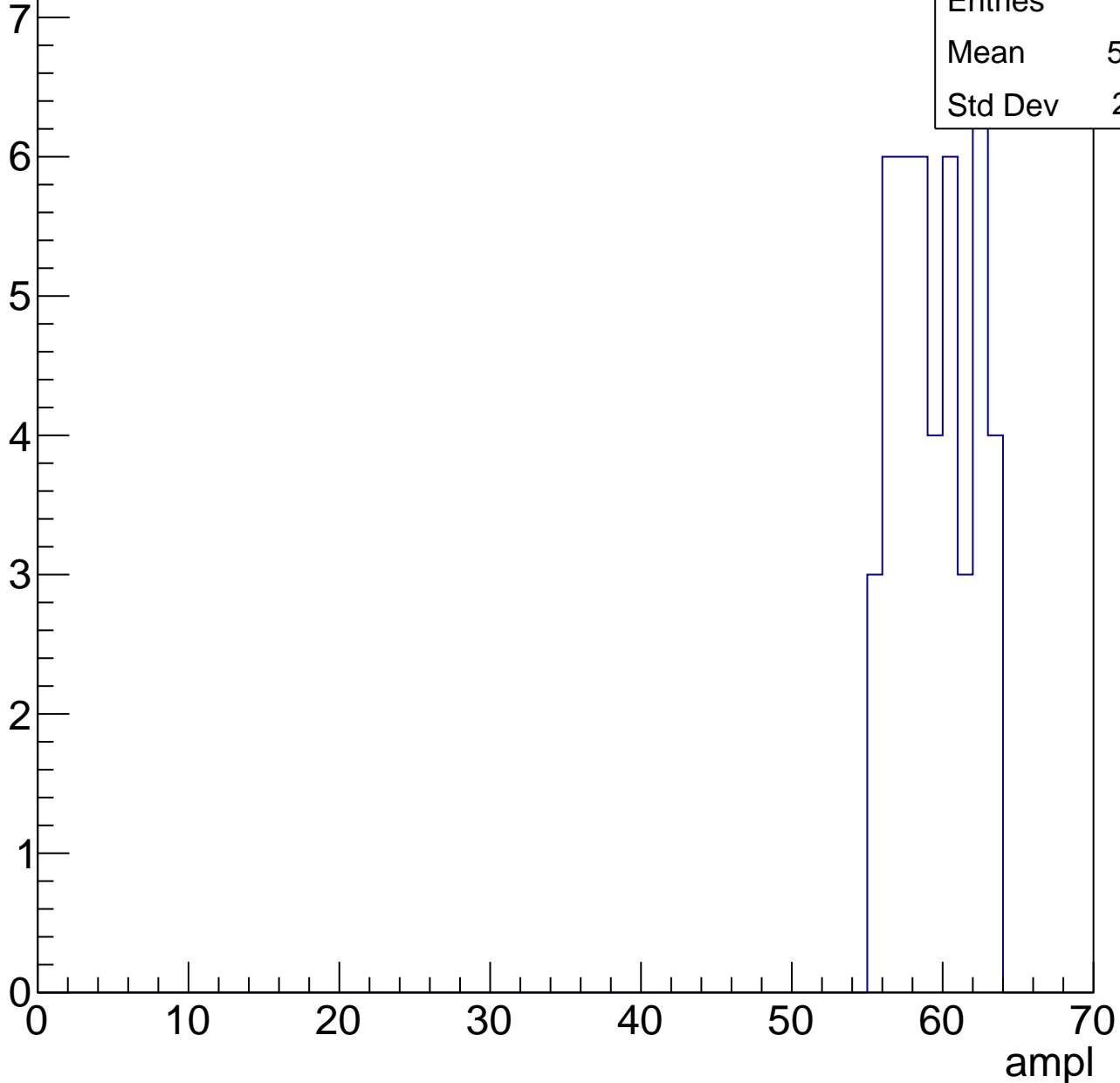


B1L103S, U1-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	59.02
Std Dev	2.481

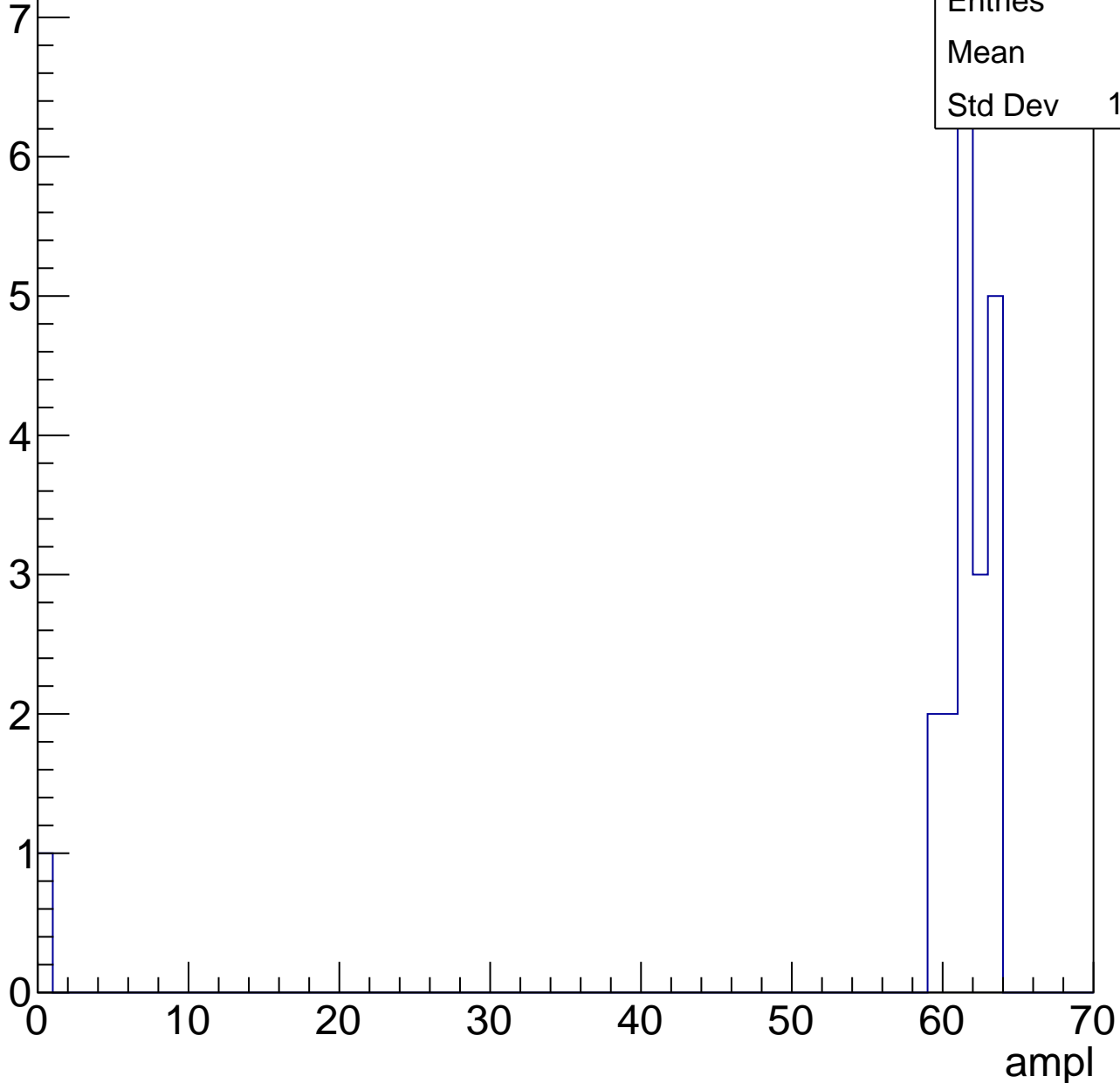


B1L103S, U1-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

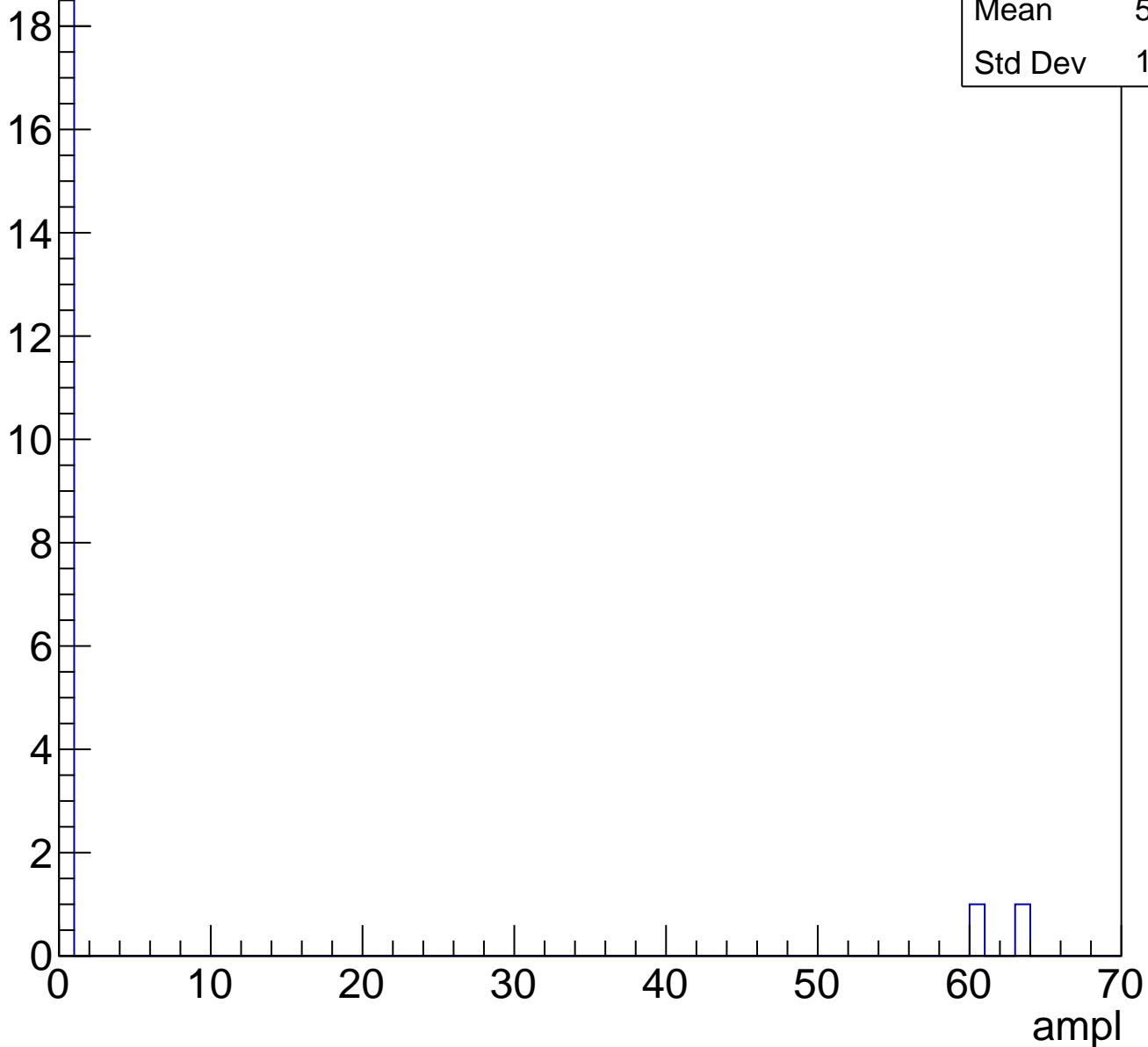
Entries	20
Mean	58.3
Std Dev	13.43



B1L103S, U1-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	5.857
Std Dev	18.06

B1L103S, U1-ch50, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	20.24
Std Dev	12.65

Gaus mean : 28.4894

Gaus Width: 3.6917

Entry

25

20

15

10

5

0

0

10

20

30

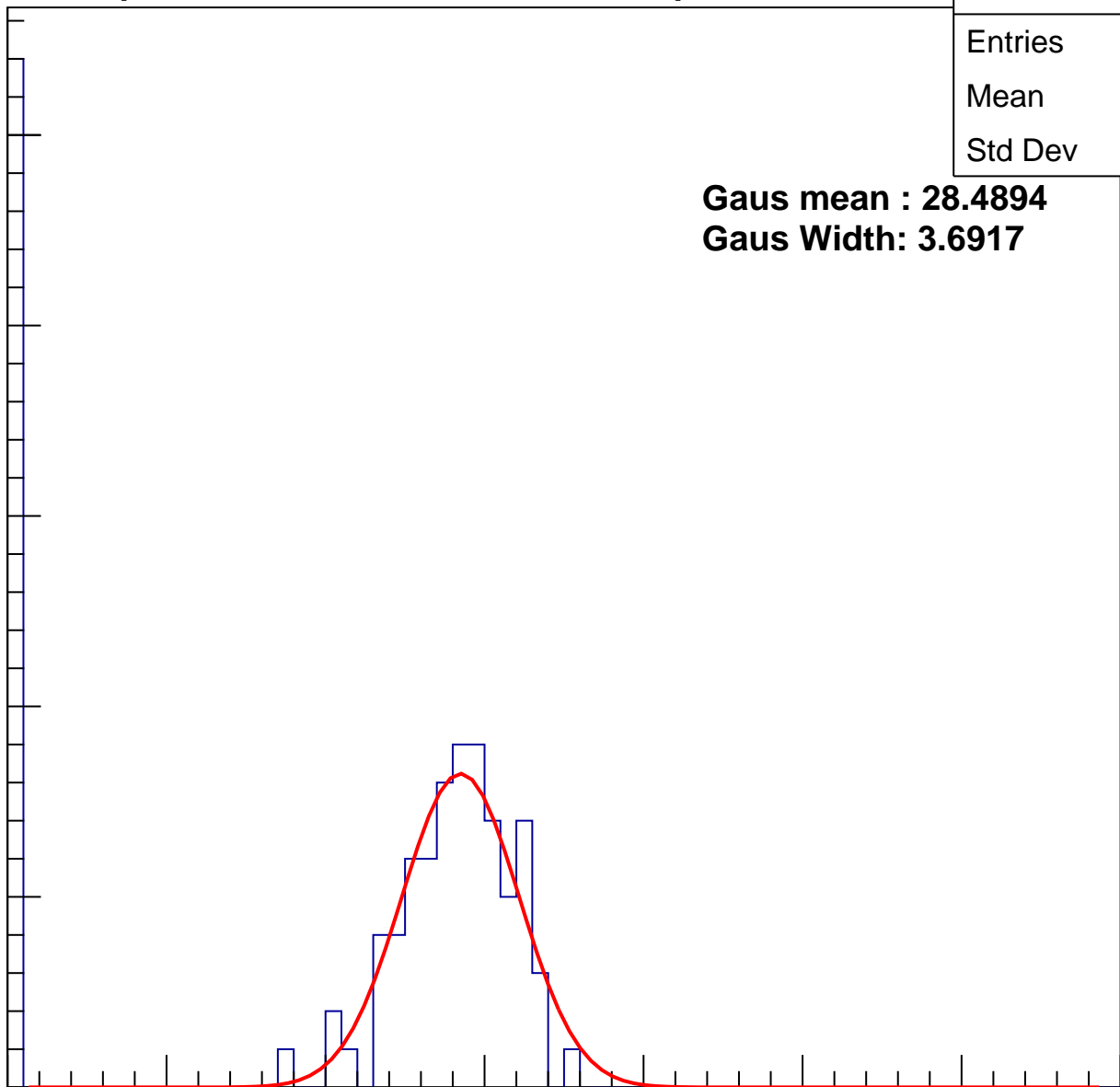
40

50

60

70

ampl



B1L103S, U1-ch50, adc1

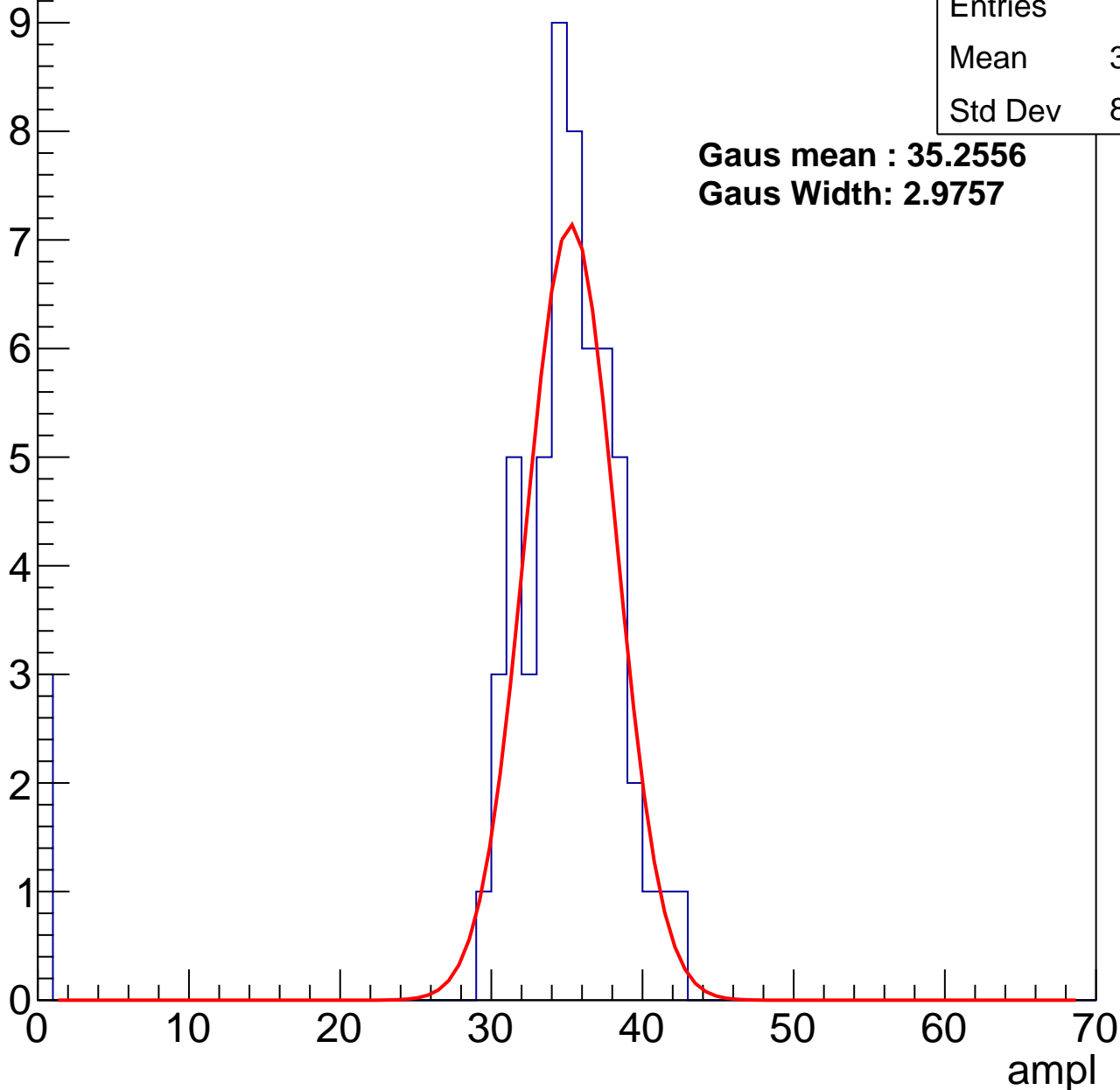
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	33.05
Std Dev	8.146

Gaus mean : 35.2556

Gaus Width: 2.9757



B1L103S, U1-ch50, adc2

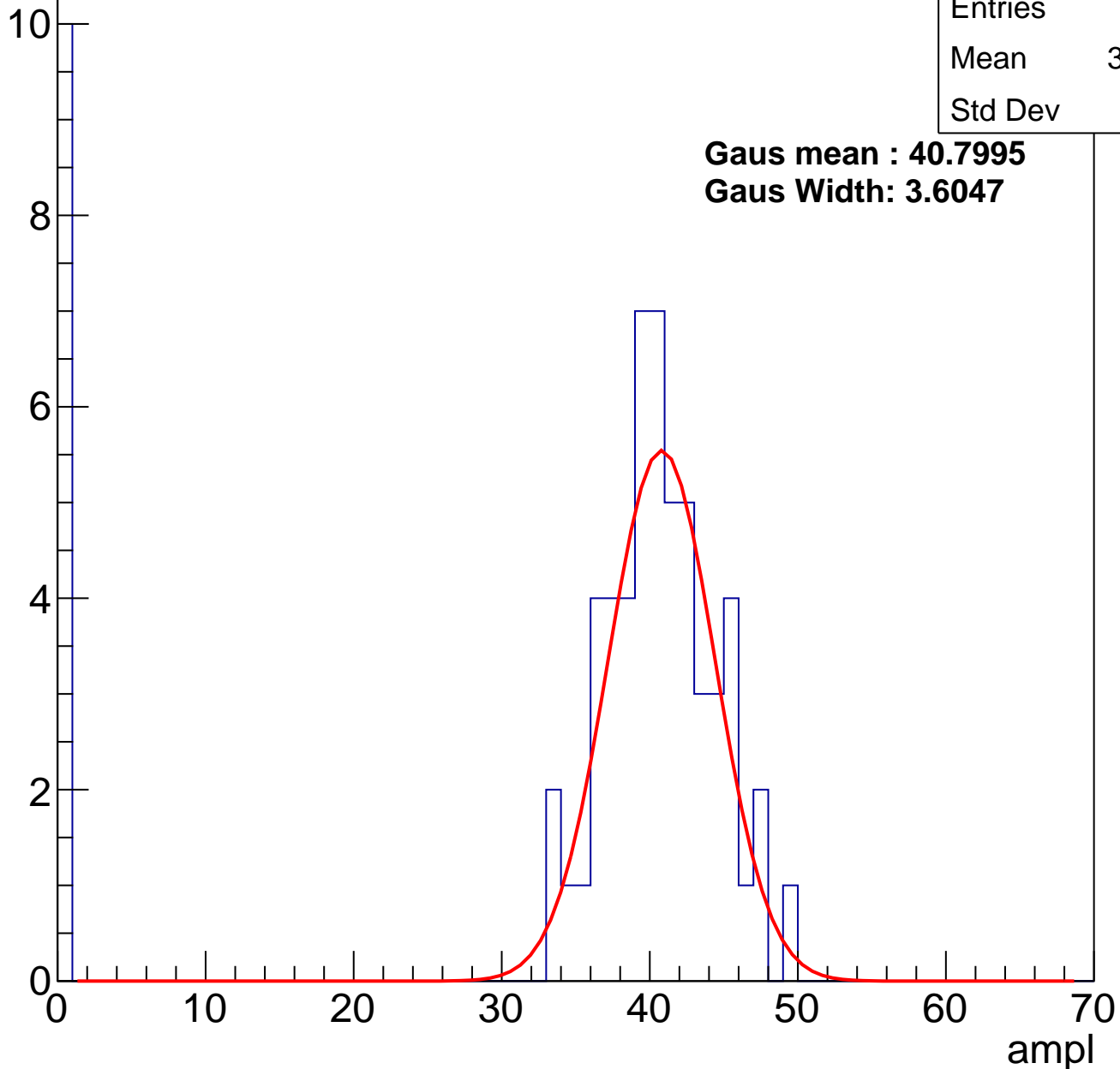
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	34.02
Std Dev	15

Gaus mean : 40.7995

Gaus Width: 3.6047

Entry

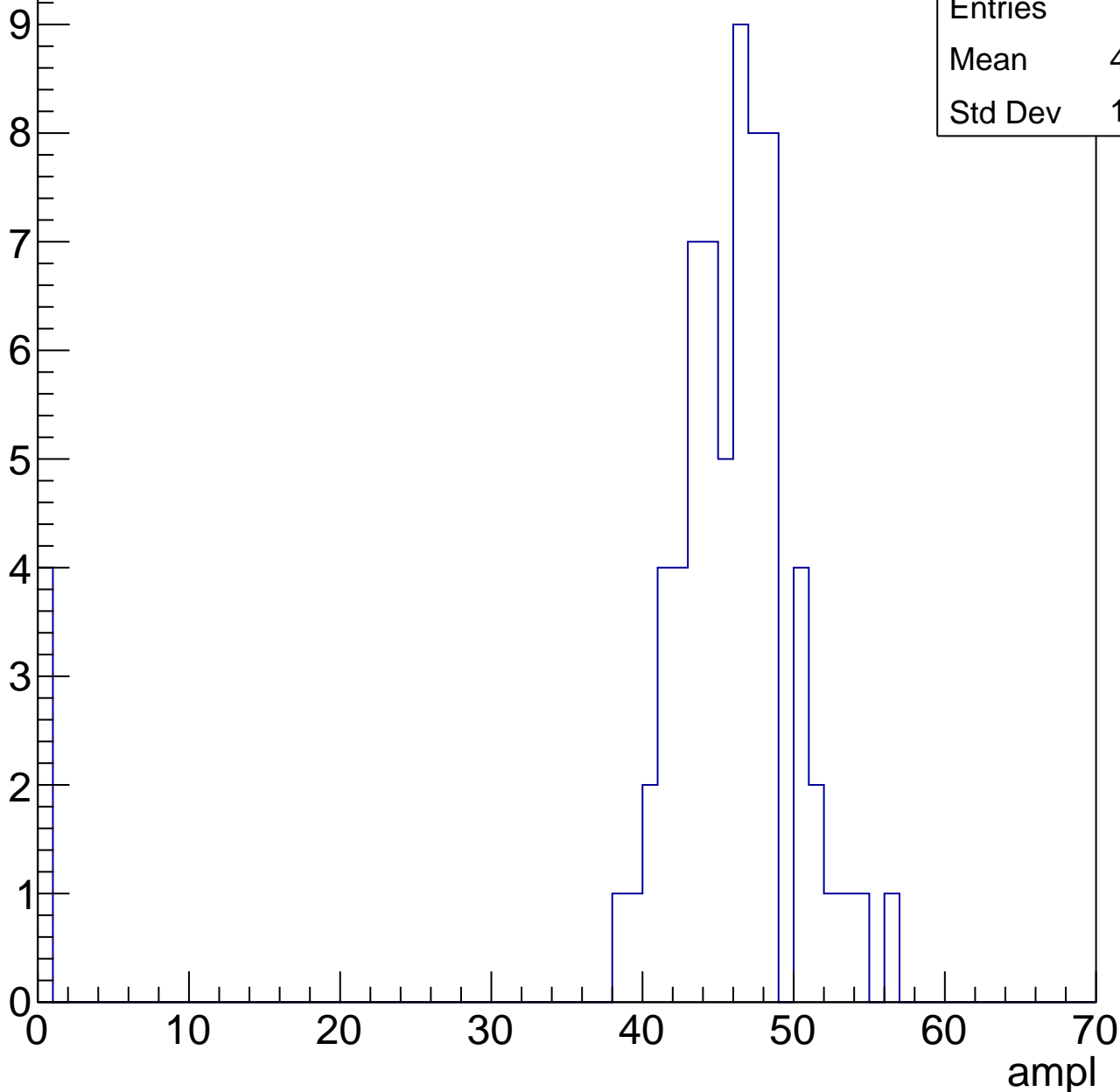


B1L103S, U1-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	43.06
Std Dev	11.15

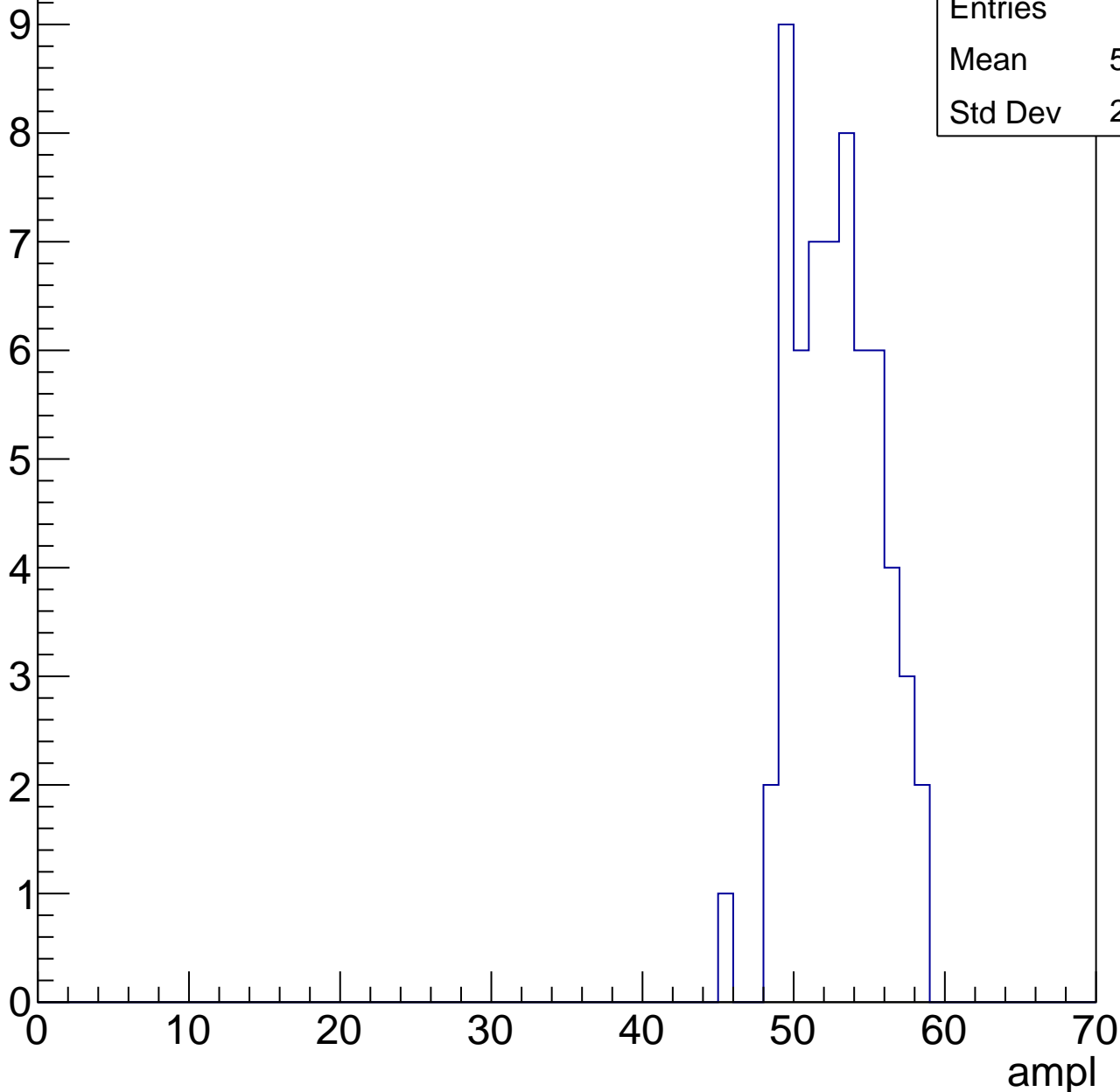


B1L103S, U1-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	52.33
Std Dev	2.815

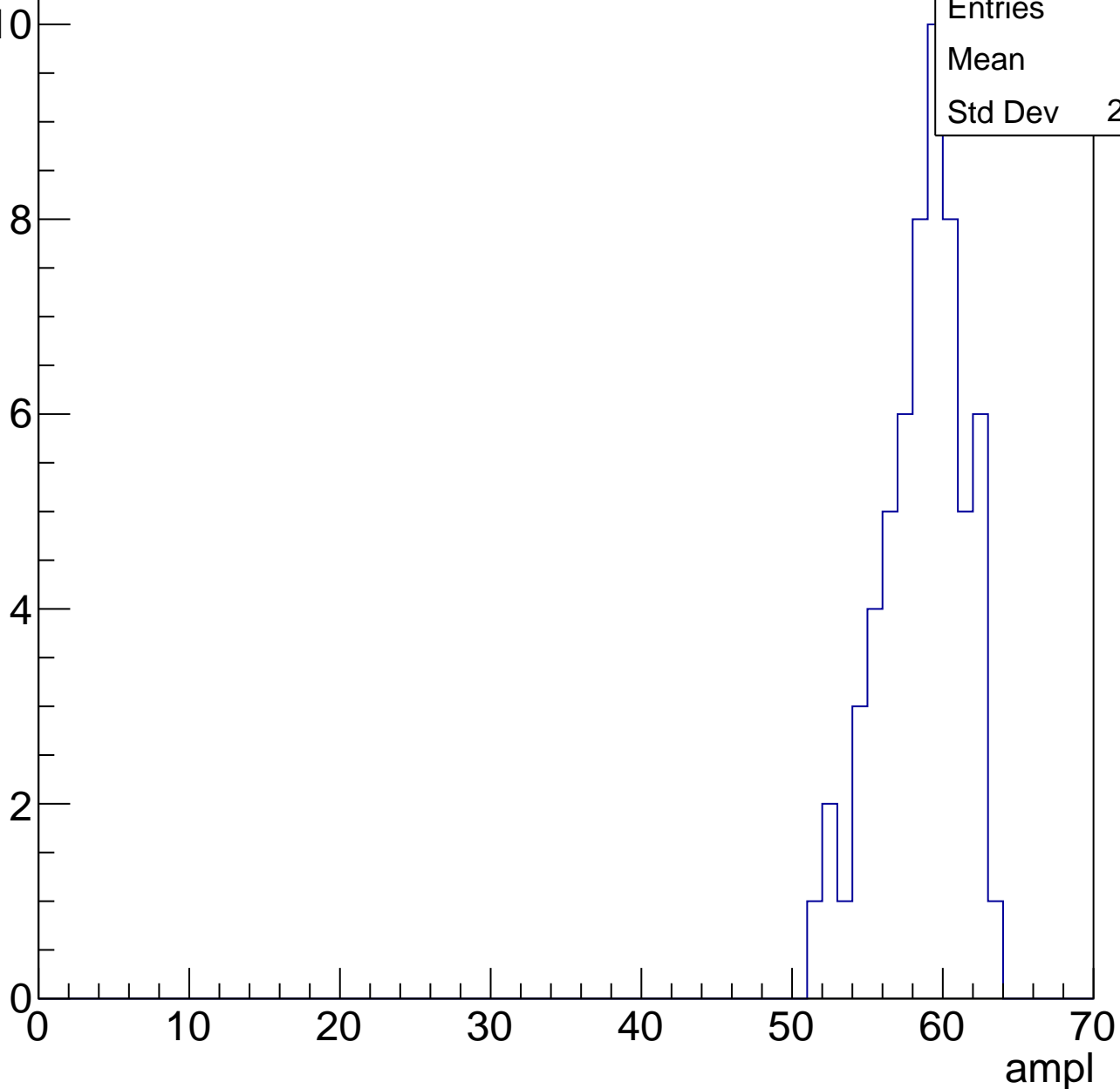


B1L103S, U1-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.1
Std Dev	2.779

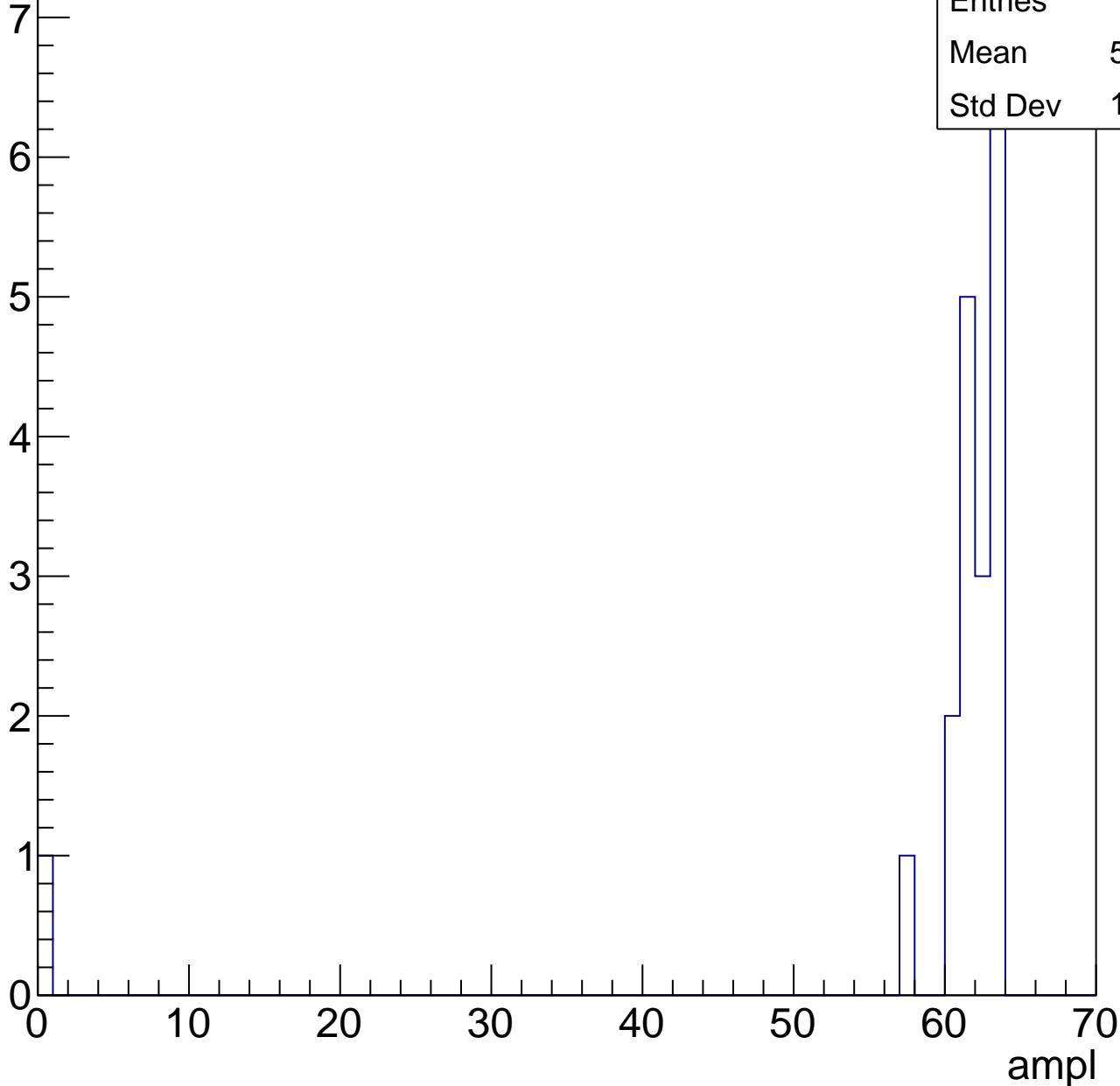


B1L103S, U1-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.37
Std Dev	13.84



B1L103S, U1-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



B1L103S, U1-ch51, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	20.99
Std Dev	12.36

Gaus mean : 28.3354

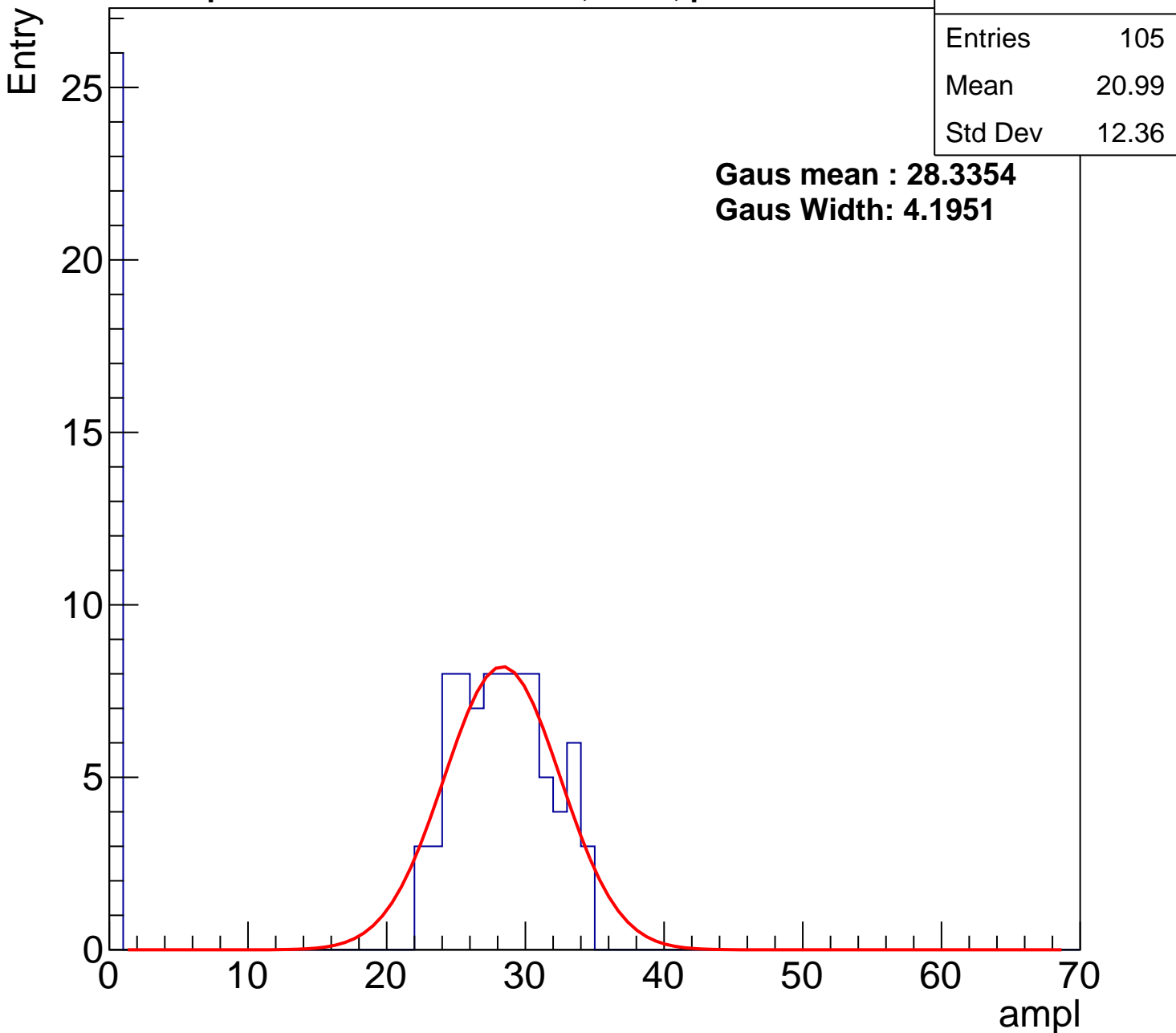
Gaus Width: 4.1951

Entry

25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch51, adc1

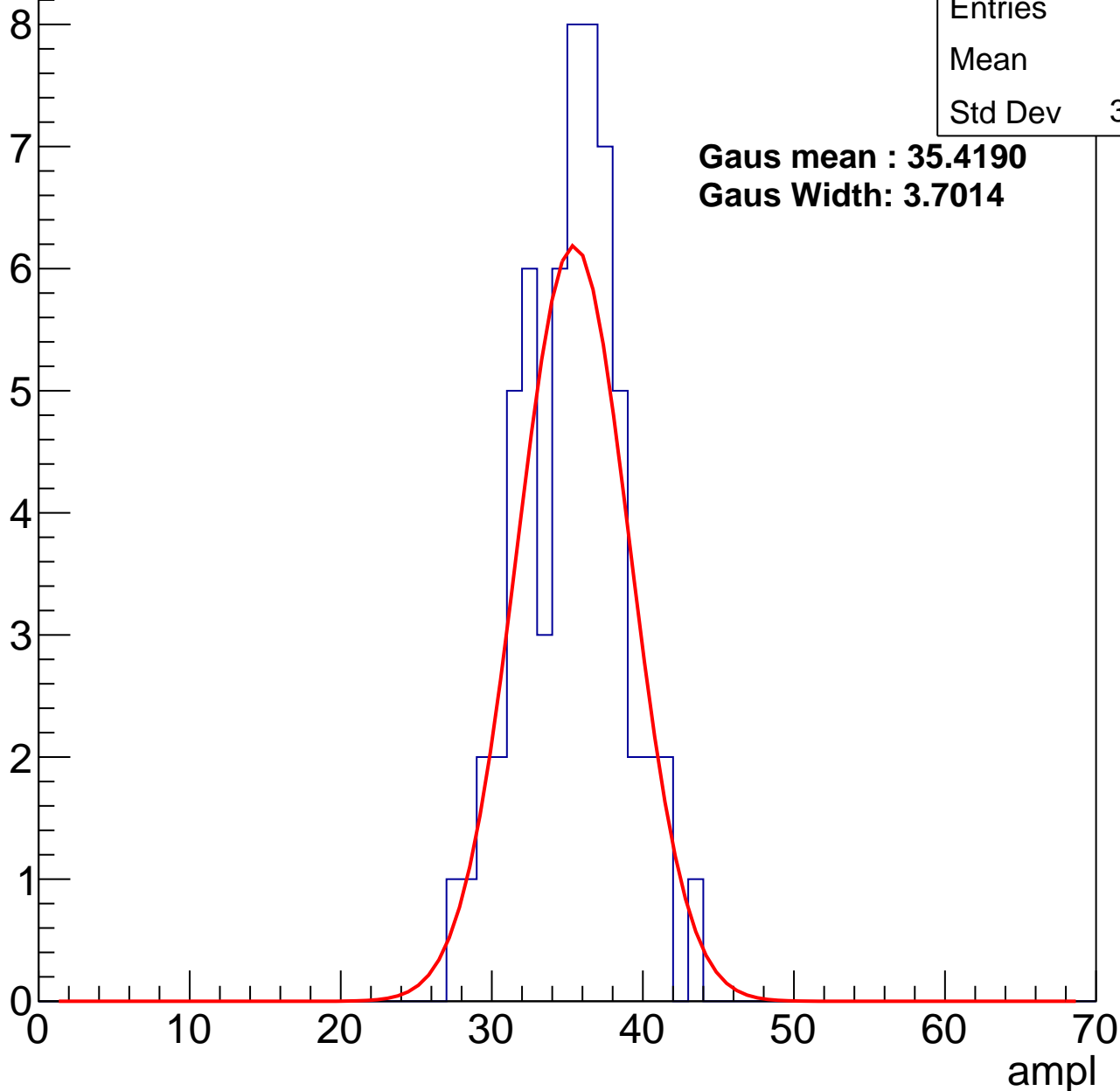
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.8
Std Dev	3.348

Gaus mean : 35.4190

Gaus Width: 3.7014



B1L103S, U1-ch51, adc2

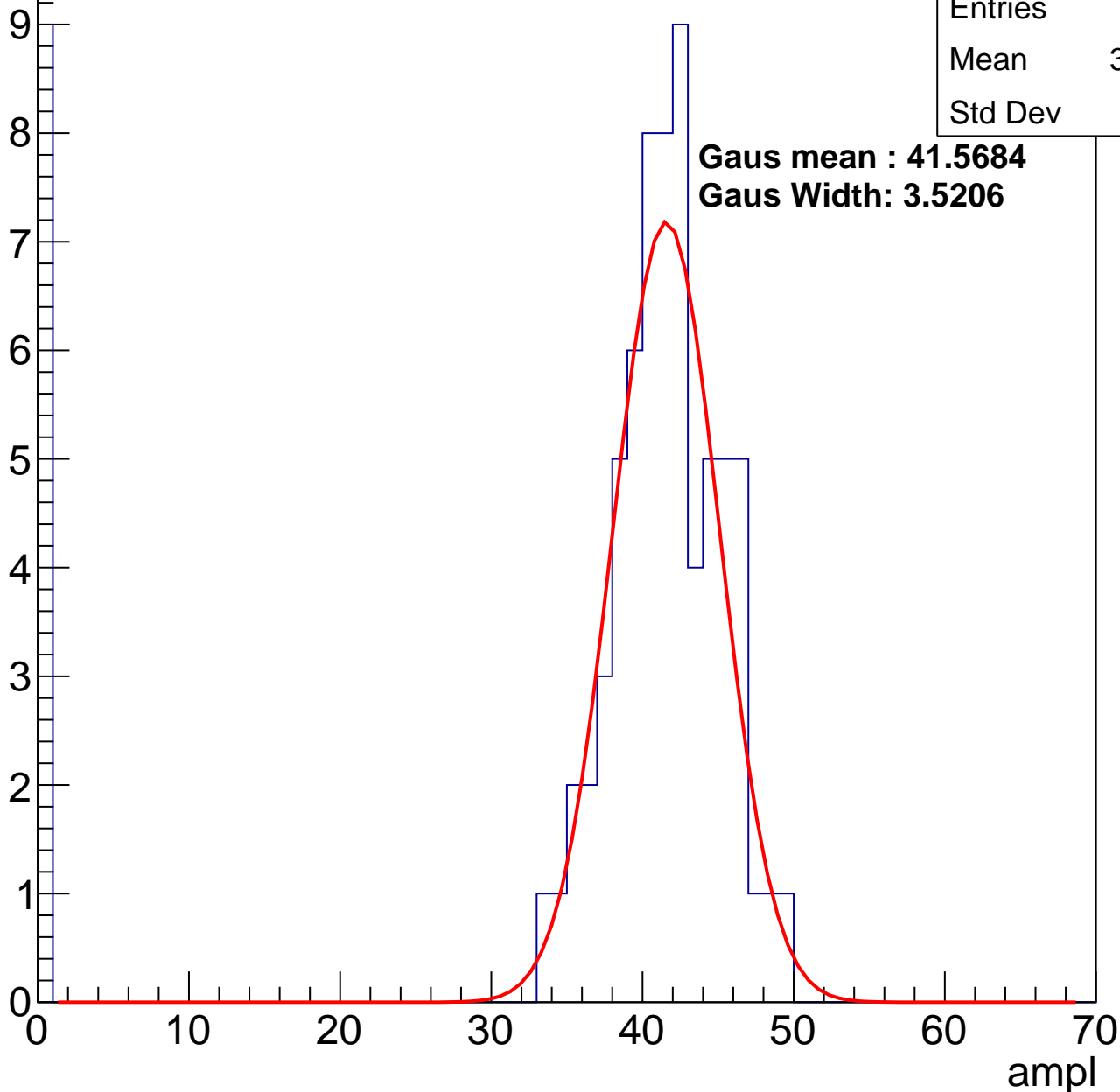
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.33
Std Dev	13.7

Gaus mean : 41.5684

Gaus Width: 3.5206

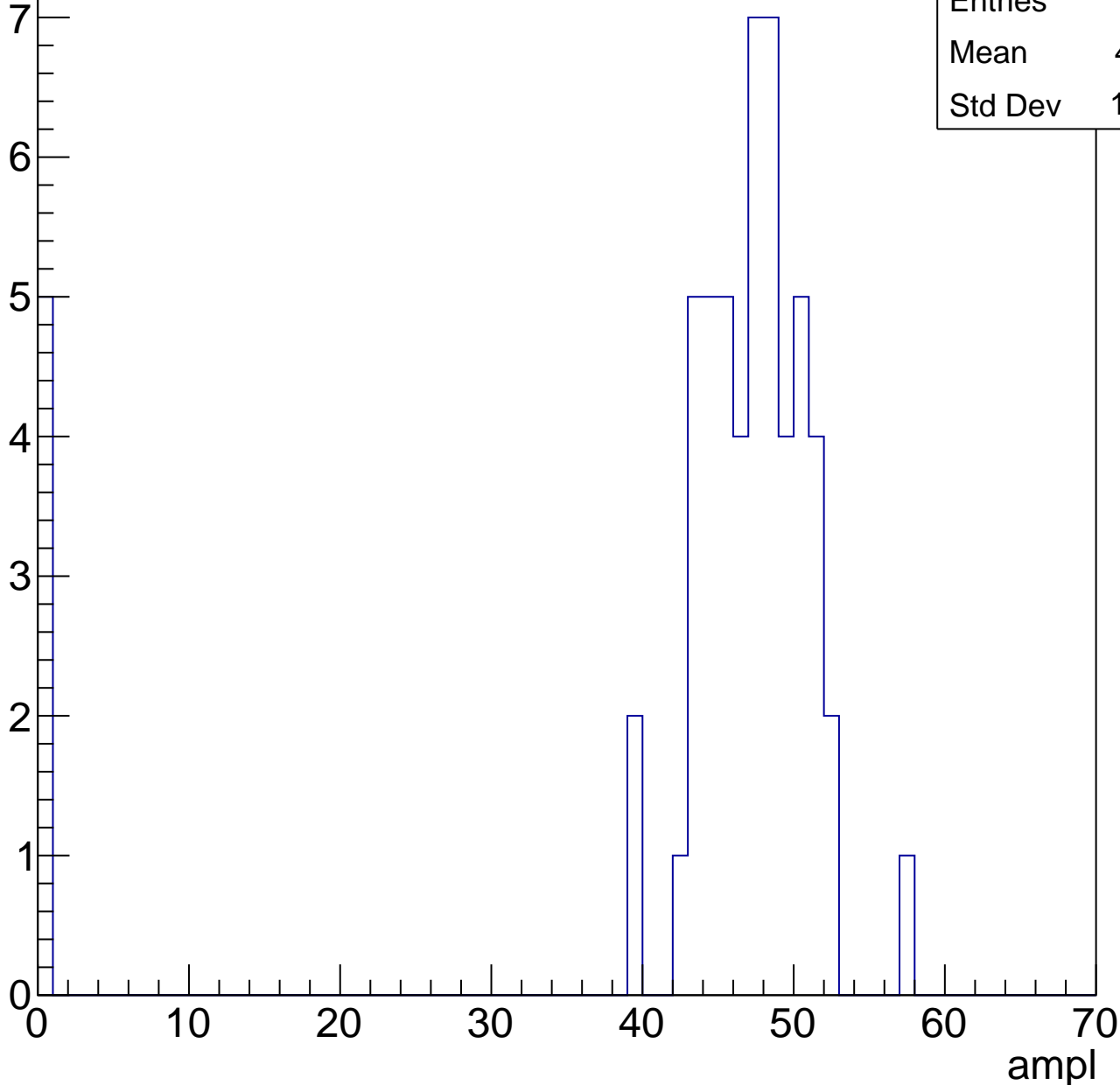


B1L103S, U1-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	42.81
Std Dev	13.65

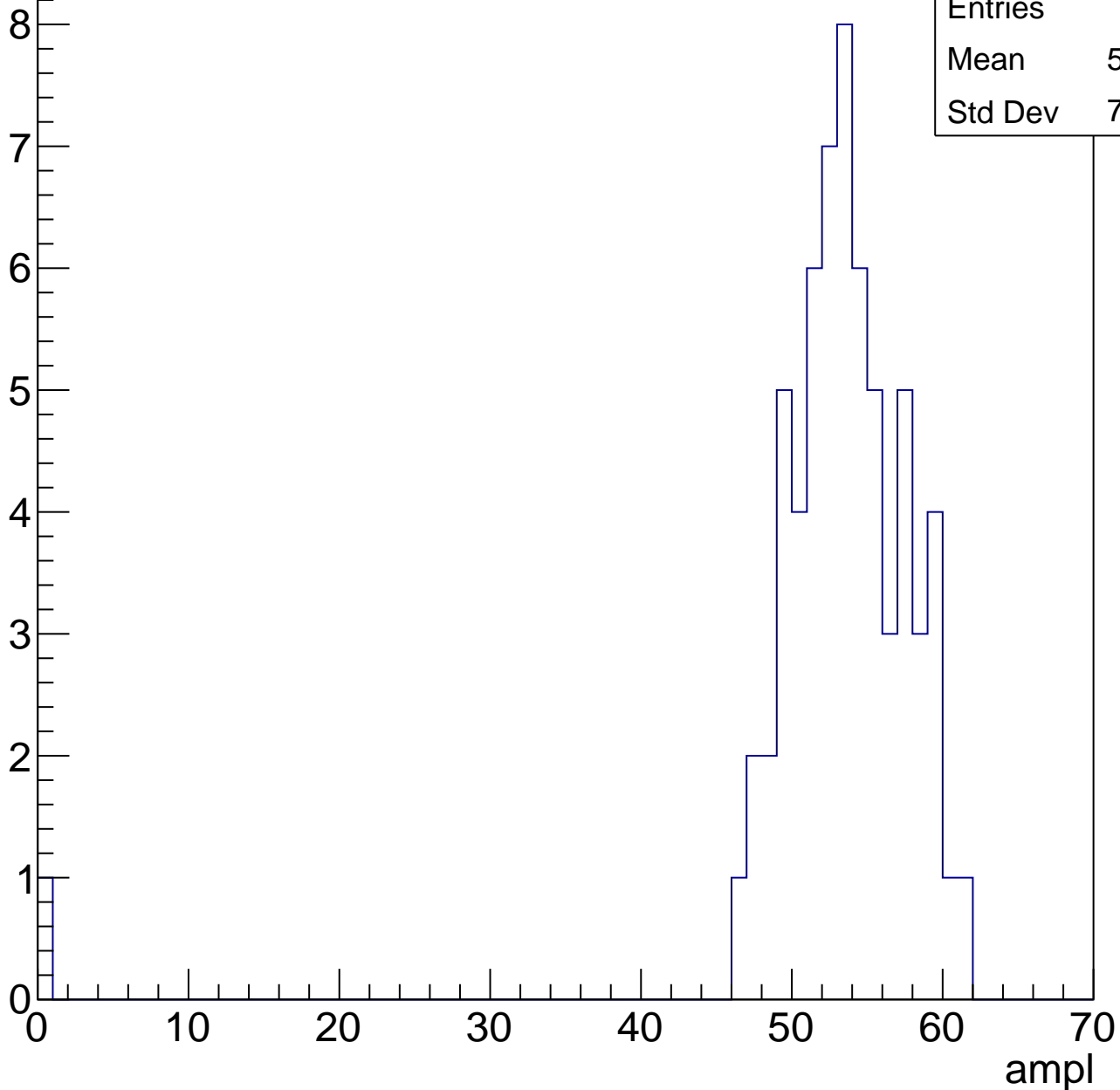


B1L103S, U1-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.47
Std Dev	7.473

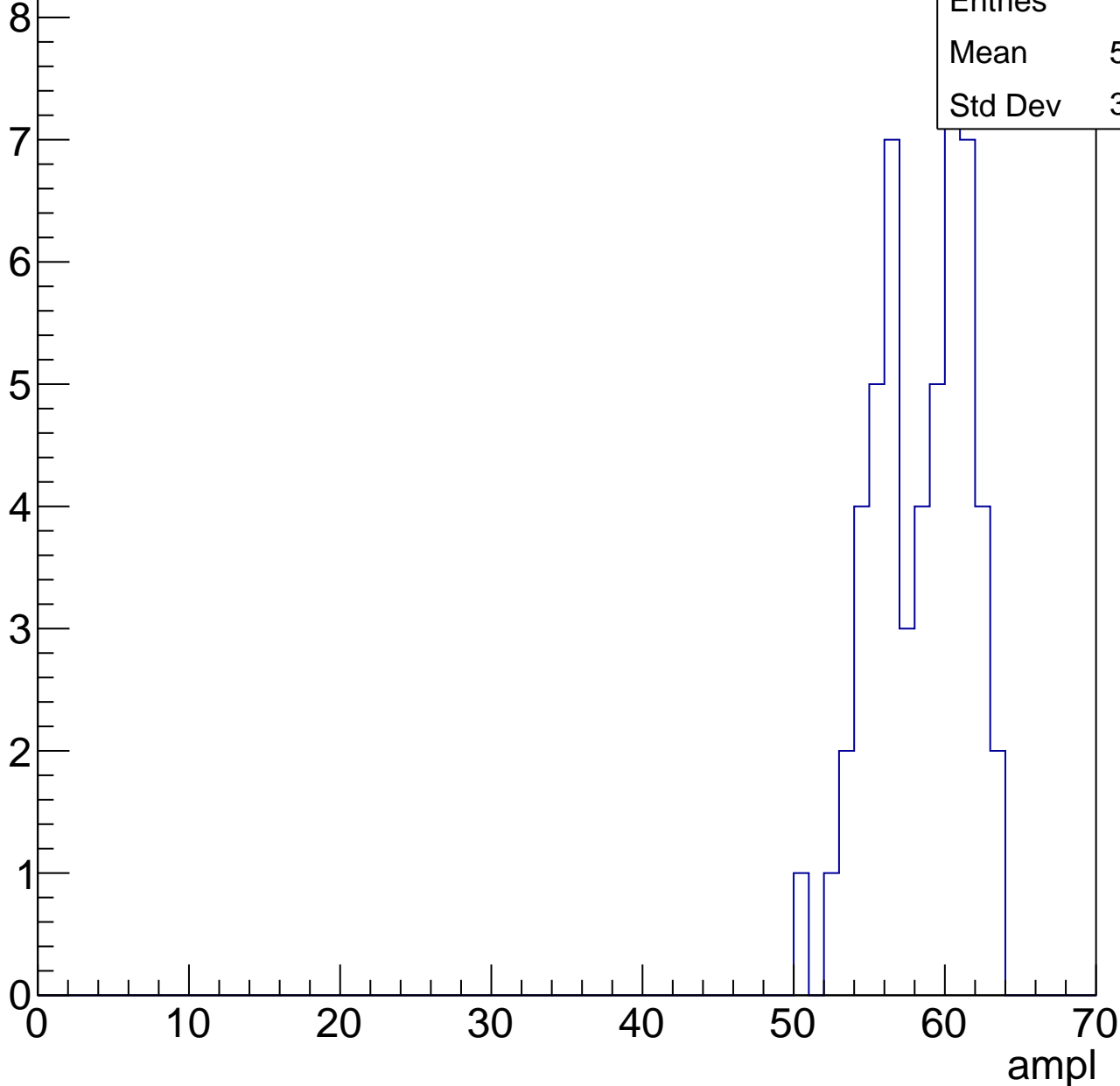


B1L103S, U1-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.92
Std Dev	3.083

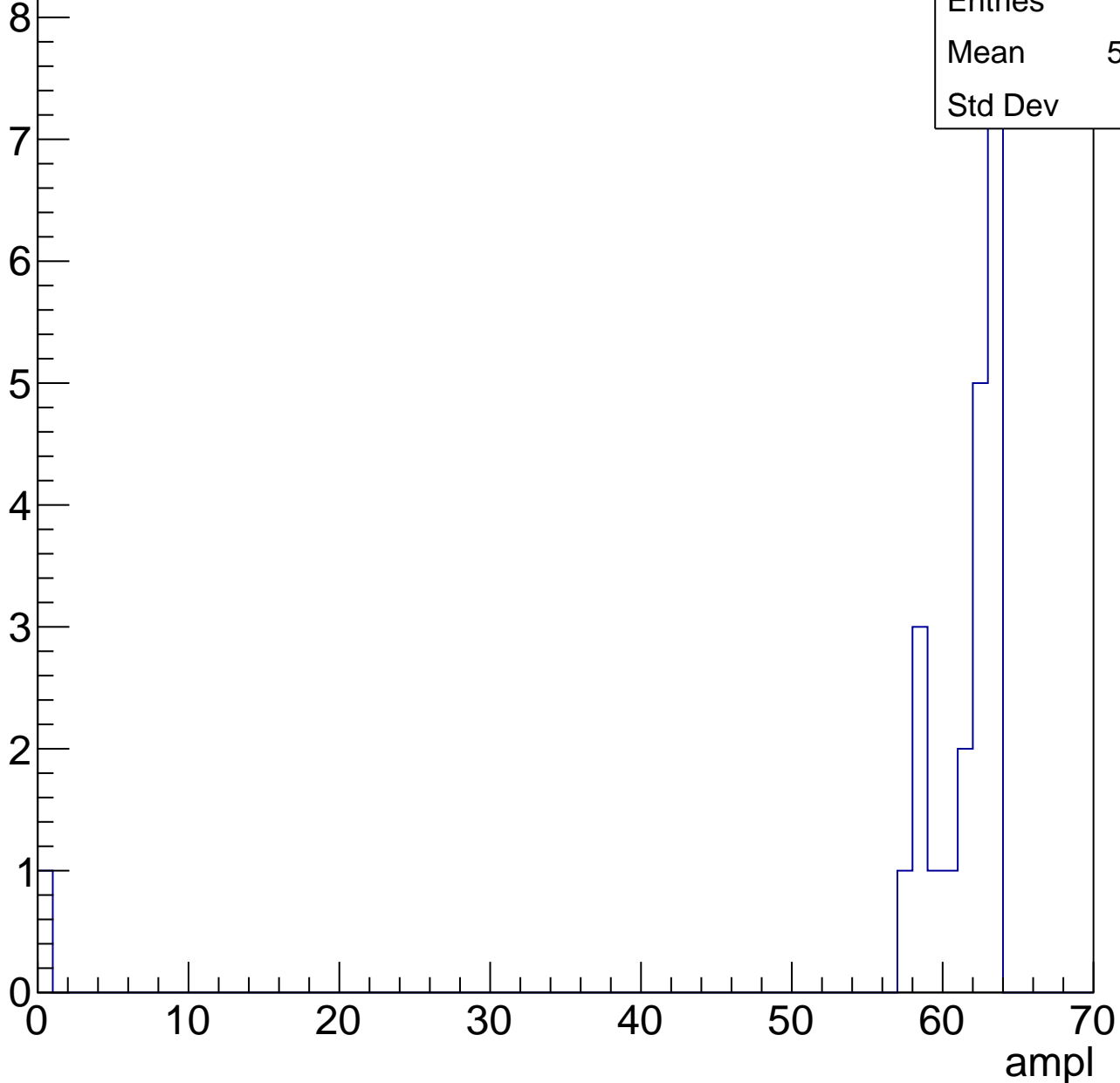


B1L103S, U1-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.45
Std Dev	12.9

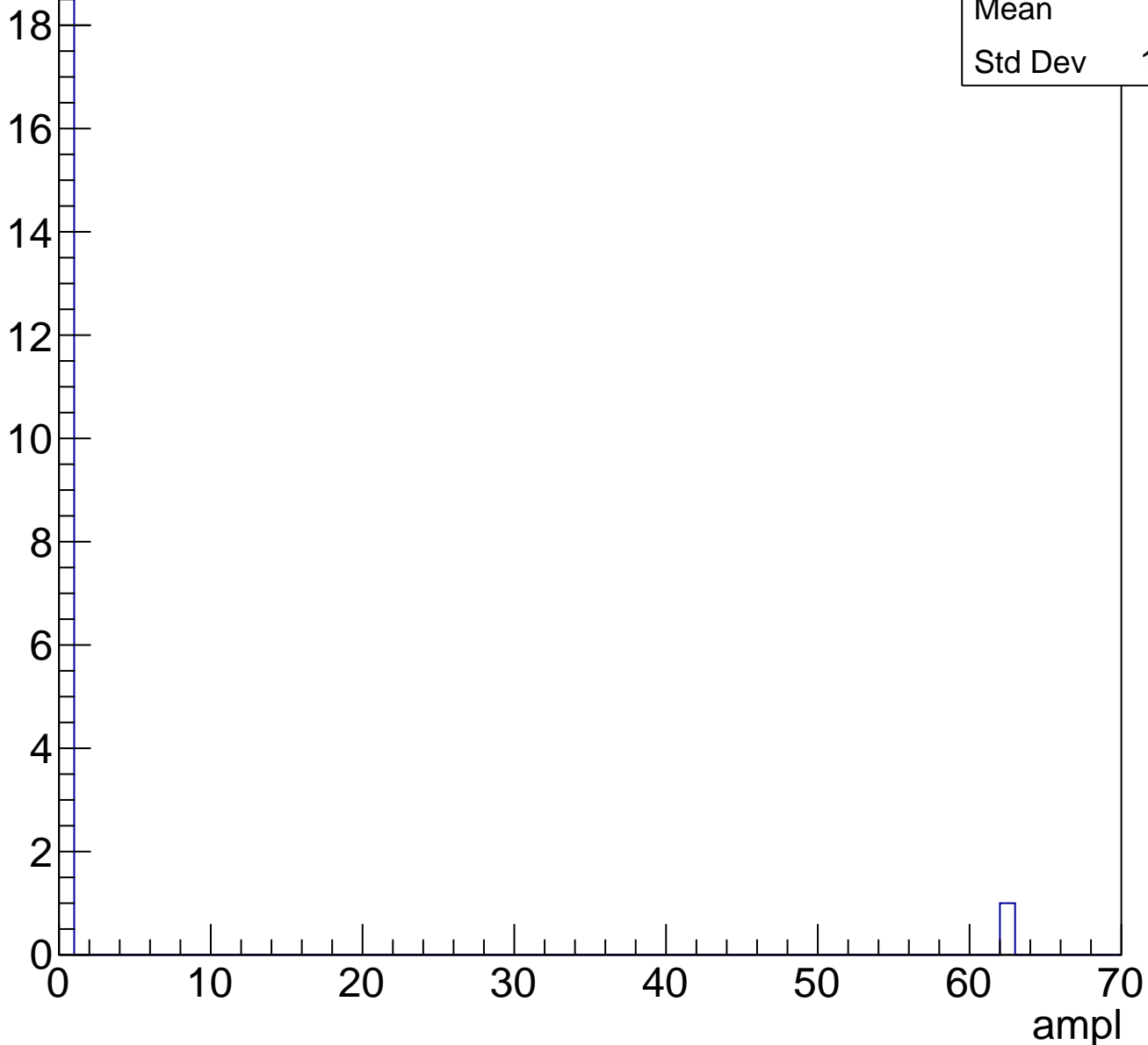


B1L103S, U1-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U1-ch52, adc0

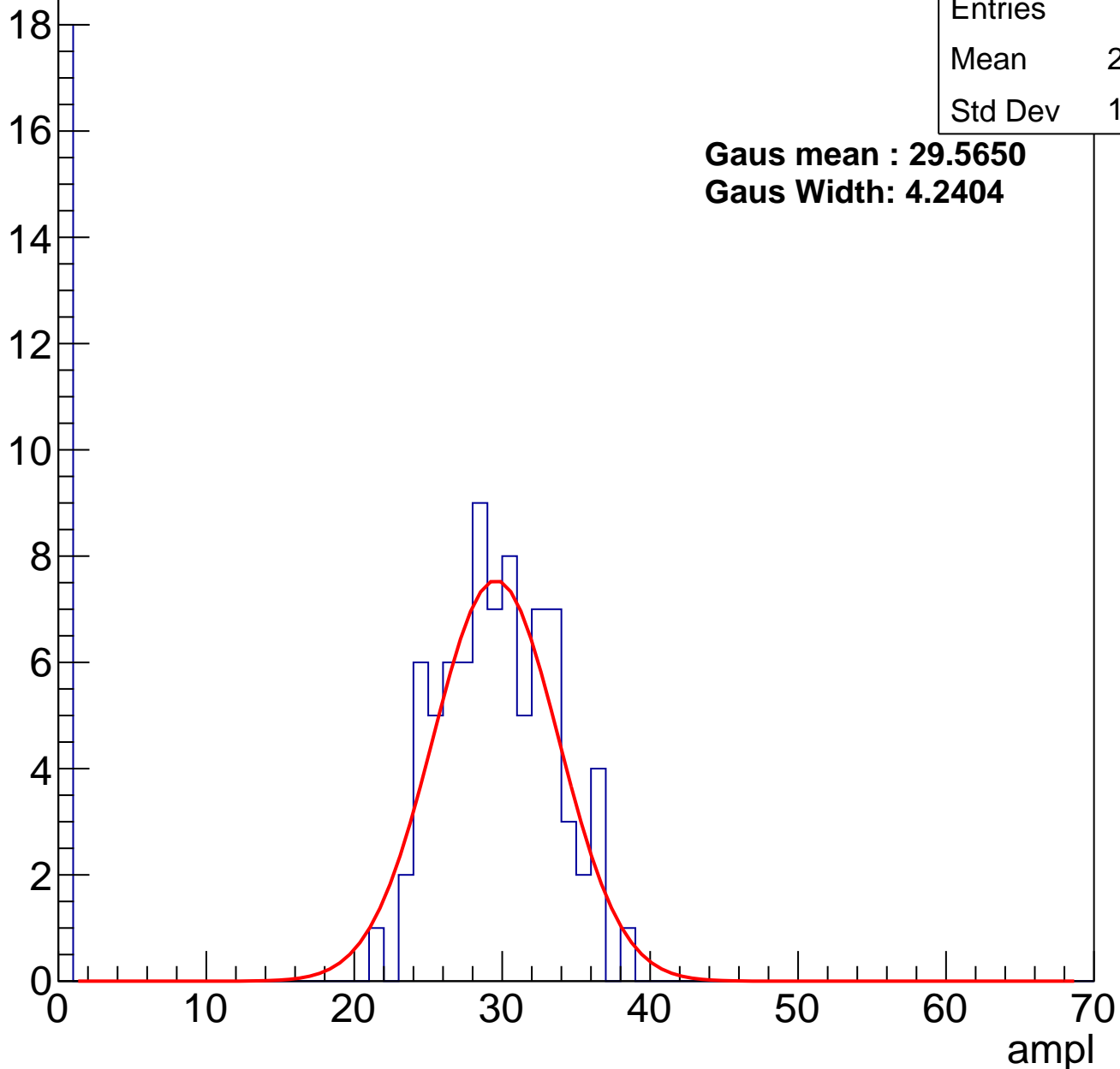
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	23.85
Std Dev	11.86

Gaus mean : 29.5650

Gaus Width: 4.2404

Entry



B1L103S, U1-ch52, adc1

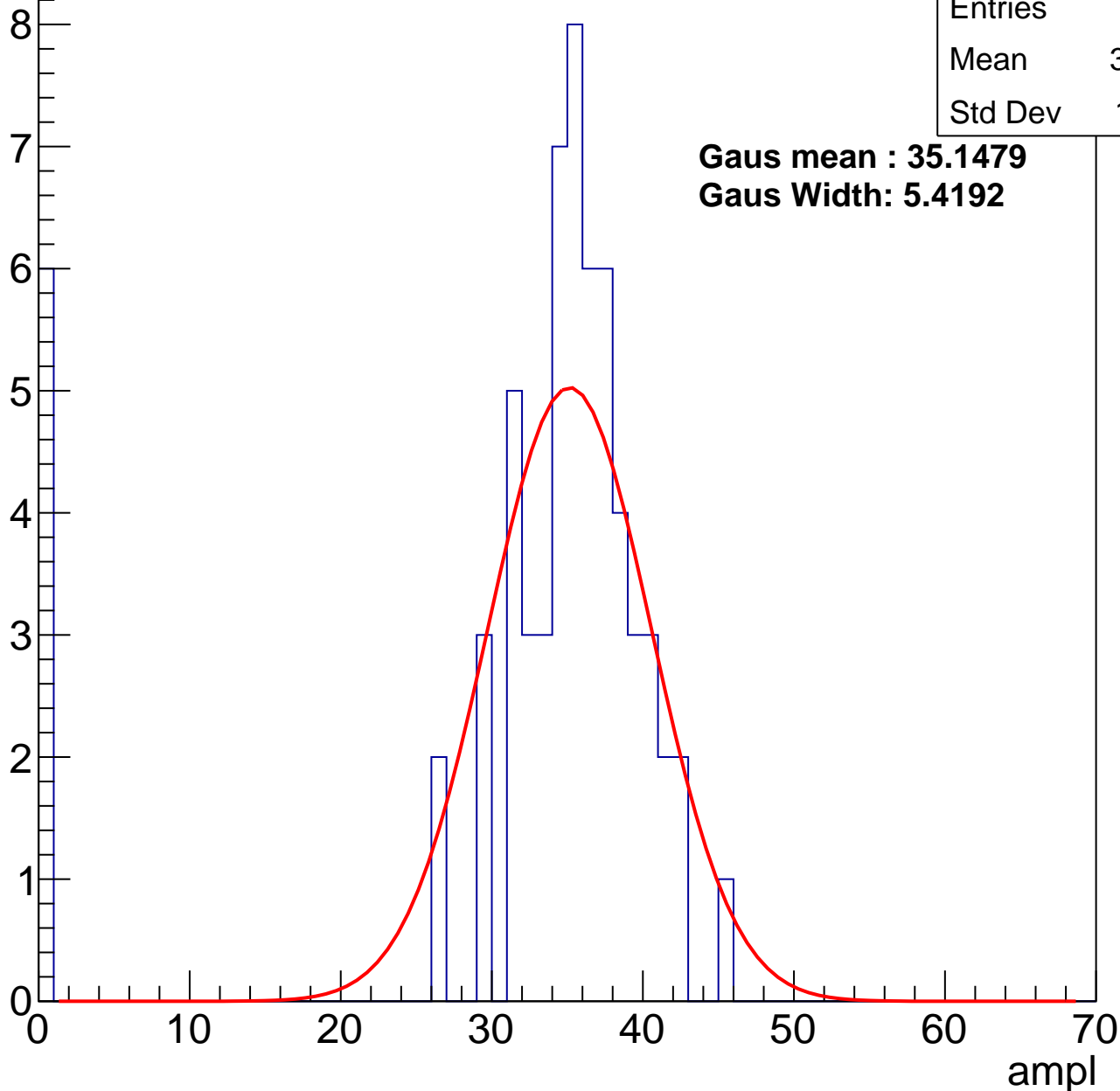
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	31.95
Std Dev	10.91

Gaus mean : 35.1479

Gaus Width: 5.4192



B1L103S, U1-ch52, adc2

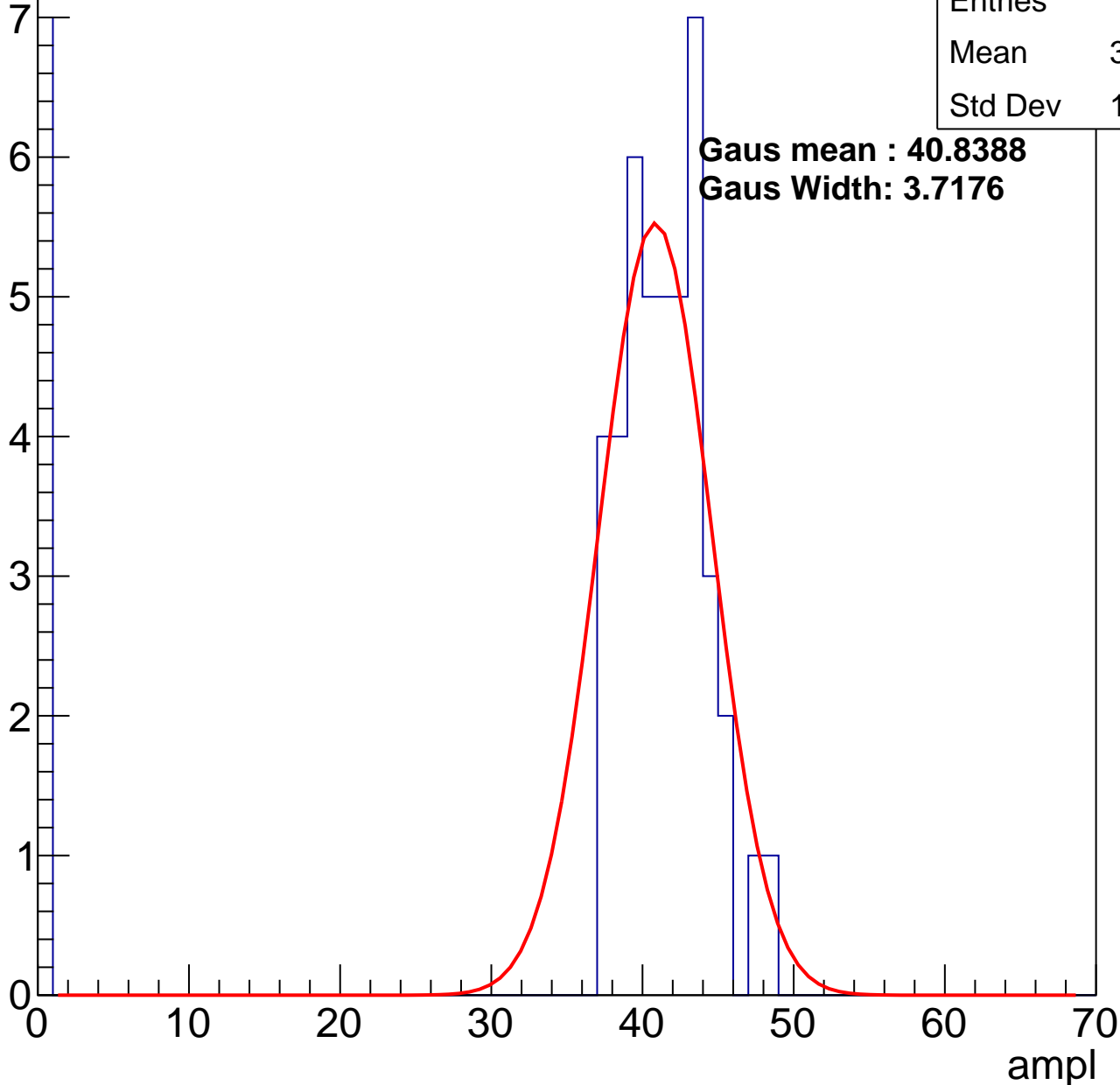
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	35.34
Std Dev	14.47

Gaus mean : 40.8388

Gaus Width: 3.7176

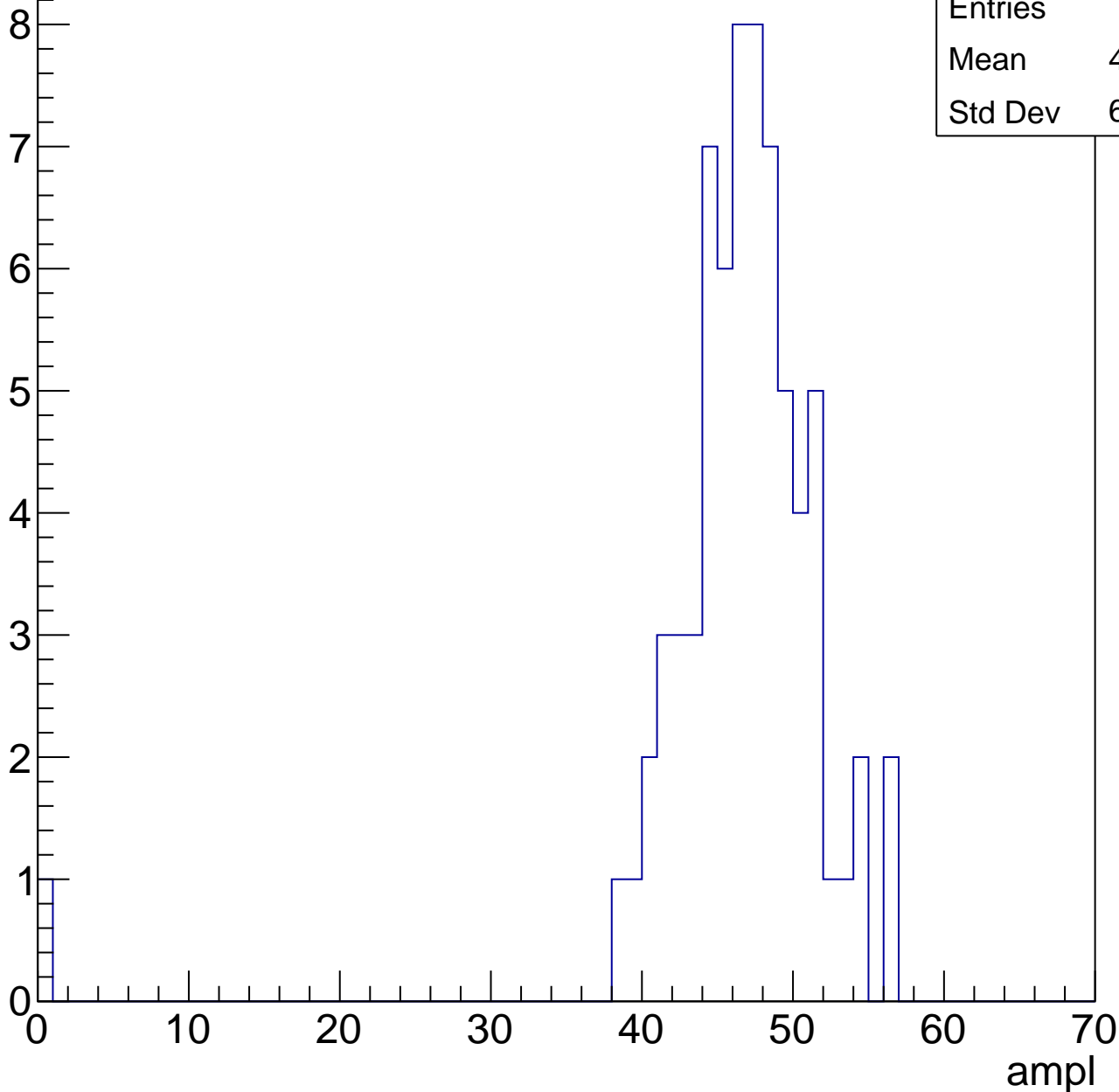


B1L103S, U1-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.97
Std Dev	6.727

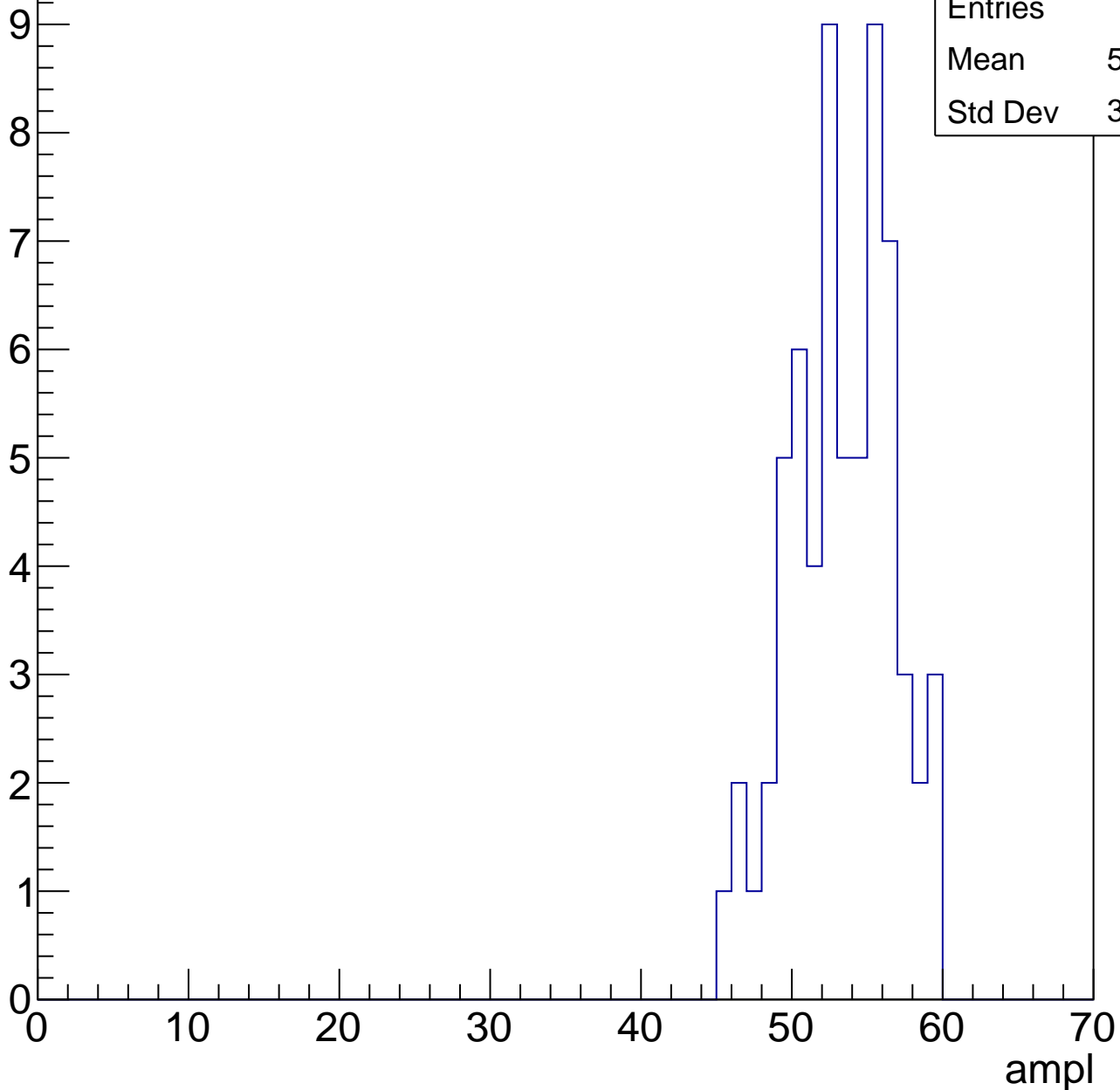


B1L103S, U1-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

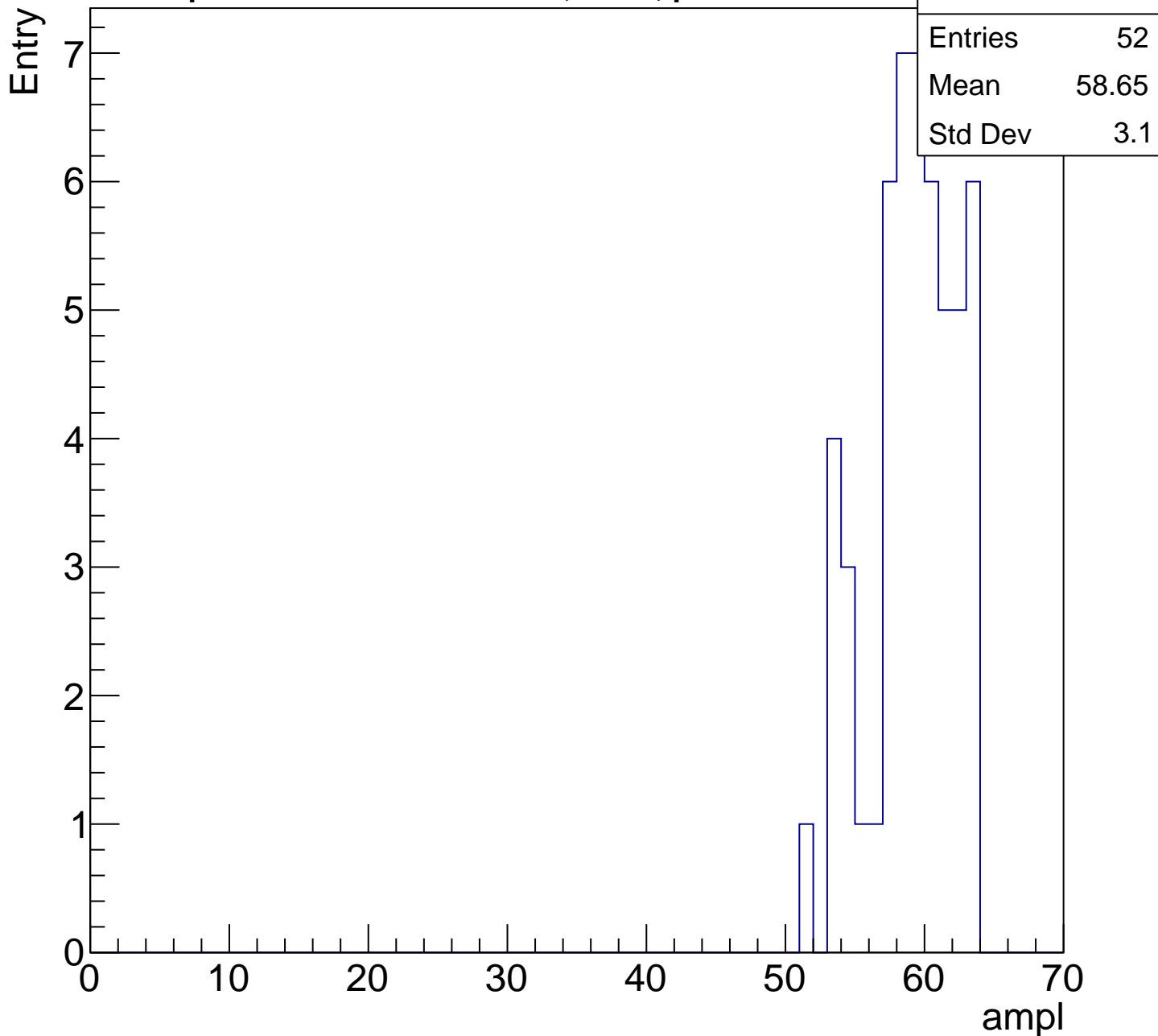
Entry

Entries	64
Mean	52.86
Std Dev	3.344



B1L103S, U1-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

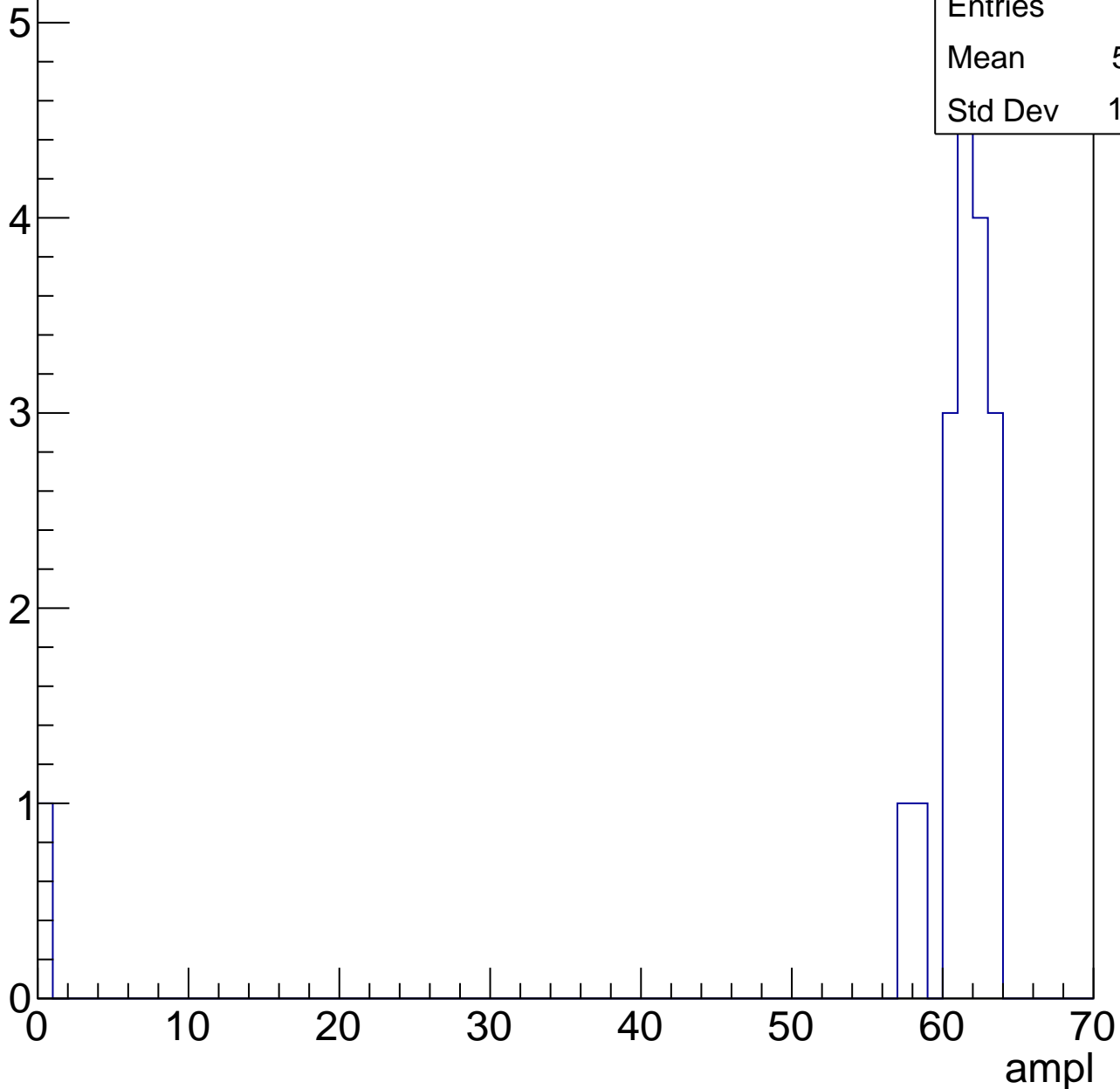


B1L103S, U1-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

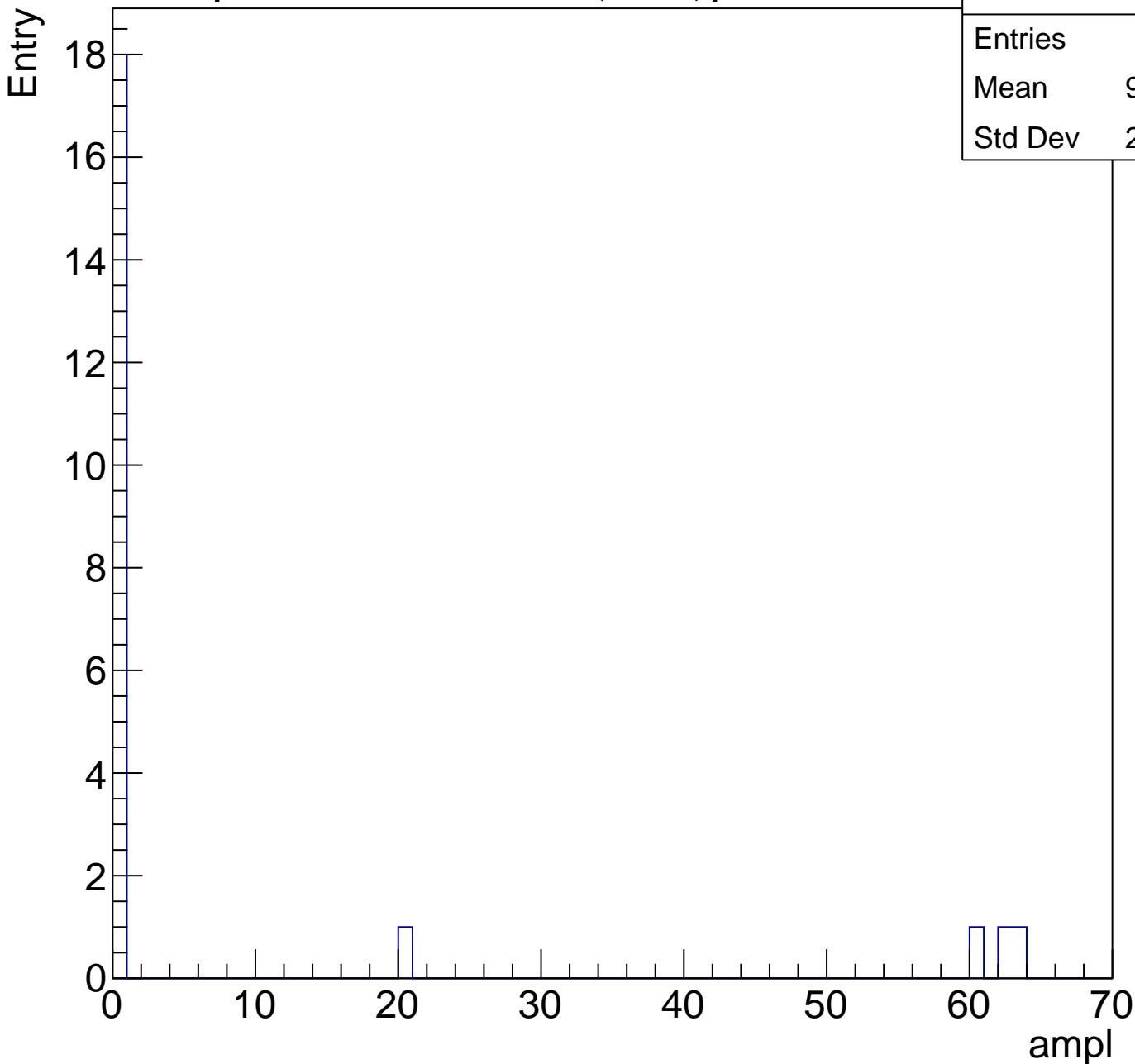
Entries	18
Mean	57.61
Std Dev	14.06



B1L103S, U1-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.318
Std Dev	21.22



B1L103S, U1-ch53, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	20.48
Std Dev	13.26

Gaus mean : 28.9112

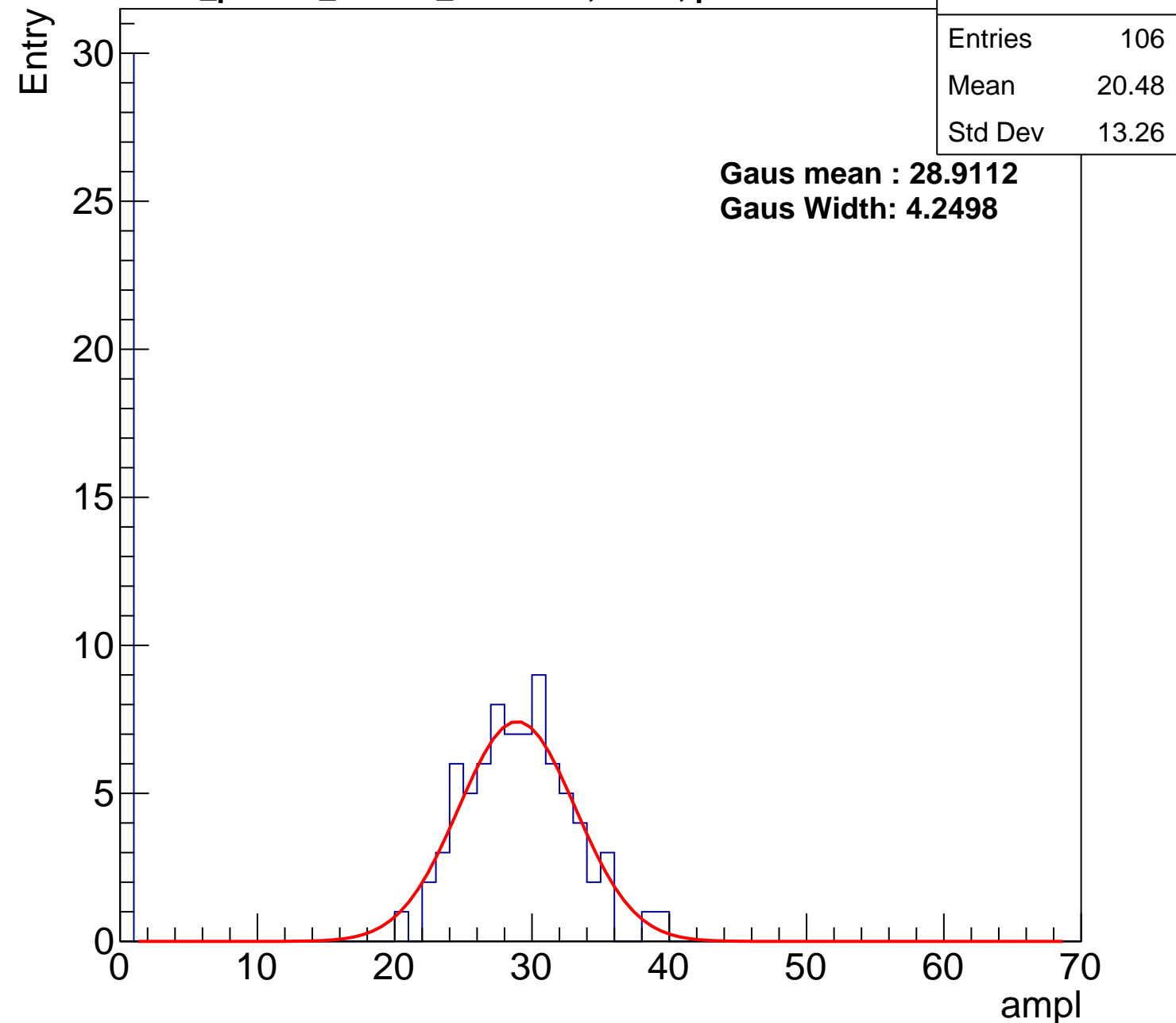
Gaus Width: 4.2498

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch53, adc1

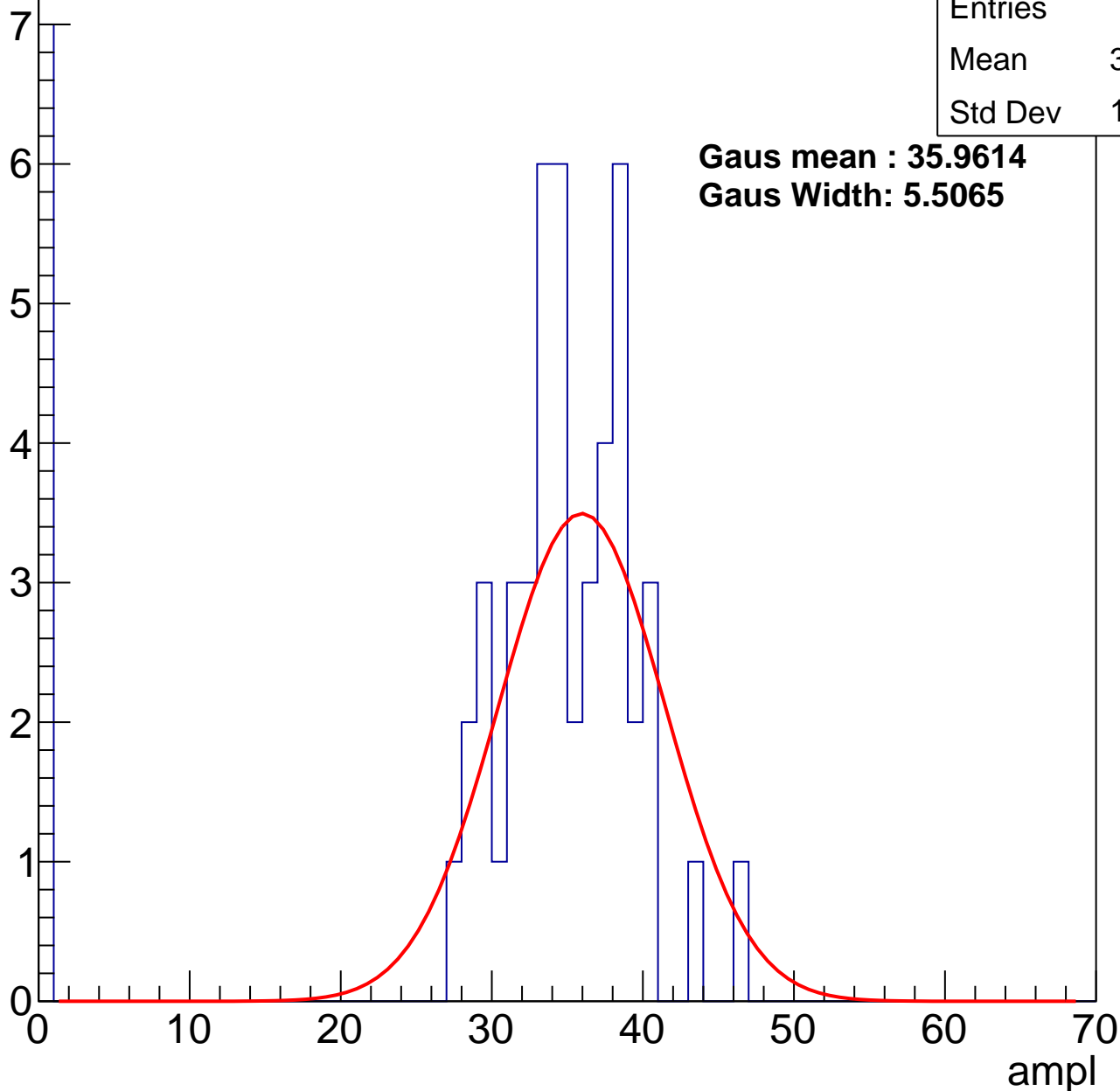
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	30.22
Std Dev	12.25

Gaus mean : 35.9614

Gaus Width: 5.5065



B1L103S, U1-ch53, adc2

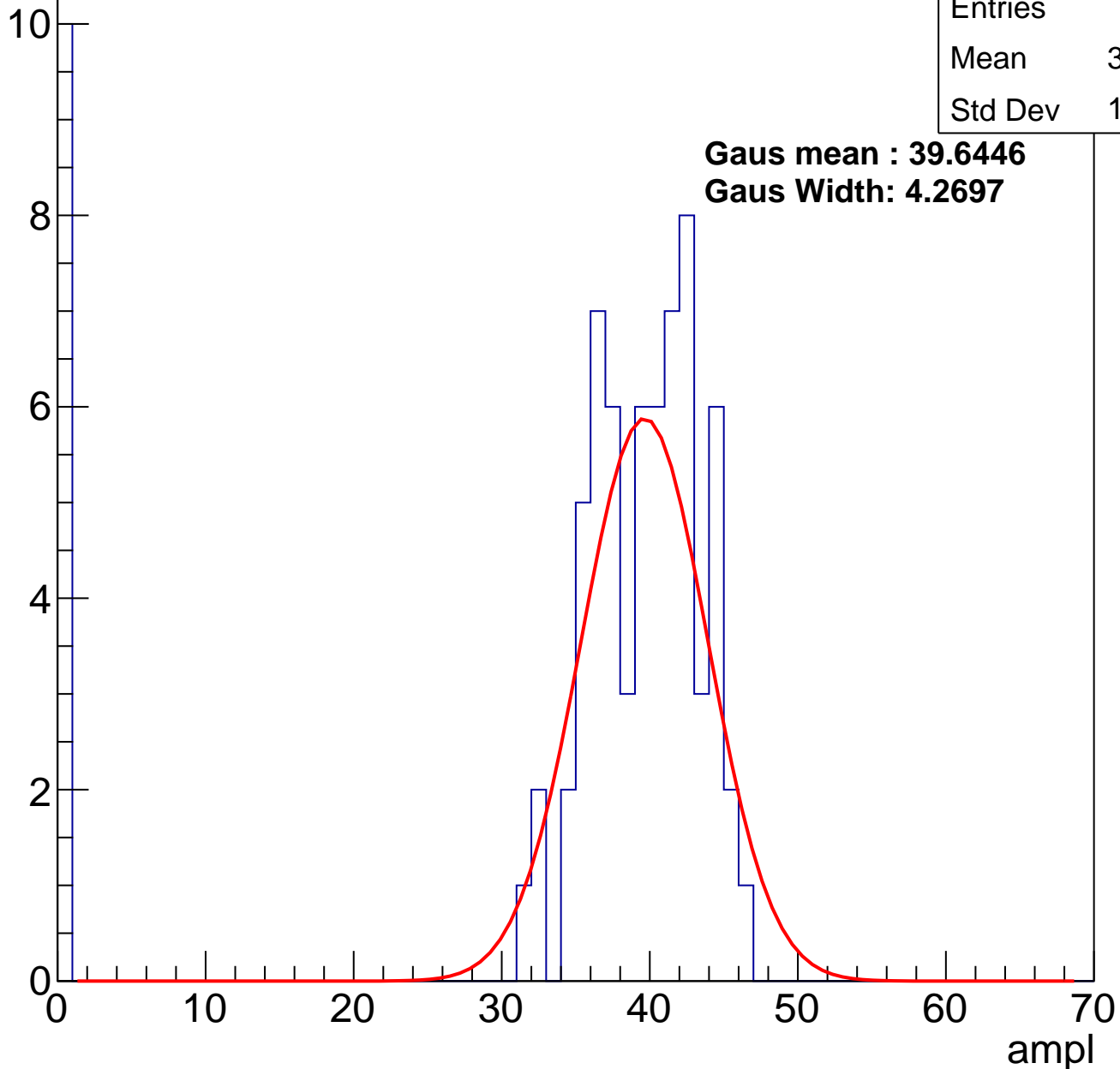
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	34.03
Std Dev	13.74

Gaus mean : 39.6446

Gaus Width: 4.2697

Entry



B1L103S, U1-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

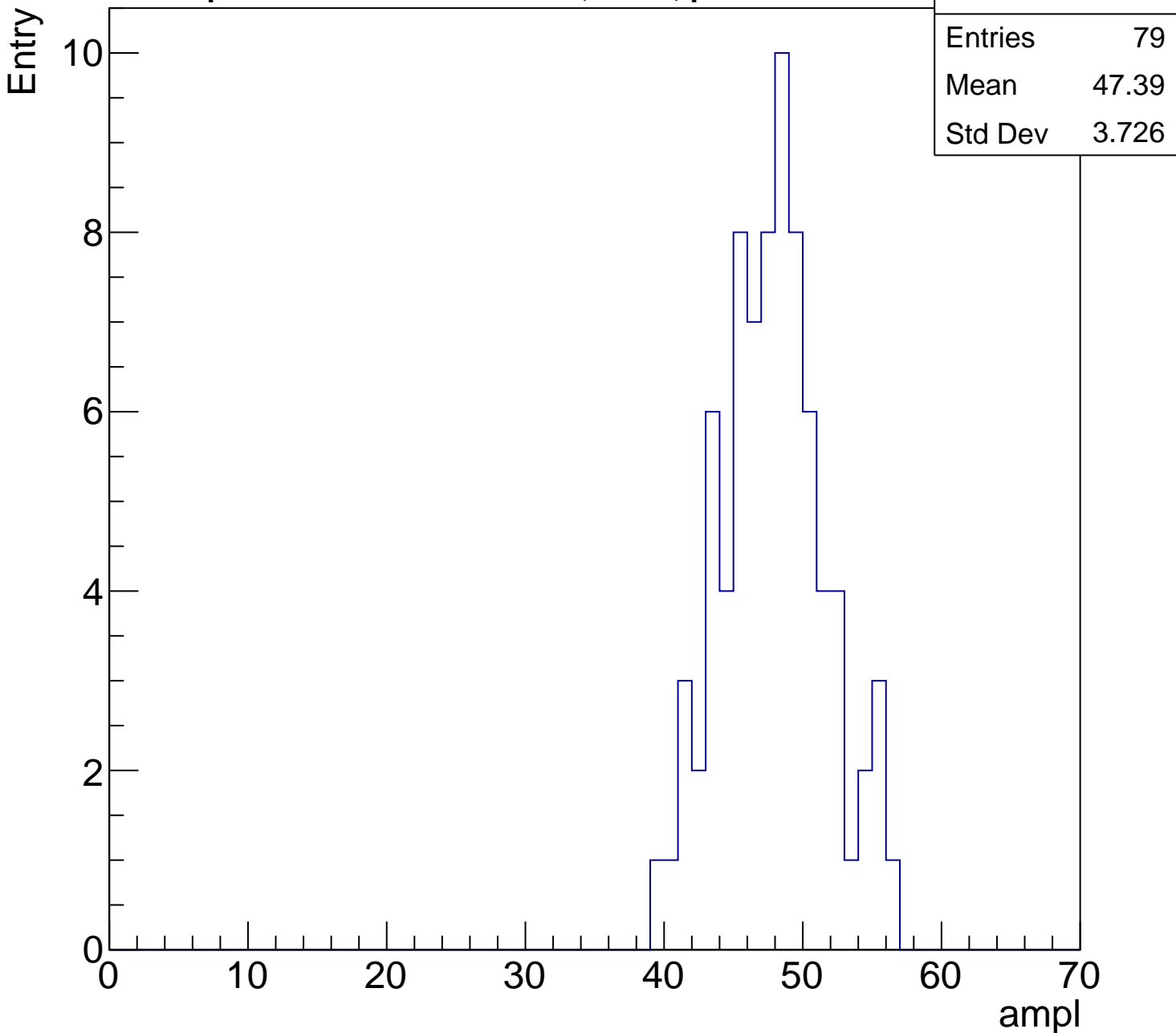
Entries	79
Mean	47.39
Std Dev	3.726

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

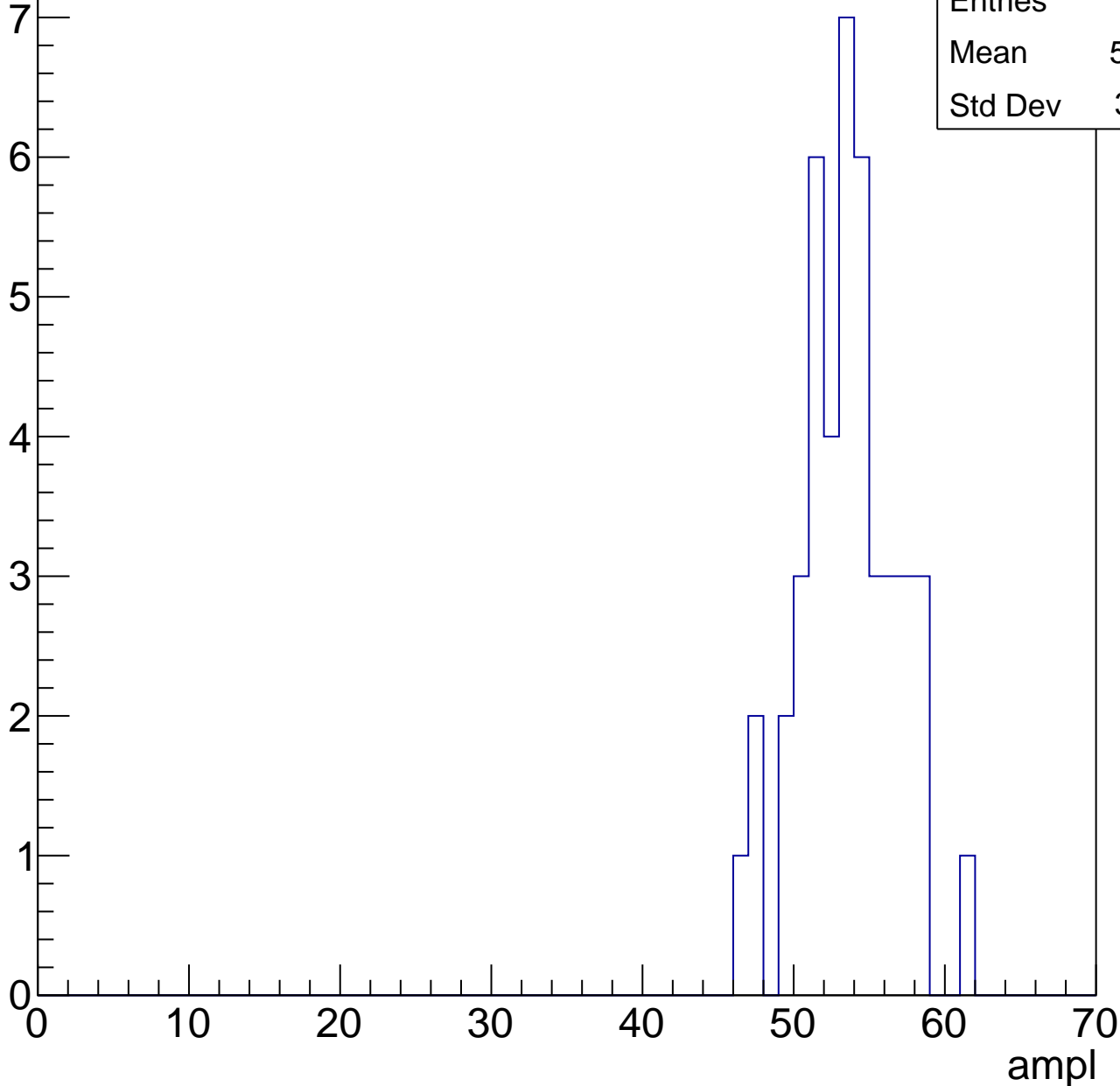


B1L103S, U1-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	53.09
Std Dev	3.161

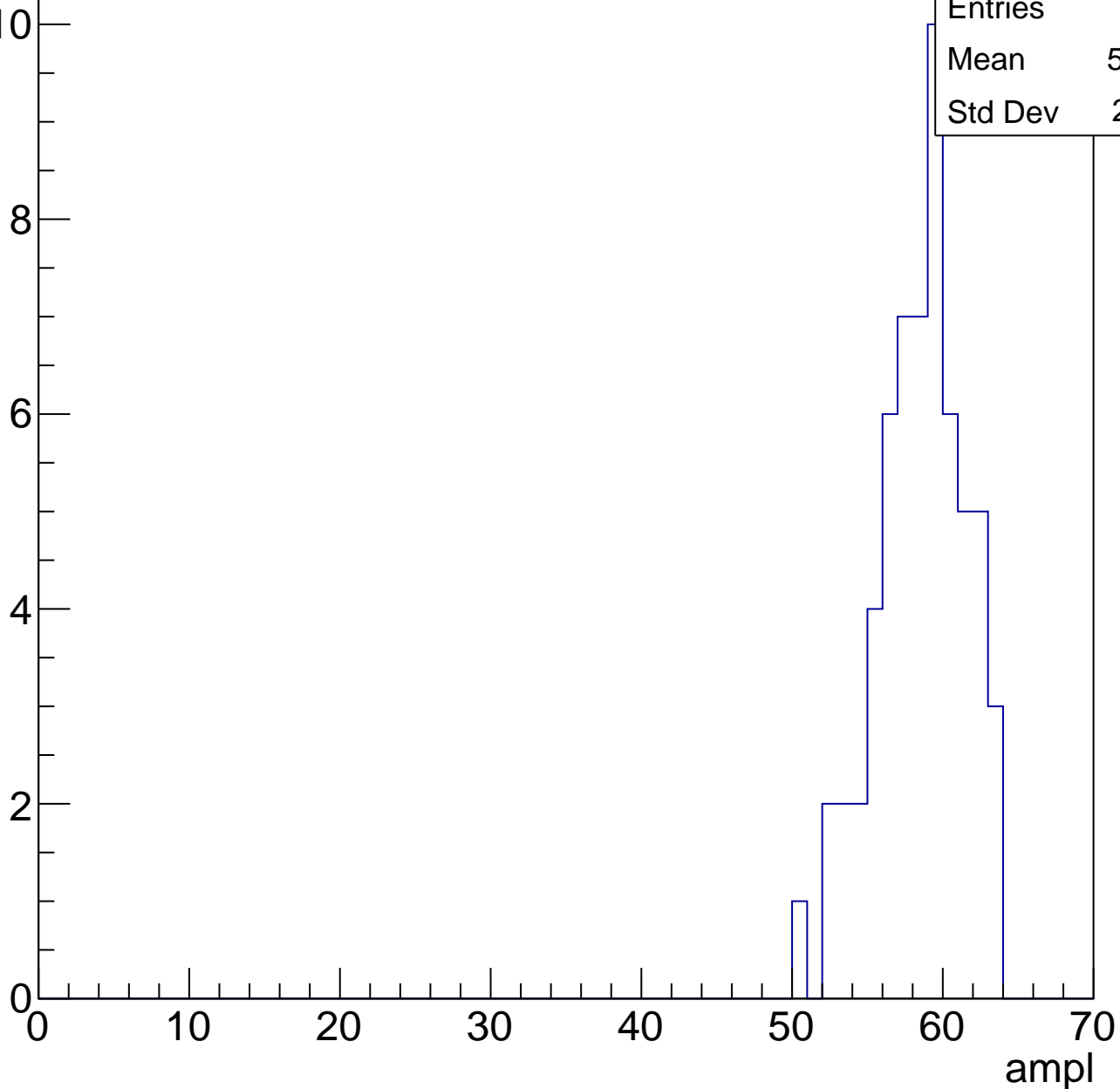


B1L103S, U1-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	58.05
Std Dev	2.941

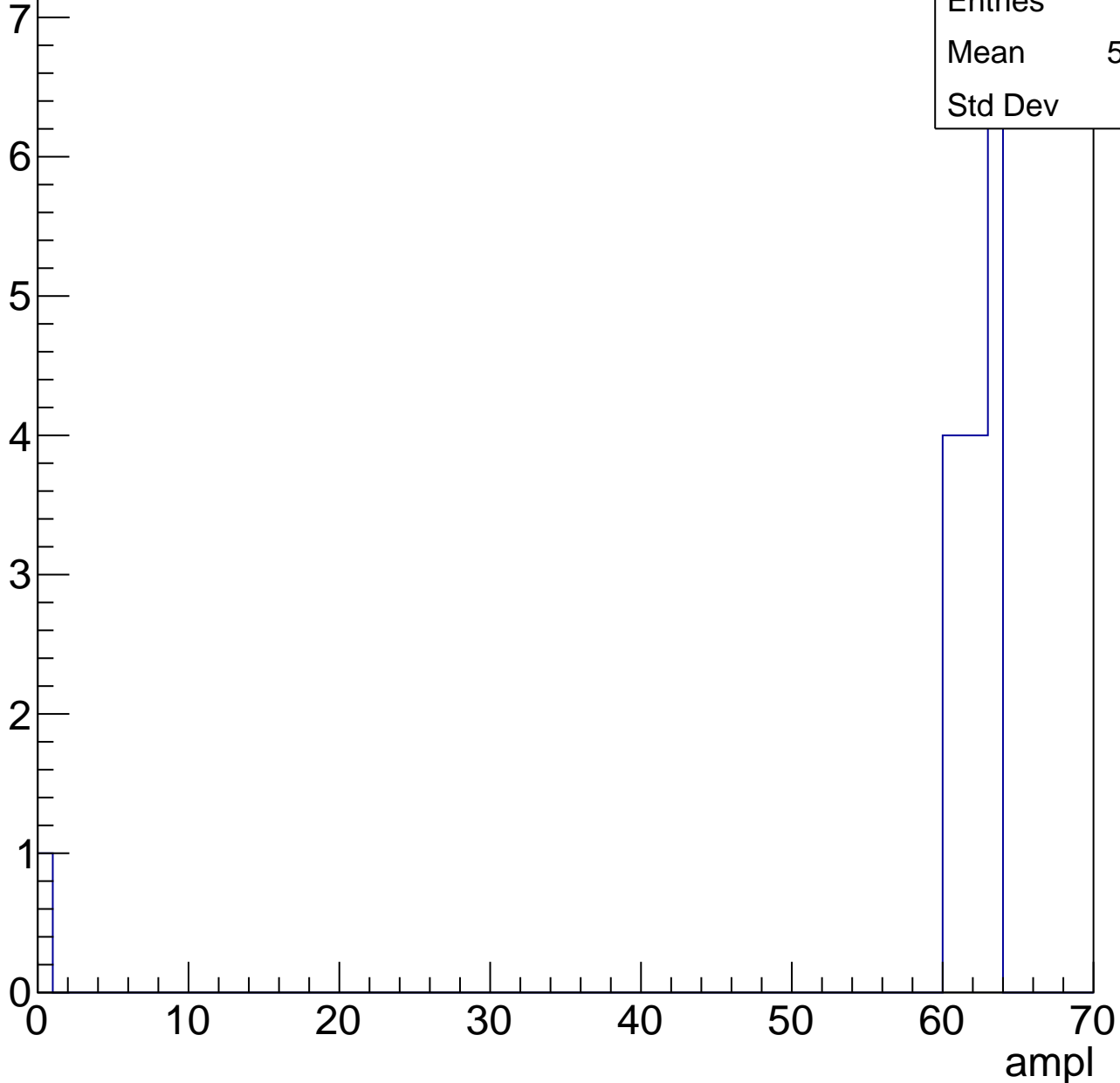


B1L103S, U1-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.65
Std Dev	13.5



B1L103S, U1-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



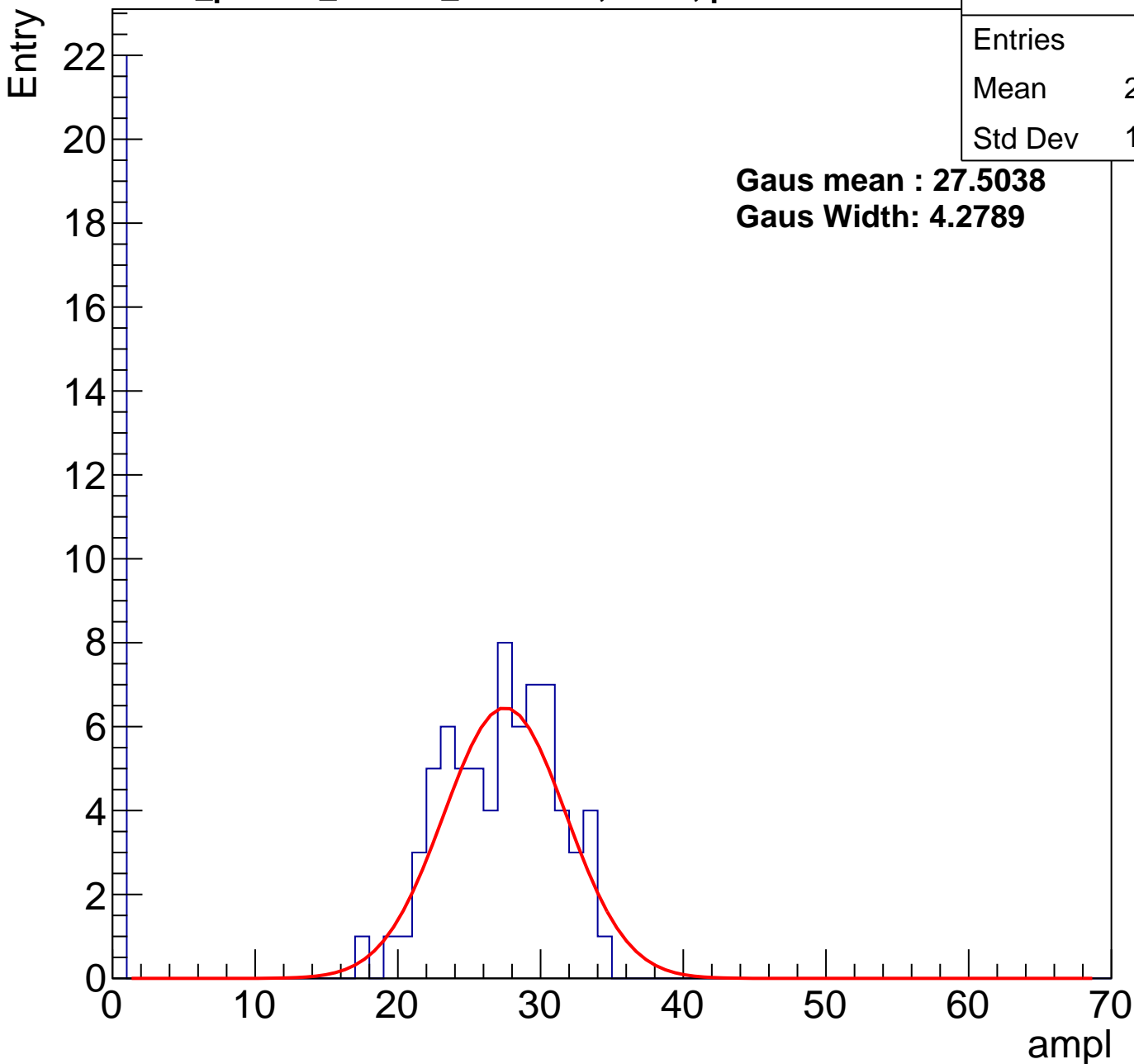
B1L103S, U1-ch54, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	20.42
Std Dev	11.85

Gaus mean : 27.5038

Gaus Width: 4.2789



B1L103S, U1-ch54, adc1

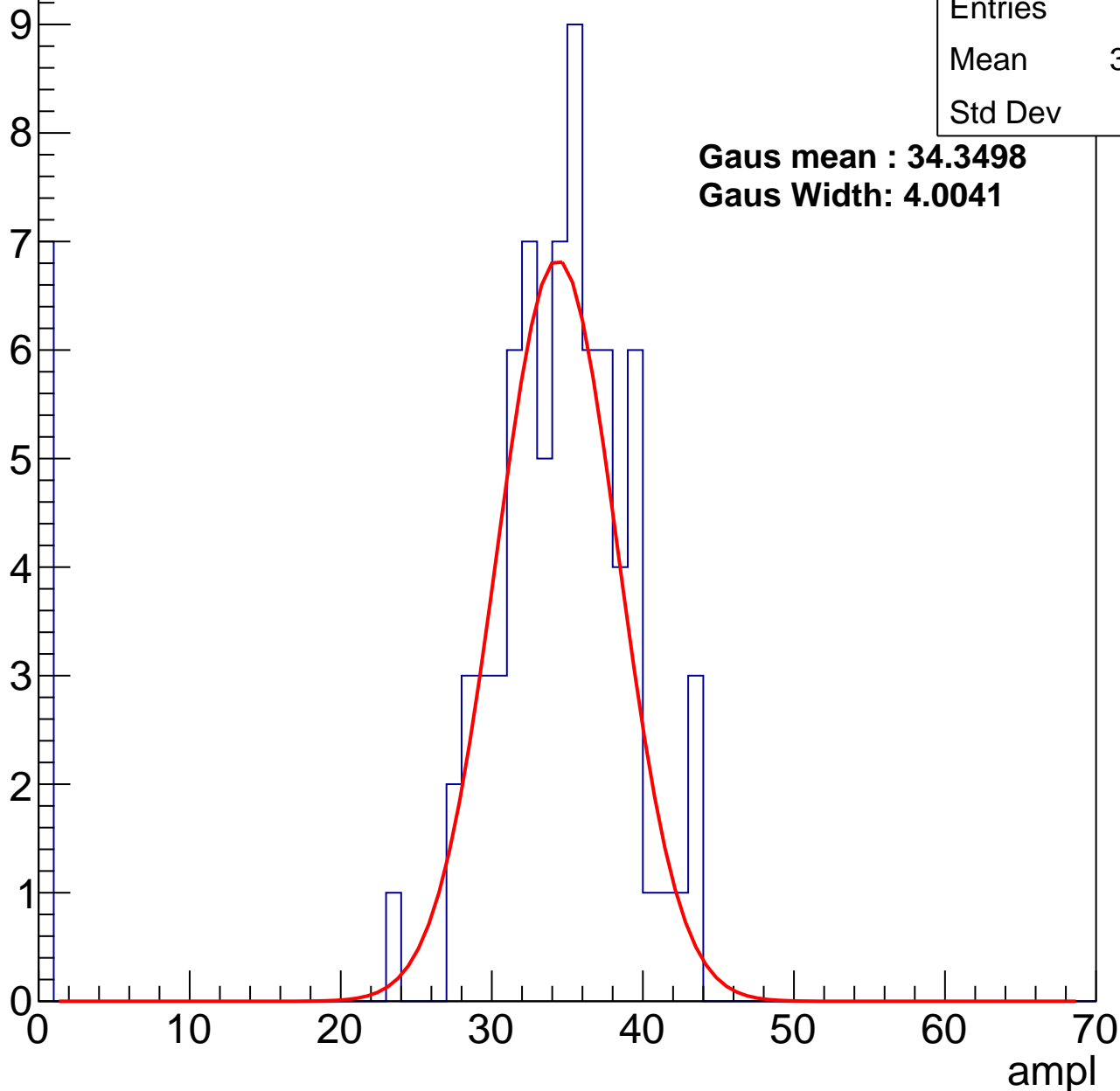
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	31.38
Std Dev	10.4

Gaus mean : 34.3498

Gaus Width: 4.0041



B1L103S, U1-ch54, adc2

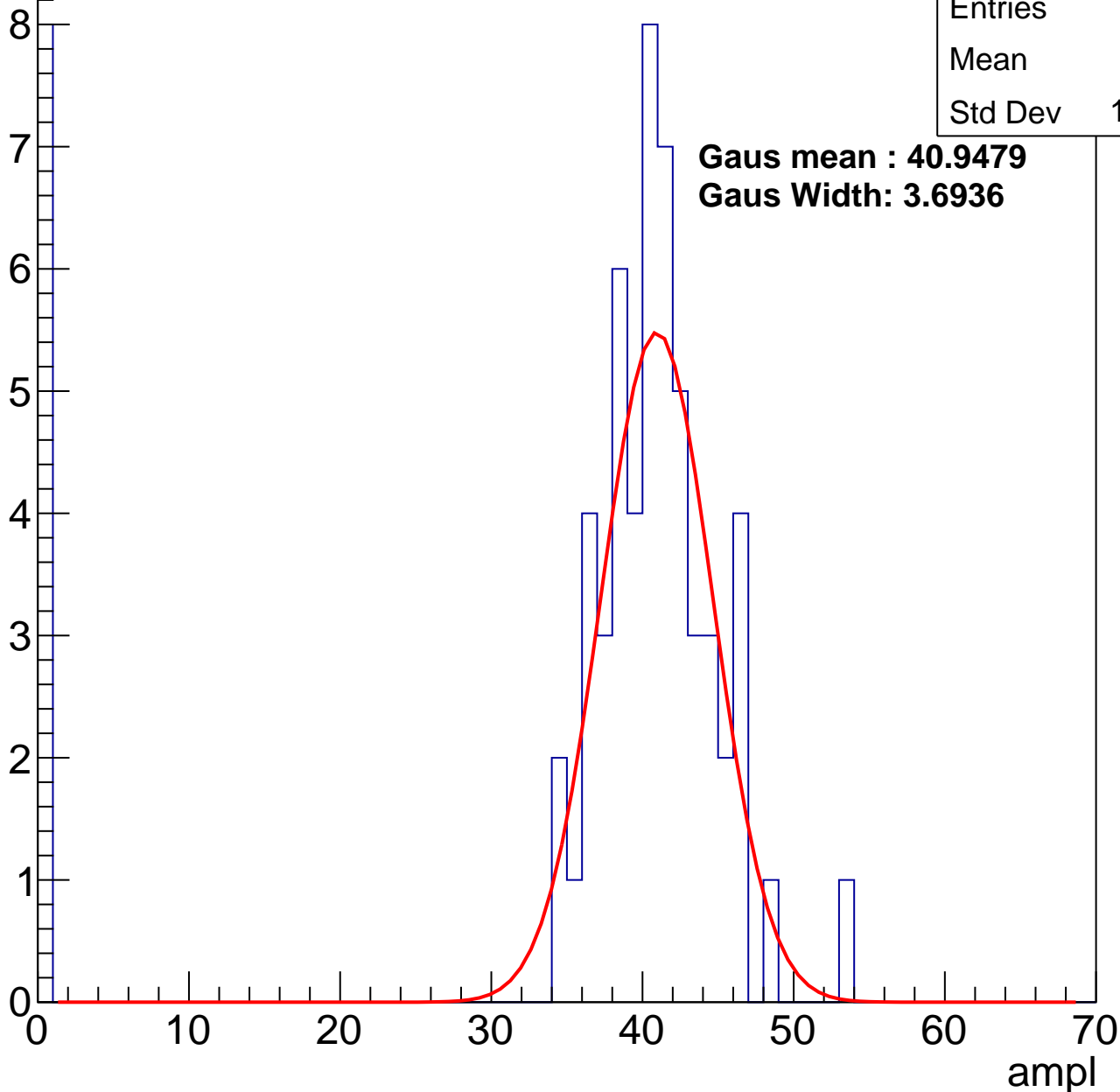
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	35.4
Std Dev	14.05

Gaus mean : 40.9479

Gaus Width: 3.6936

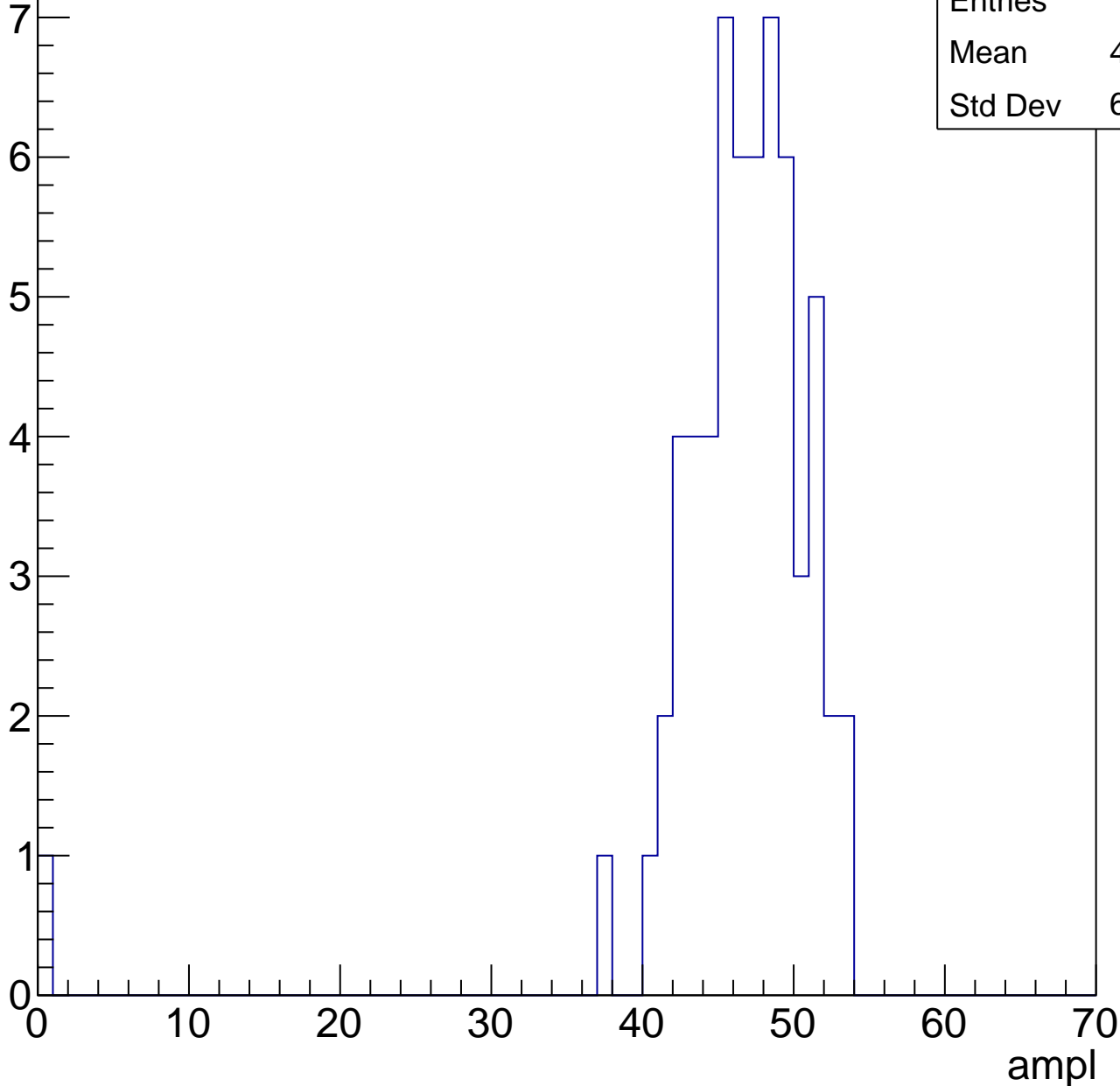


B1L103S, U1-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	45.79
Std Dev	6.816

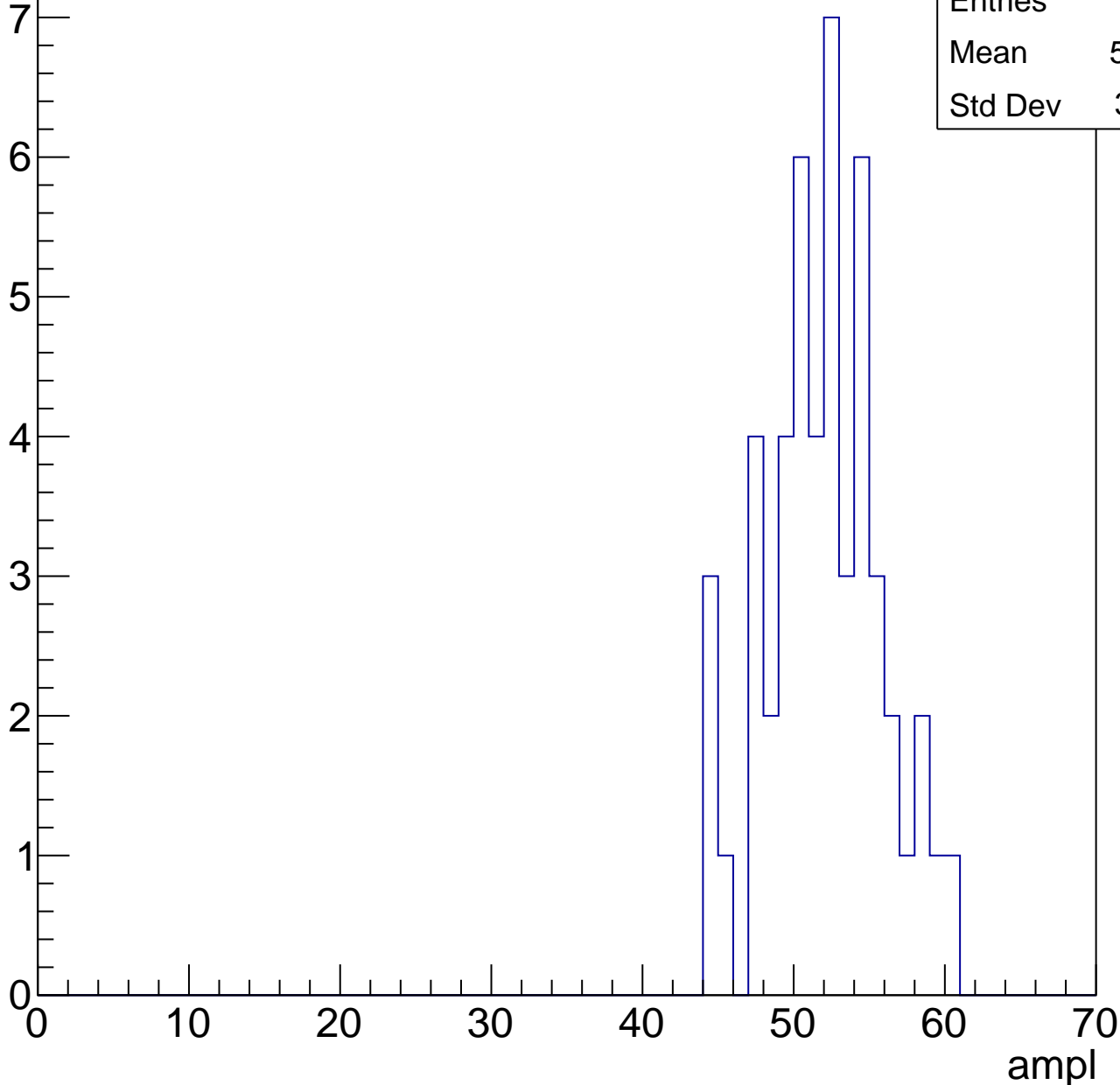


B1L103S, U1-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

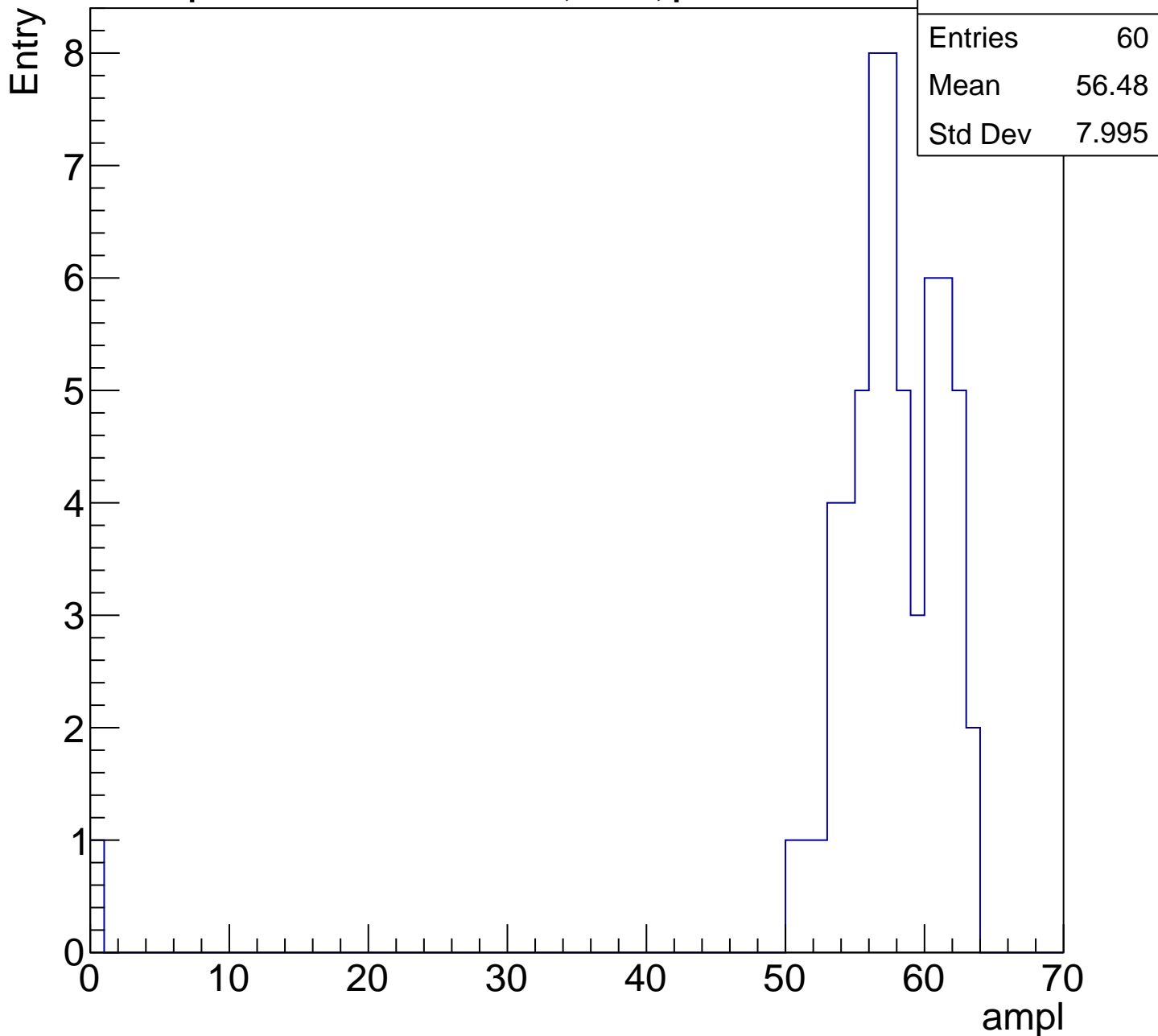
Entry

Entries	50
Mean	51.54
Std Dev	3.801



B1L103S, U1-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

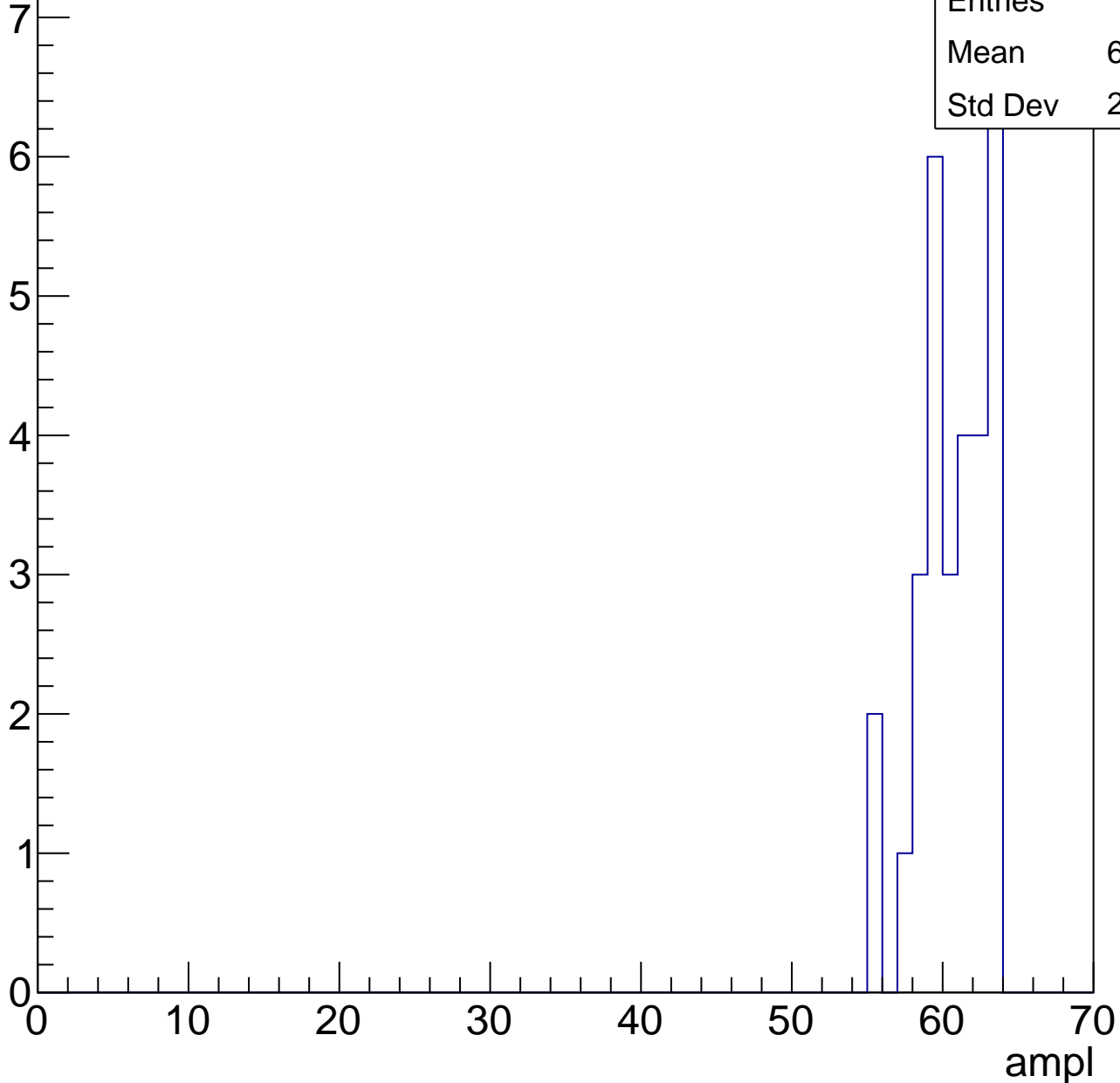


B1L103S, U1-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	60.27
Std Dev	2.294



B1L103S, U1-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U1-ch55, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	18.24
Std Dev	12.63

Gaus mean : 27.1373

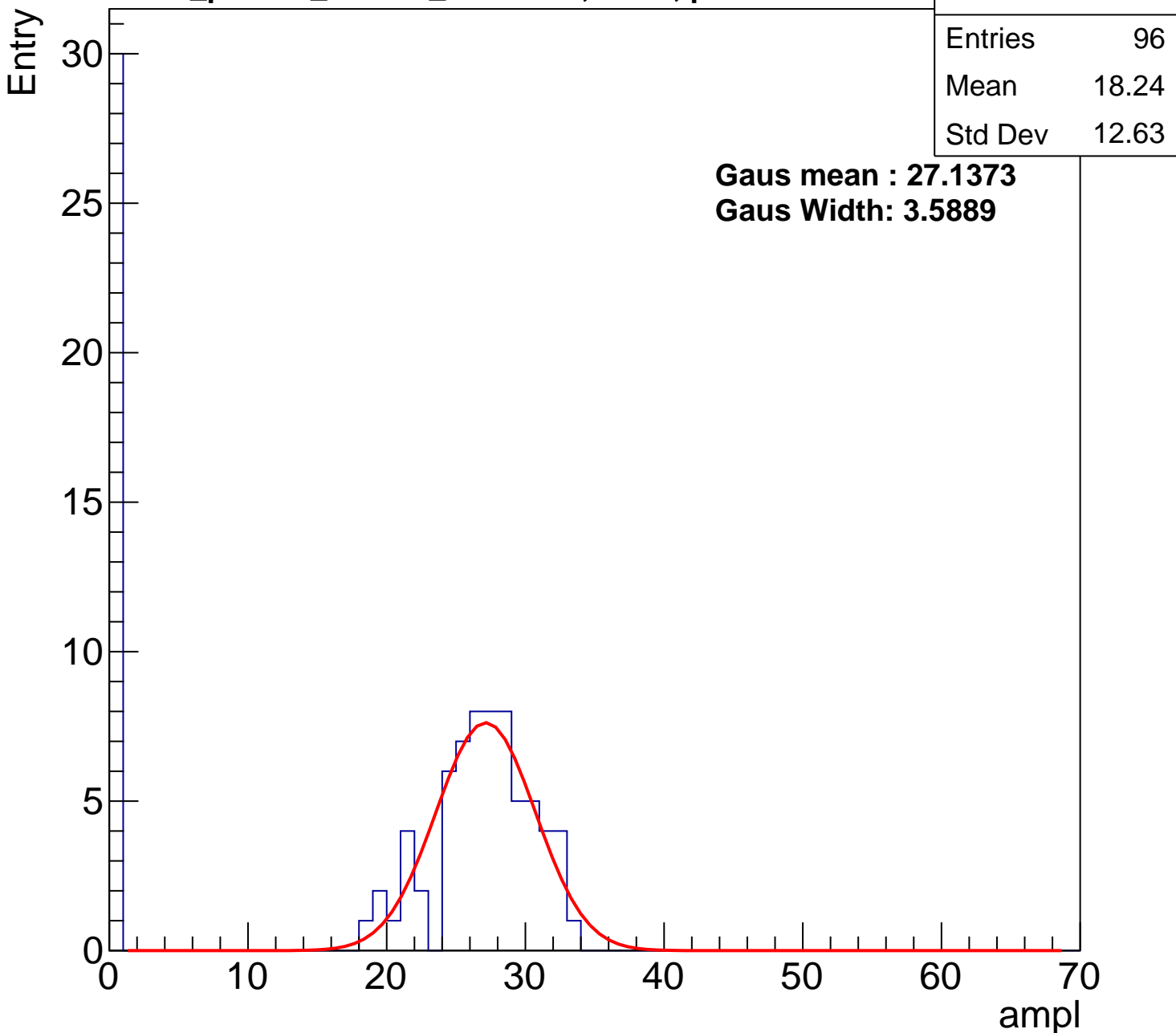
Gaus Width: 3.5889

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch55, adc1

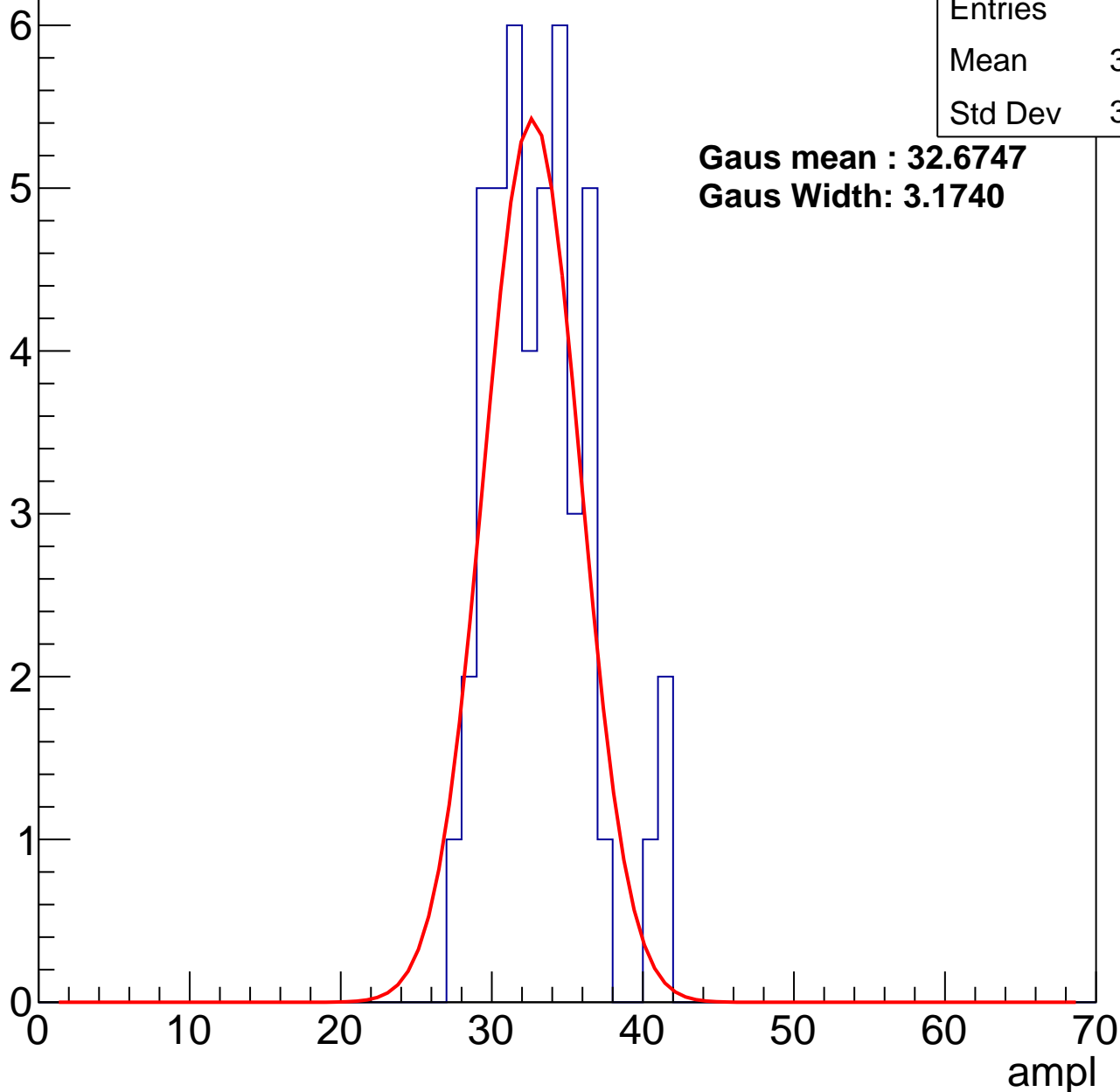
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	32.72
Std Dev	3.268

Gaus mean : 32.6747

Gaus Width: 3.1740



B1L103S, U1-ch55, adc2

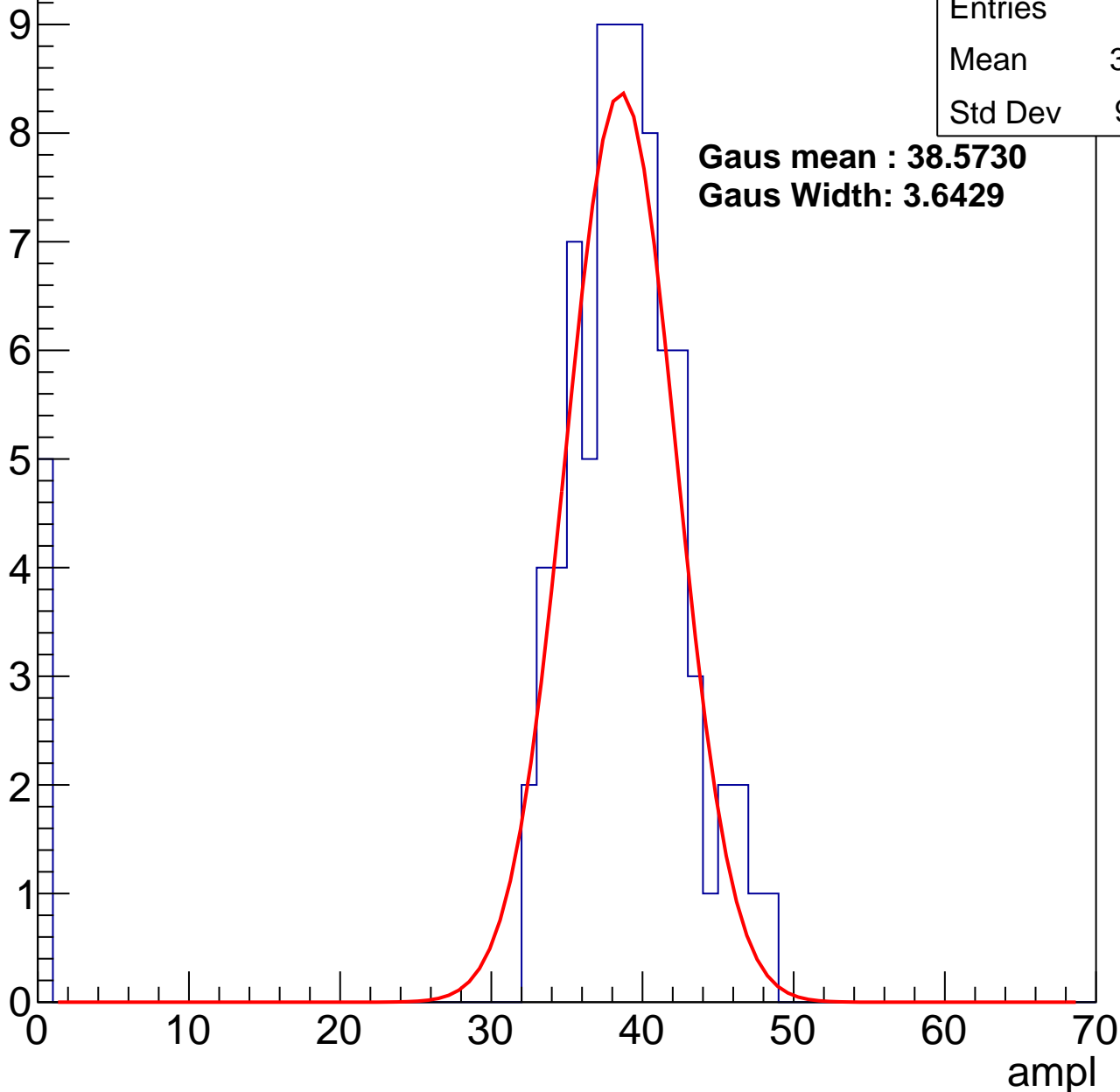
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	36.32
Std Dev	9.771

Gaus mean : 38.5730

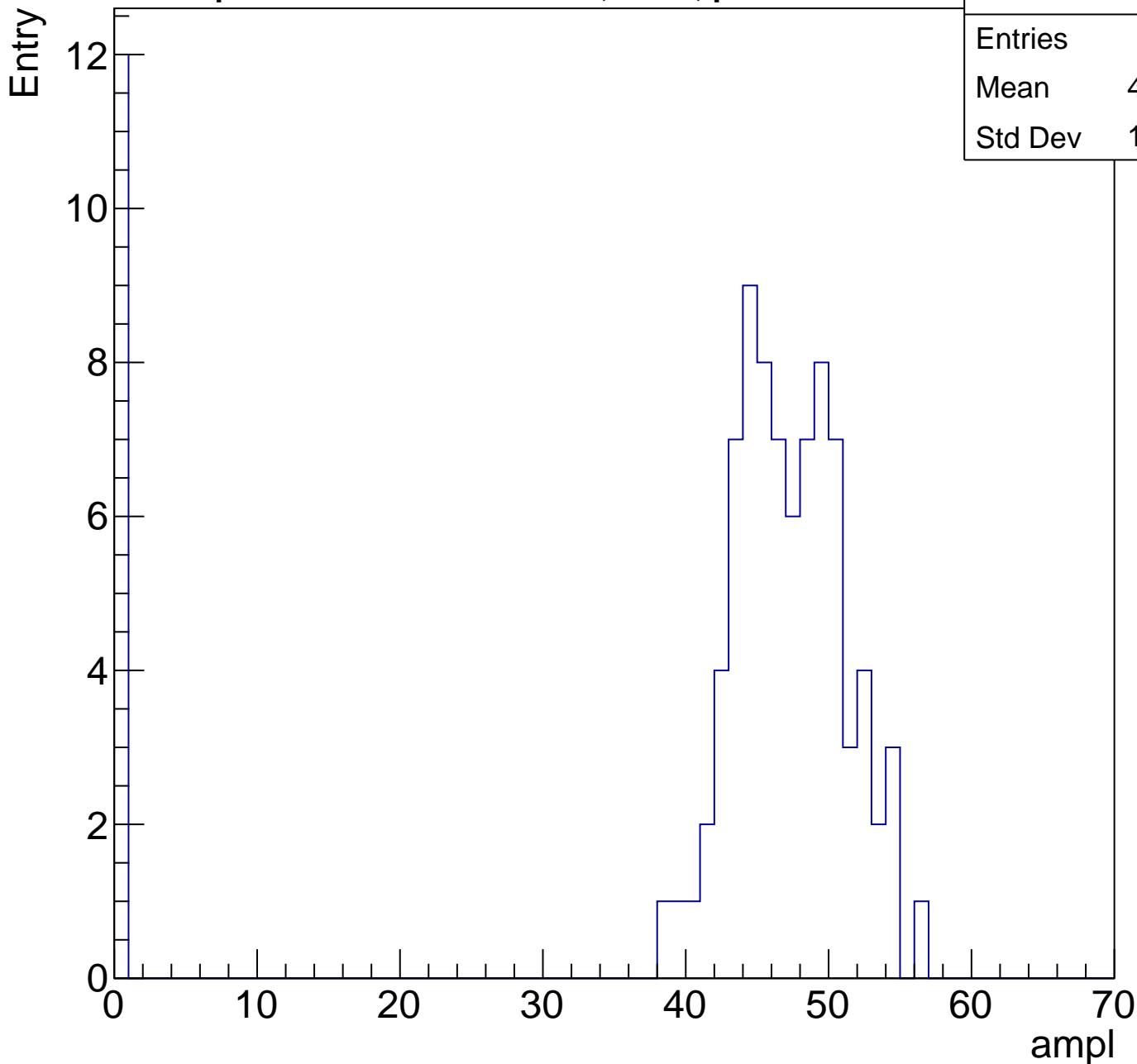
Gaus Width: 3.6429



B1L103S, U1-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	40.76
Std Dev	16.08

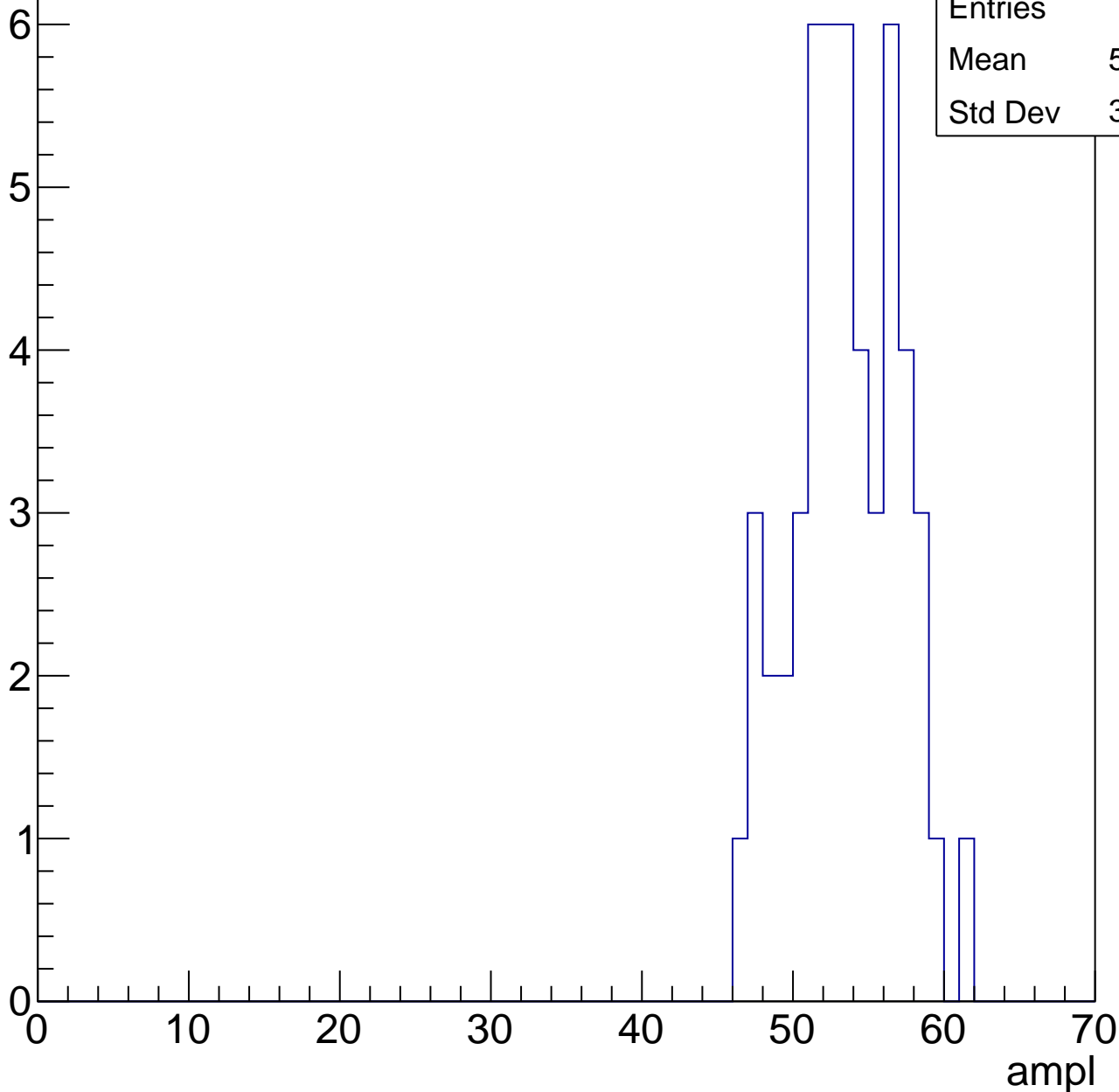


B1L103S, U1-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

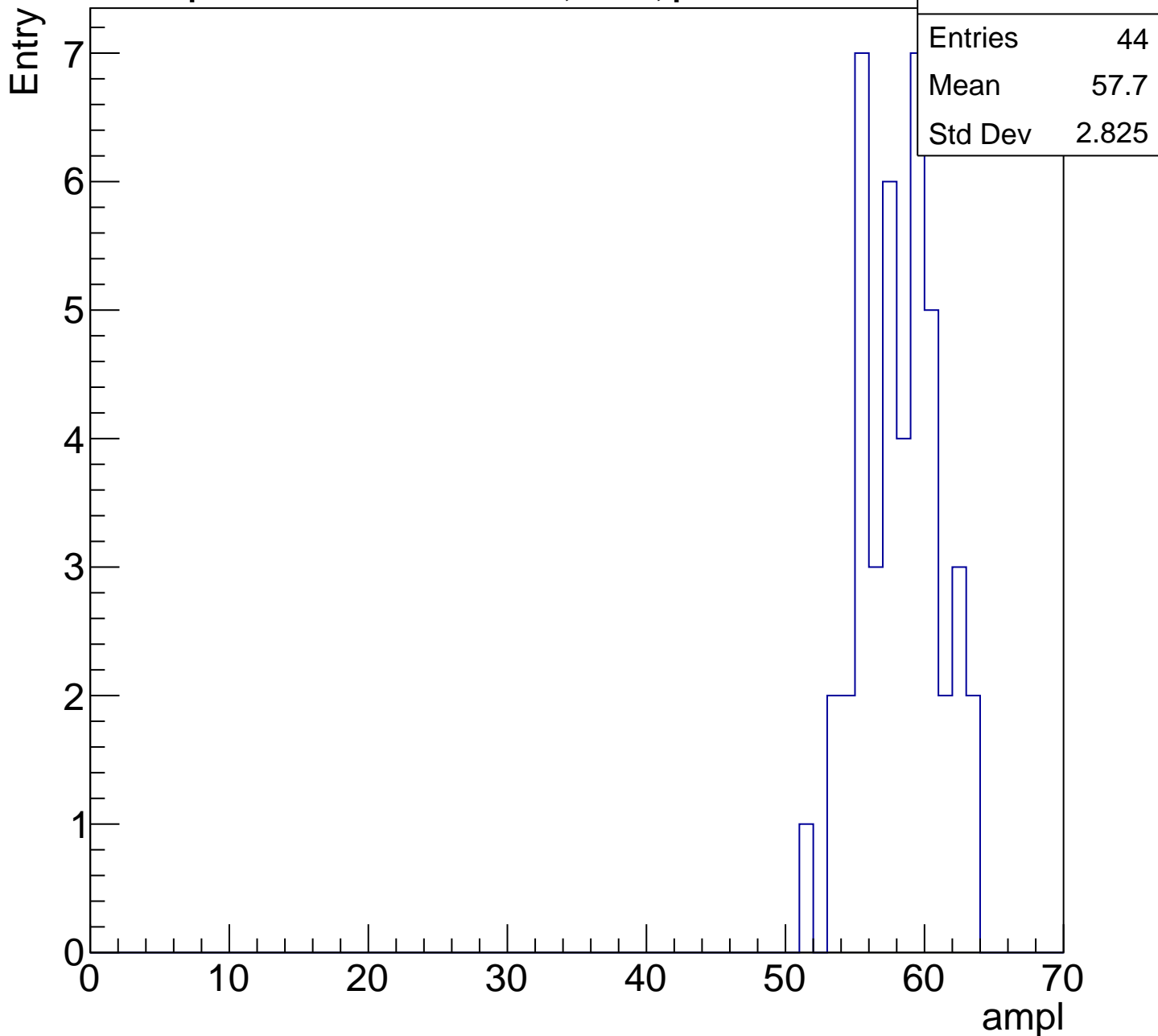
Entry

Entries	51
Mean	53.06
Std Dev	3.444



B1L103S, U1-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

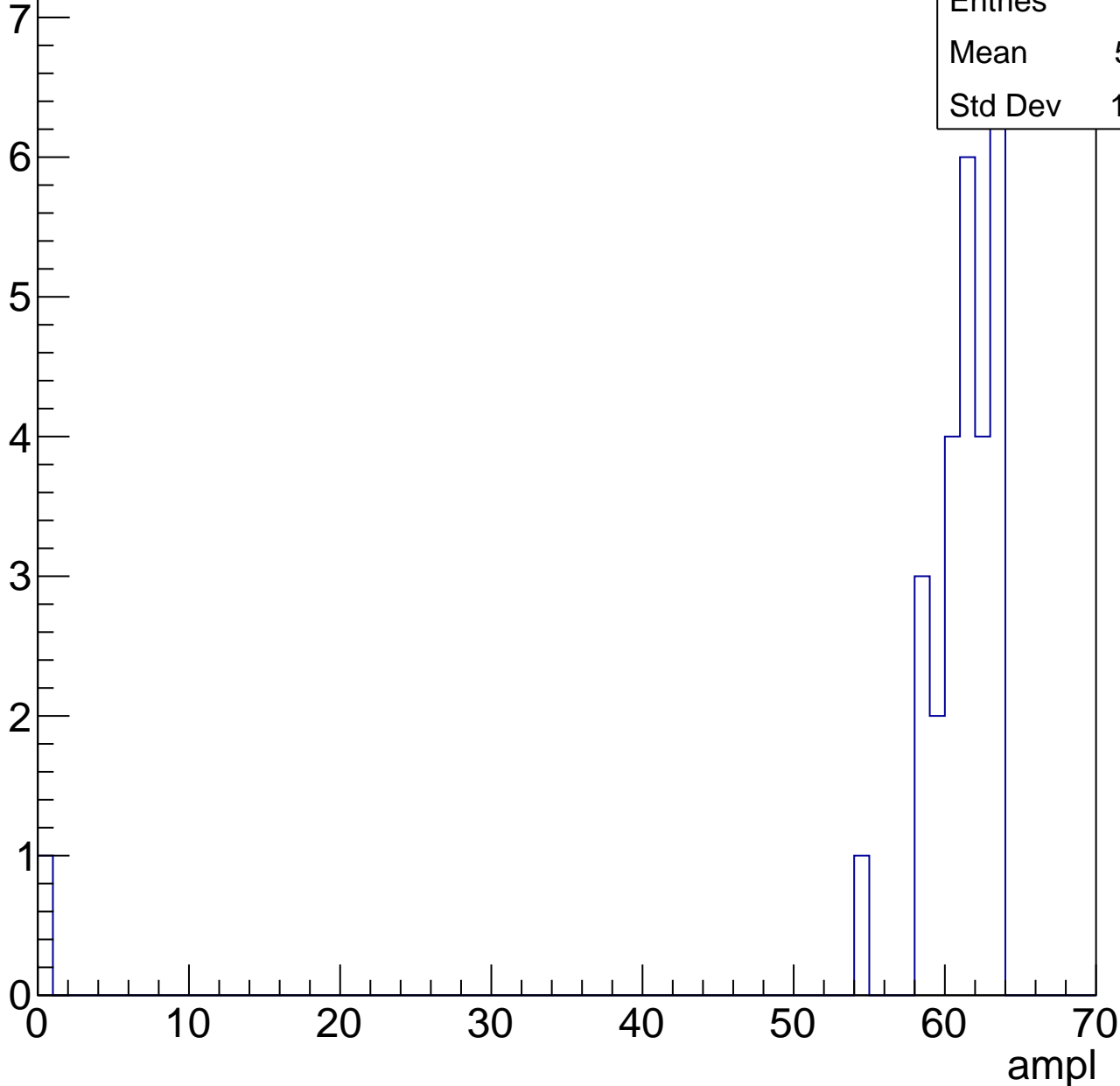


B1L103S, U1-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.61
Std Dev	11.47

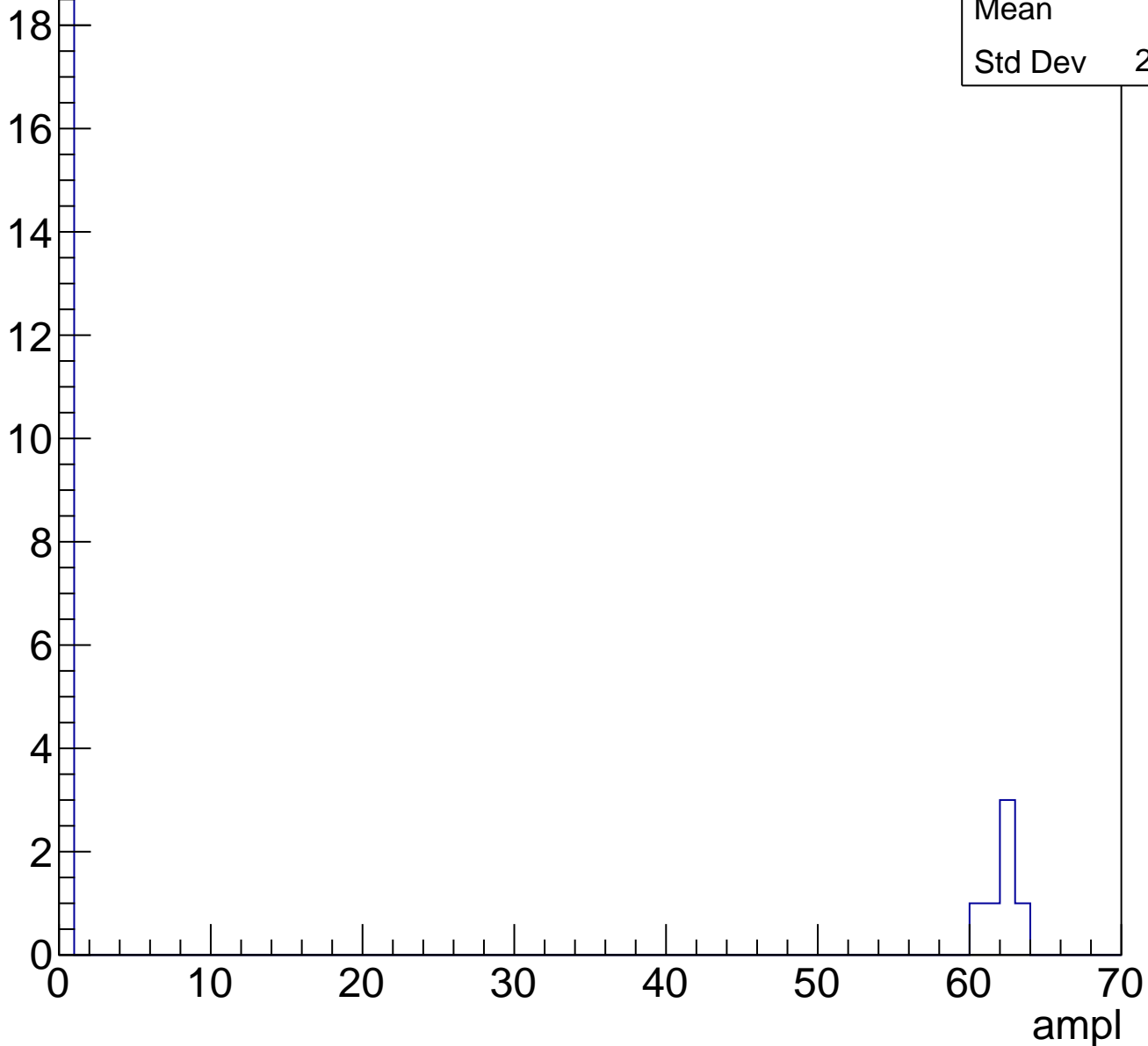


B1L103S, U1-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	25
Mean	14.8
Std Dev	26.34

Entry



B1L103S, U1-ch56, adc0

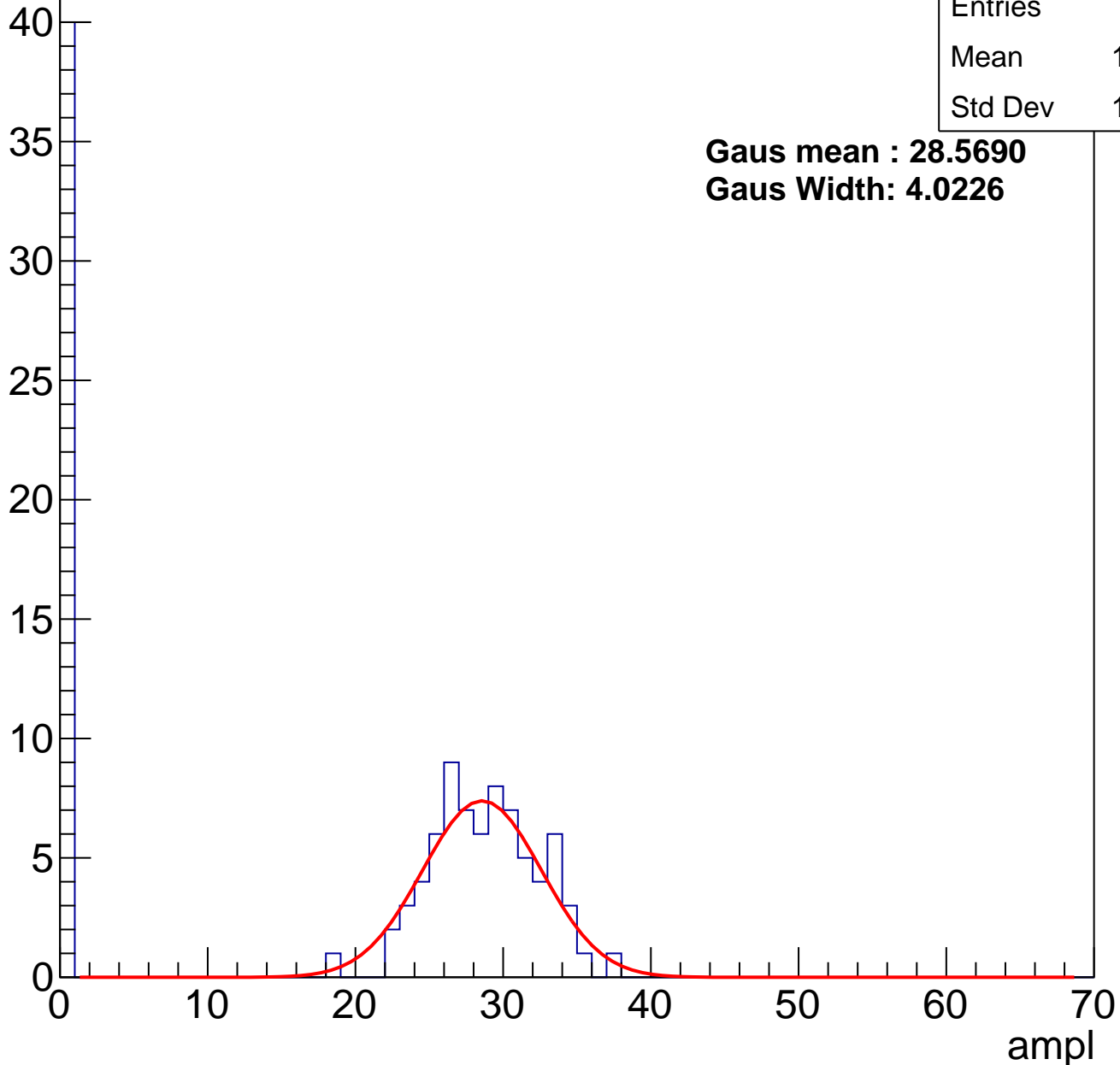
calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	18.27
Std Dev	13.83

Gaus mean : 28.5690

Gaus Width: 4.0226

Entry



B1L103S, U1-ch56, adc1

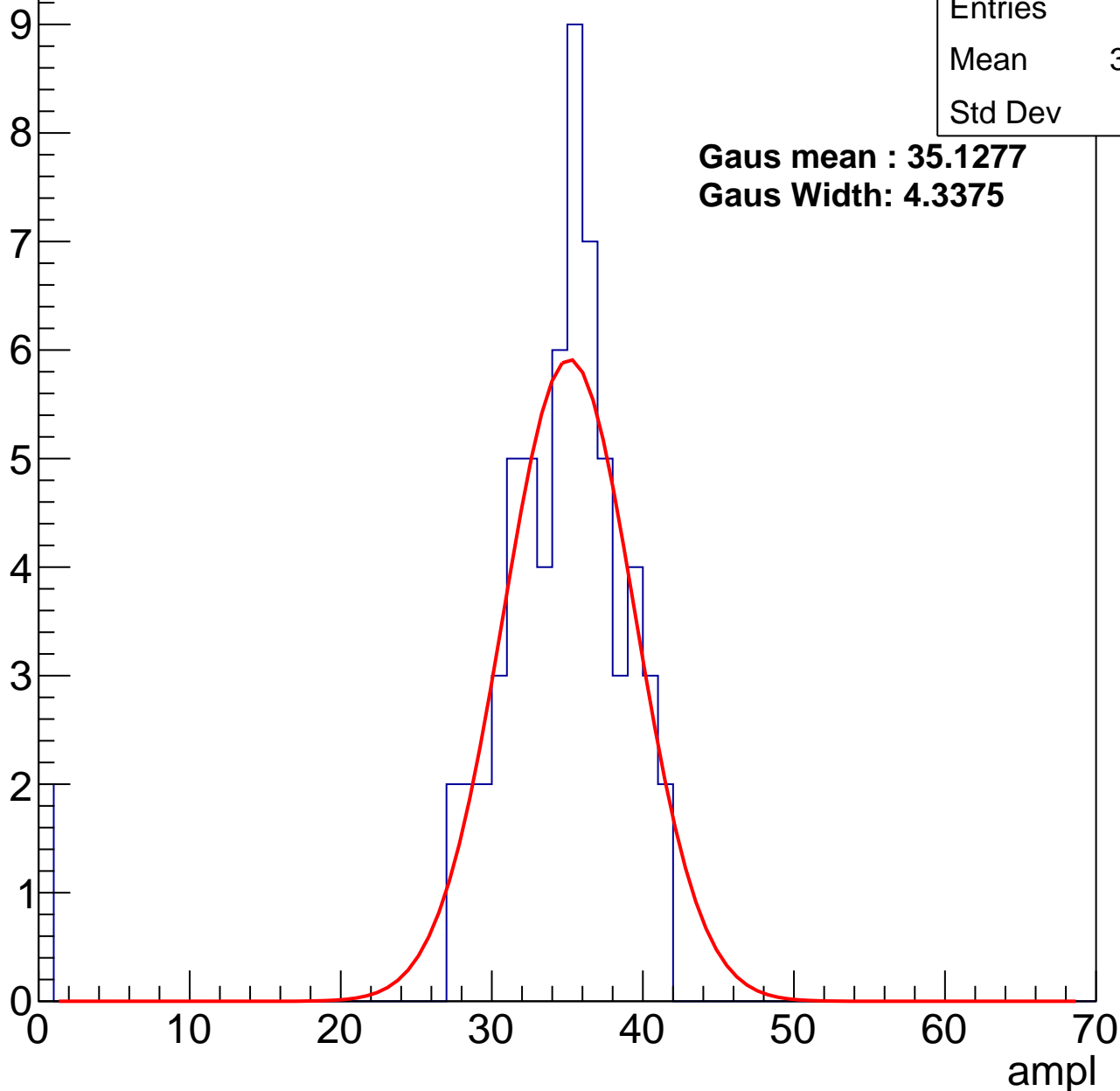
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.33
Std Dev	6.91

Gaus mean : 35.1277

Gaus Width: 4.3375



B1L103S, U1-ch56, adc2

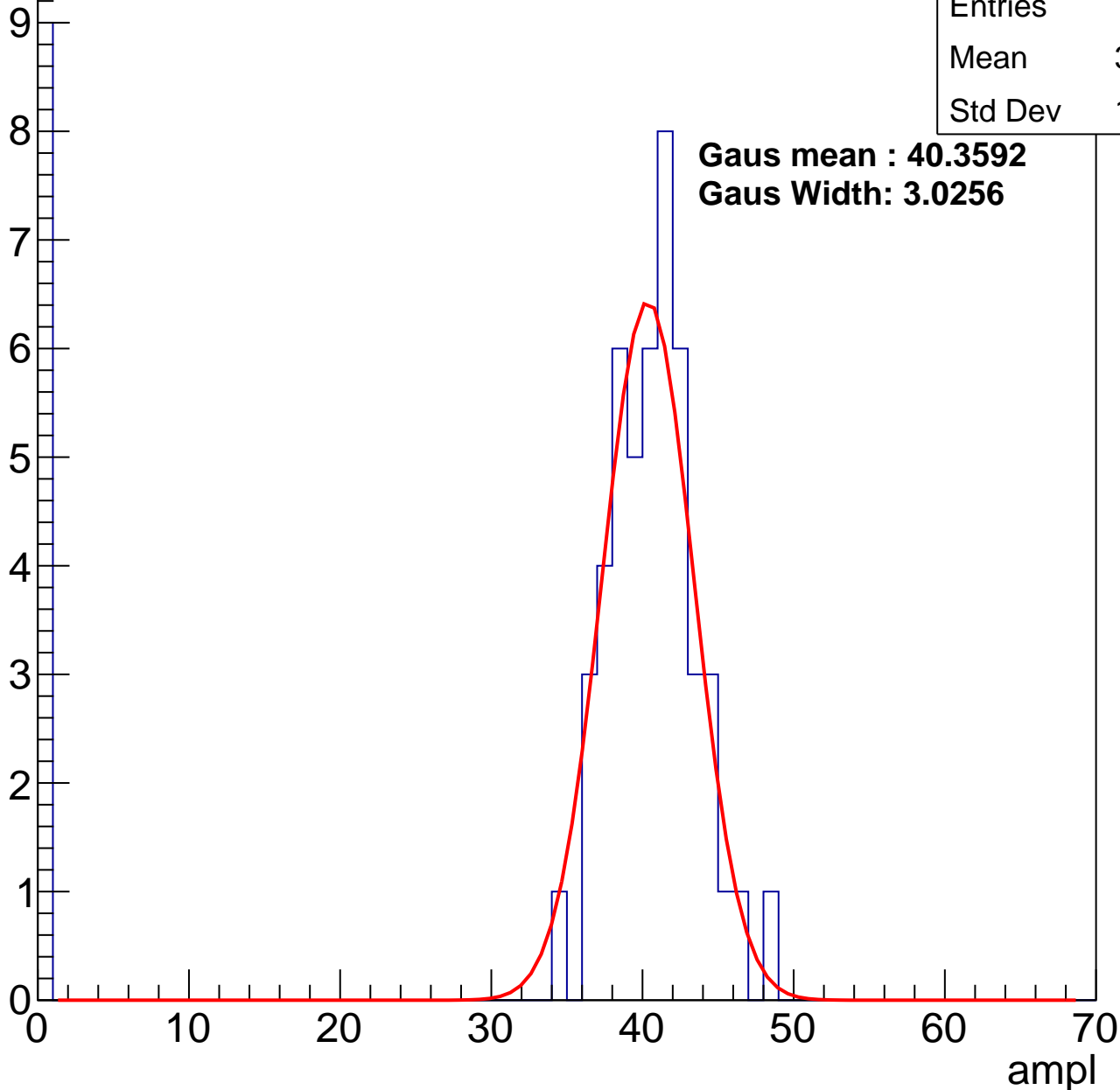
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	33.91
Std Dev	14.91

Gaus mean : 40.3592

Gaus Width: 3.0256

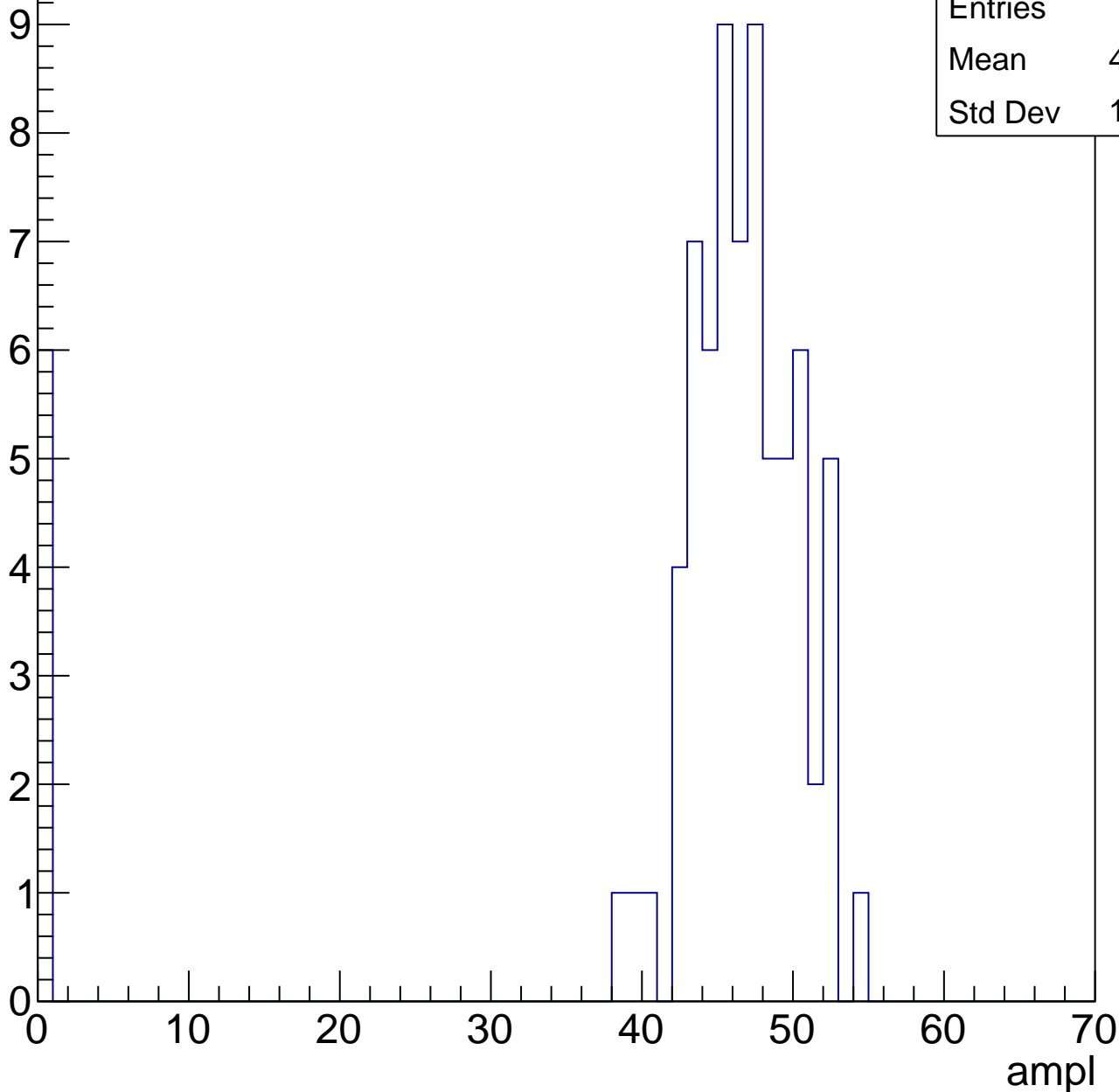


B1L103S, U1-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	42.68
Std Dev	12.98



B1L103S, U1-ch56, adc4

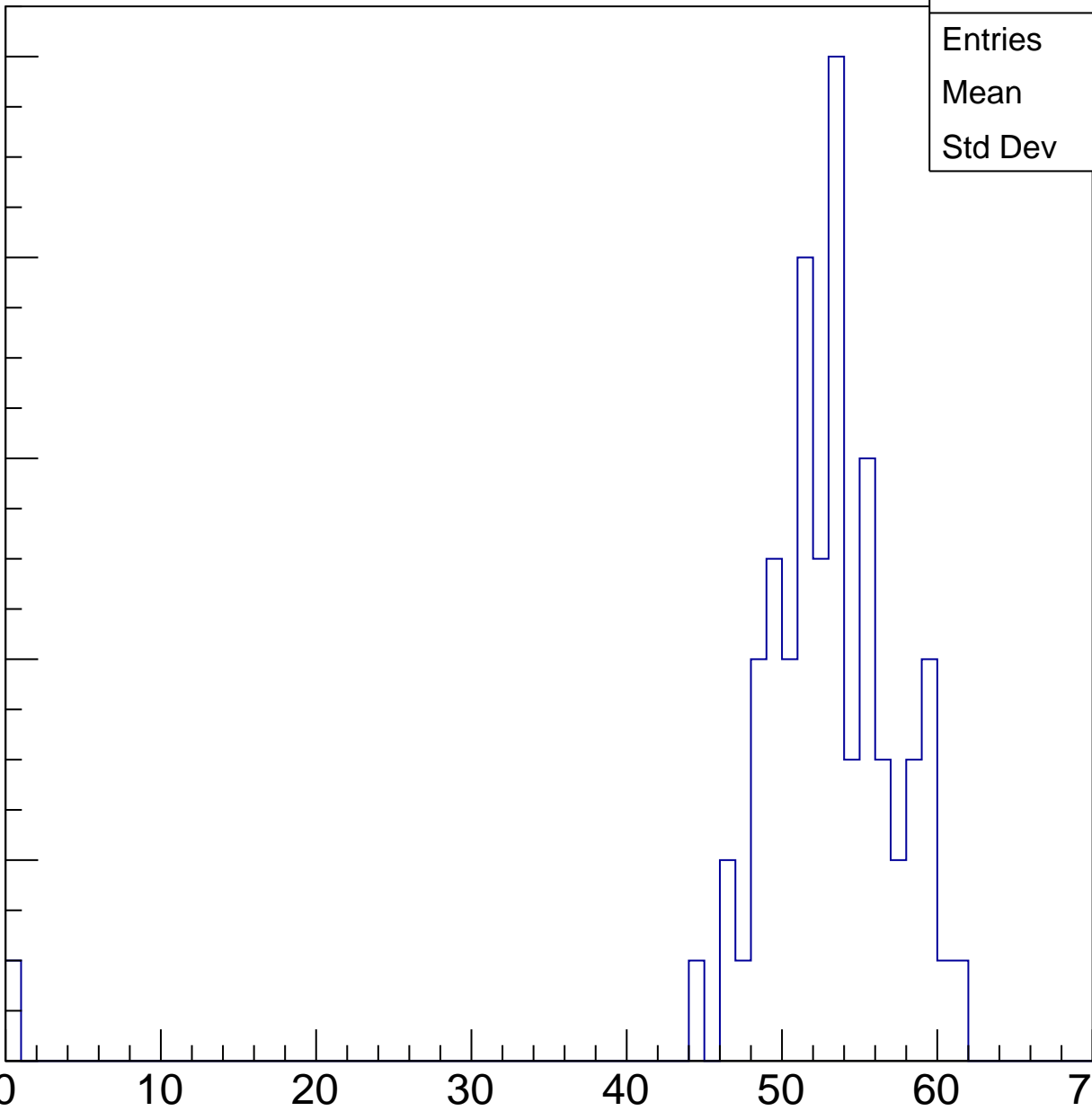
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	51.92
Std Dev	7.521

Entry

10
8
6
4
2
0

ampl

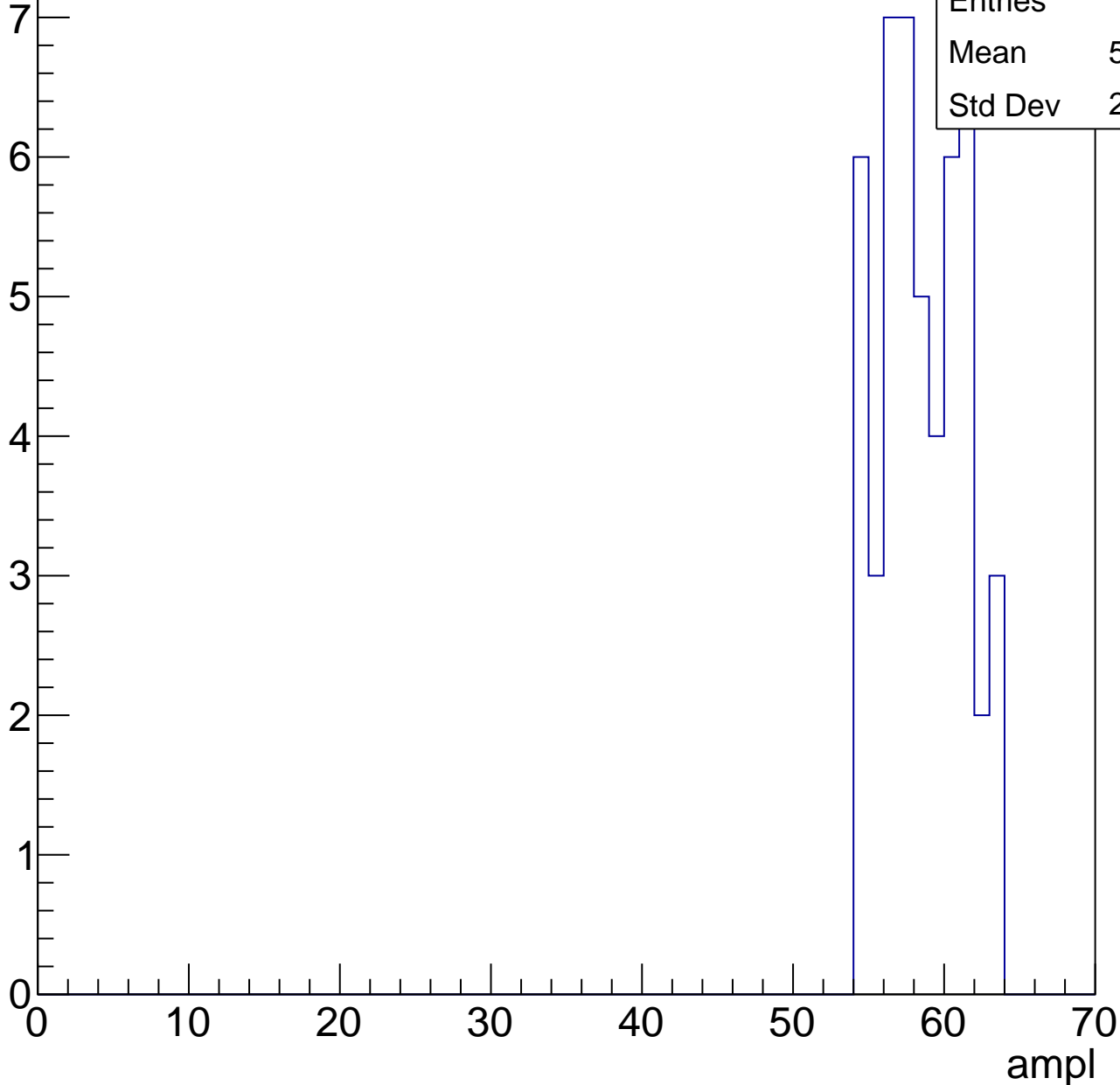


B1L103S, U1-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.12
Std Dev	2.666

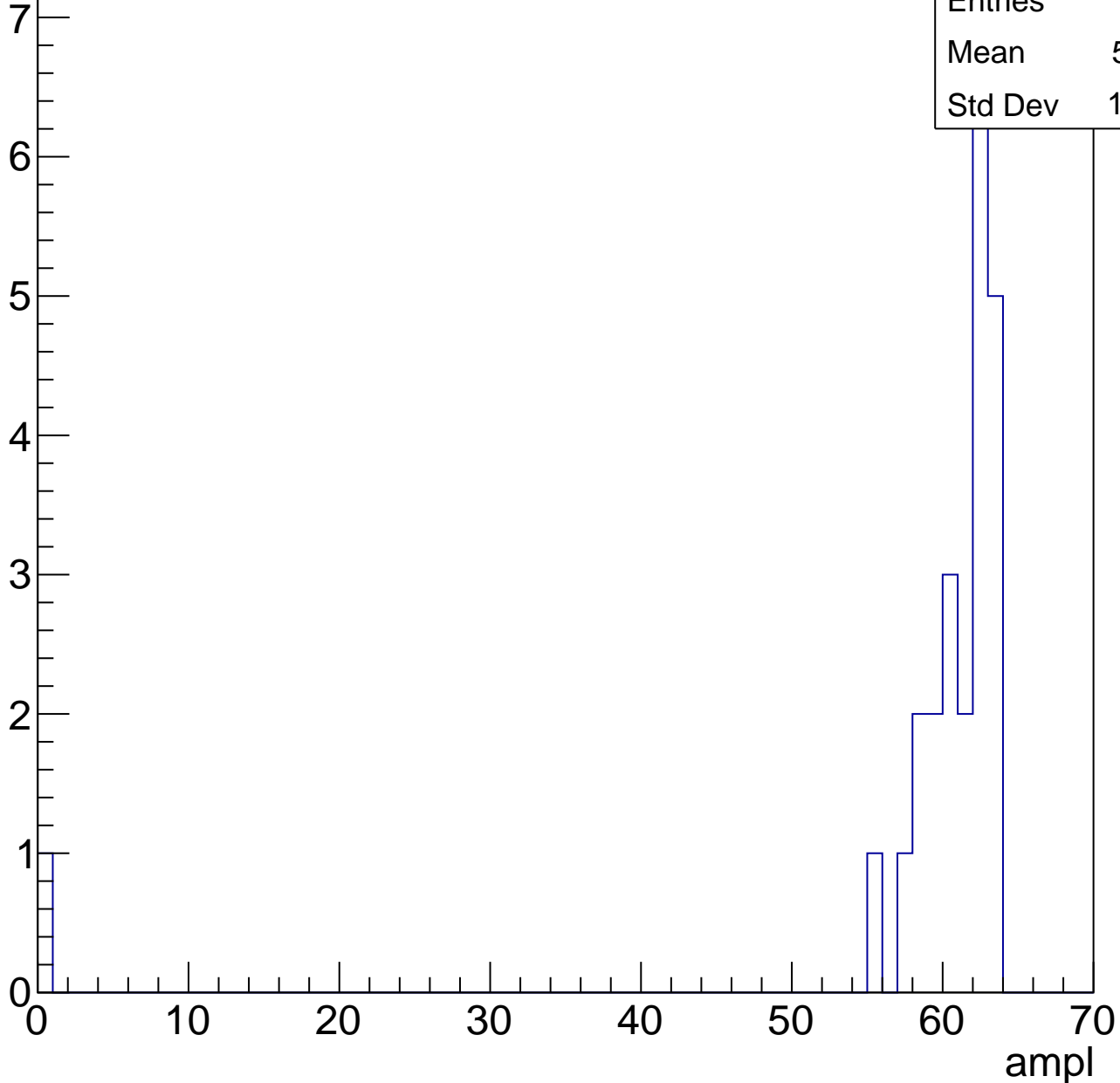


B1L103S, U1-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.21
Std Dev	12.32



B1L103S, U1-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



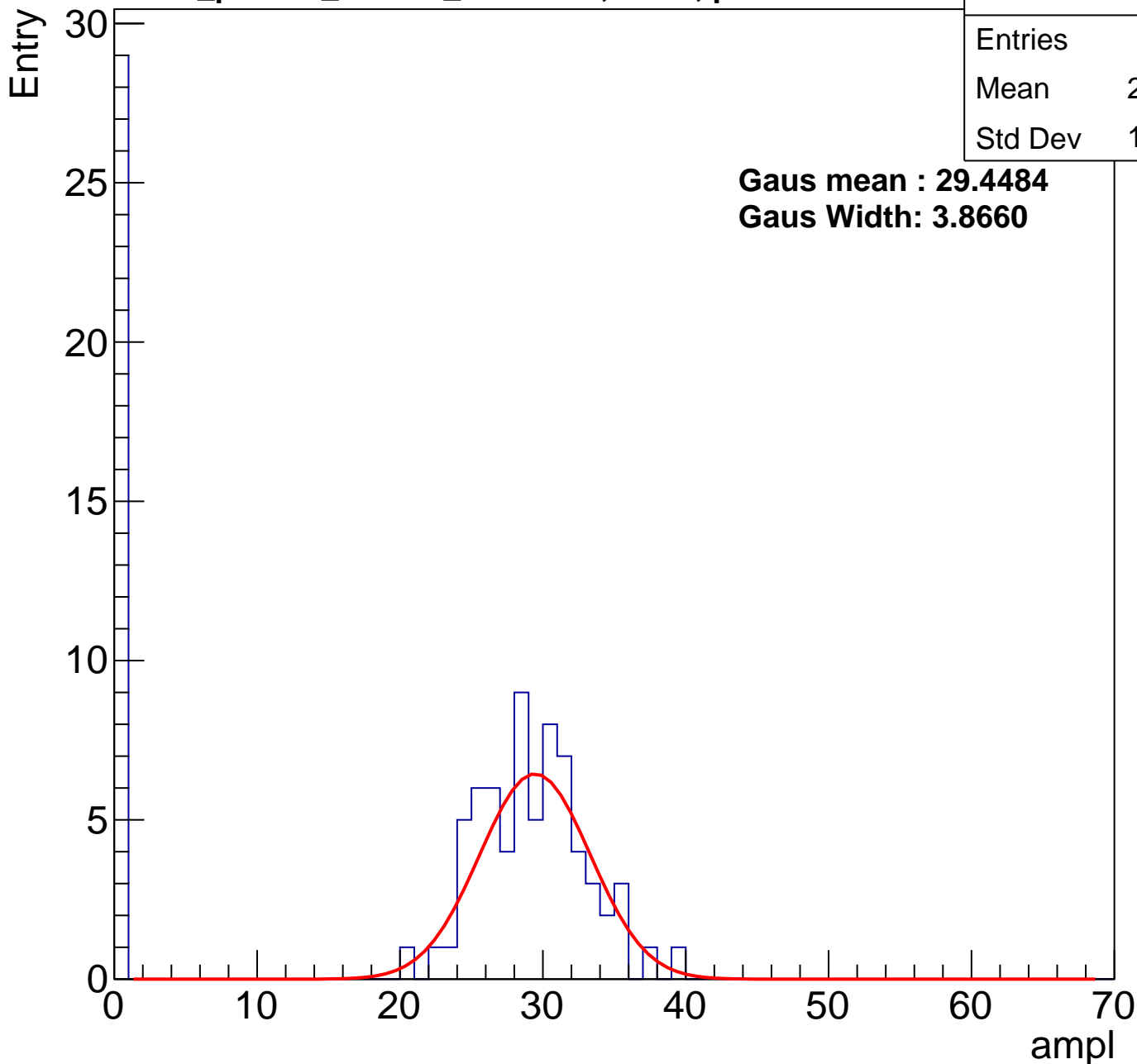
B1L103S, U1-ch57, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	20.09
Std Dev	13.57

Gaus mean : 29.4484

Gaus Width: 3.8660



B1L103S, U1-ch57, adc1

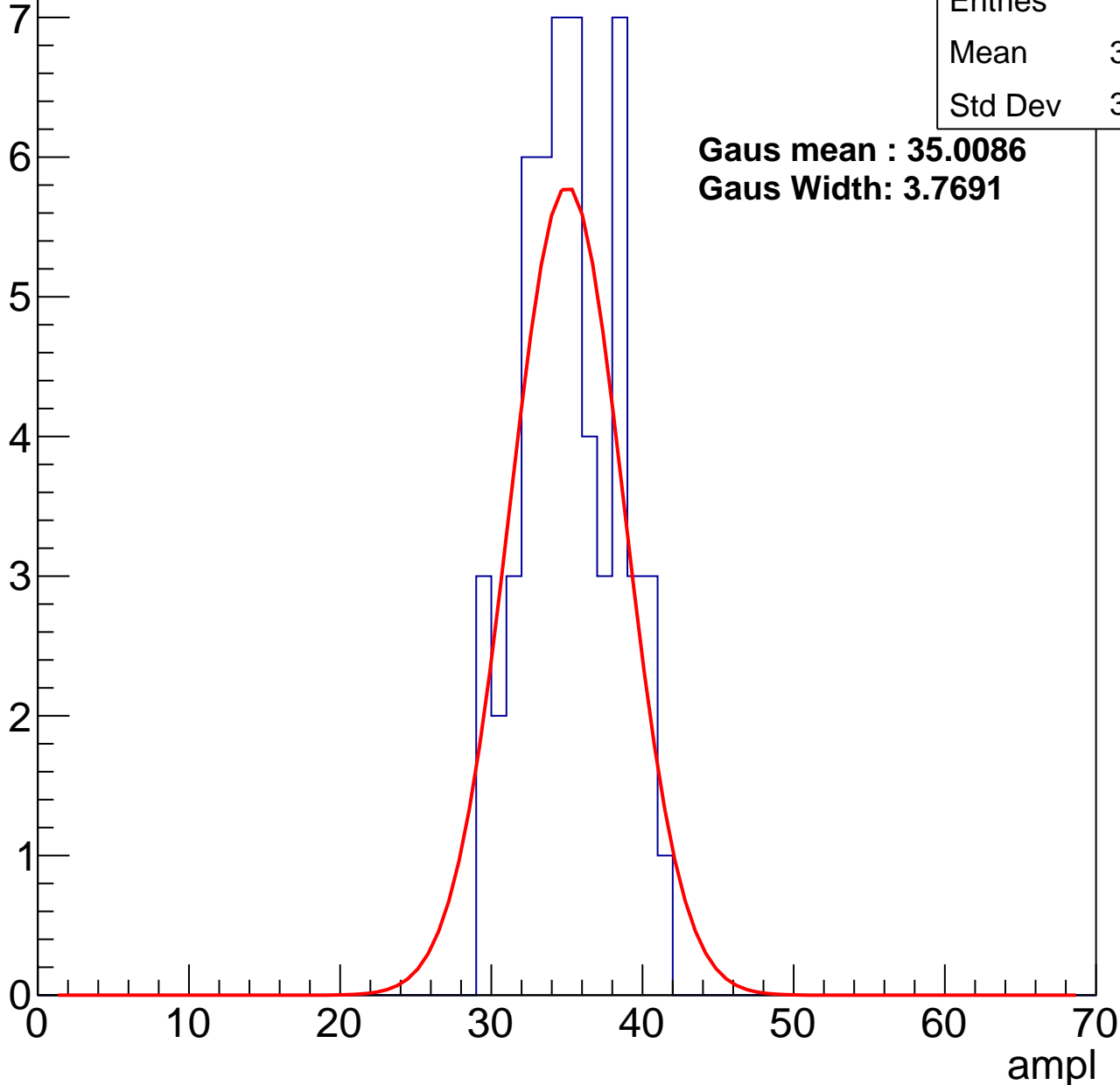
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	34.76
Std Dev	3.092

Gaus mean : 35.0086

Gaus Width: 3.7691



B1L103S, U1-ch57, adc2

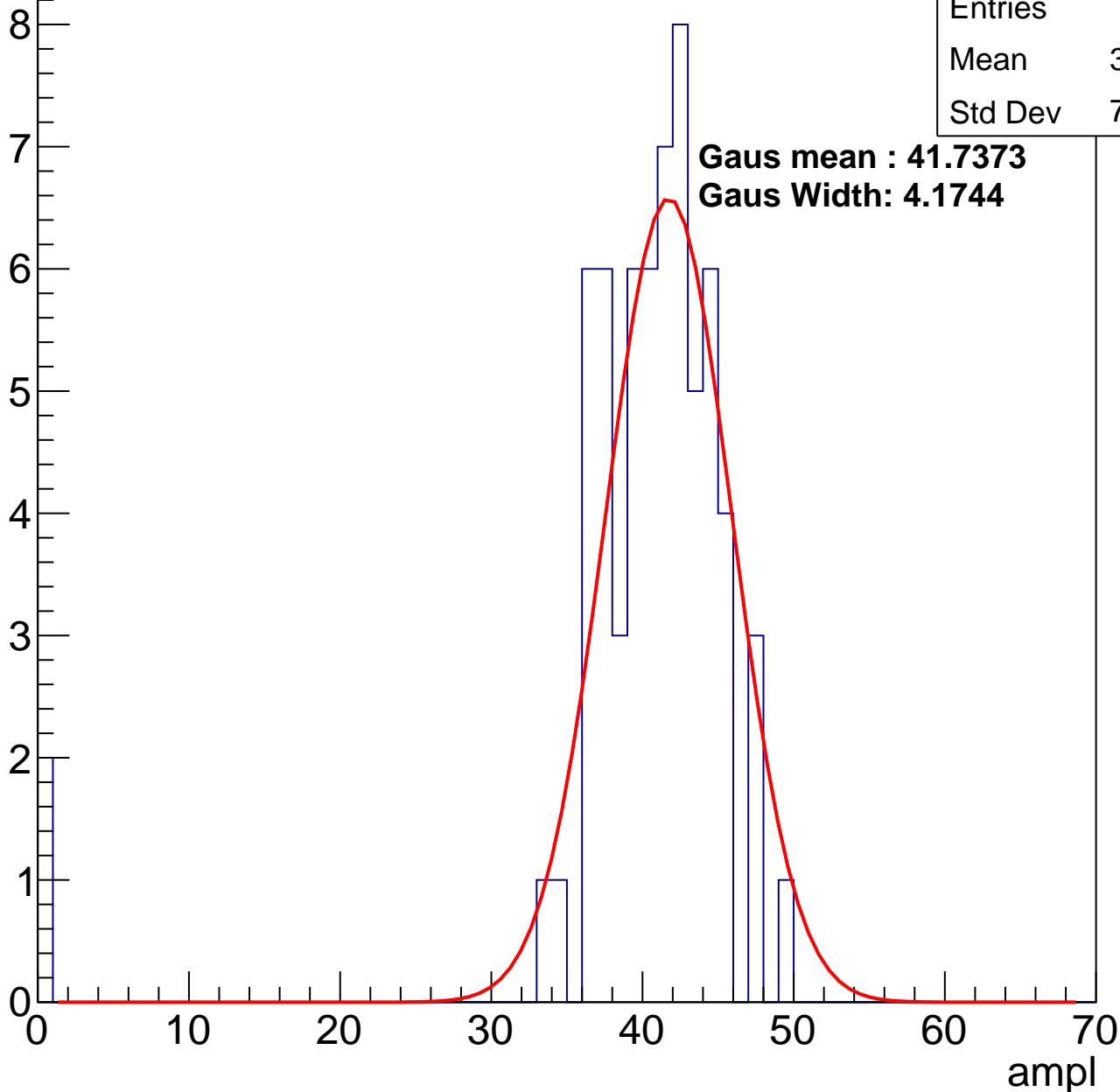
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.46
Std Dev	7.788

Gaus mean : 41.7373

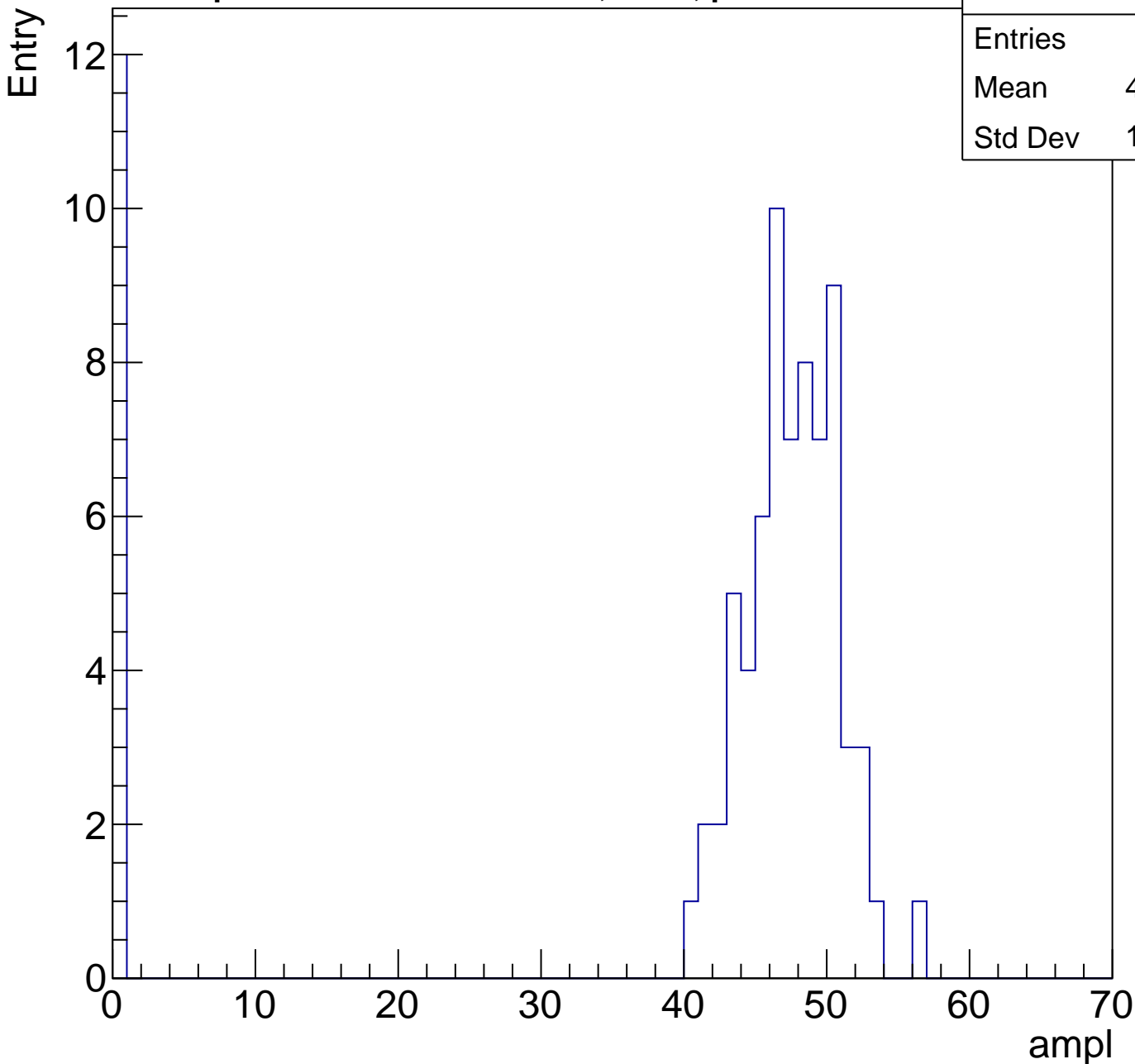
Gaus Width: 4.1744



B1L103S, U1-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	40.14
Std Dev	16.99



B1L103S, U1-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	50.83
Std Dev	13.21

Entry

10

8

6

4

2

0

0

10

20

30

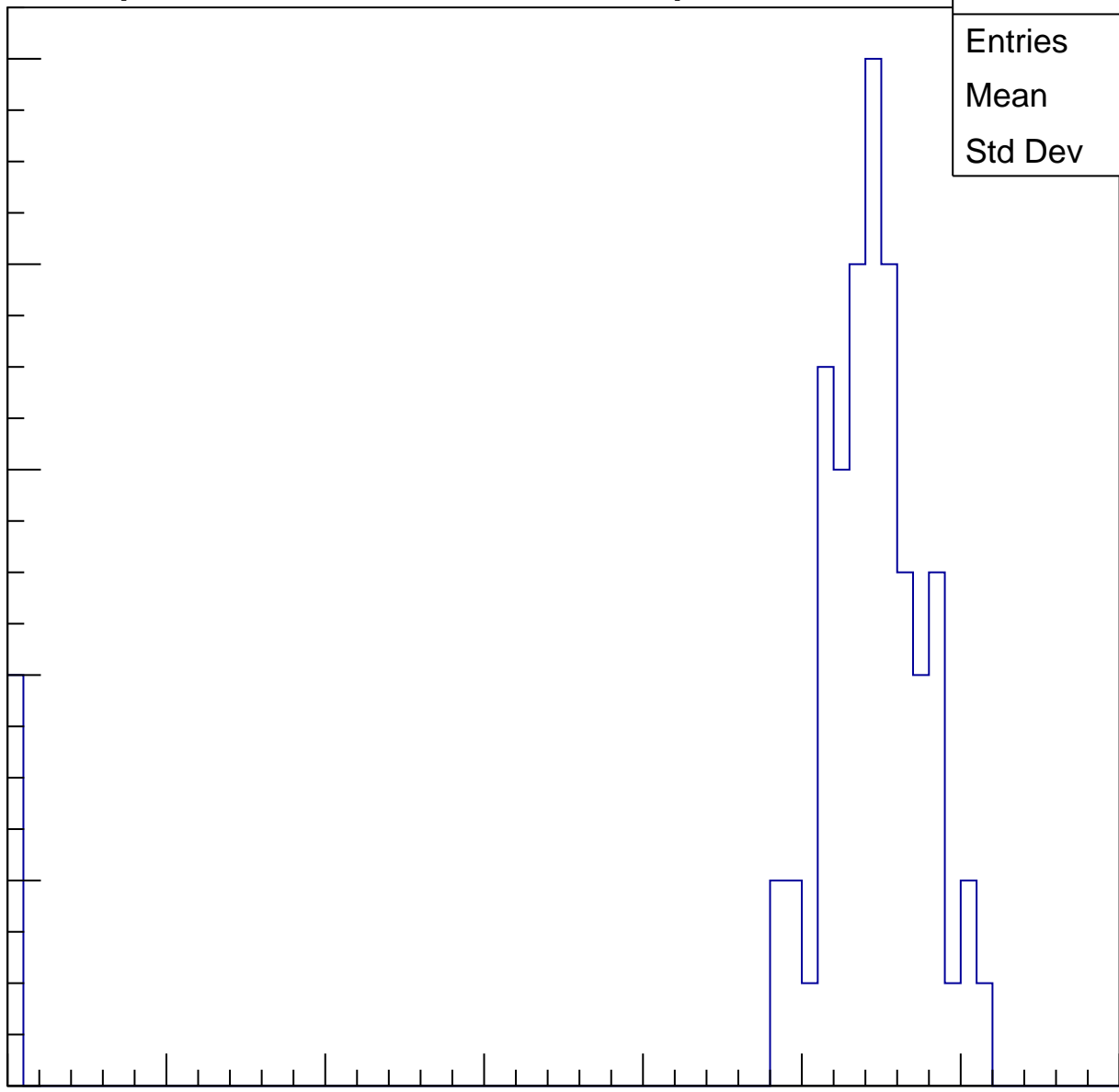
40

50

60

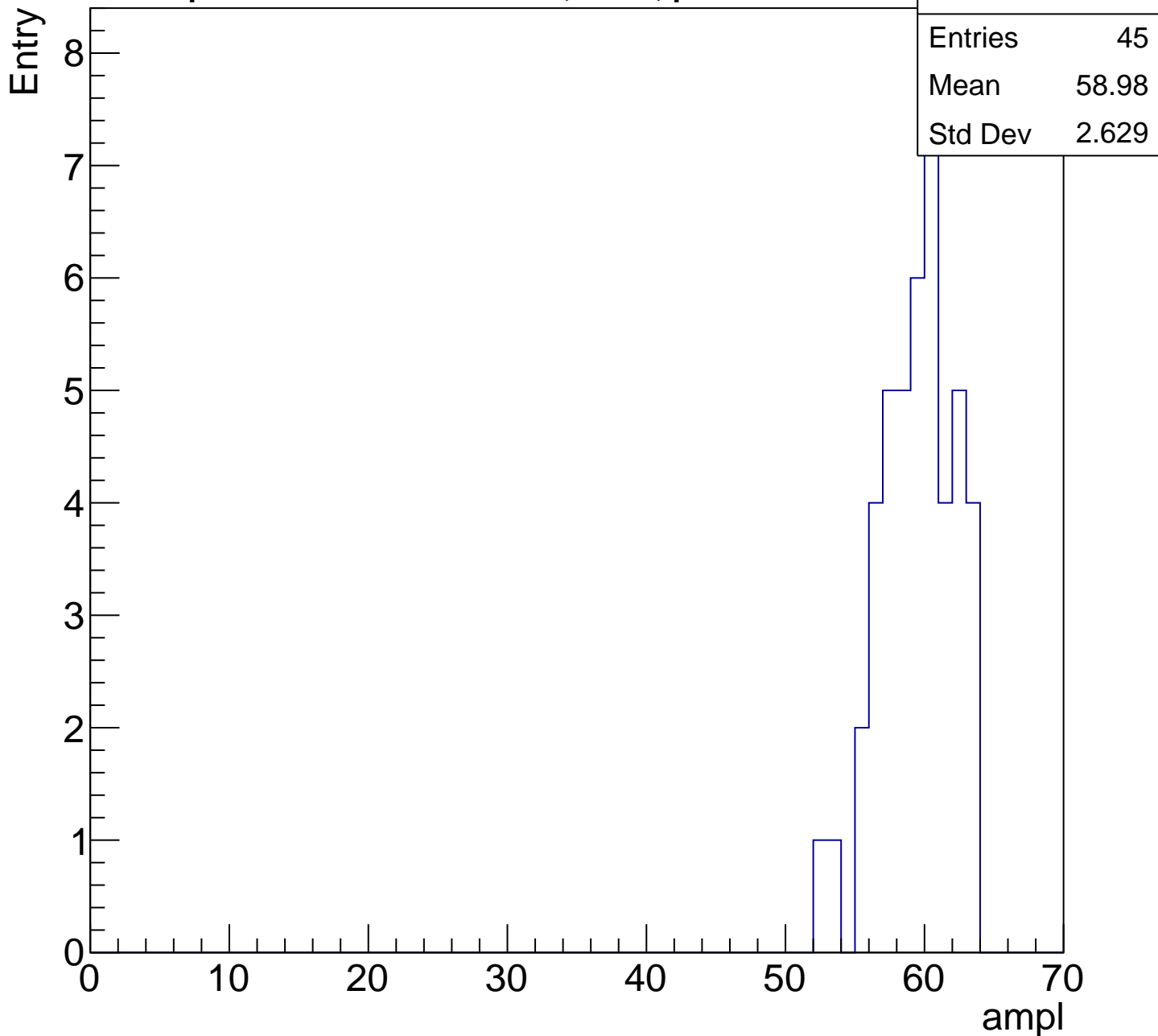
70

ampl



B1L103S, U1-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

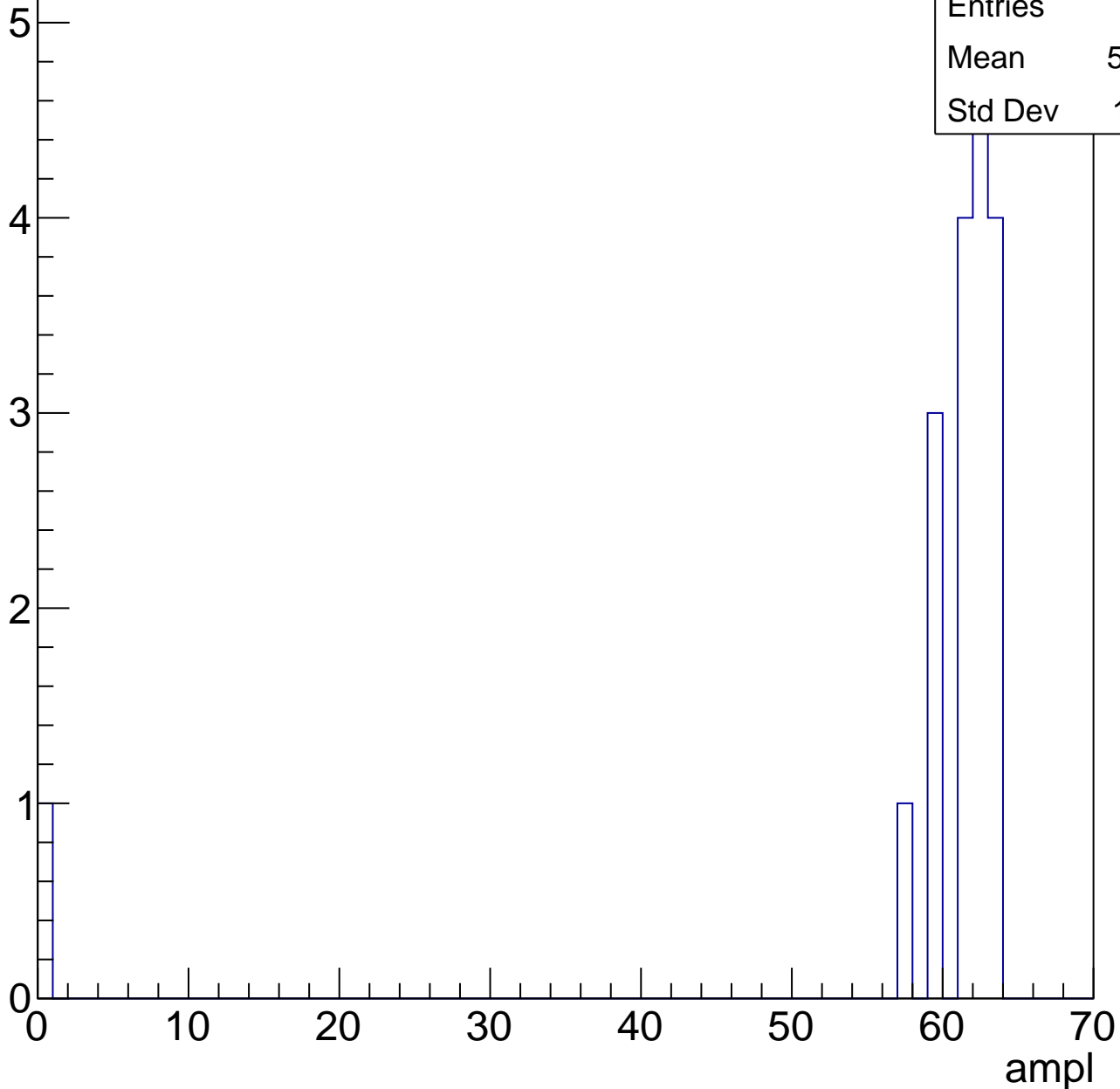


B1L103S, U1-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.78
Std Dev	14.11

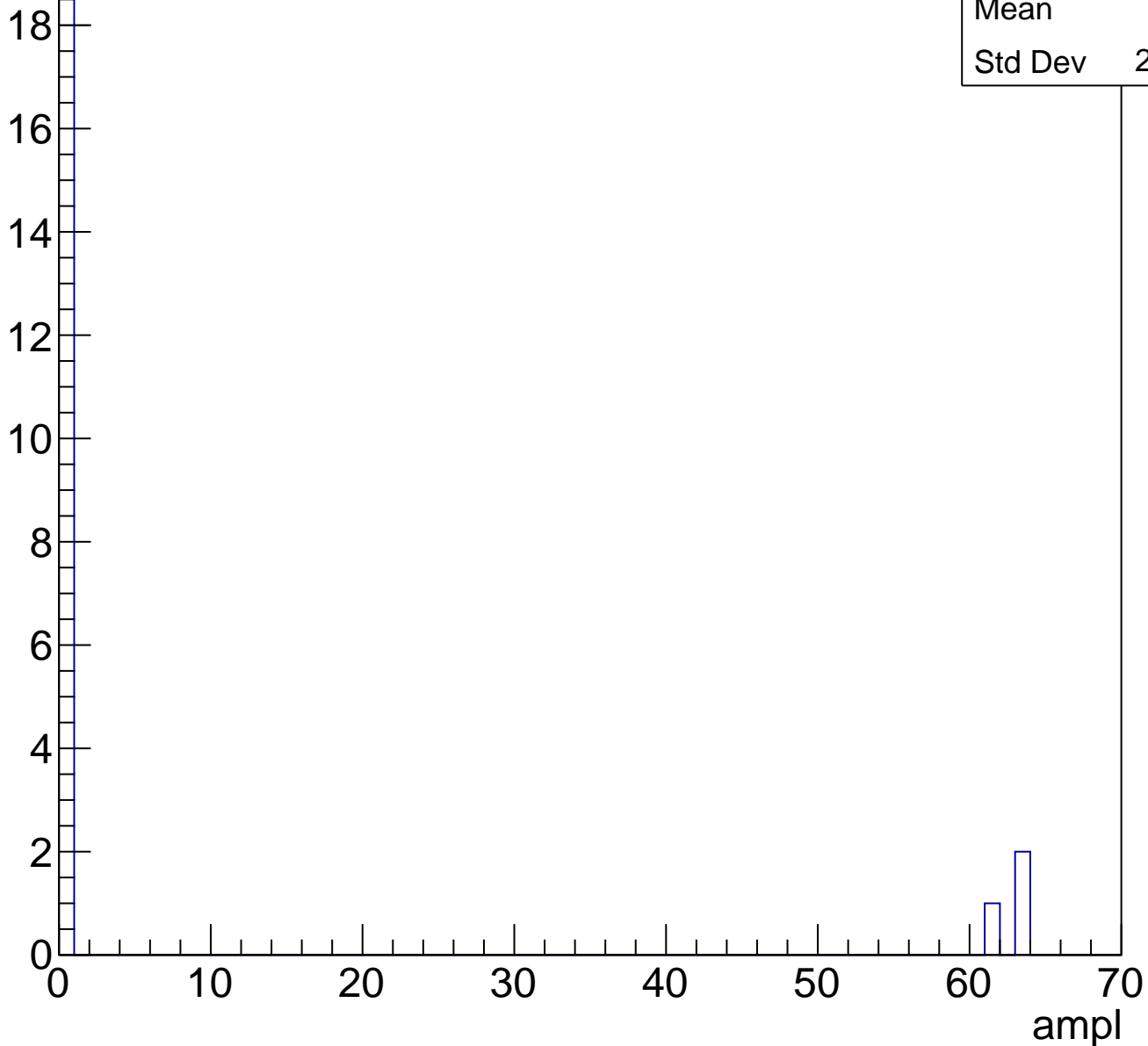


B1L103S, U1-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U1-ch58, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	22.72
Std Dev	10.43

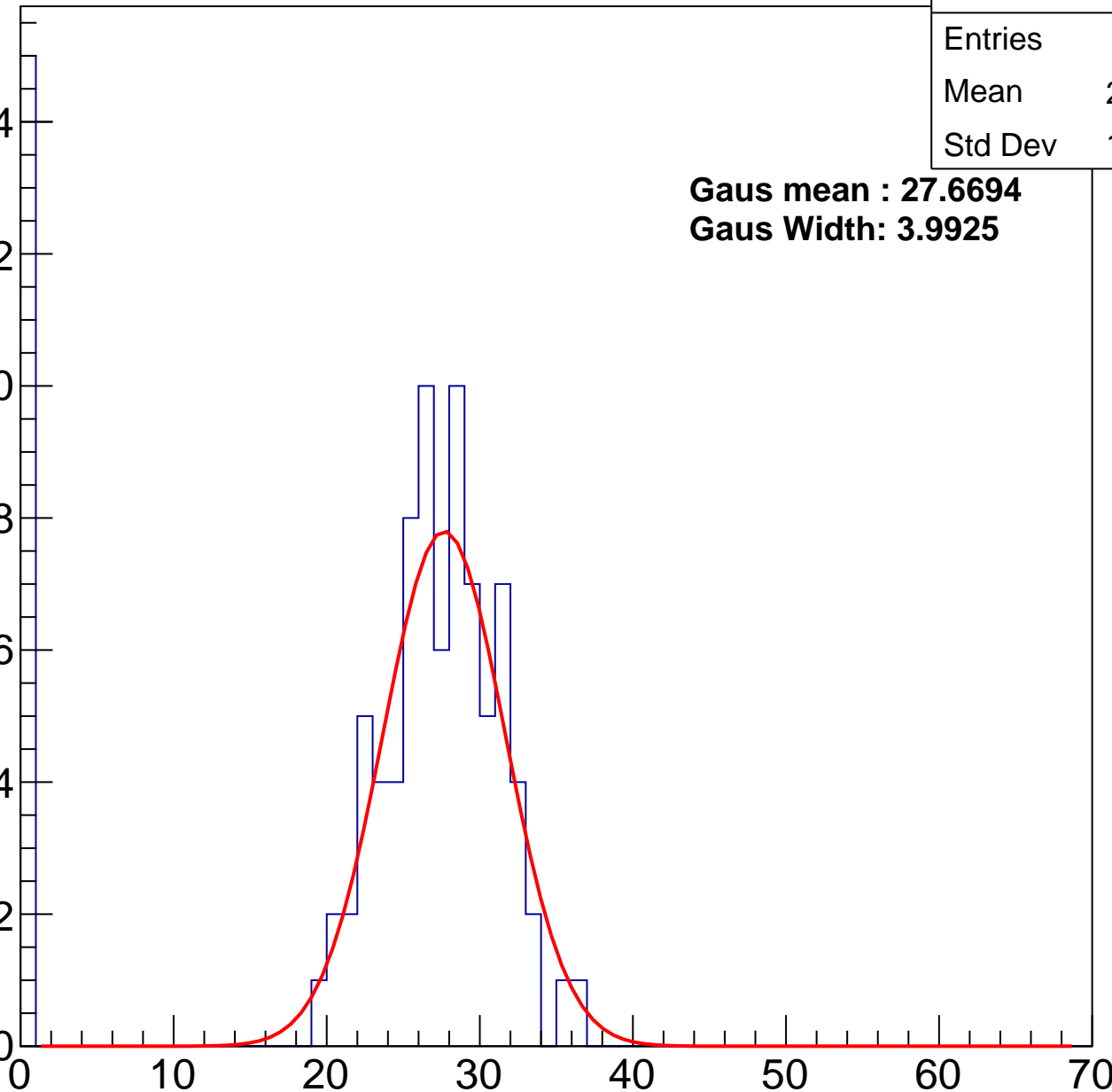
Gaus mean : 27.6694

Gaus Width: 3.9925

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U1-ch58, adc1

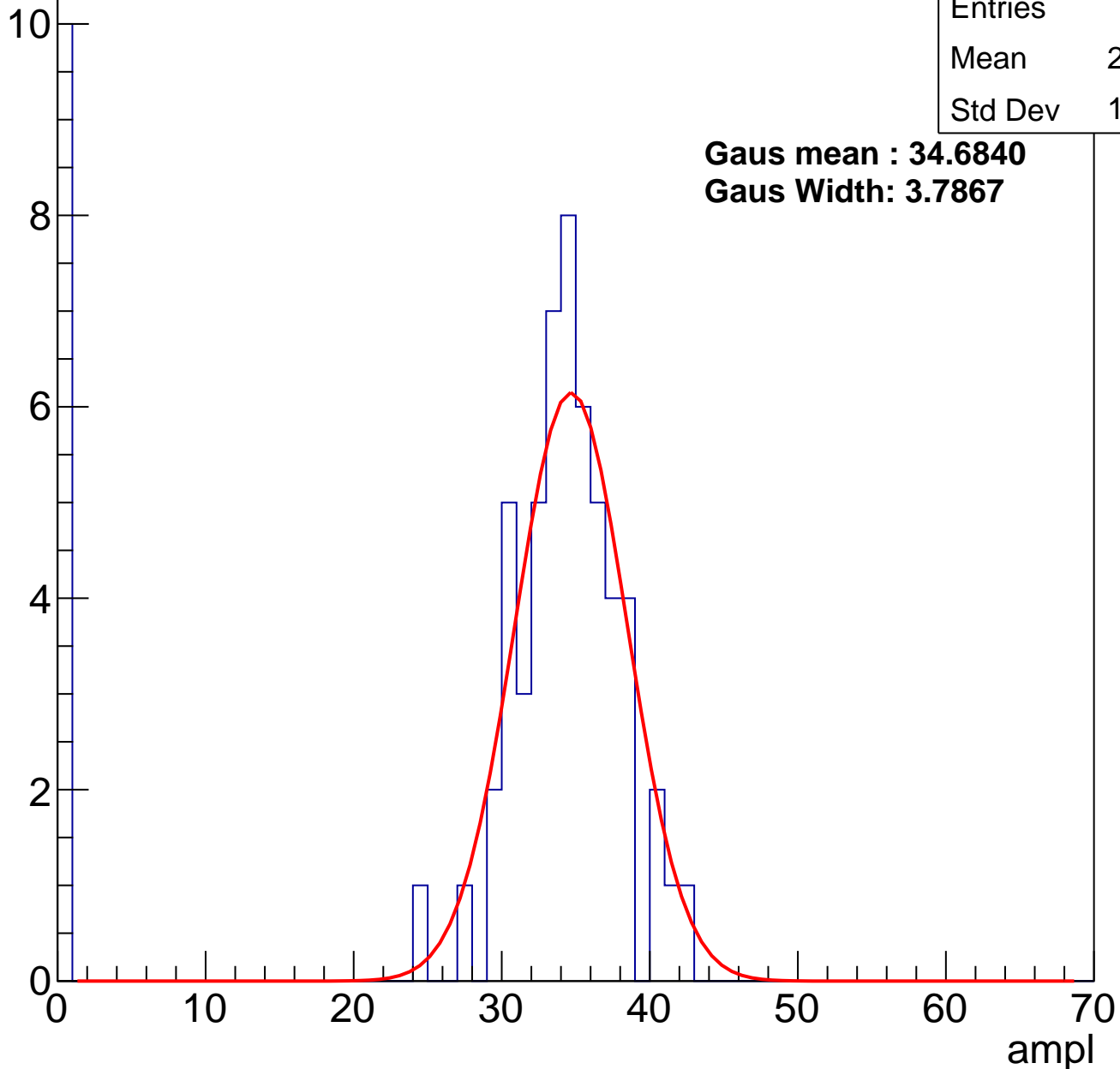
calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	28.74
Std Dev	12.65

Gaus mean : 34.6840

Gaus Width: 3.7867

Entry



B1L103S, U1-ch58, adc2

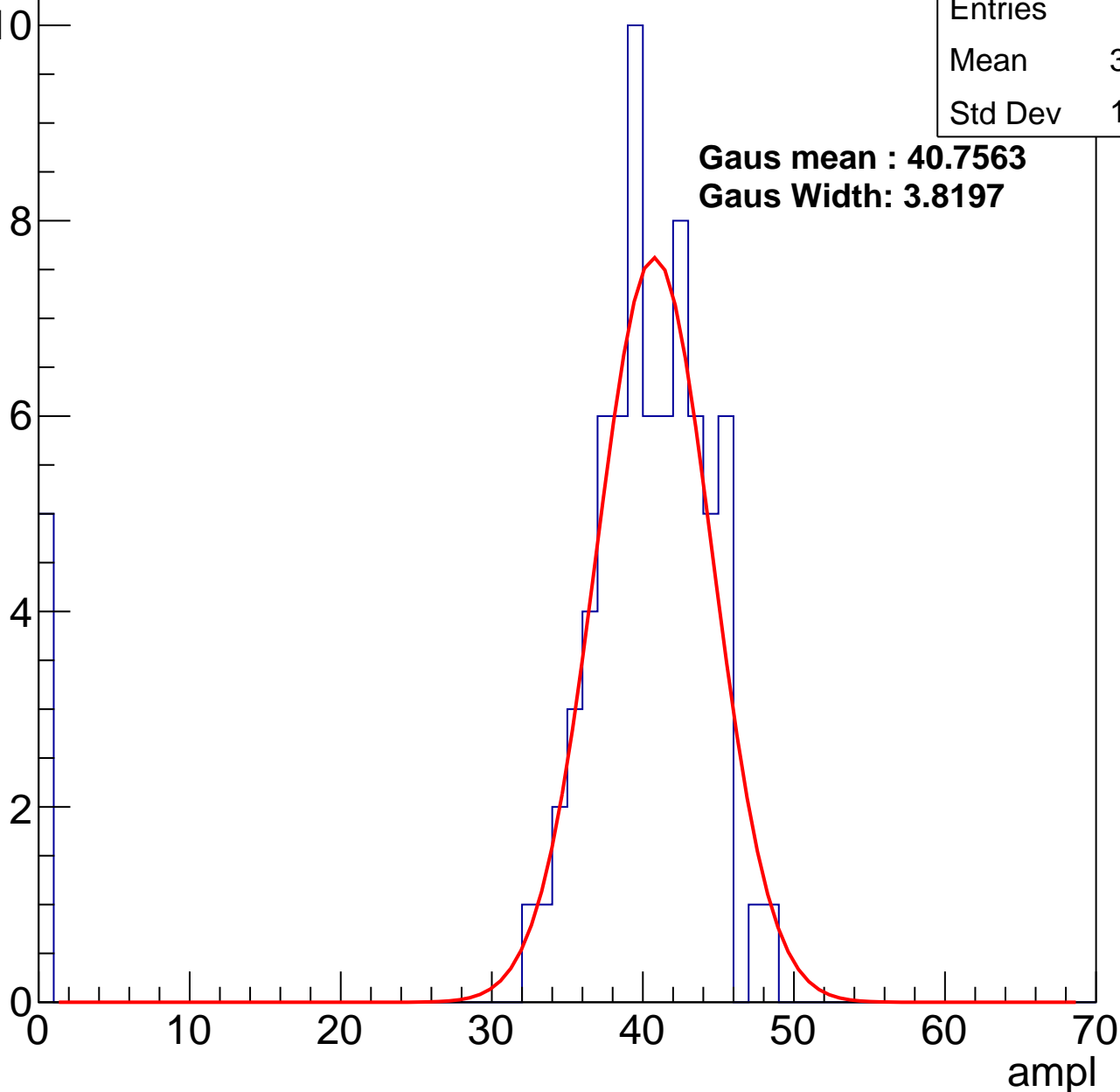
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	37.49
Std Dev	10.42

Gaus mean : 40.7563

Gaus Width: 3.8197

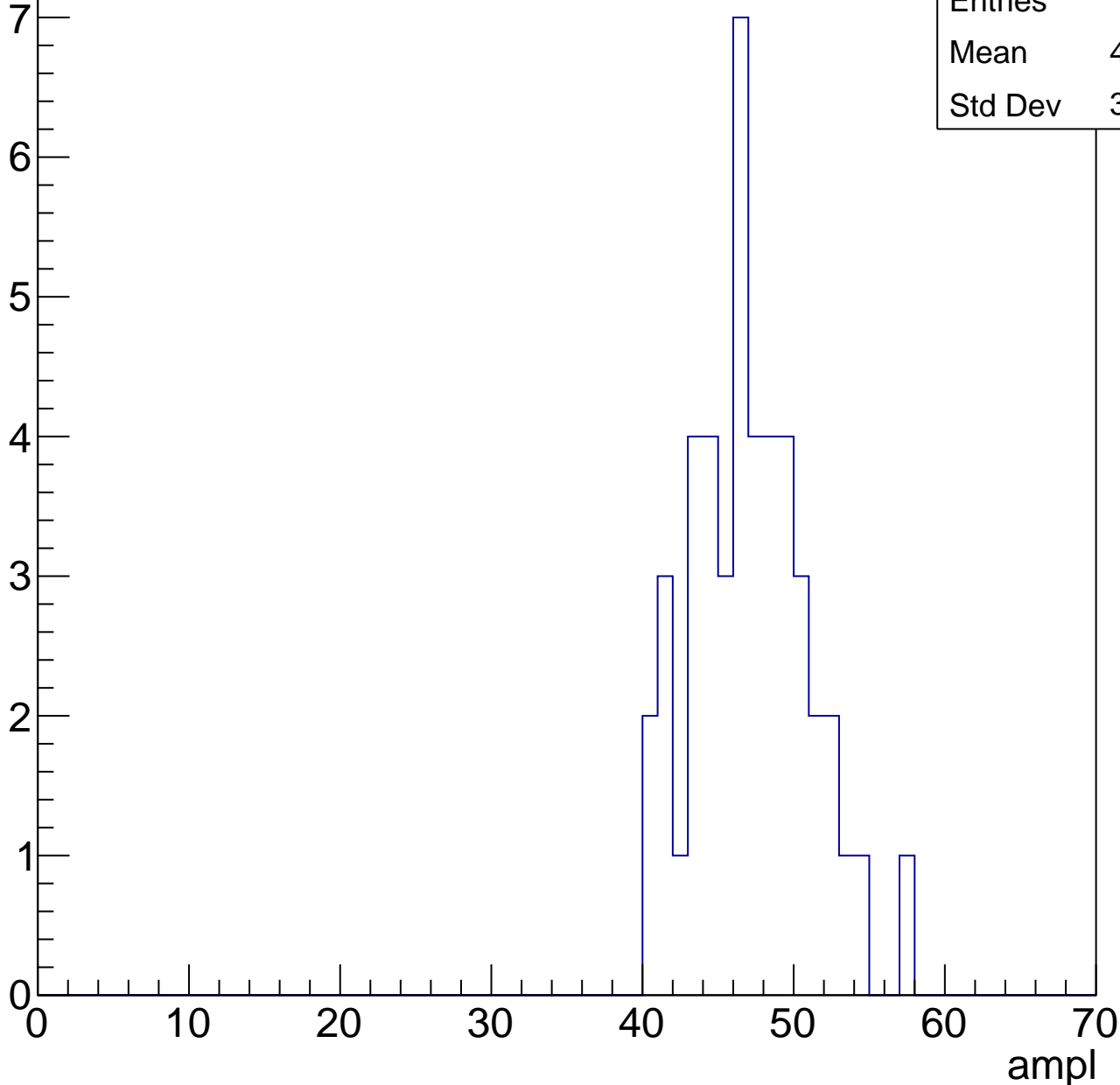


B1L103S, U1-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

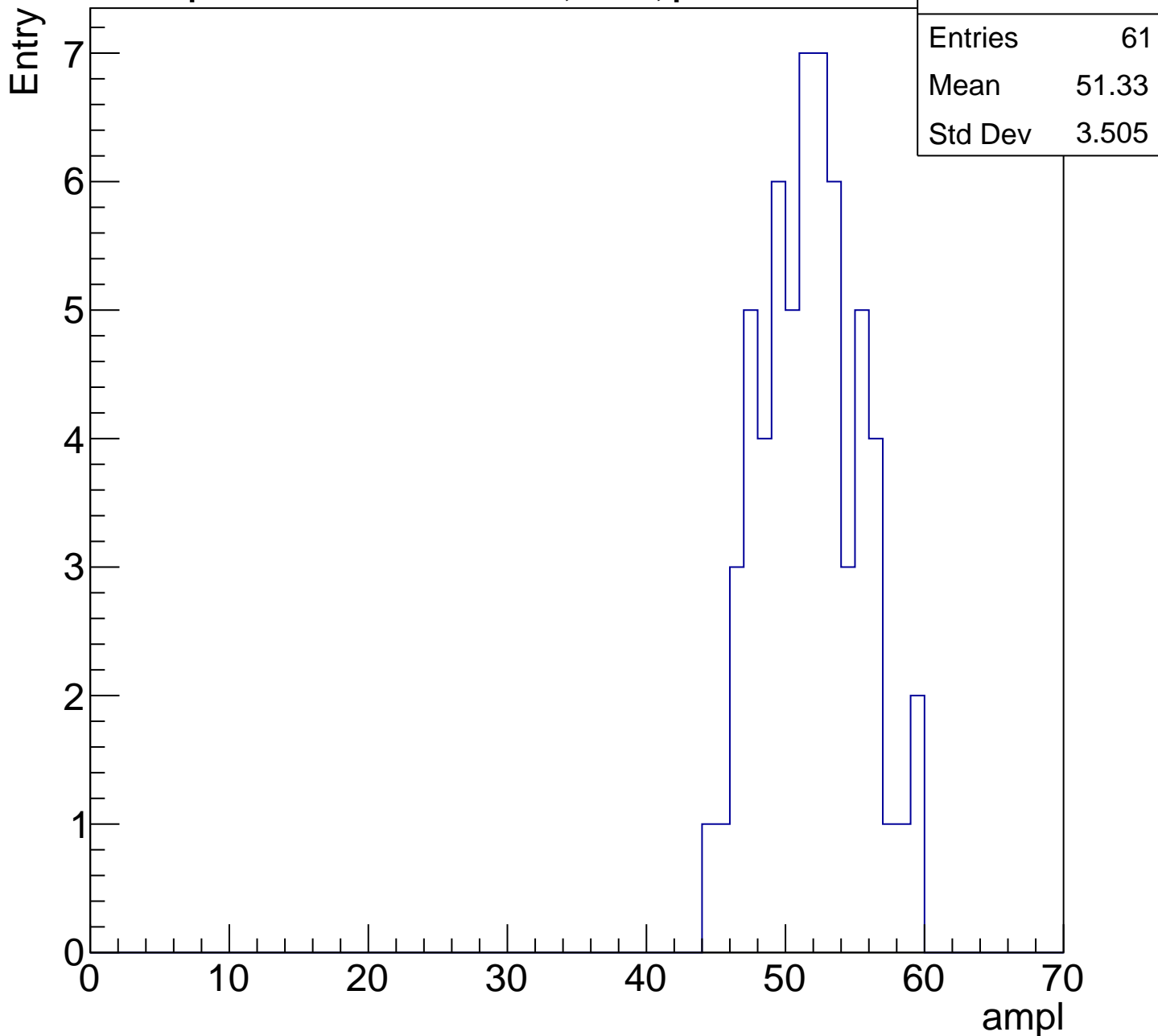
Entry

Entries	46
Mean	46.65
Std Dev	3.783



B1L103S, U1-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

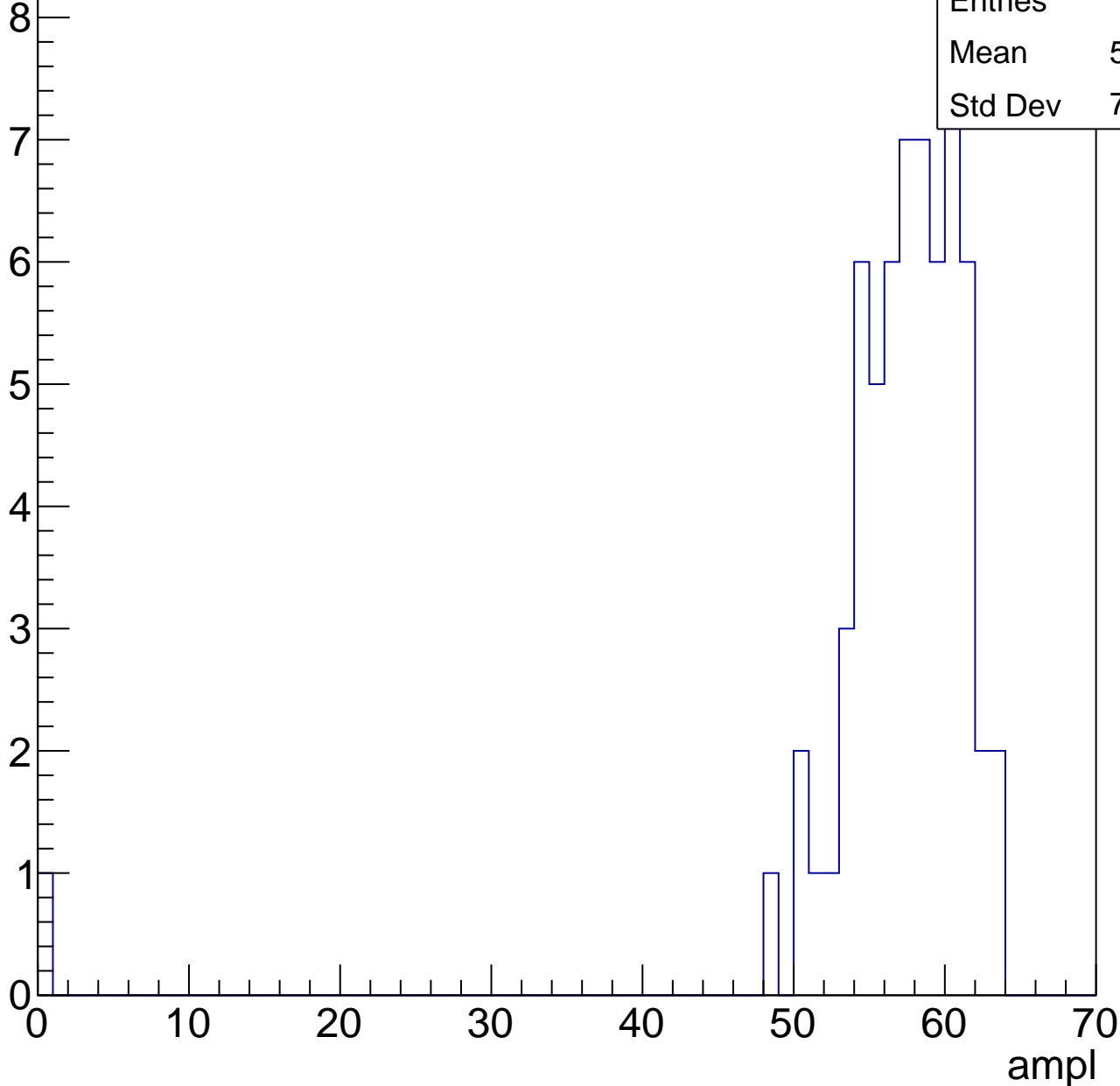


B1L103S, U1-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	56.25
Std Dev	7.808

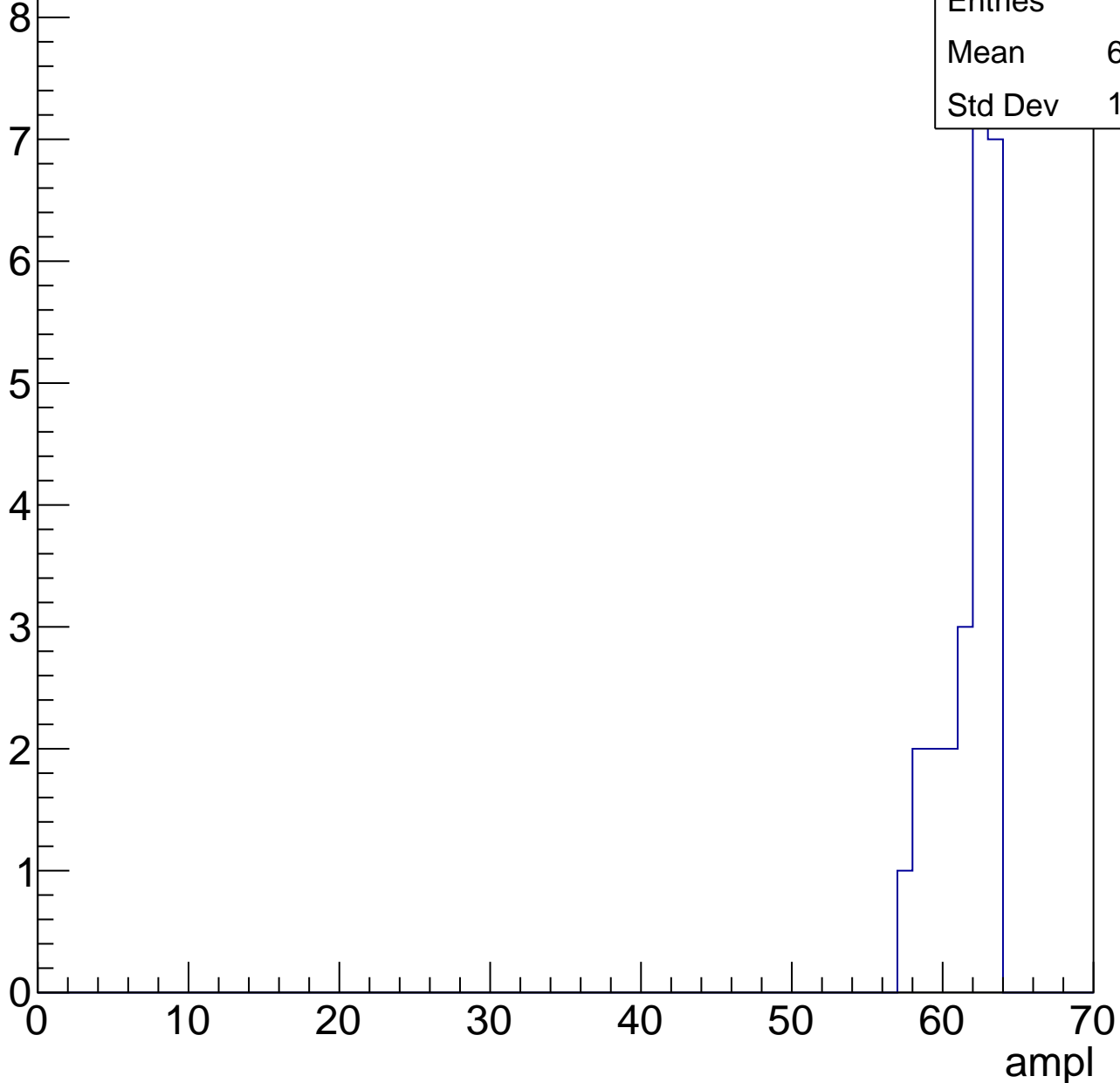


B1L103S, U1-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

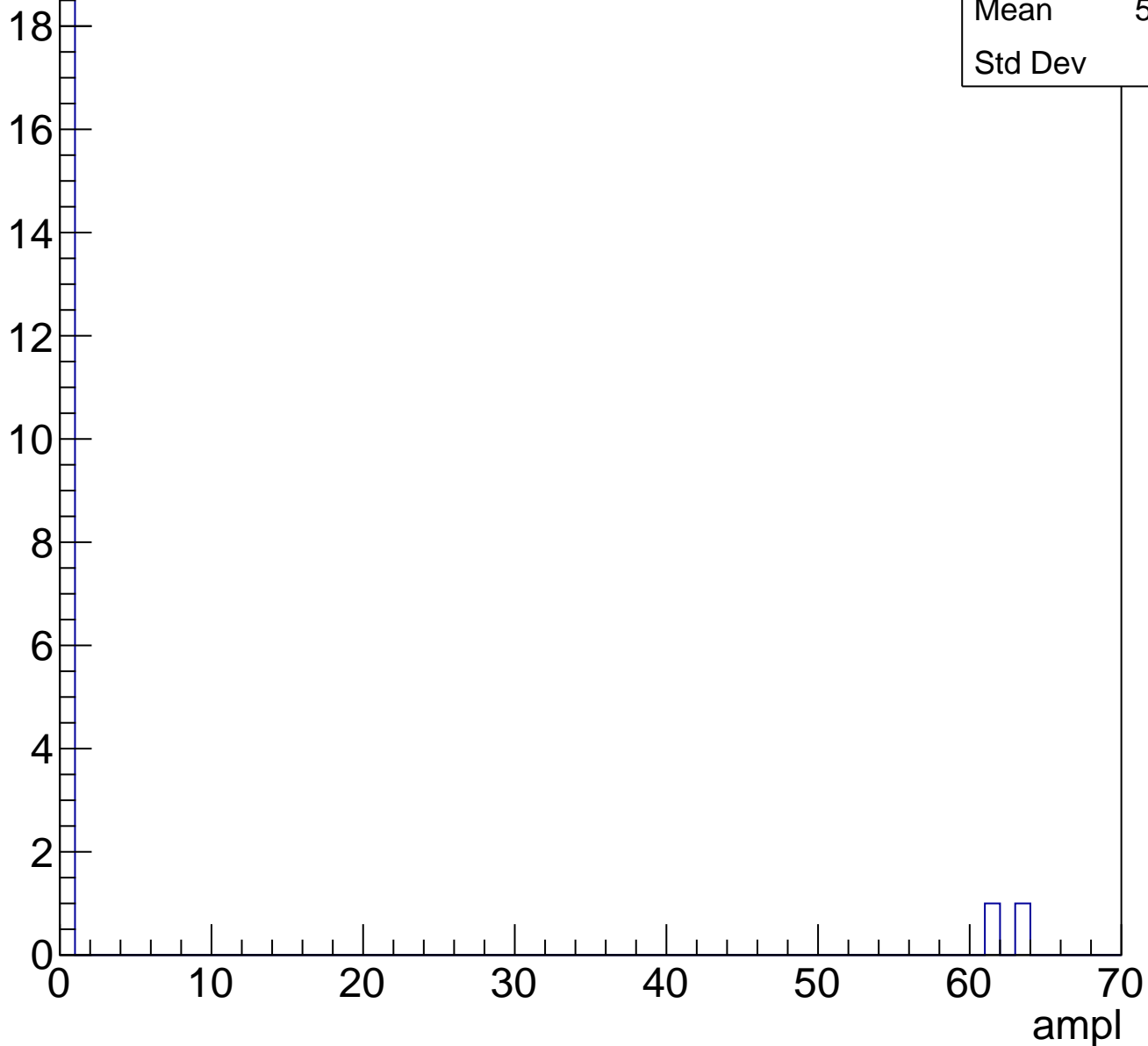
Entries	25
Mean	61.24
Std Dev	1.773



B1L103S, U1-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch59, adc0

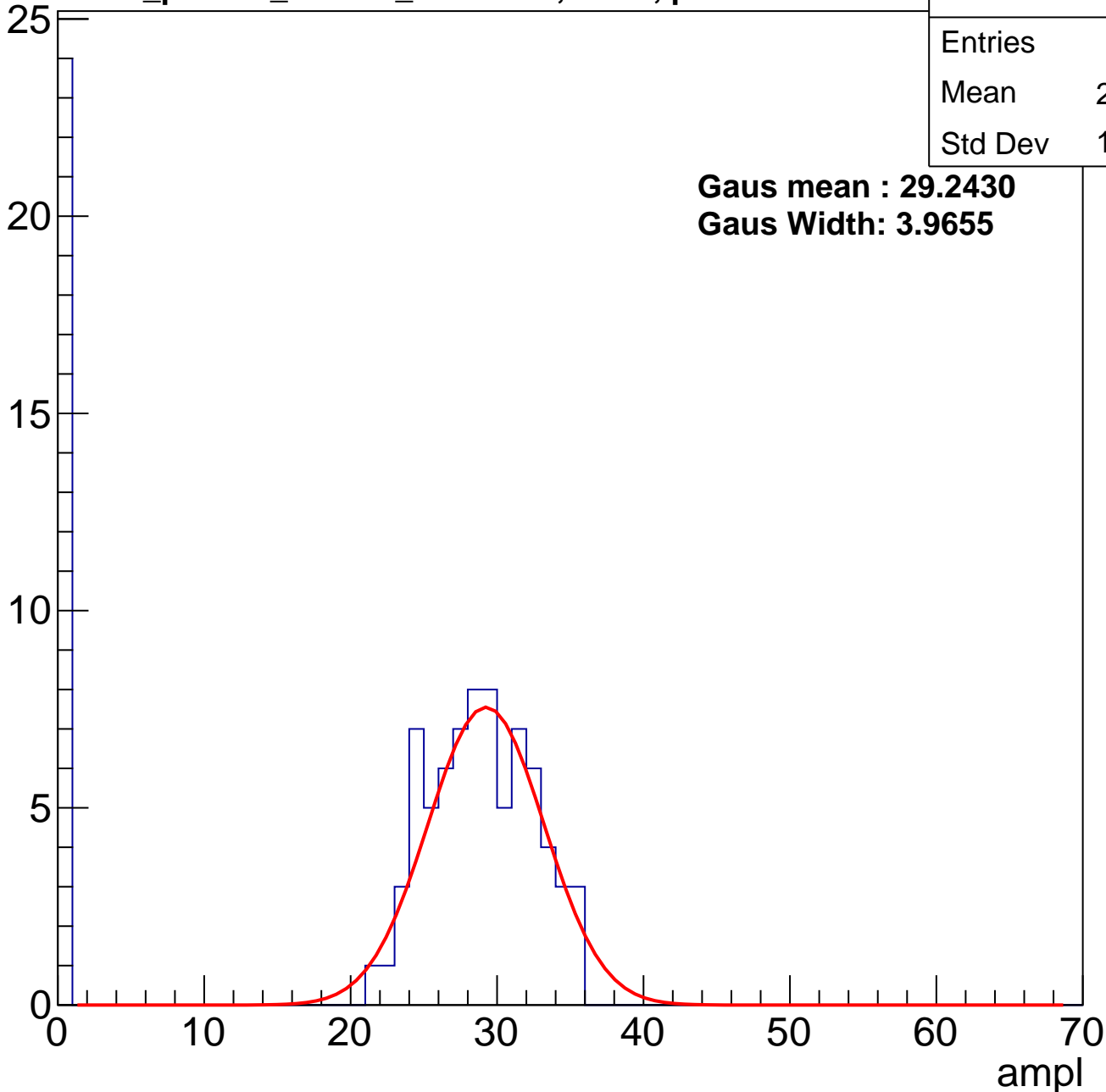
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	21.47
Std Dev	12.59

Gaus mean : 29.2430

Gaus Width: 3.9655

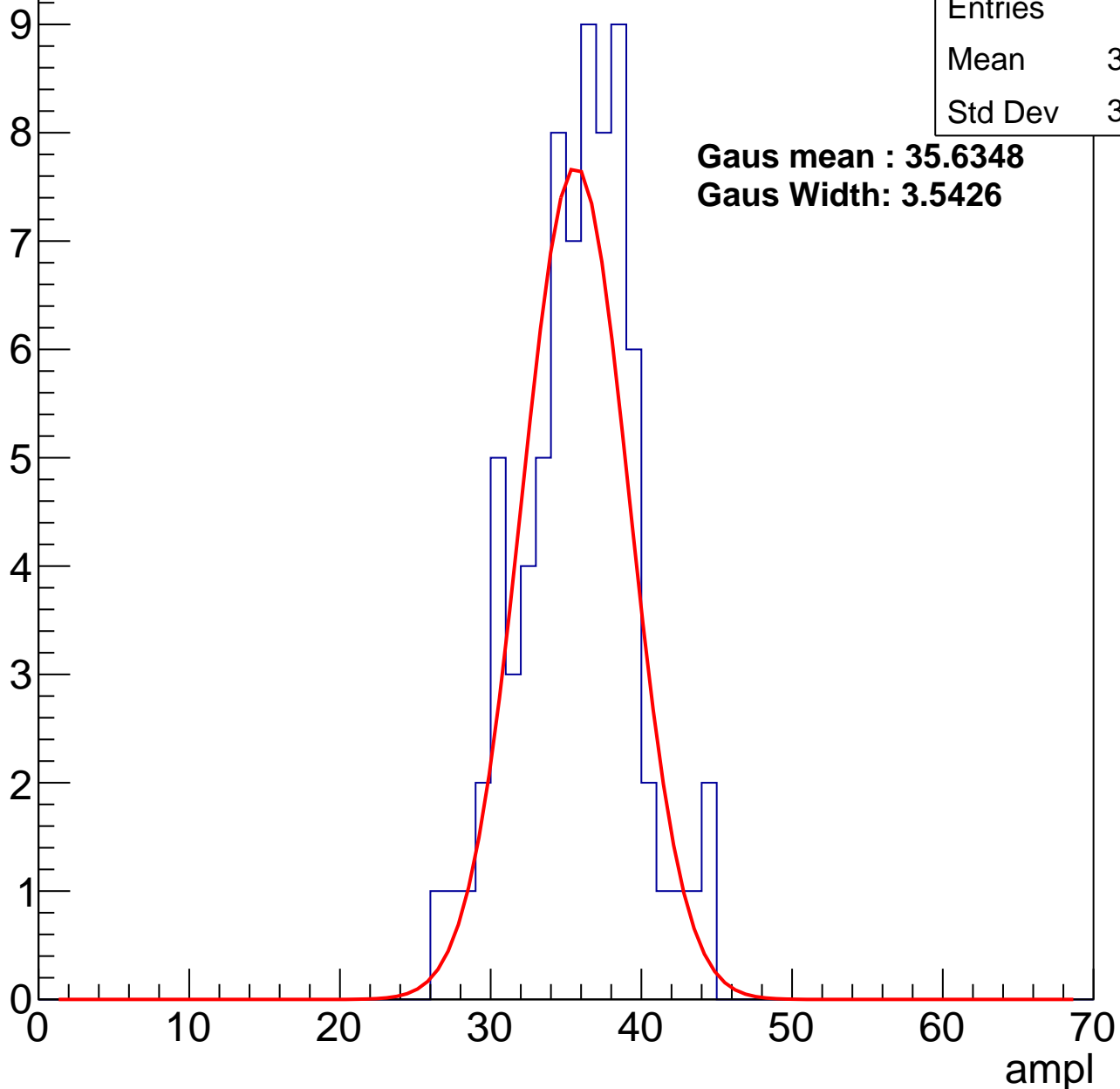
Entry



B1L103S, U1-ch59, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	76
Mean	35.29
Std Dev	3.762

Gaus mean : 35.6348

Gaus Width: 3.5426

B1L103S, U1-ch59, adc2

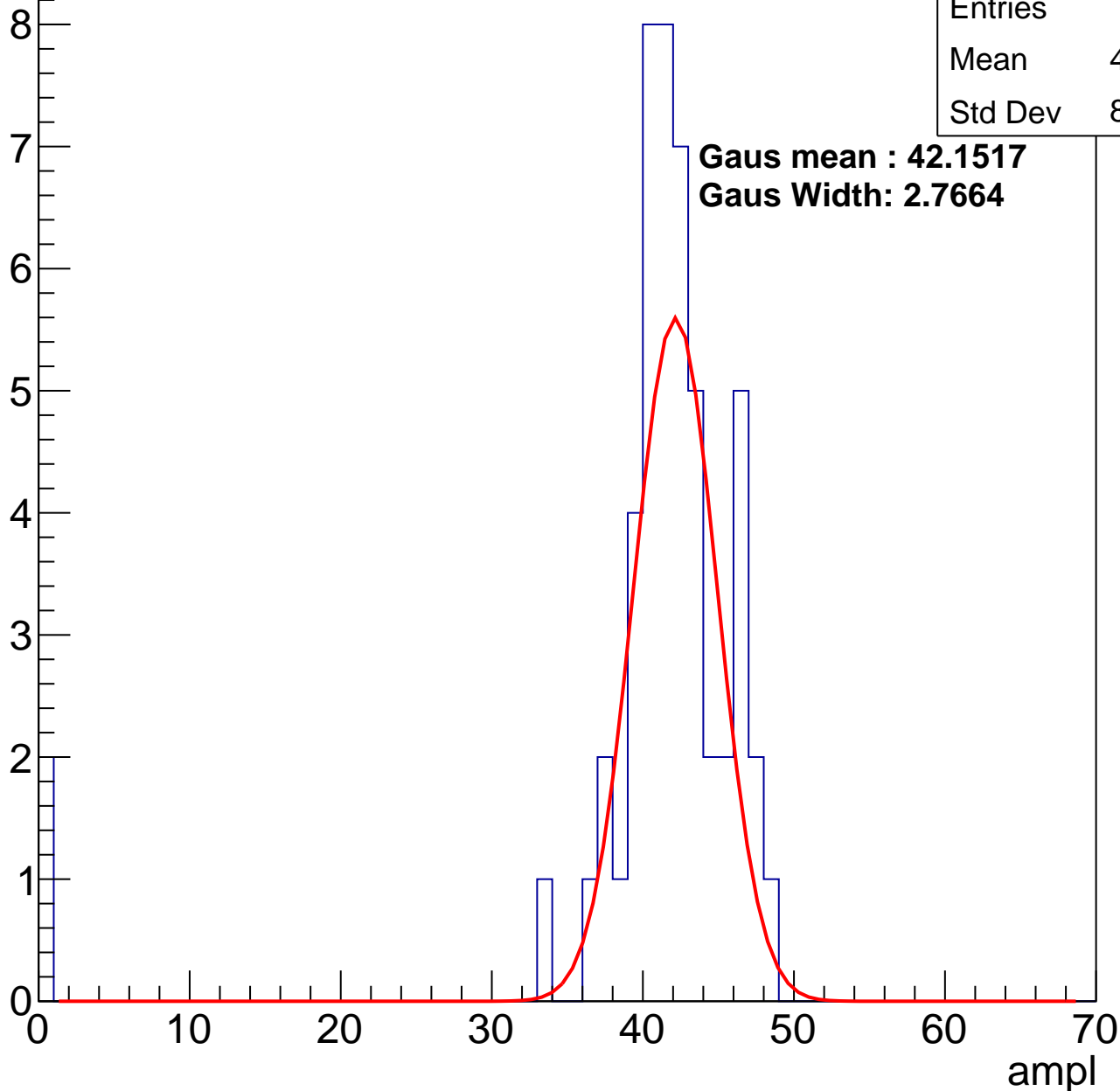
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	40.08
Std Dev	8.627

Gaus mean : 42.1517

Gaus Width: 2.7664



B1L103S, U1-ch59, adc3

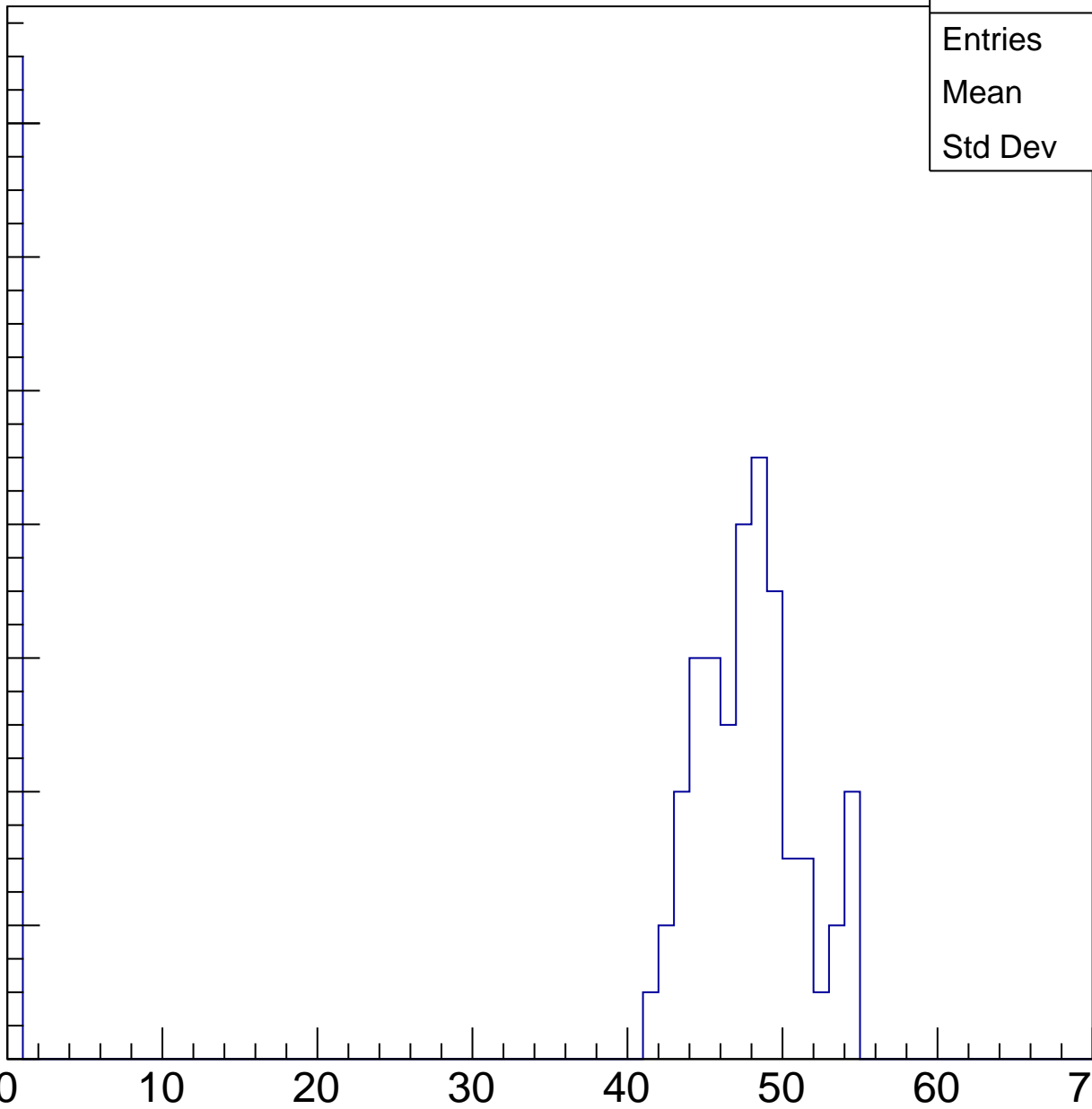
calib_packv5_041523_1651.root, FC#0, port C2

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	38.01
Std Dev	19.07

ampl

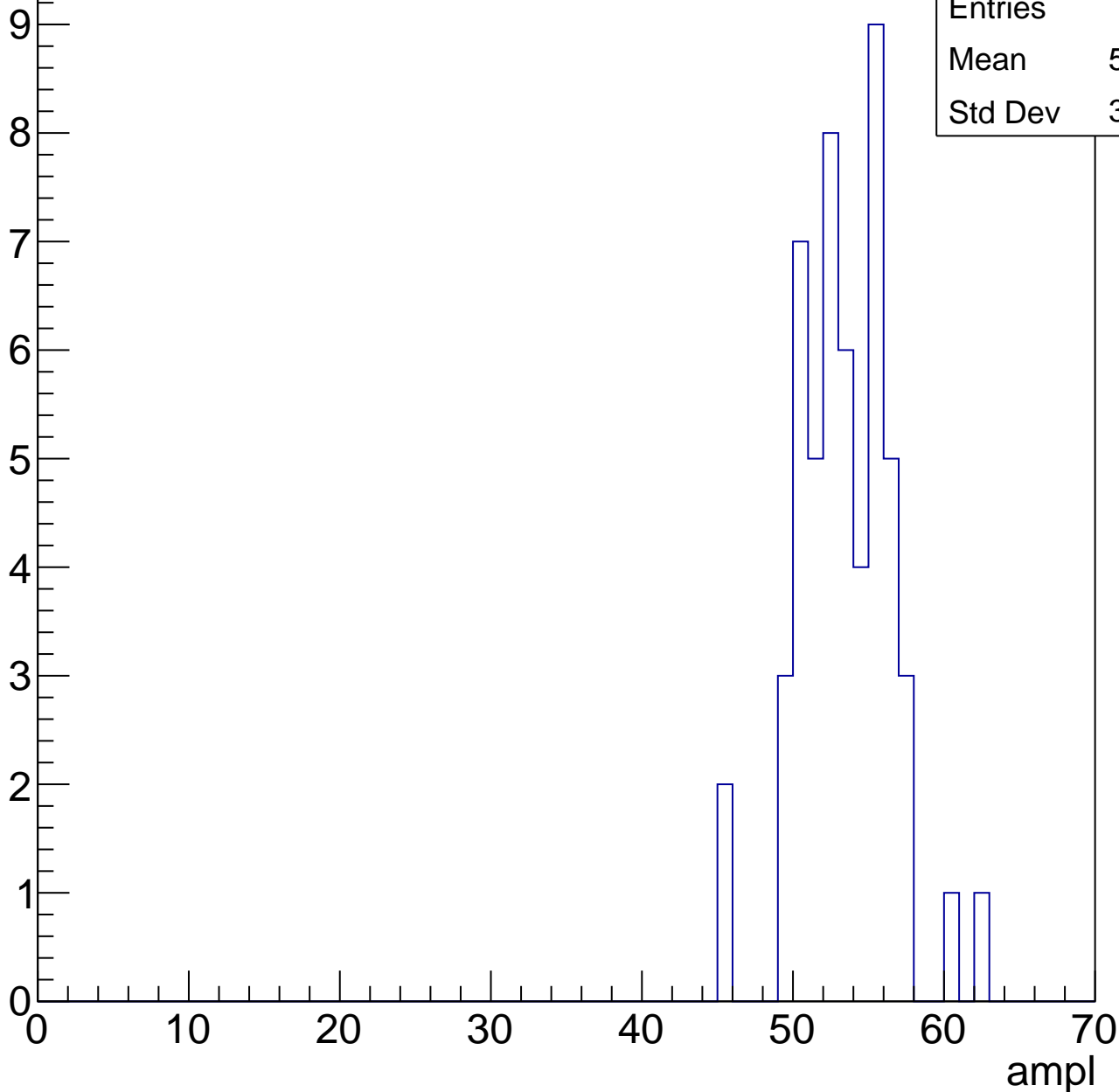


B1L103S, U1-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

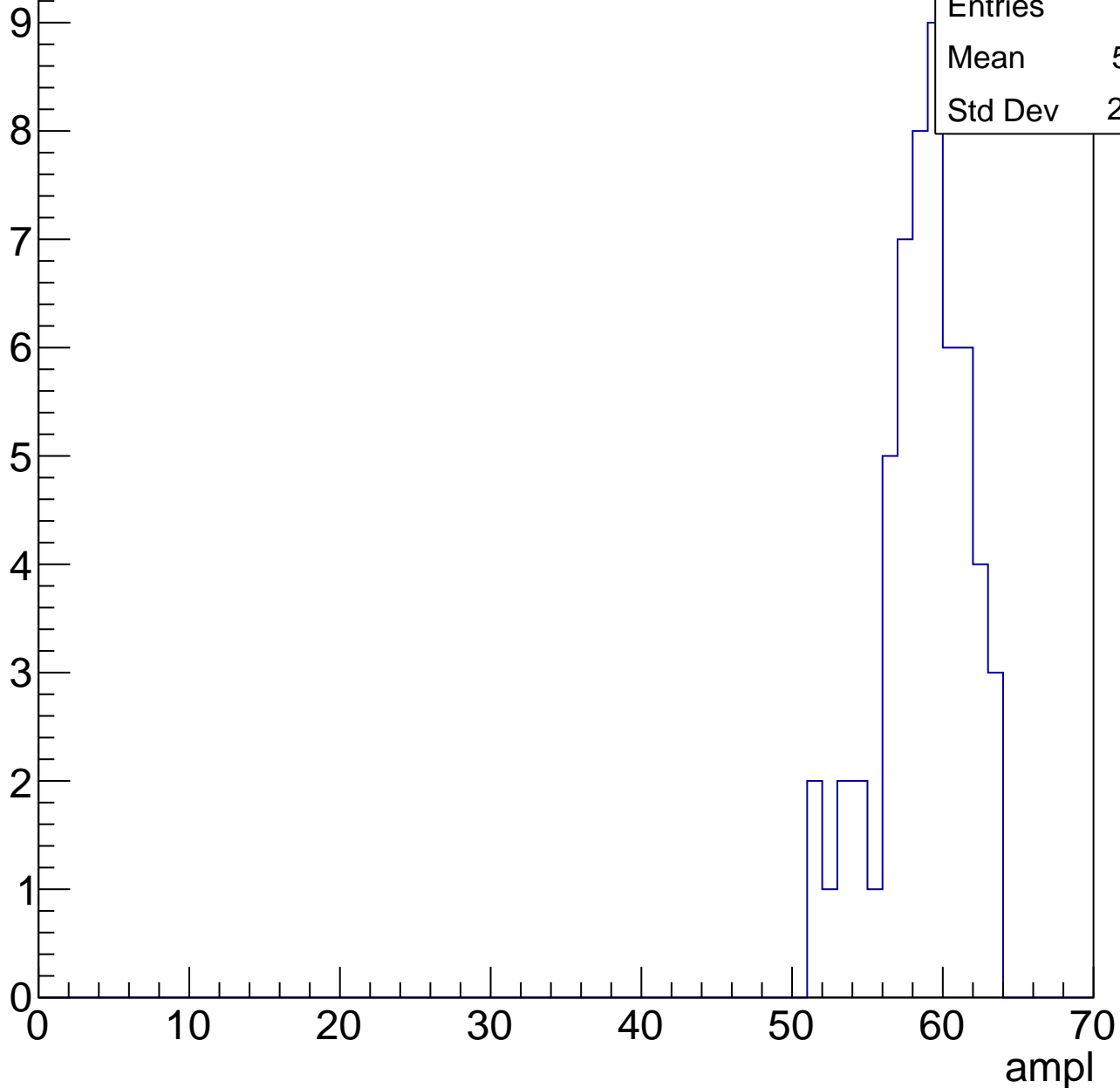
Entries	54
Mean	52.96
Std Dev	3.133



B1L103S, U1-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



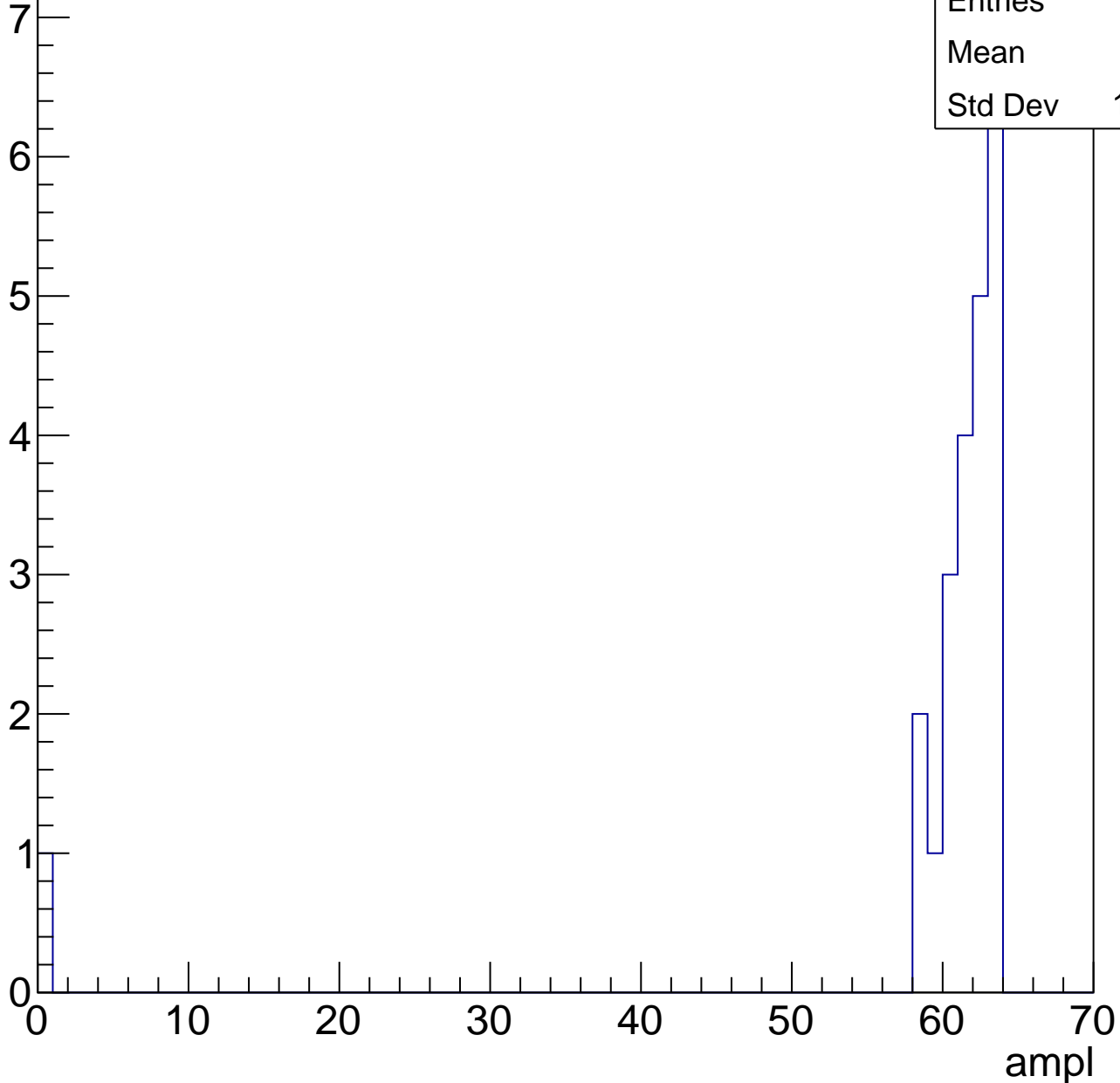
Entries	56
Mean	58.21
Std Dev	2.914

B1L103S, U1-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.7
Std Dev	12.61



B1L103S, U1-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

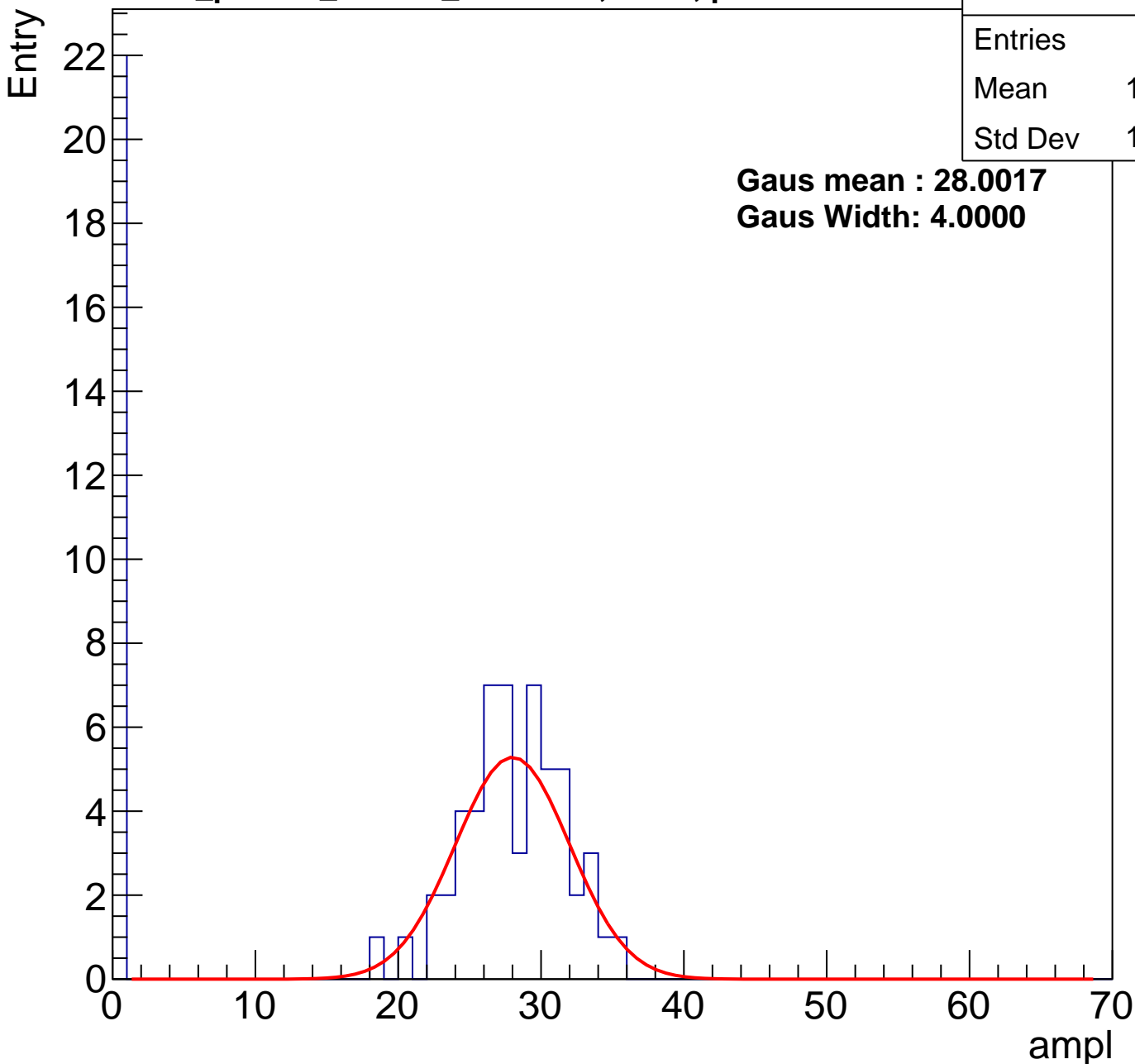
B1L103S, U1-ch60, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	19.73
Std Dev	12.82

Gaus mean : 28.0017

Gaus Width: 4.0000



B1L103S, U1-ch60, adc1

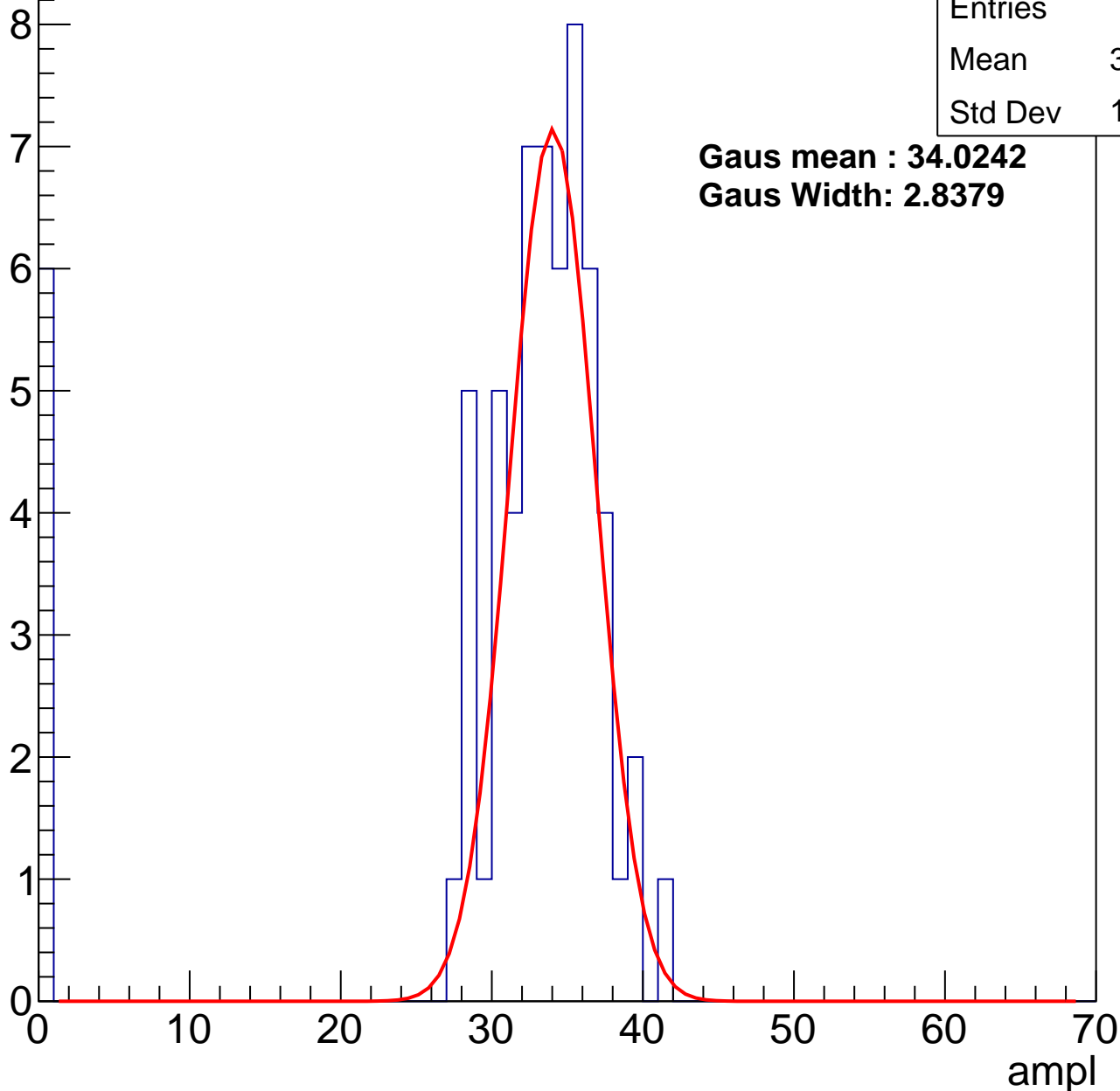
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	30.16
Std Dev	10.14

Gaus mean : 34.0242

Gaus Width: 2.8379



B1L103S, U1-ch60, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	33.37
Std Dev	14.58

Gaus mean : 40.3888

Gaus Width: 3.0655

Entry

10

8

6

4

2

0

0

10

20

30

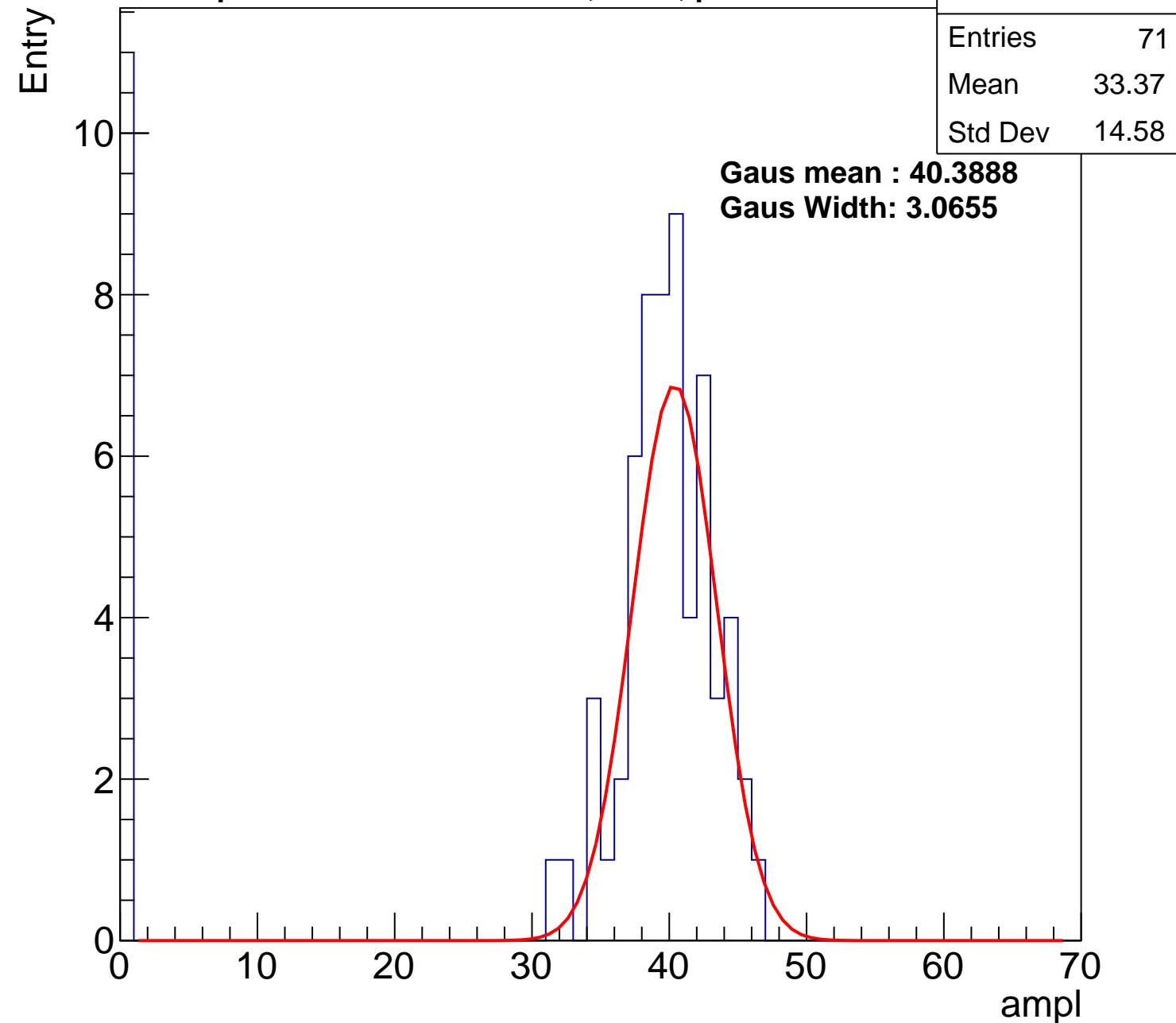
40

50

60

70

ampl

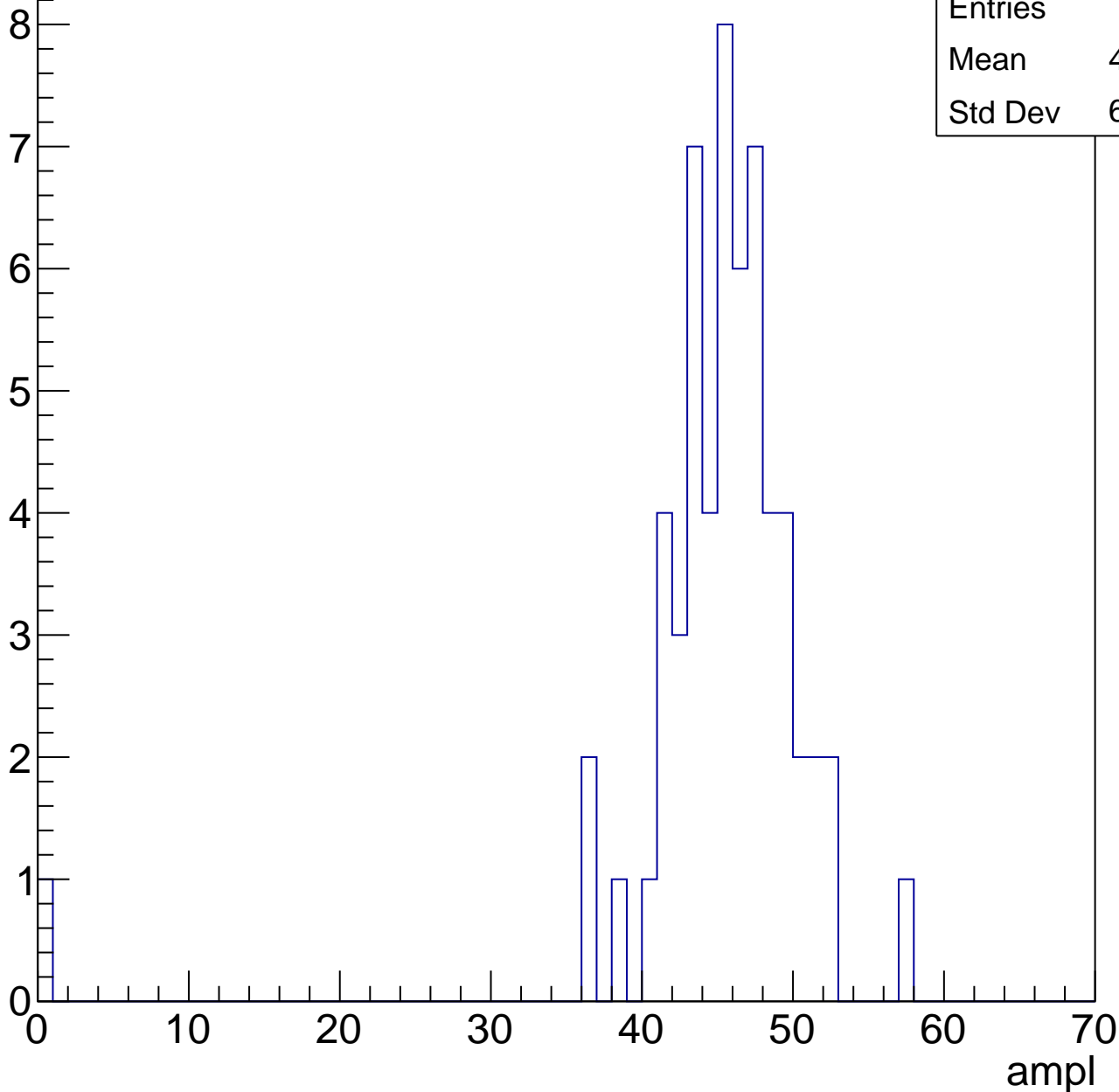


B1L103S, U1-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

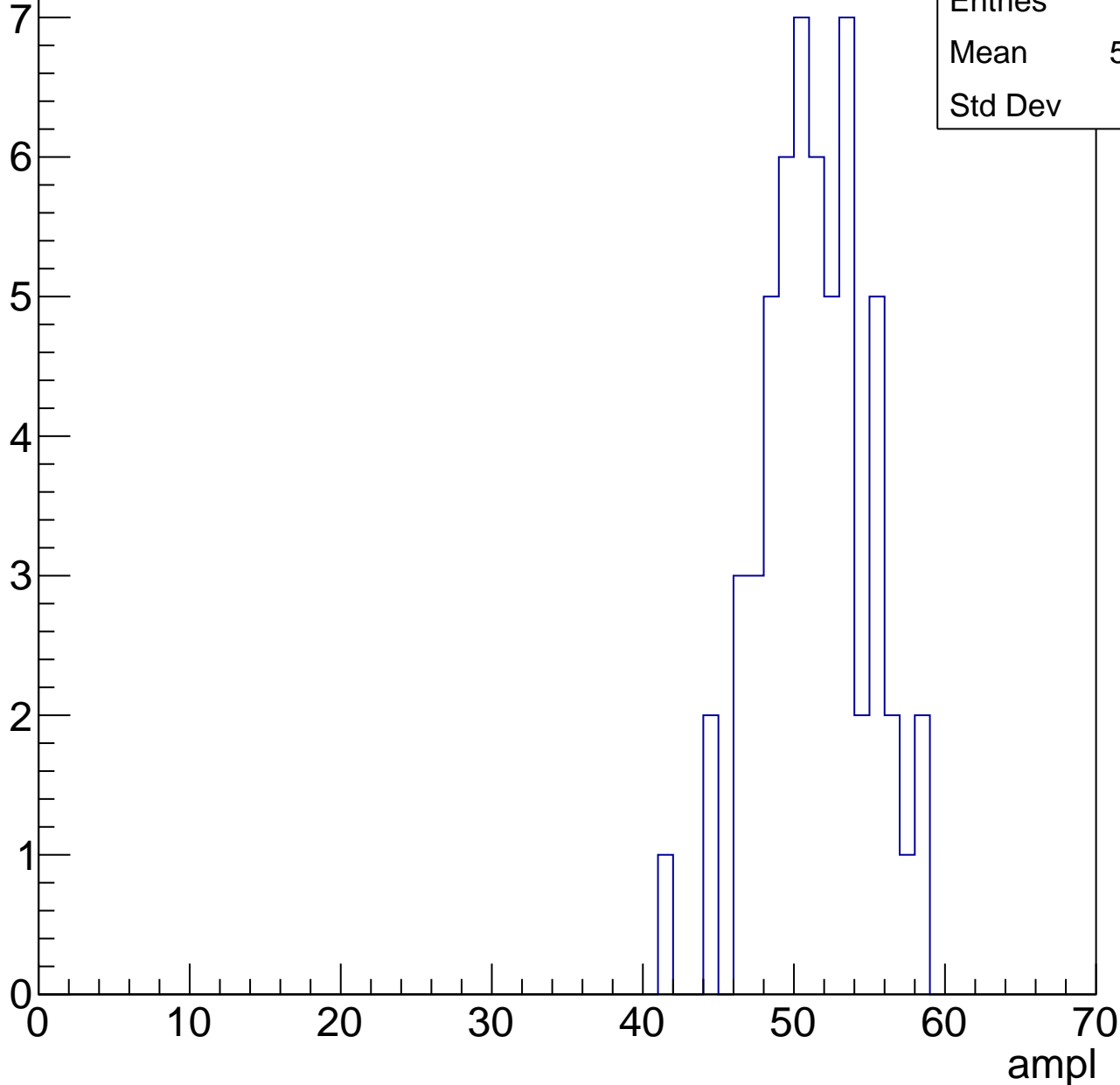
Entries	59
Mean	44.63
Std Dev	6.979



B1L103S, U1-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



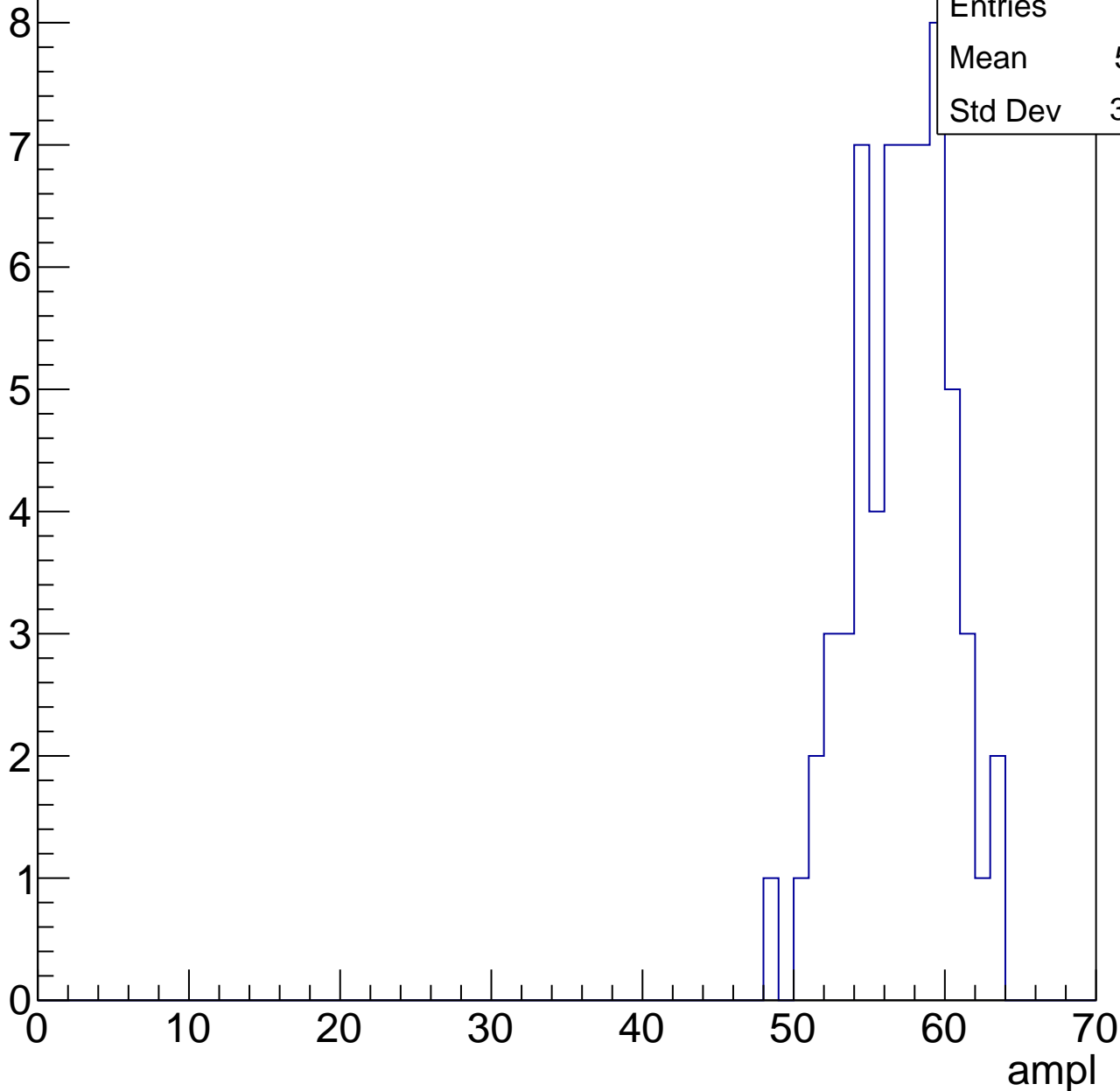
Entries	57
Mean	50.82
Std Dev	3.53

B1L103S, U1-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	56.61
Std Dev	3.215

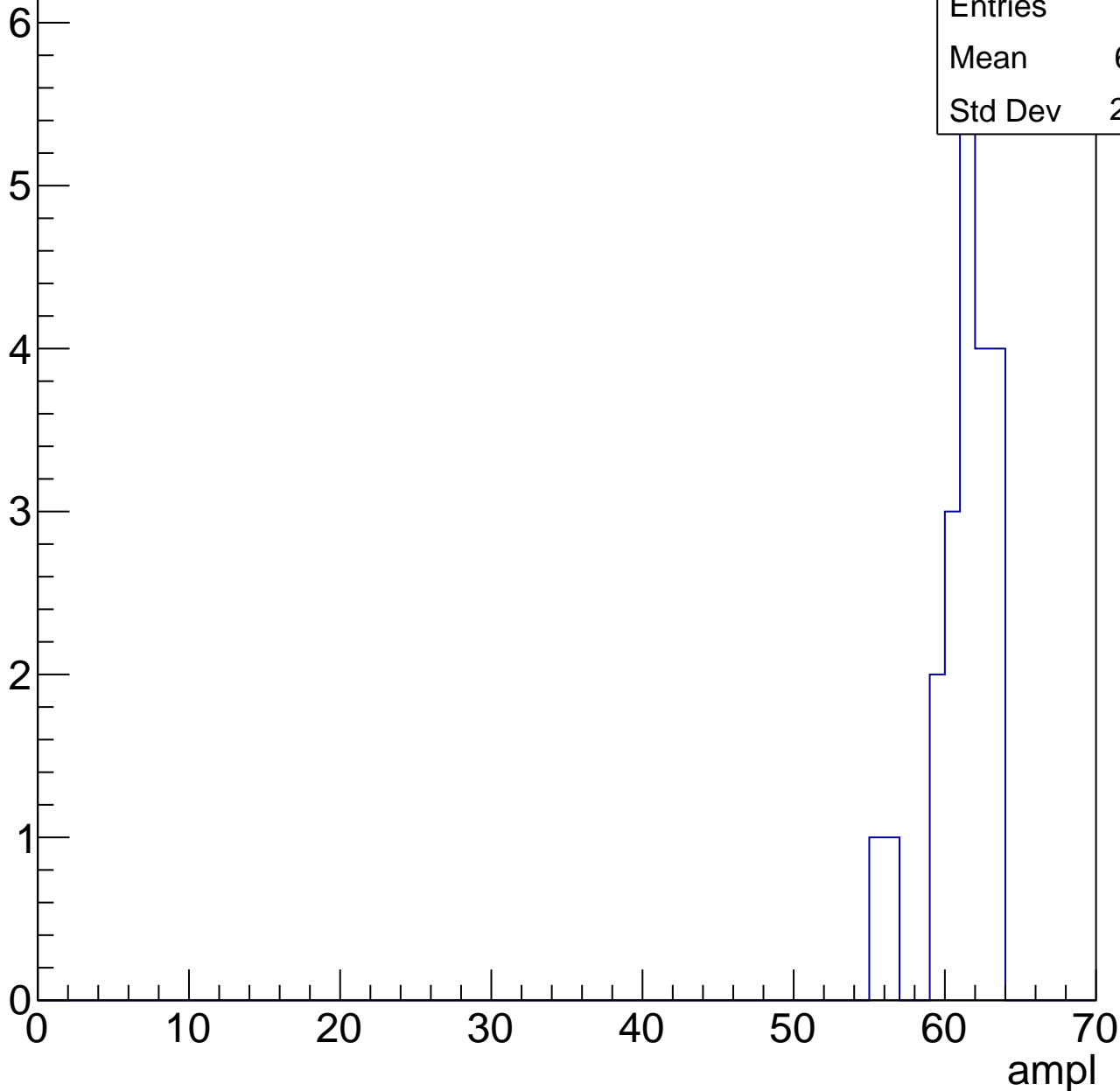


B1L103S, U1-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	60.71
Std Dev	2.073



B1L103S, U1-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	35
Mean	26.06
Std Dev	30.13

ampl

0 10 20 30 40 50 60 70

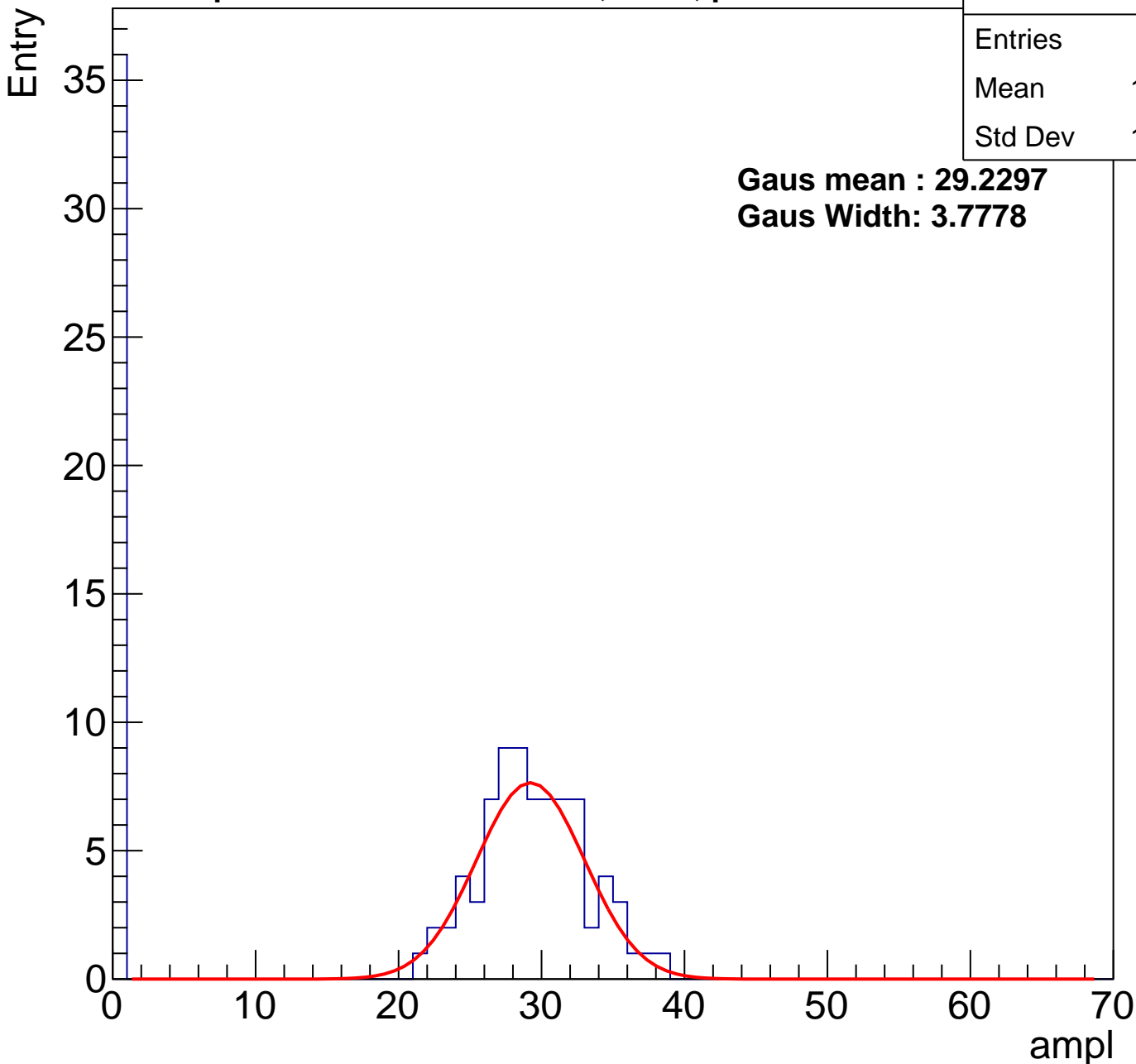
B1L103S, U1-ch61, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	19.74
Std Dev	13.83

Gaus mean : 29.2297

Gaus Width: 3.7778



B1L103S, U1-ch61, adc1

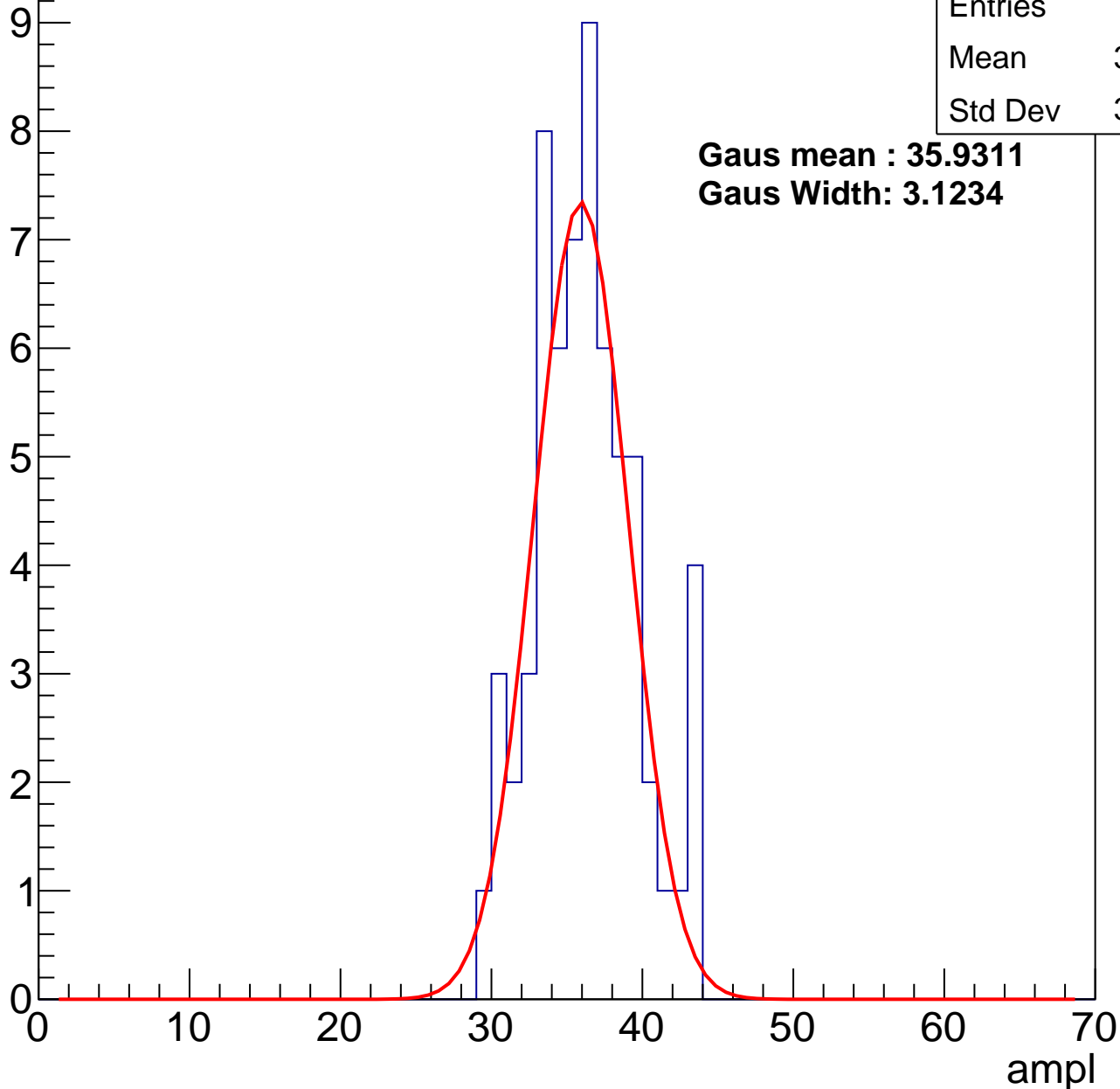
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	35.81
Std Dev	3.371

Gaus mean : 35.9311

Gaus Width: 3.1234



B1L103S, U1-ch61, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	35.58
Std Dev	15.77

Gaus mean : 42.2459

Gaus Width: 3.6287

Entry

10

8

6

4

2

0

0

10

20

30

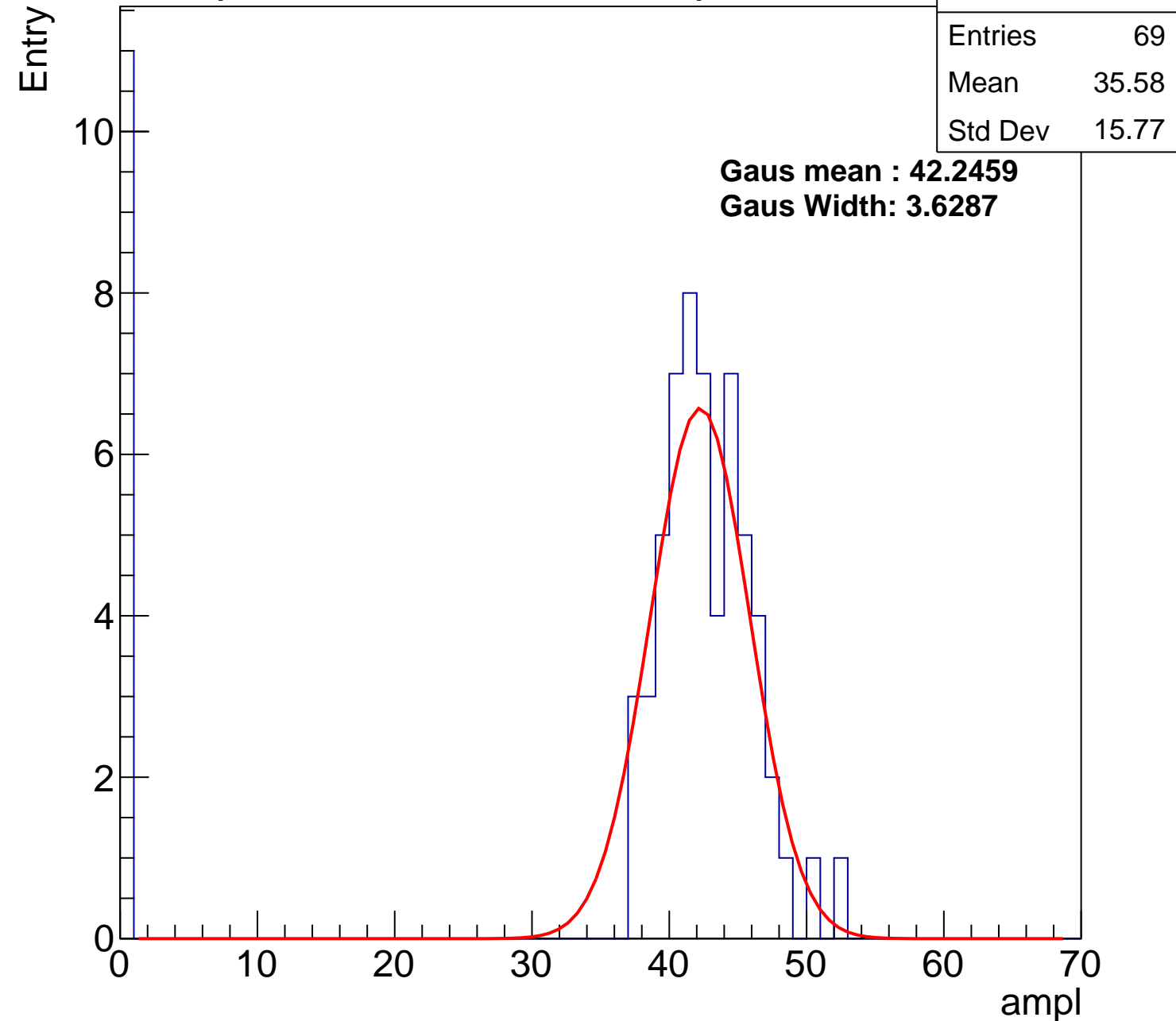
40

50

60

70

ampl

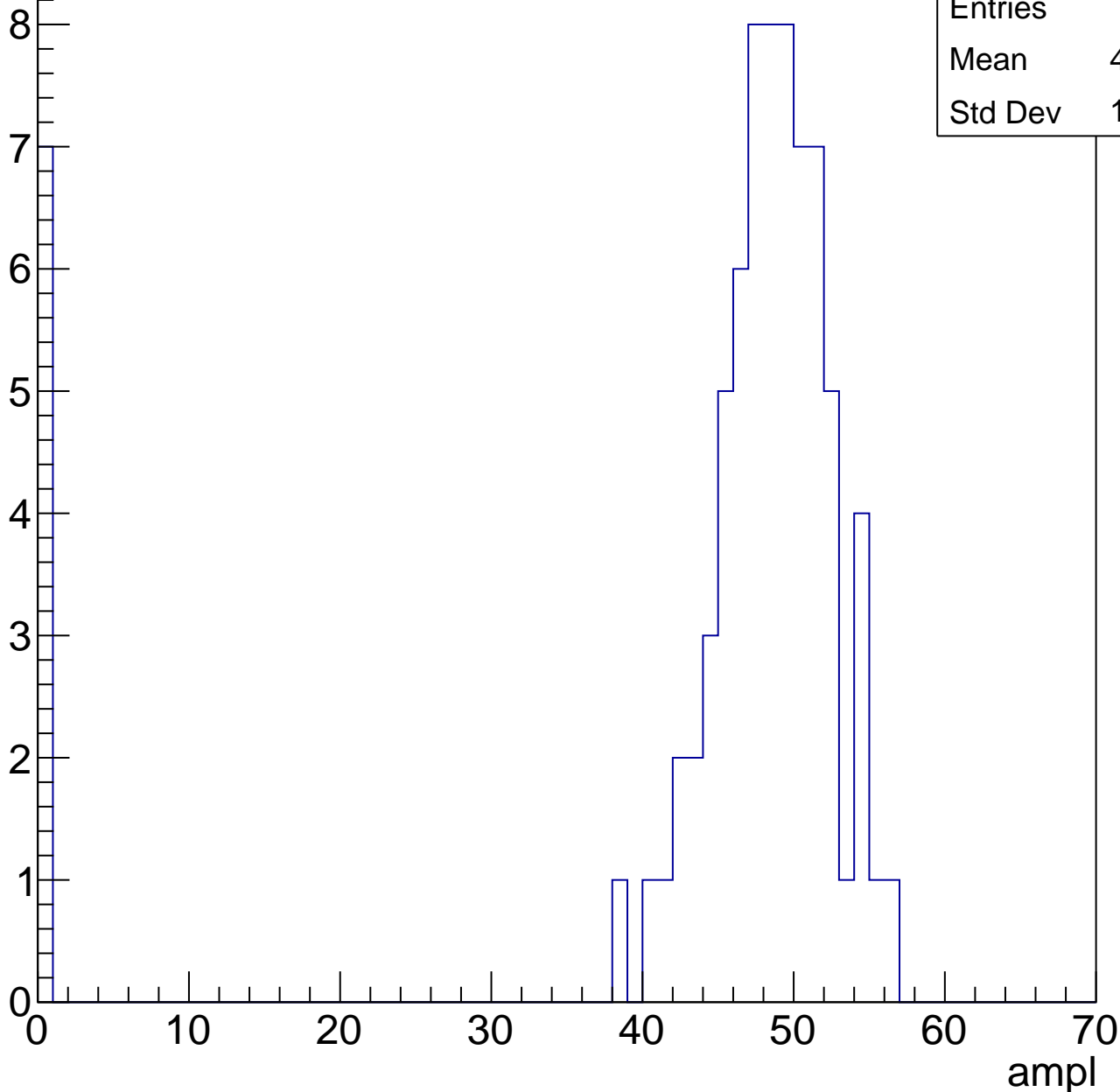


B1L103S, U1-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	43.86
Std Dev	14.19

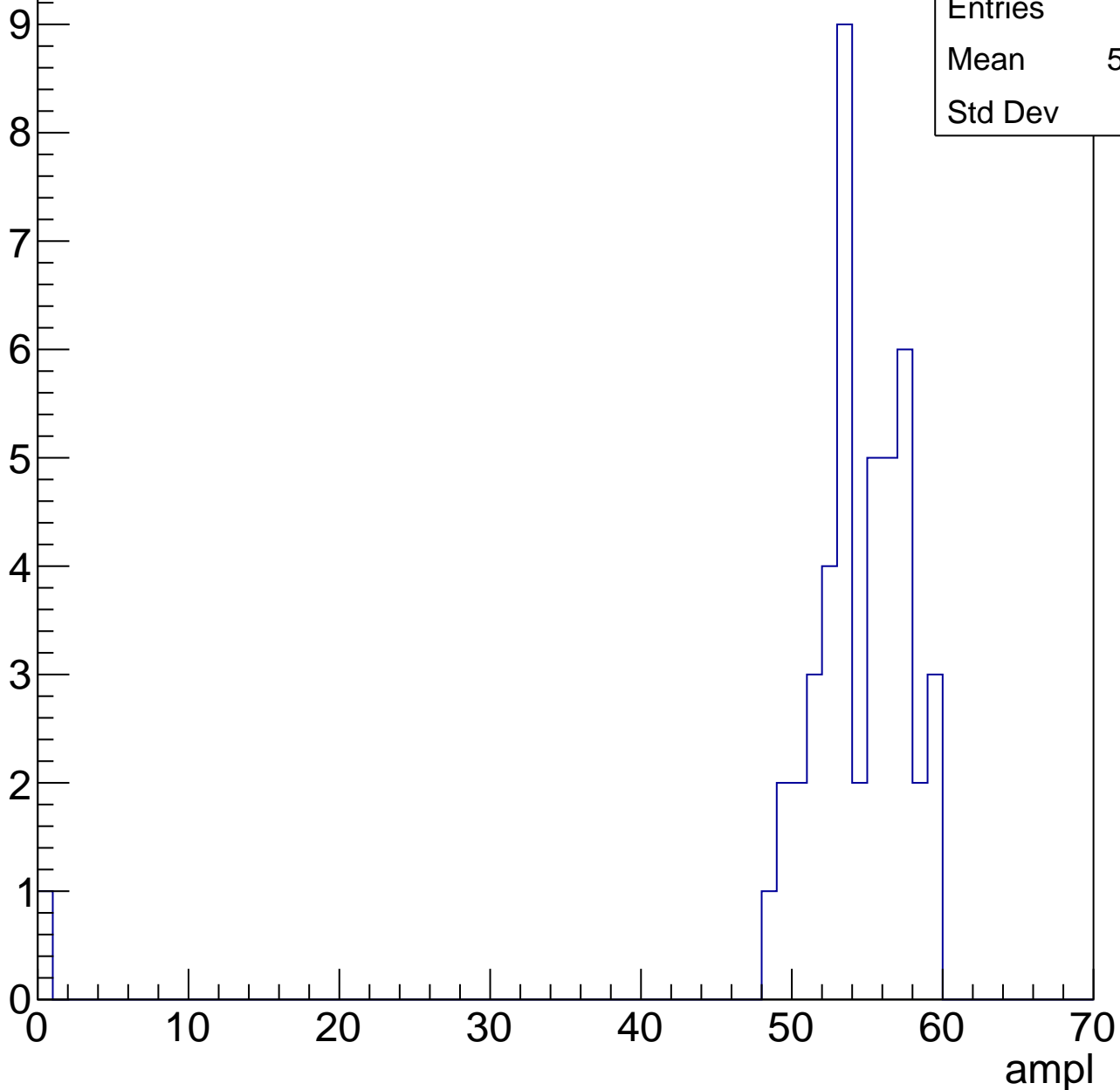


B1L103S, U1-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

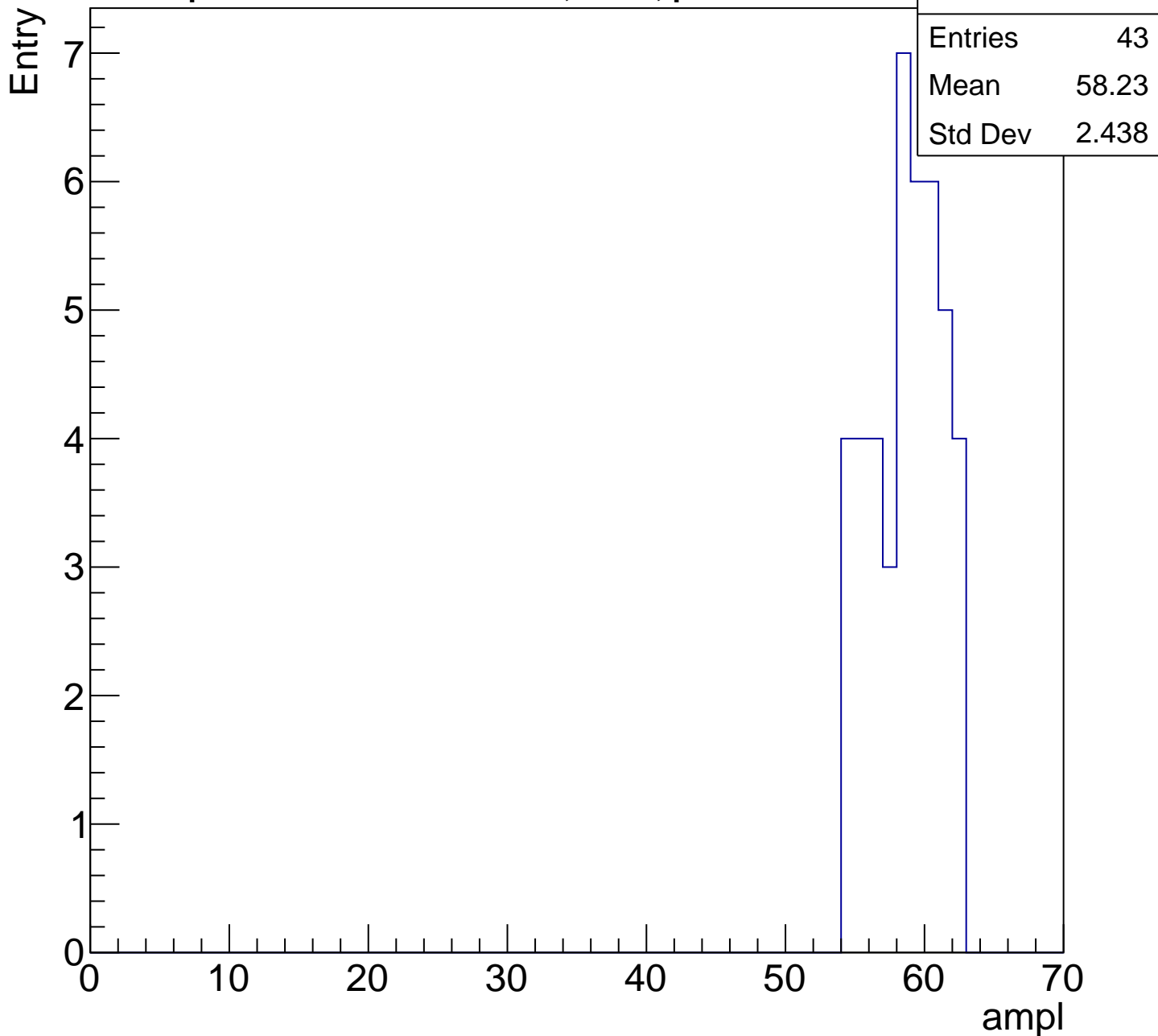
Entry

Entries	45
Mean	52.93
Std Dev	8.46



B1L103S, U1-ch61, adc5

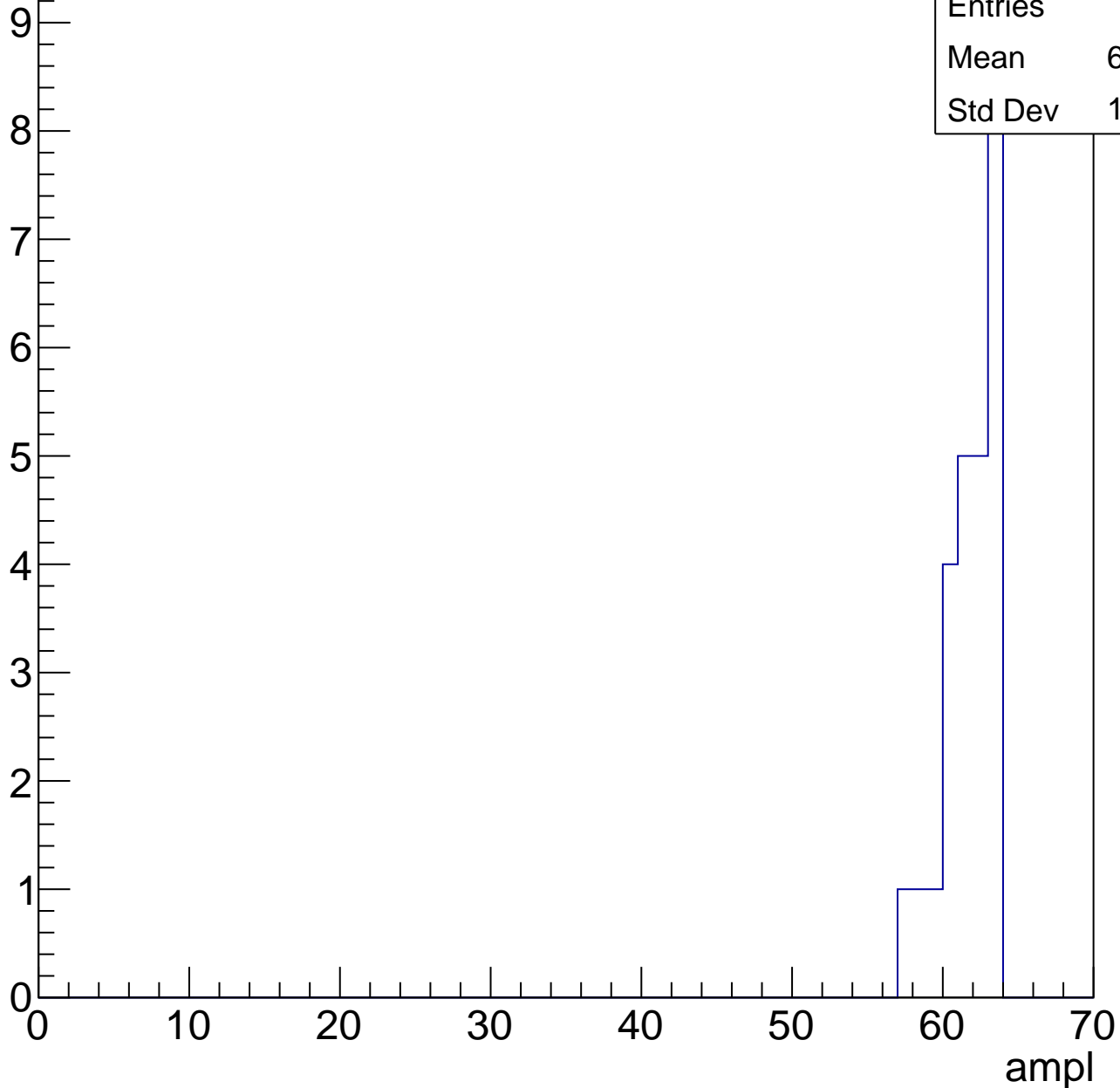
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

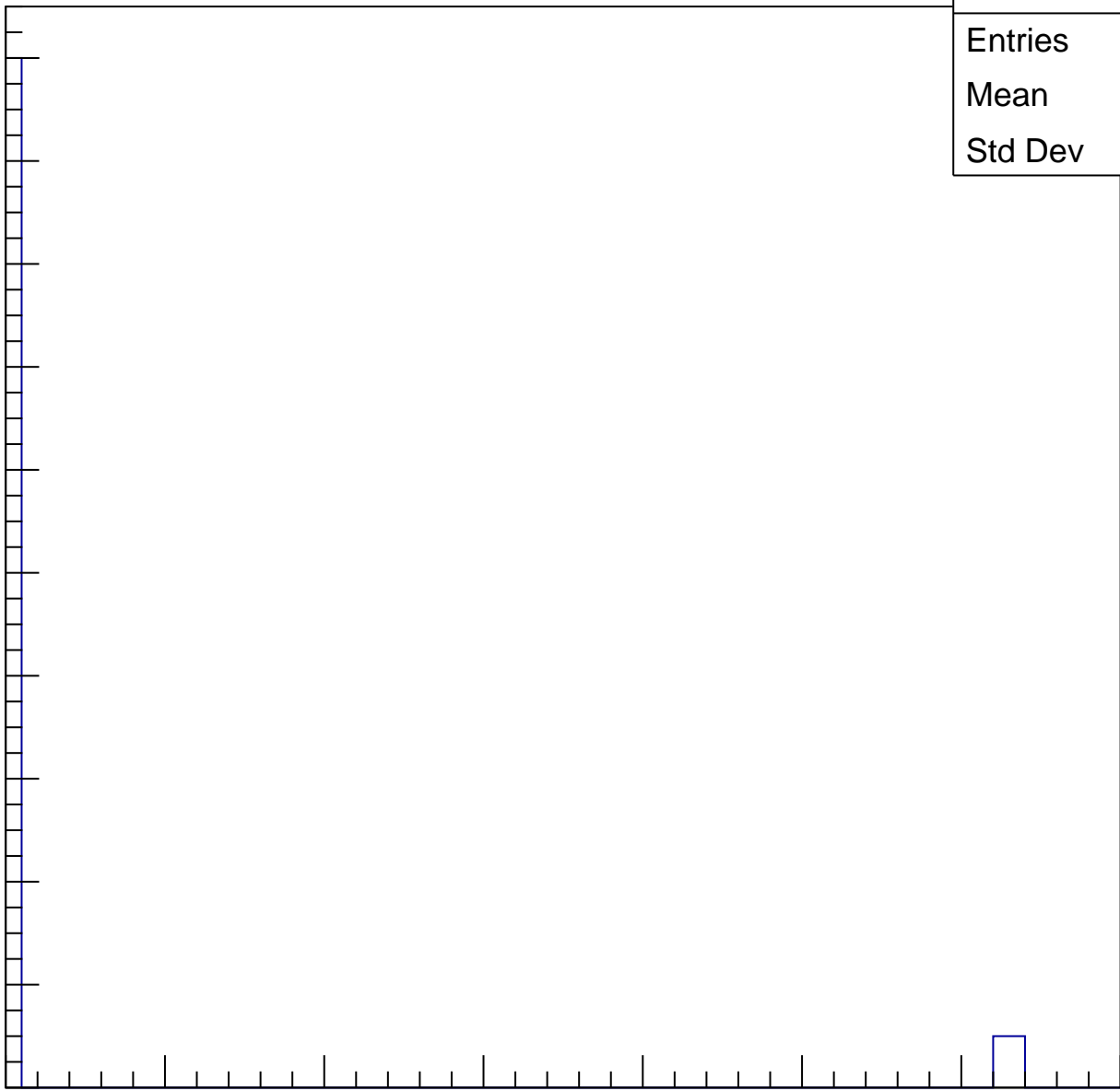
Entries	22
Mean	5.682
Std Dev	17.97

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch62, adc0

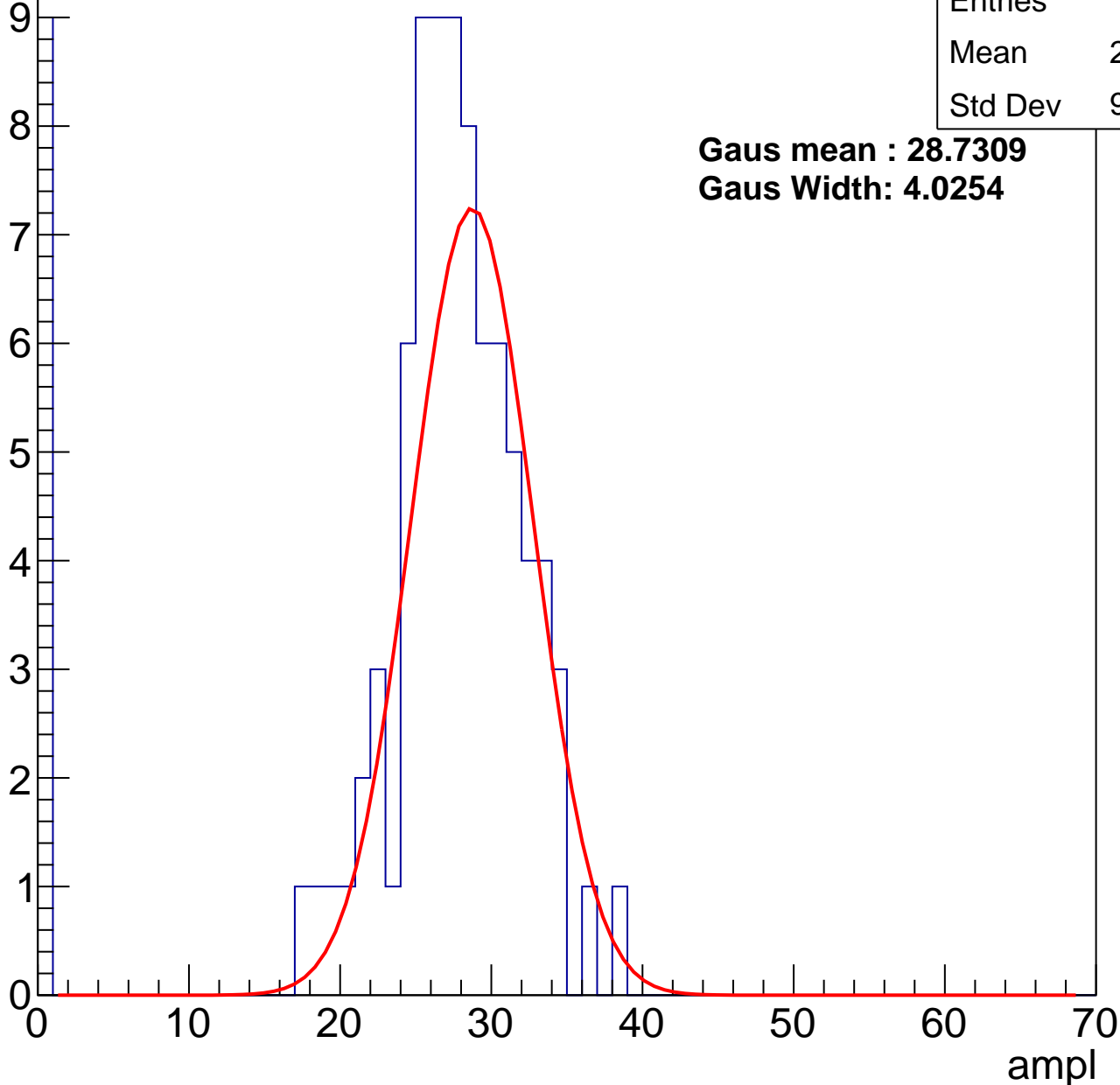
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	90
Mean	24.67
Std Dev	9.059

Gaus mean : 28.7309

Gaus Width: 4.0254



B1L103S, U1-ch62, adc1

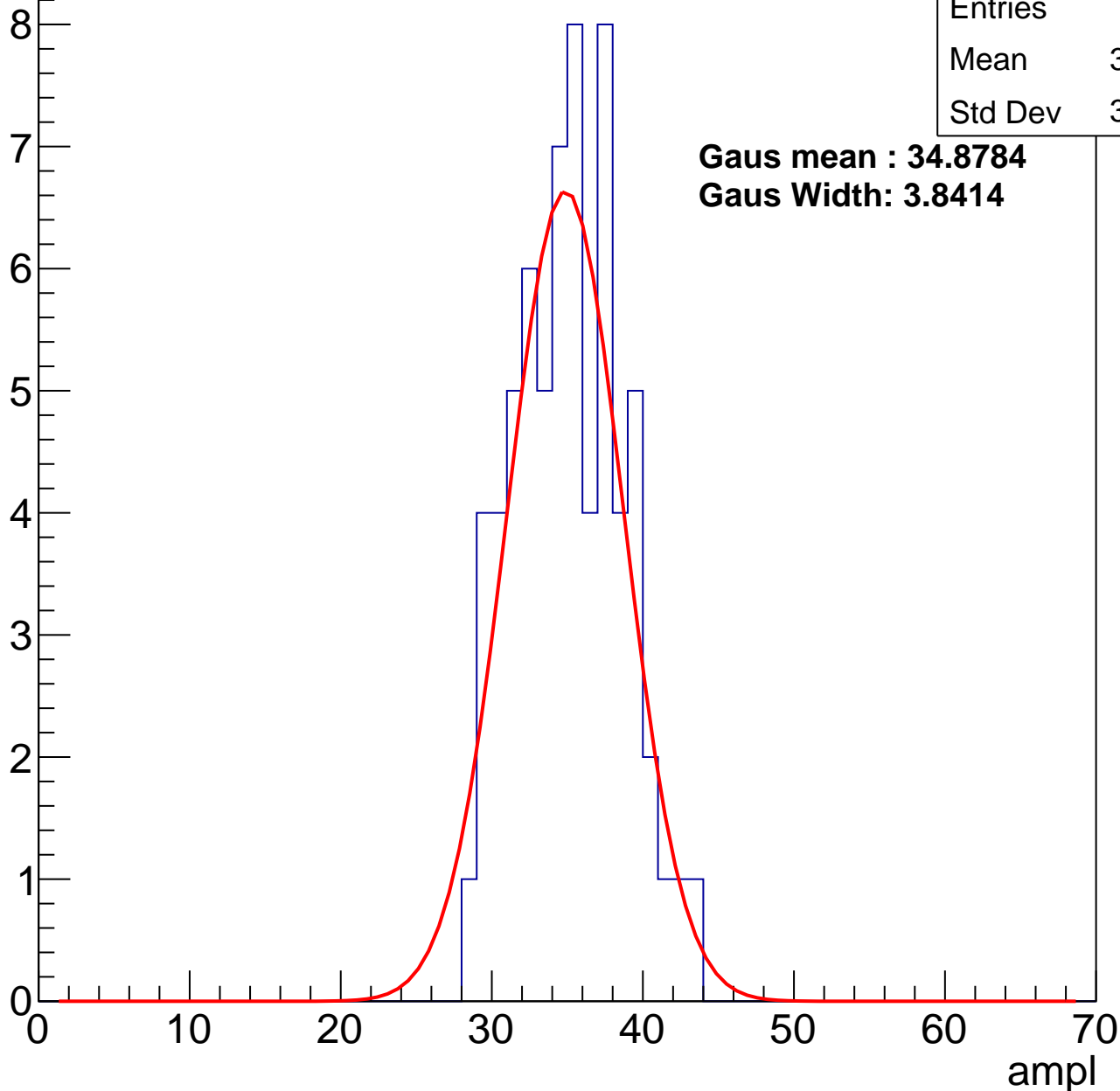
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.65
Std Dev	3.475

Gaus mean : 34.8784

Gaus Width: 3.8414



B1L103S, U1-ch62, adc2

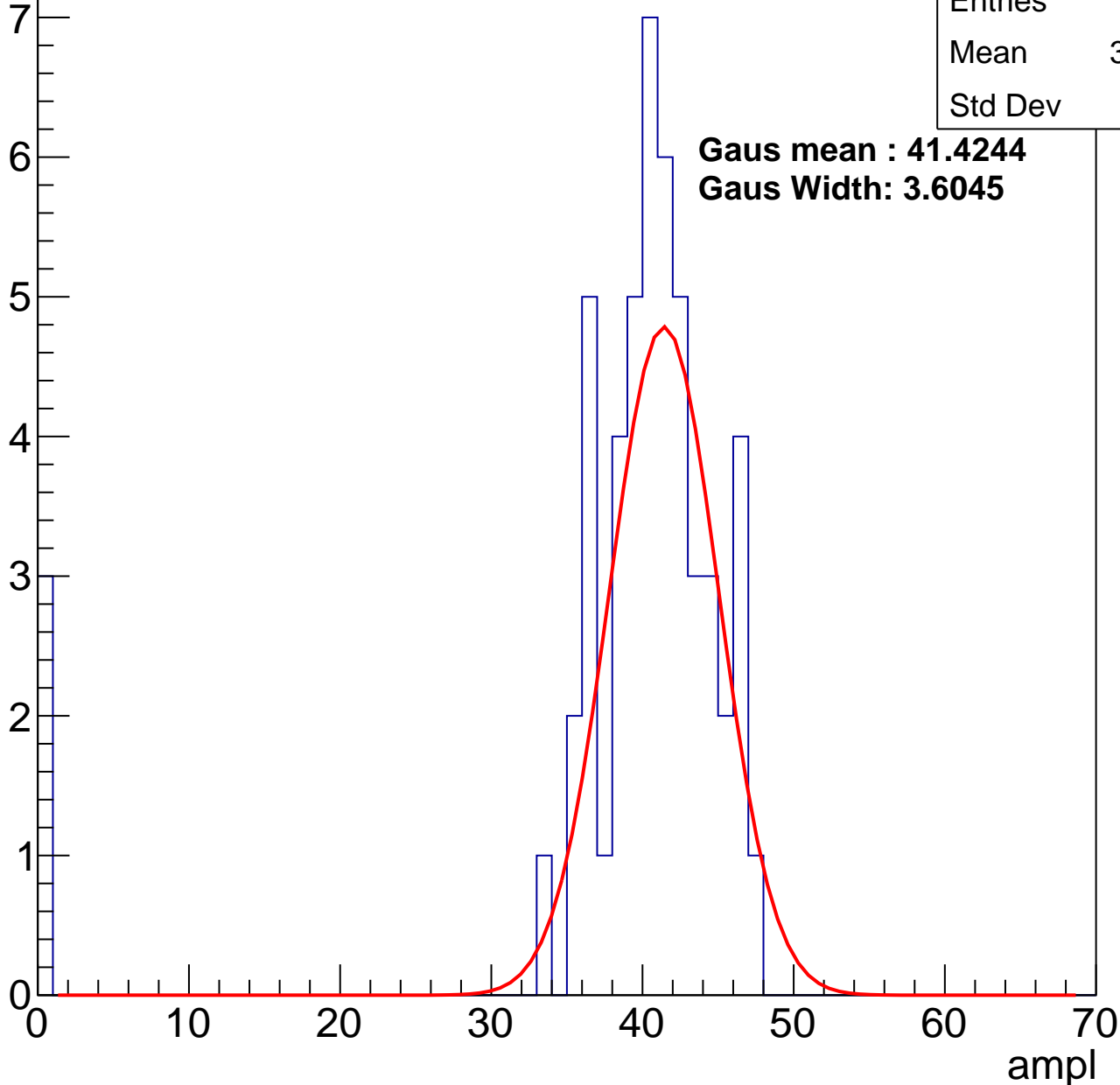
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	38.17
Std Dev	9.98

Gaus mean : 41.4244

Gaus Width: 3.6045



B1L103S, U1-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	39.48
Std Dev	17.04

Entry

10

8

6

4

2

0

0

10

20

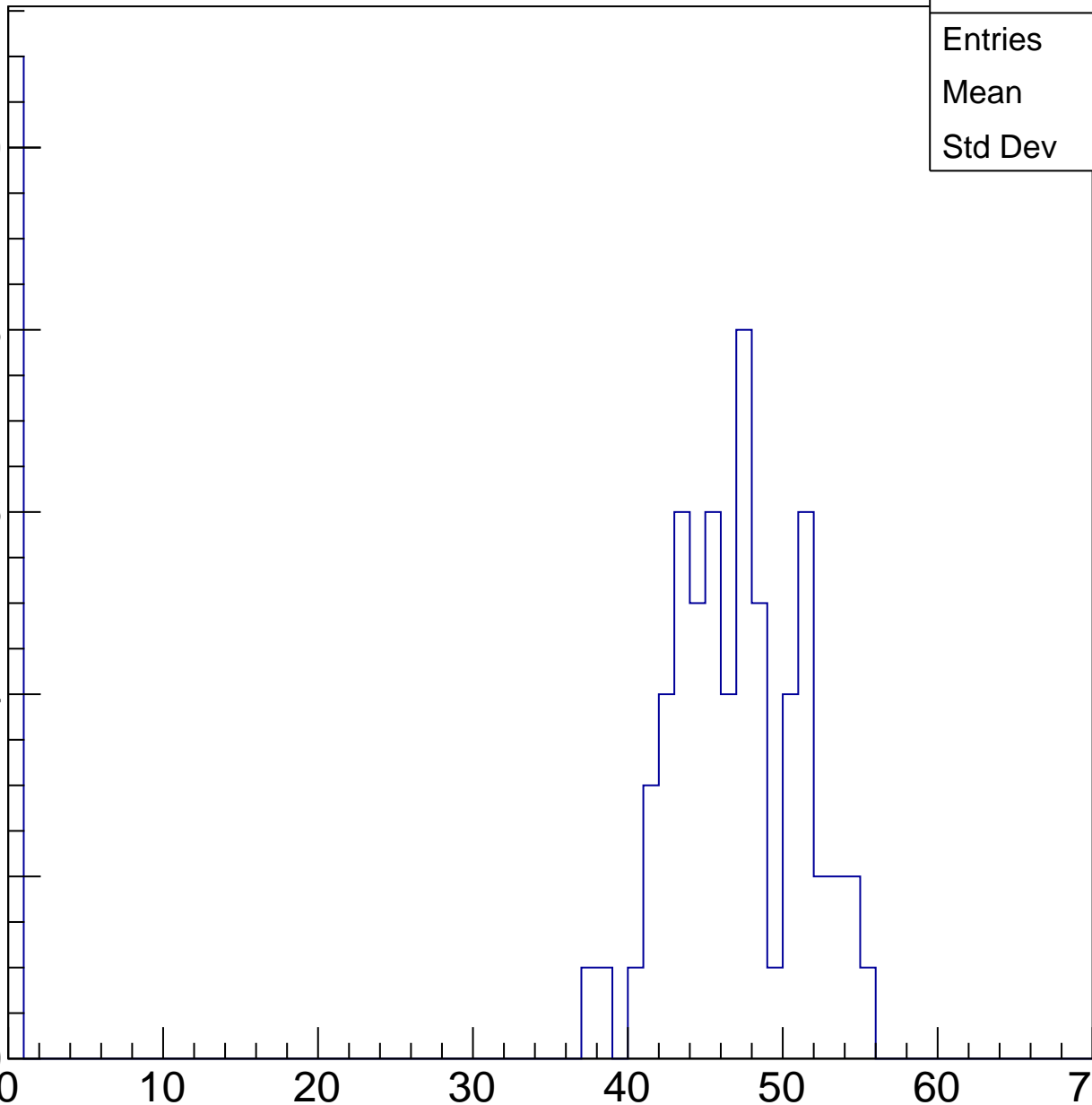
30

40

50

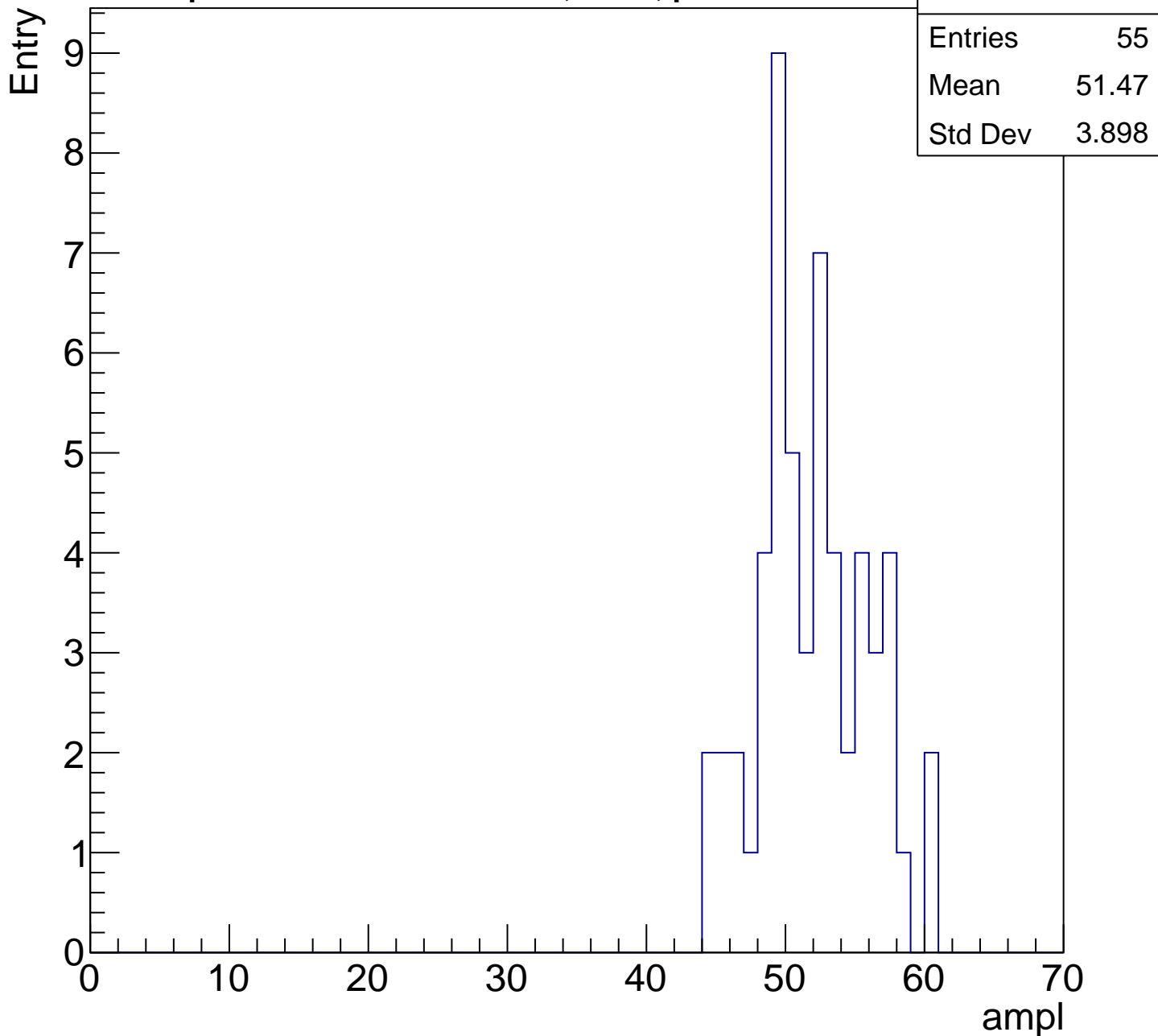
60

ampl



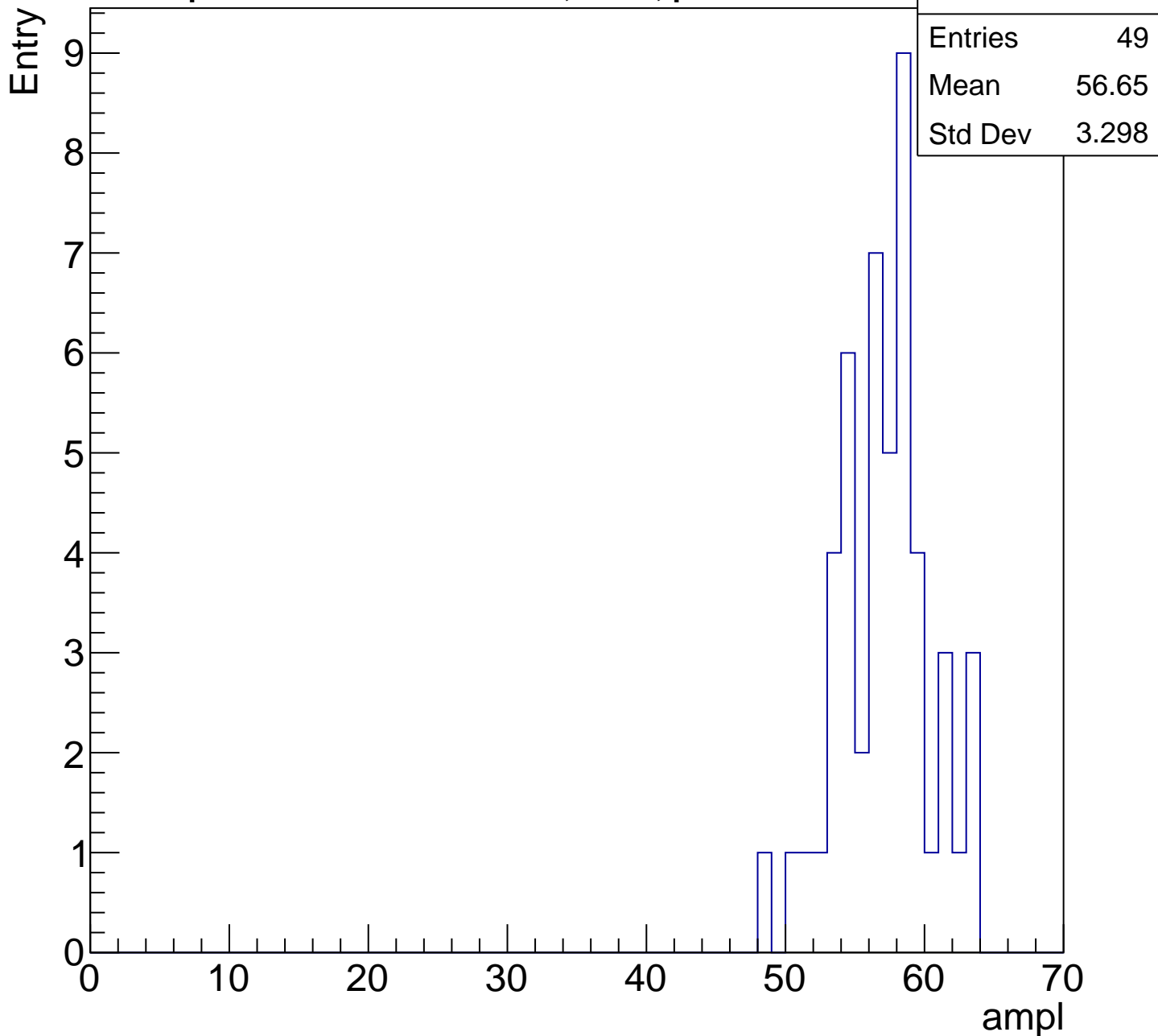
B1L103S, U1-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

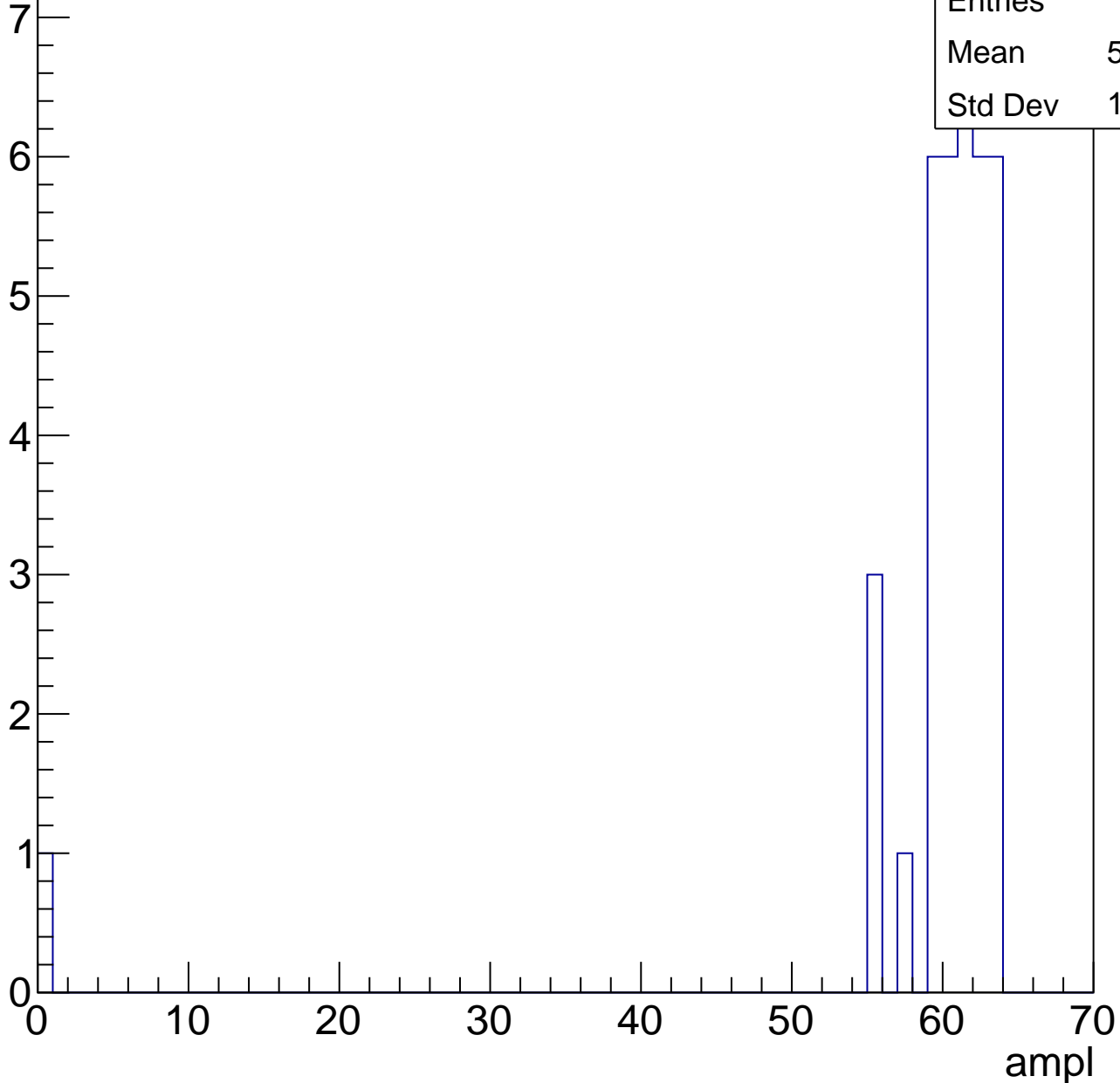


B1L103S, U1-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.69
Std Dev	10.16

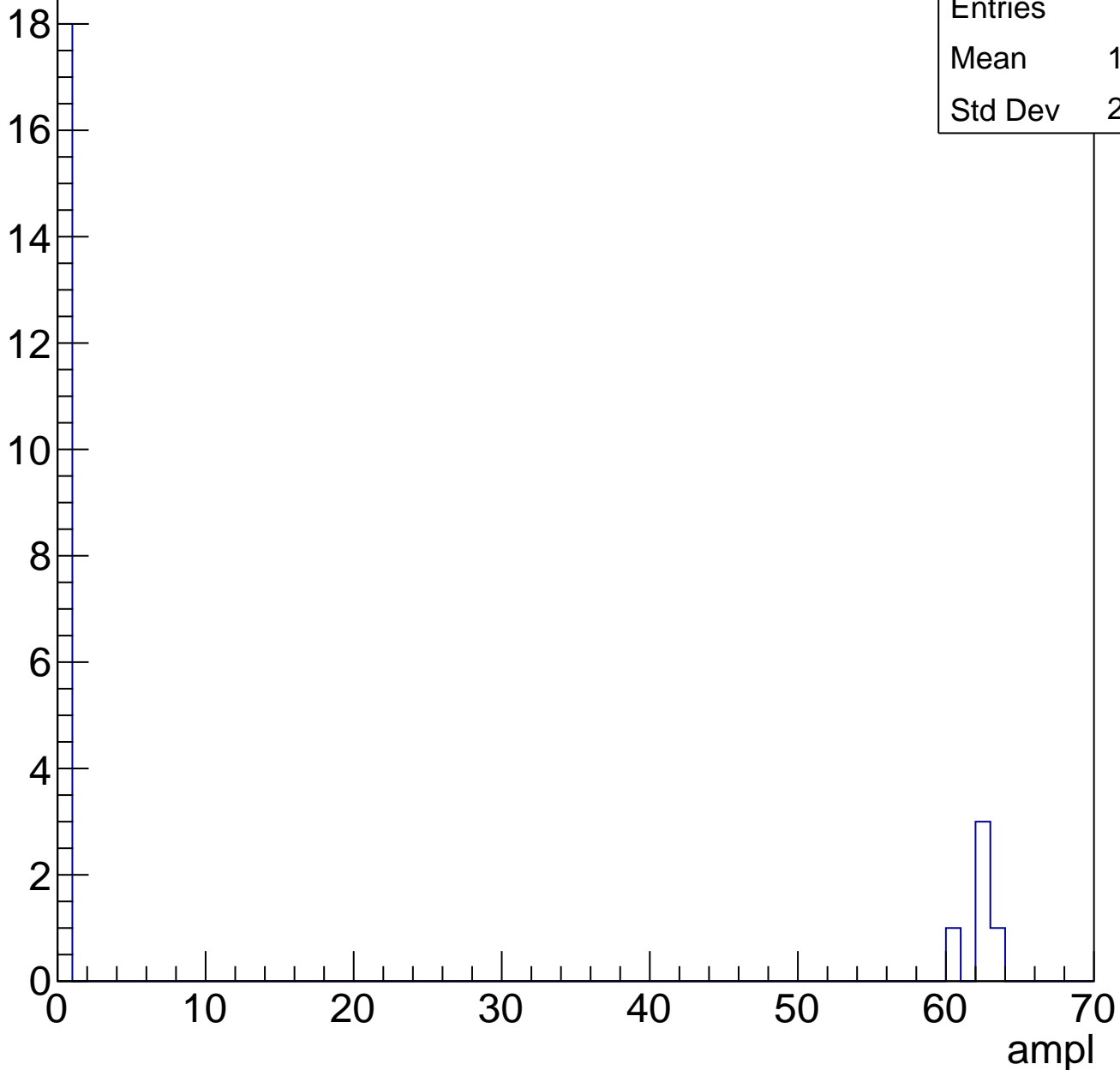


B1L103S, U1-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	13.43
Std Dev	25.49

Entry



B1L103S, U1-ch63, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	25.52
Std Dev	10.22

Gaus mean : 29.9342

Gaus Width: 3.7860

Entry

10

8

6

4

2

0

0

10

20

30

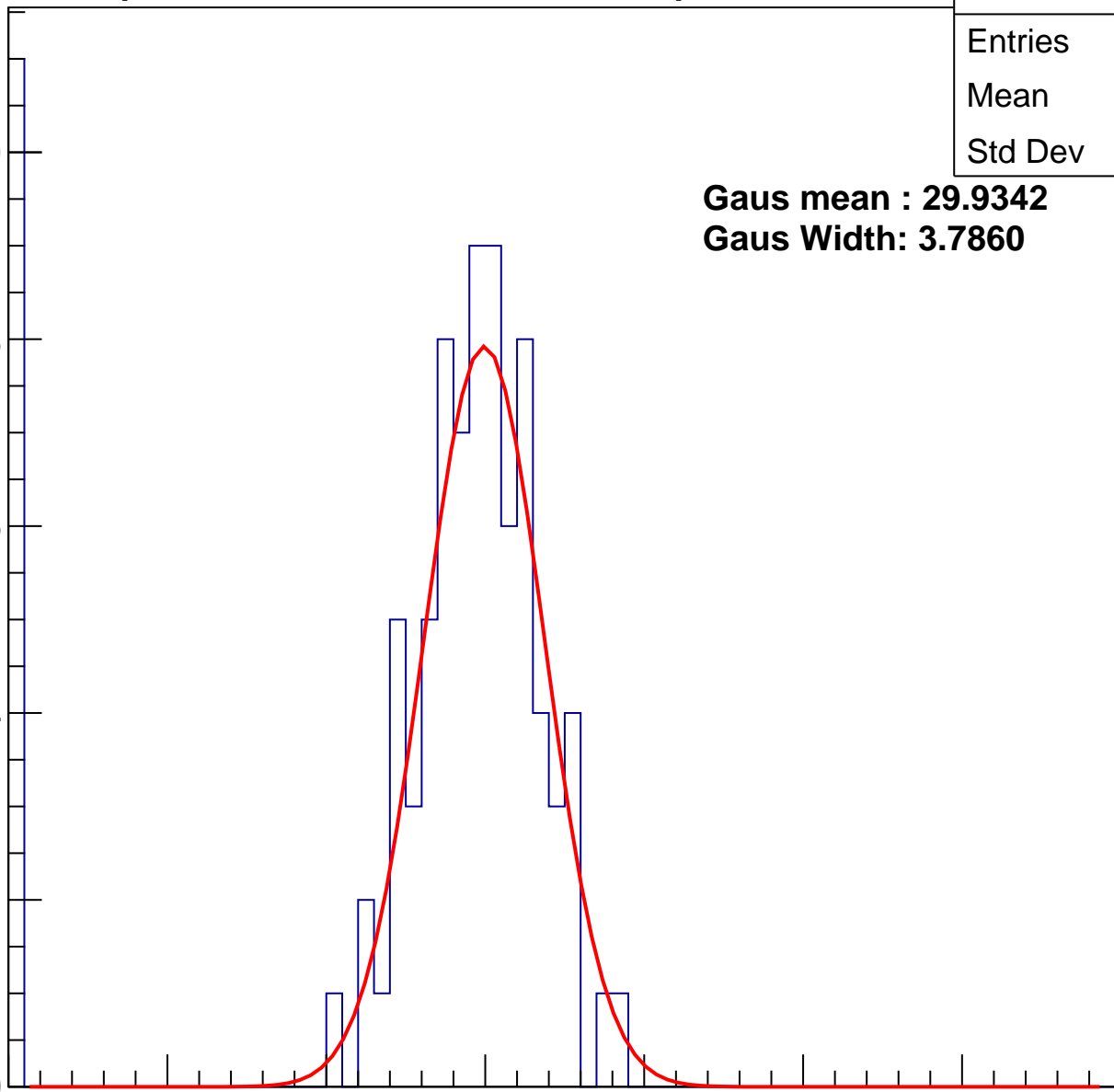
40

50

60

70

ampl



B1L103S, U1-ch63, adc1

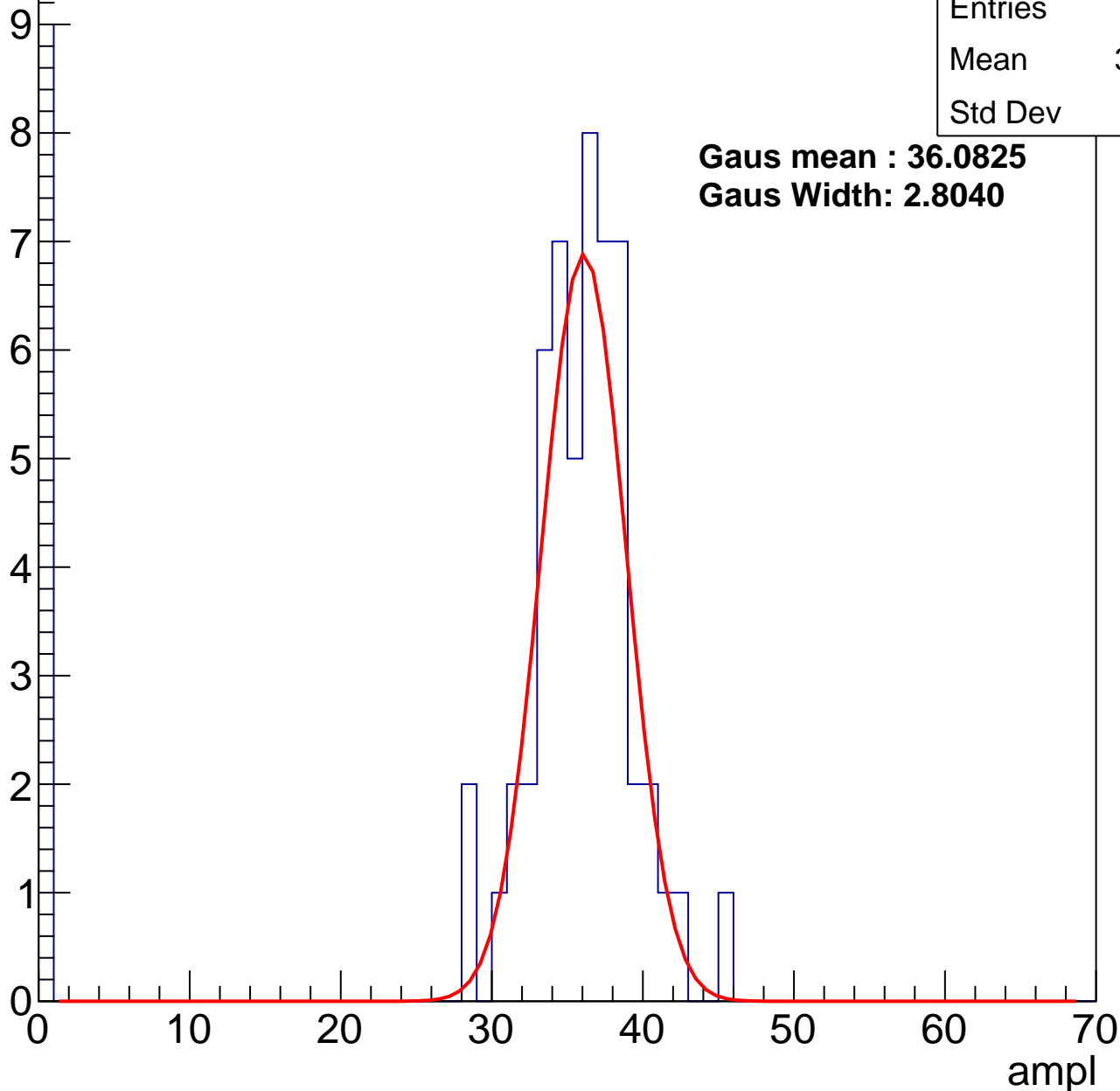
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	30.51
Std Dev	12.8

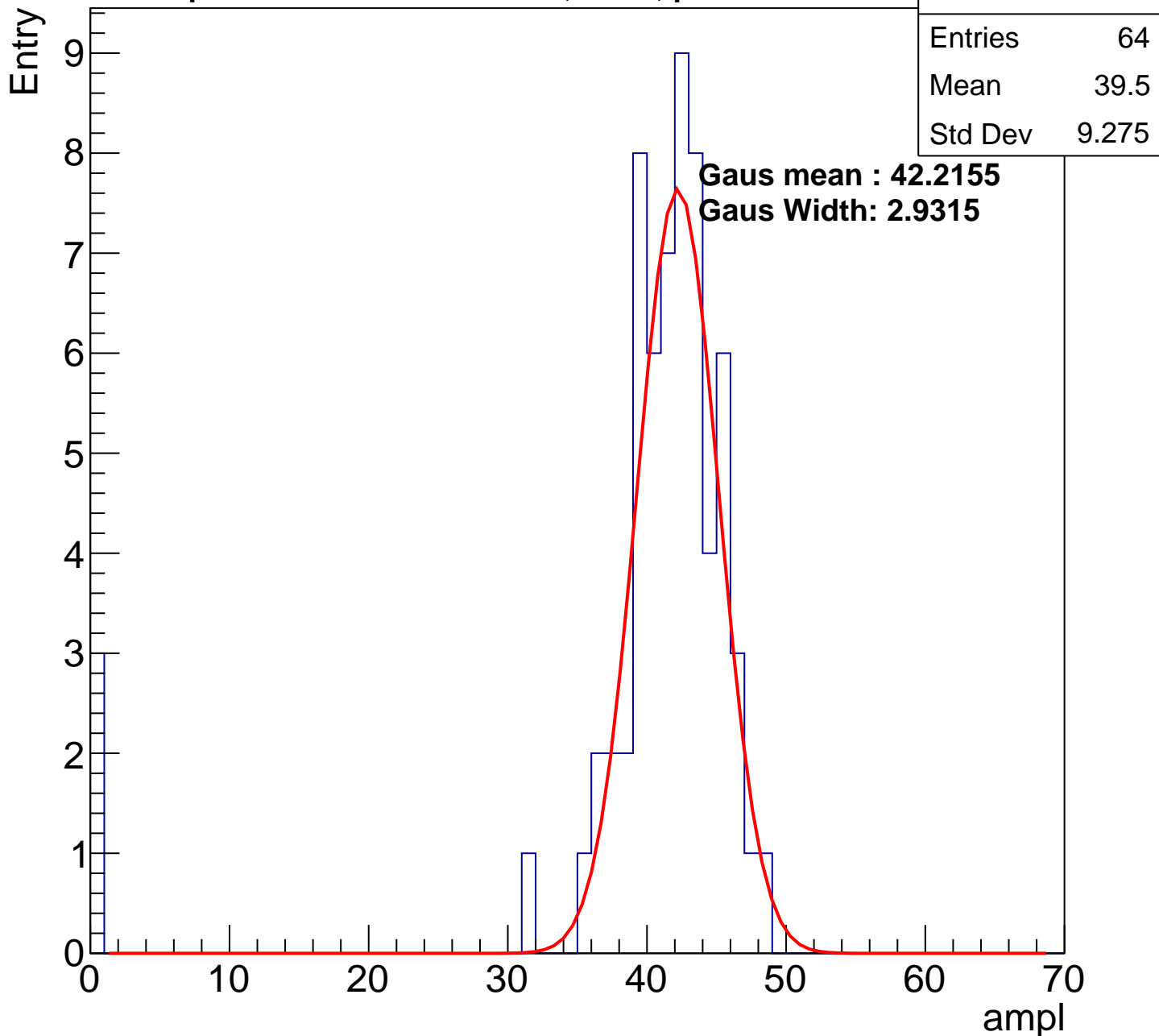
Gaus mean : 36.0825

Gaus Width: 2.8040



B1L103S, U1-ch63, adc2

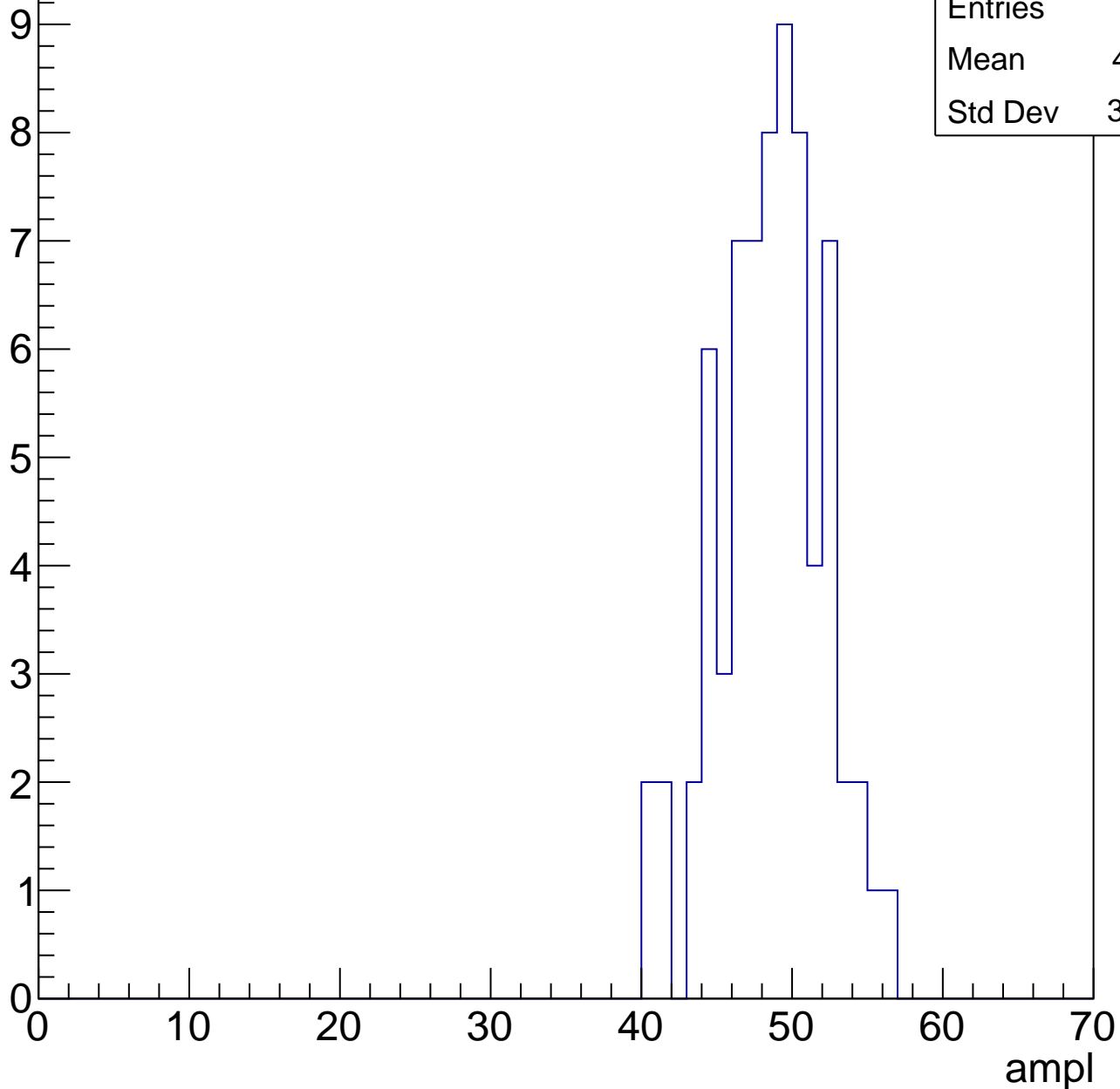
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



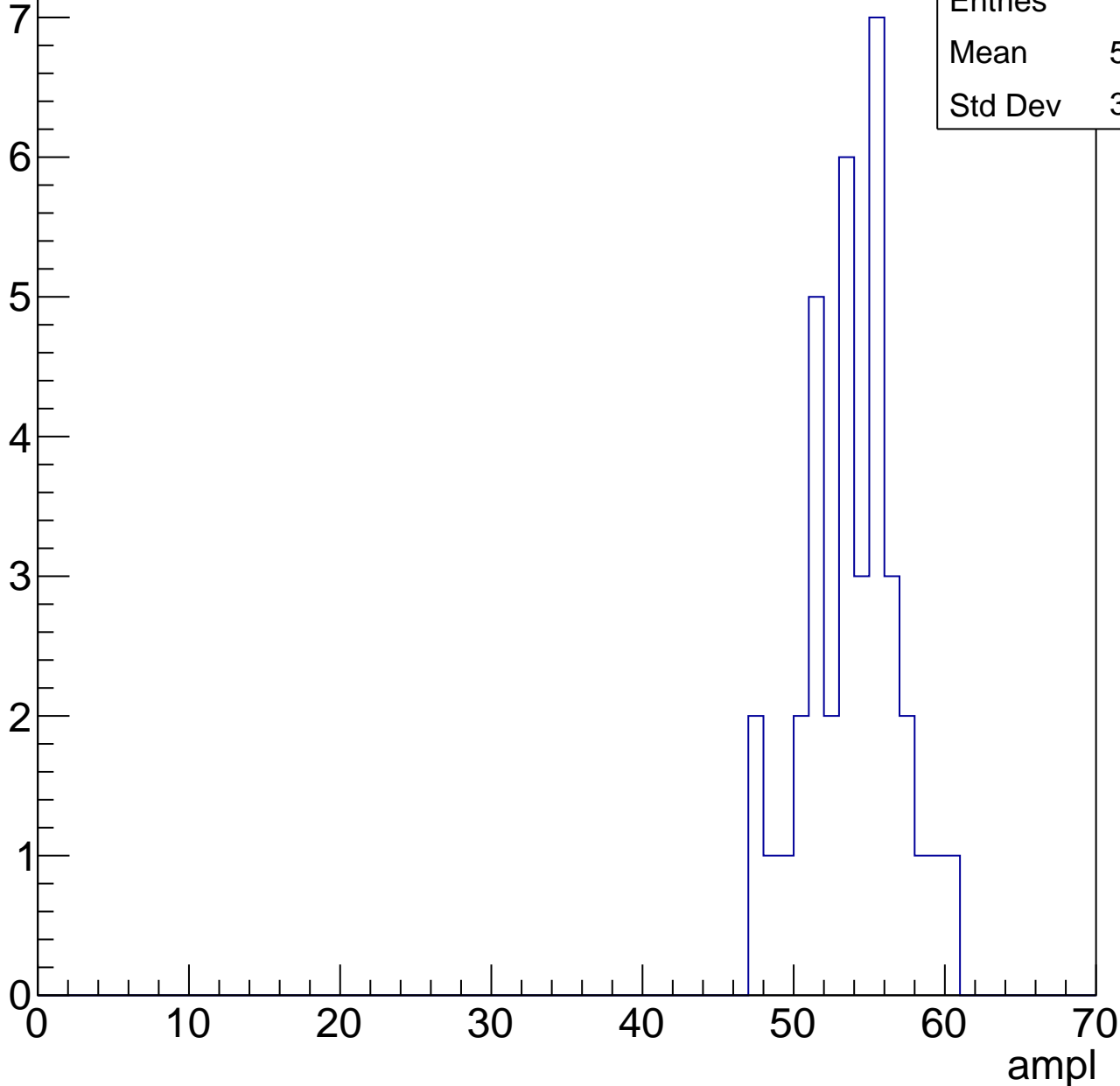
Entries	71
Mean	48.11
Std Dev	3.474

B1L103S, U1-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

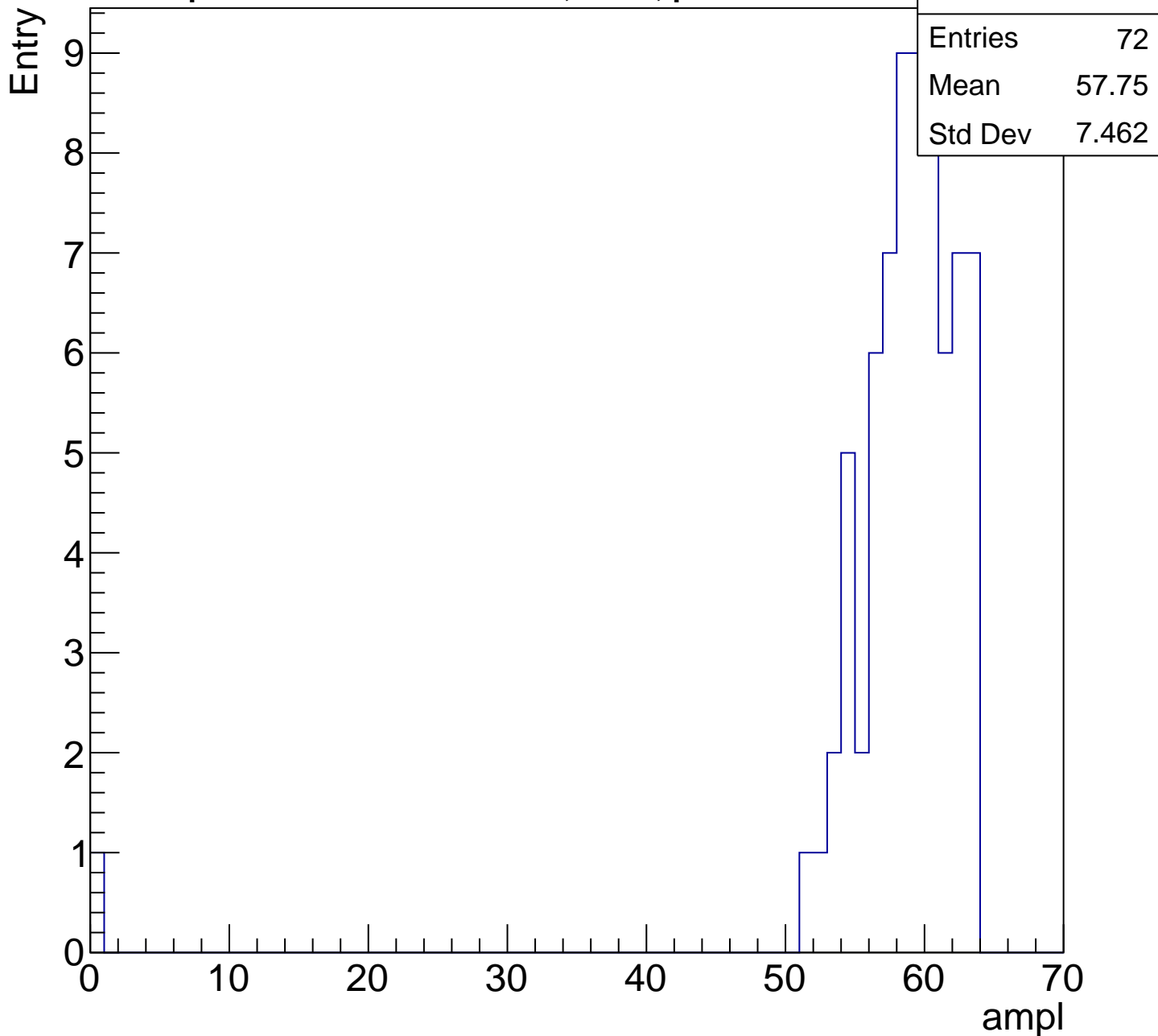
Entry

Entries	37
Mean	53.35
Std Dev	3.069



B1L103S, U1-ch63, adc5

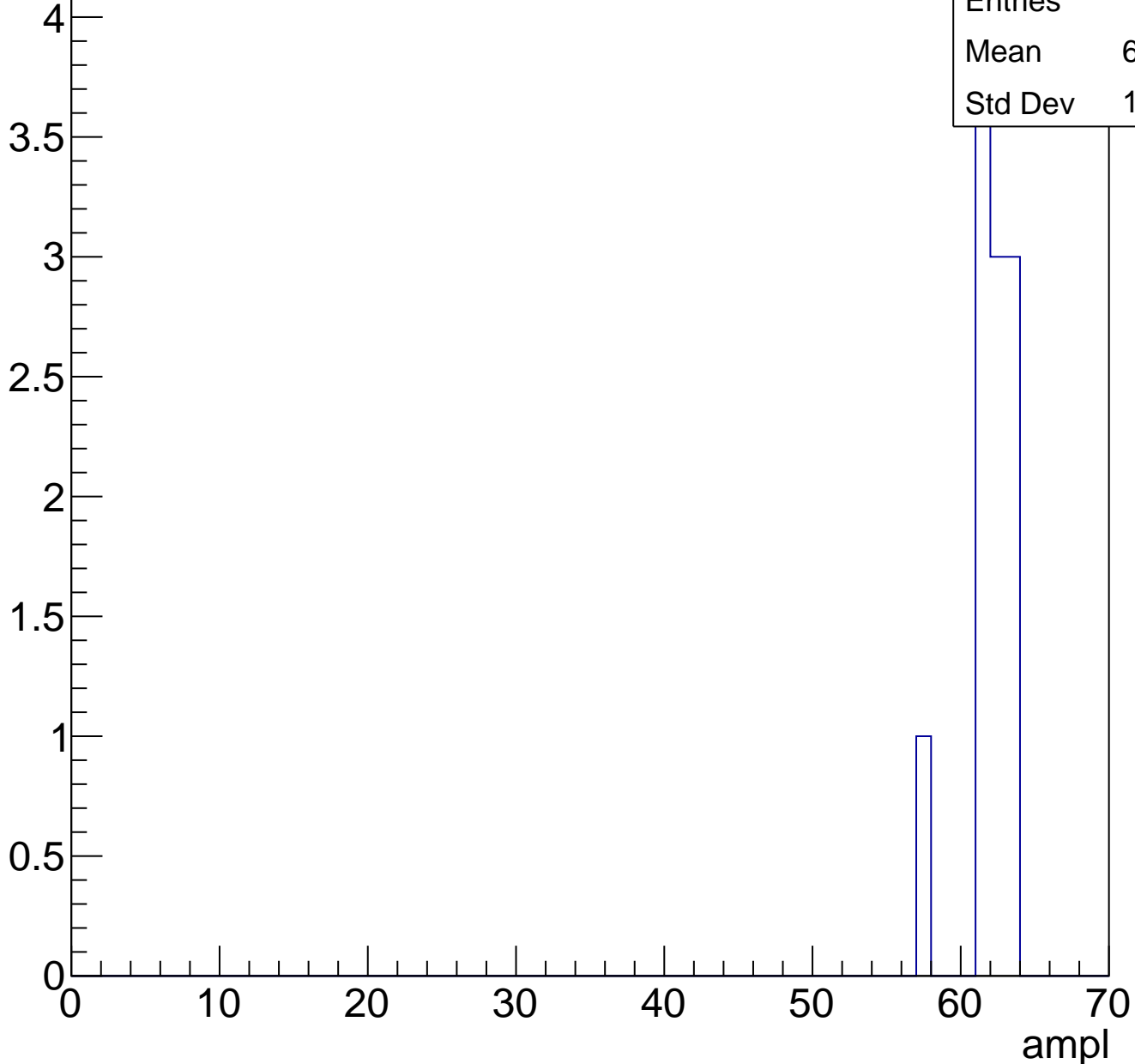
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	11
Mean	61.45
Std Dev	1.616

B1L103S, U1-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

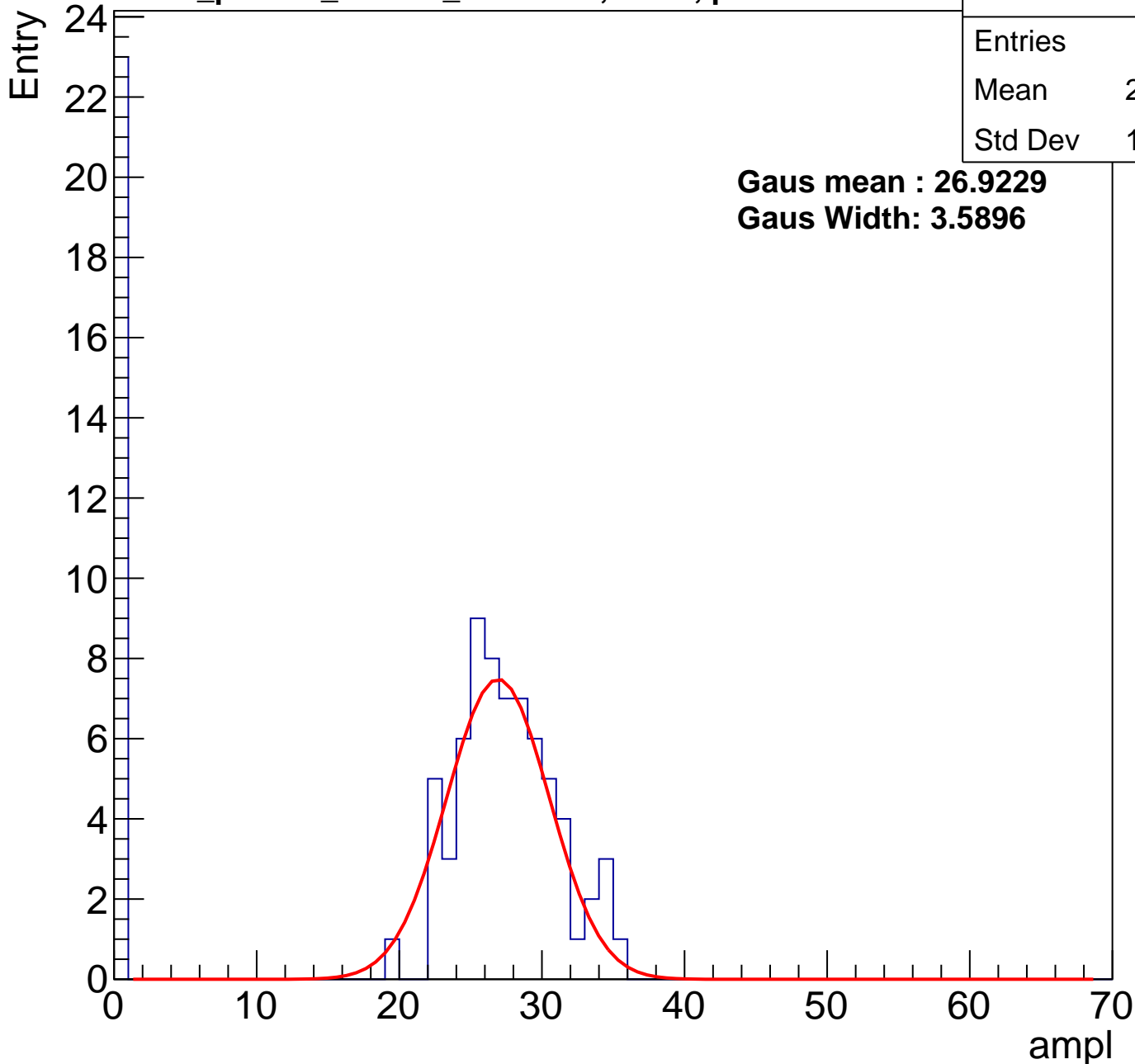
B1L103S, U1-ch64, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	20.25
Std Dev	12.14

Gaus mean : 26.9229

Gaus Width: 3.5896



B1L103S, U1-ch64, adc1

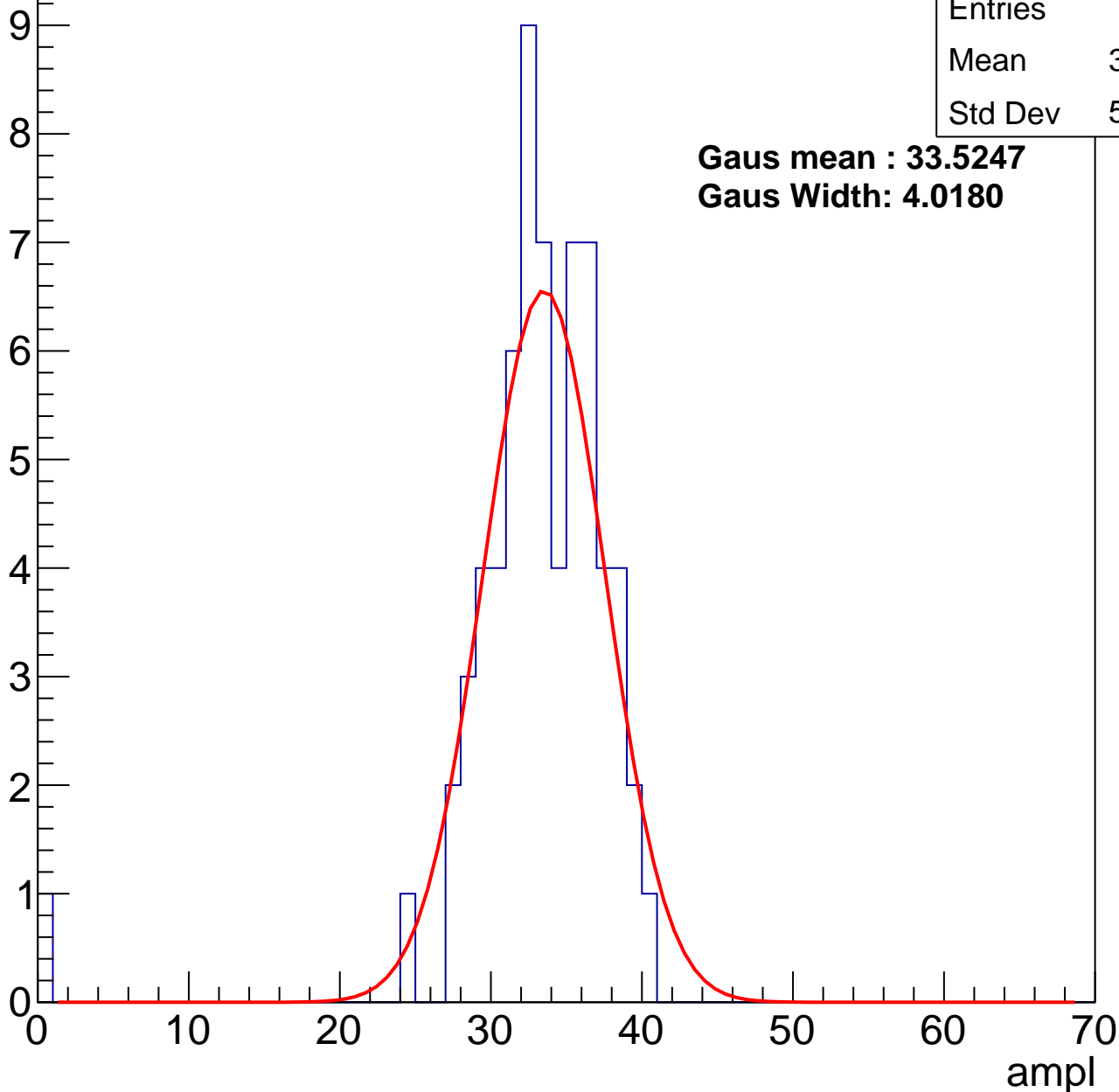
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.64
Std Dev	5.253

Gaus mean : 33.5247

Gaus Width: 4.0180



B1L103S, U1-ch64, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	33.11
Std Dev	14.95

Gaus mean : 40.1692

Gaus Width: 3.3924

Entry

10

8

6

4

2

0

0

10

20

30

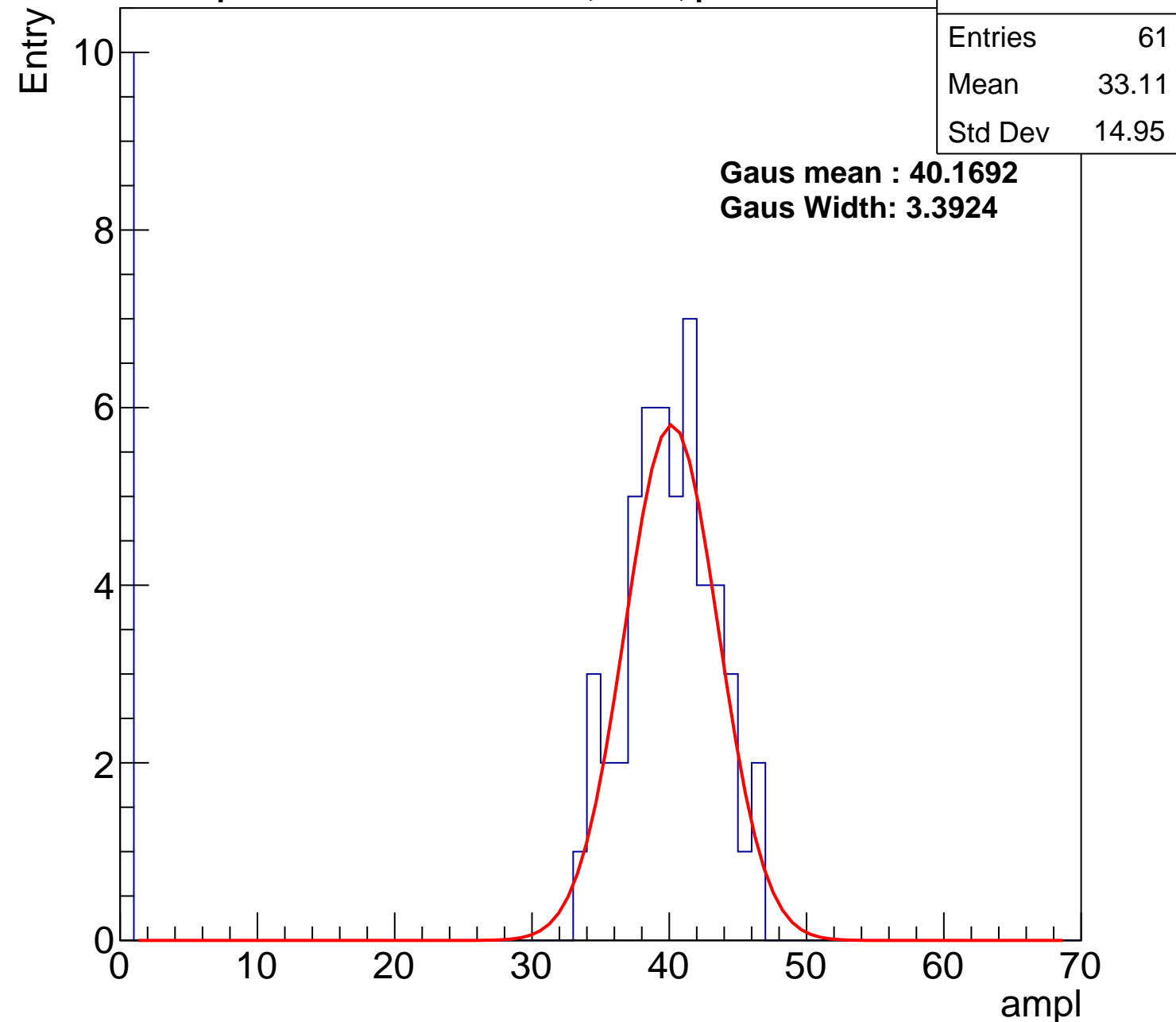
40

50

60

70

ampl

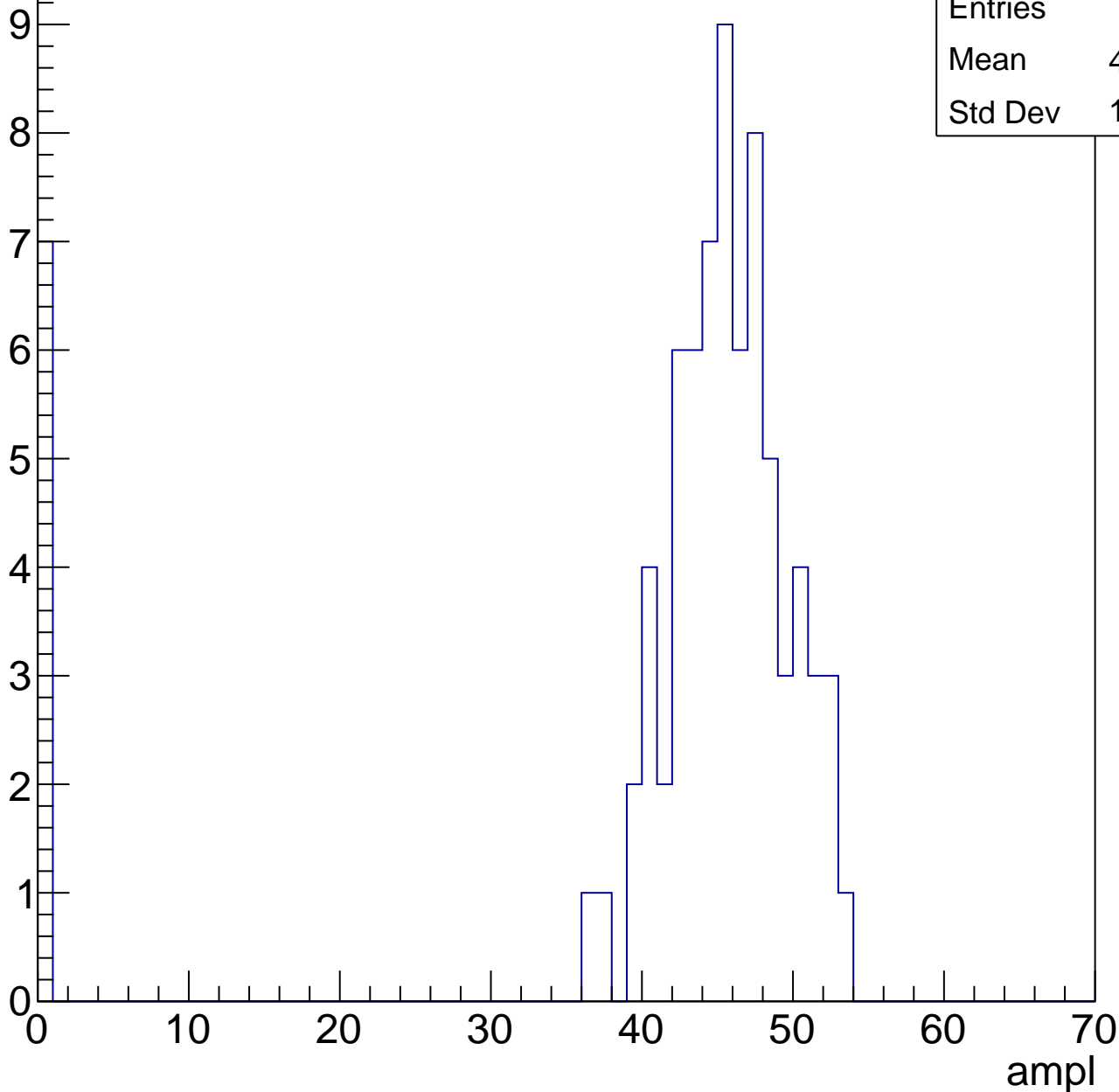


B1L103S, U1-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	41.24
Std Dev	13.43

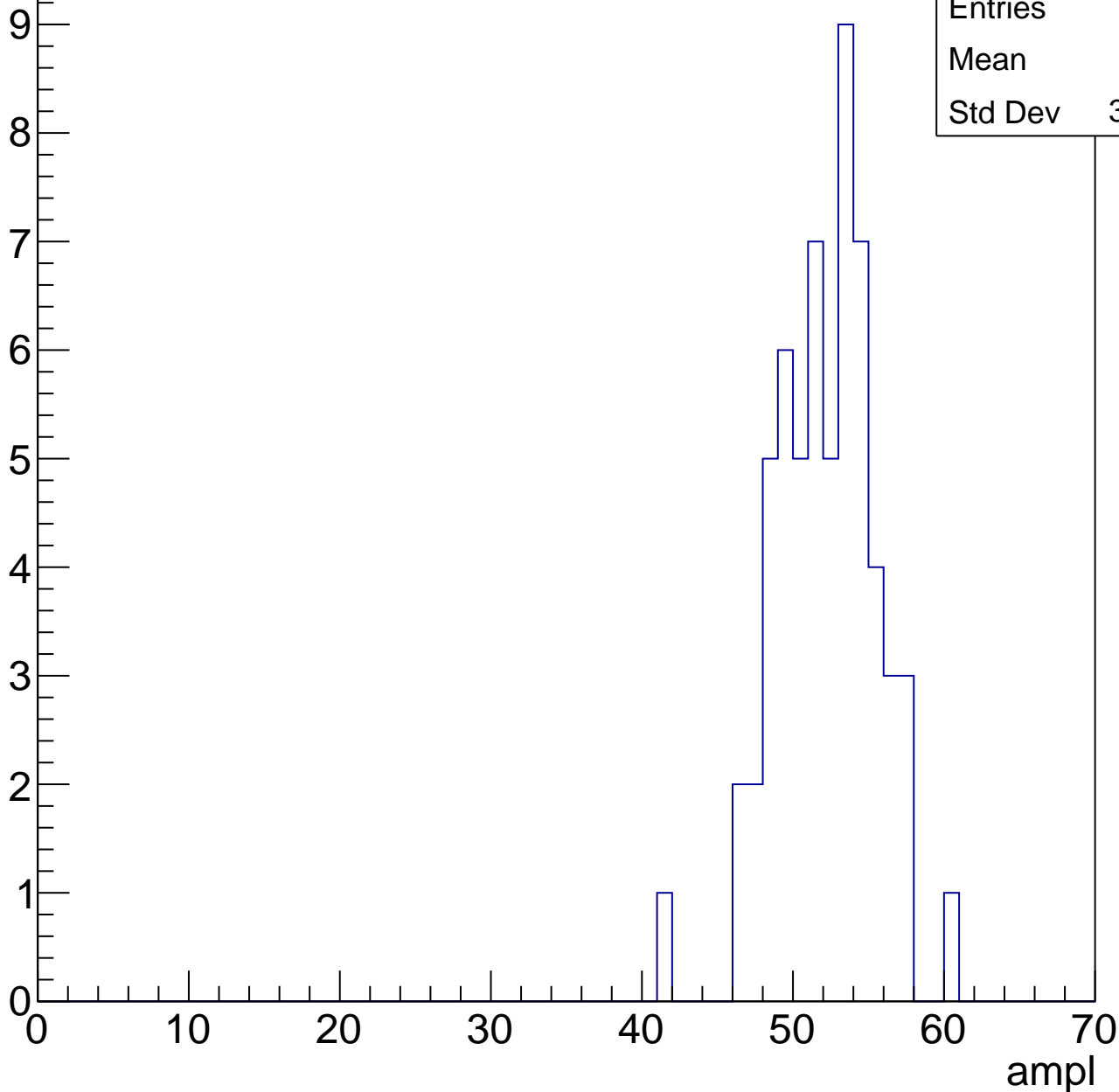


B1L103S, U1-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	51.7
Std Dev	3.313

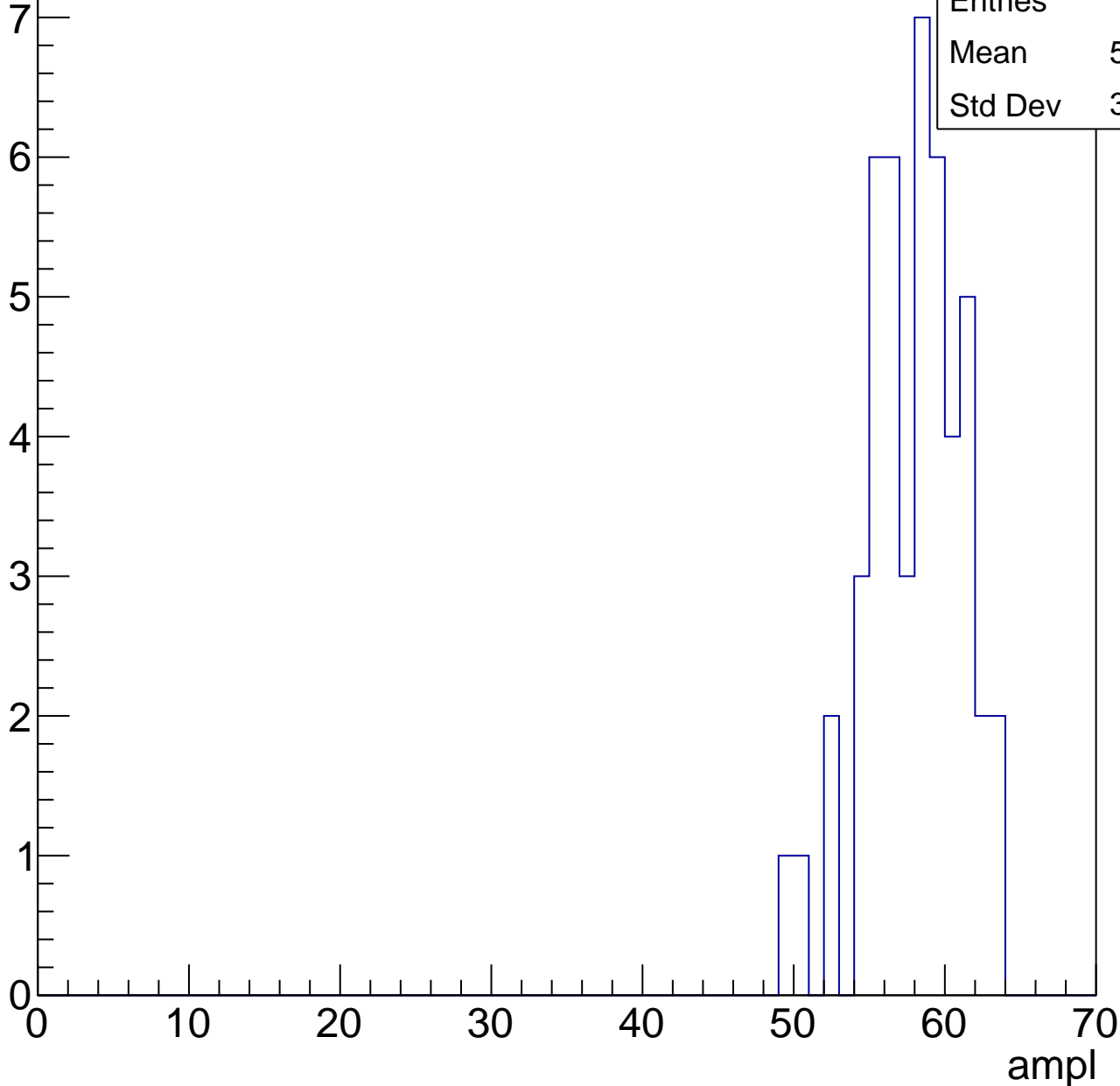


B1L103S, U1-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	57.44
Std Dev	3.162

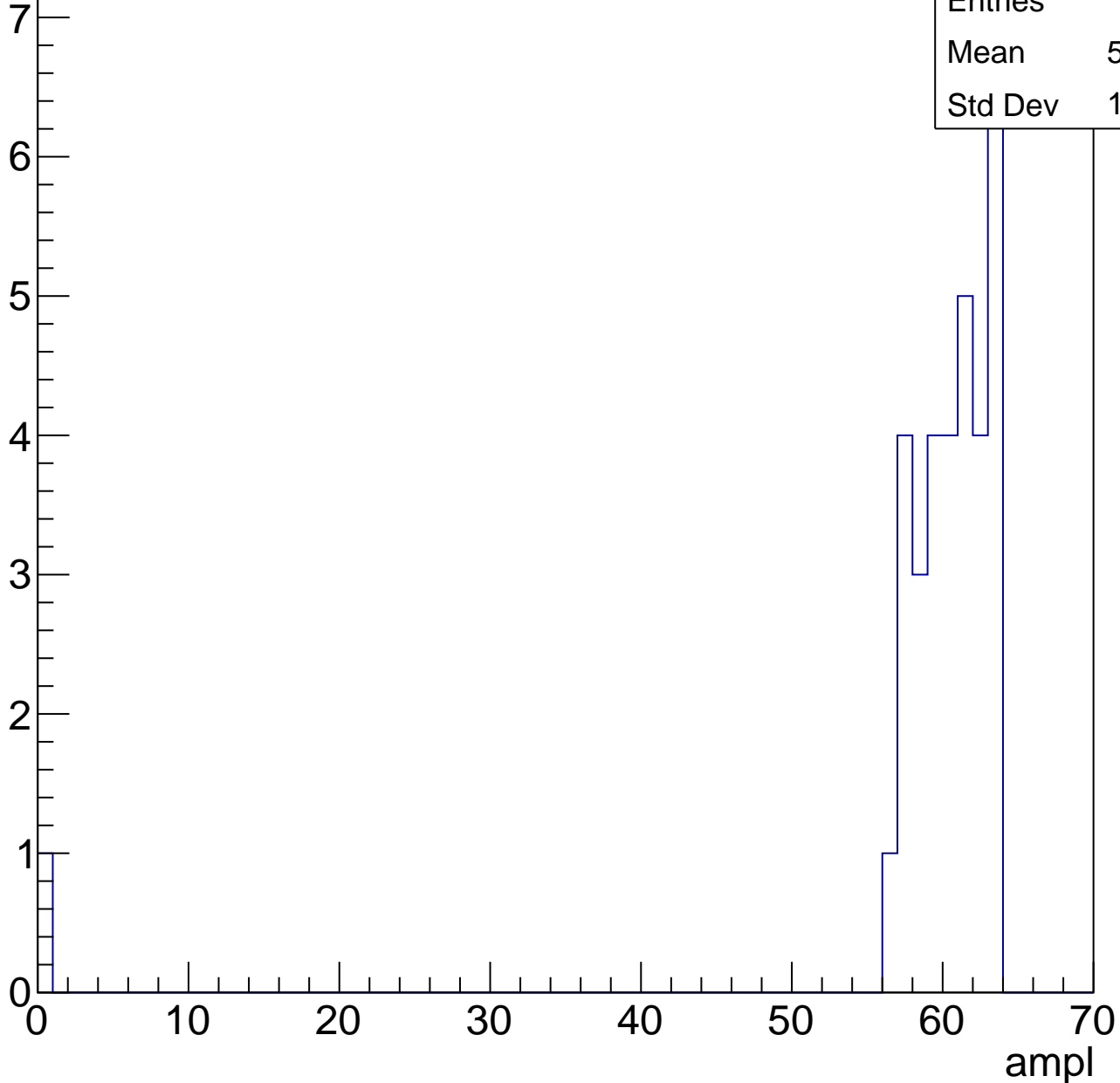


B1L103S, U1-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.42
Std Dev	10.55

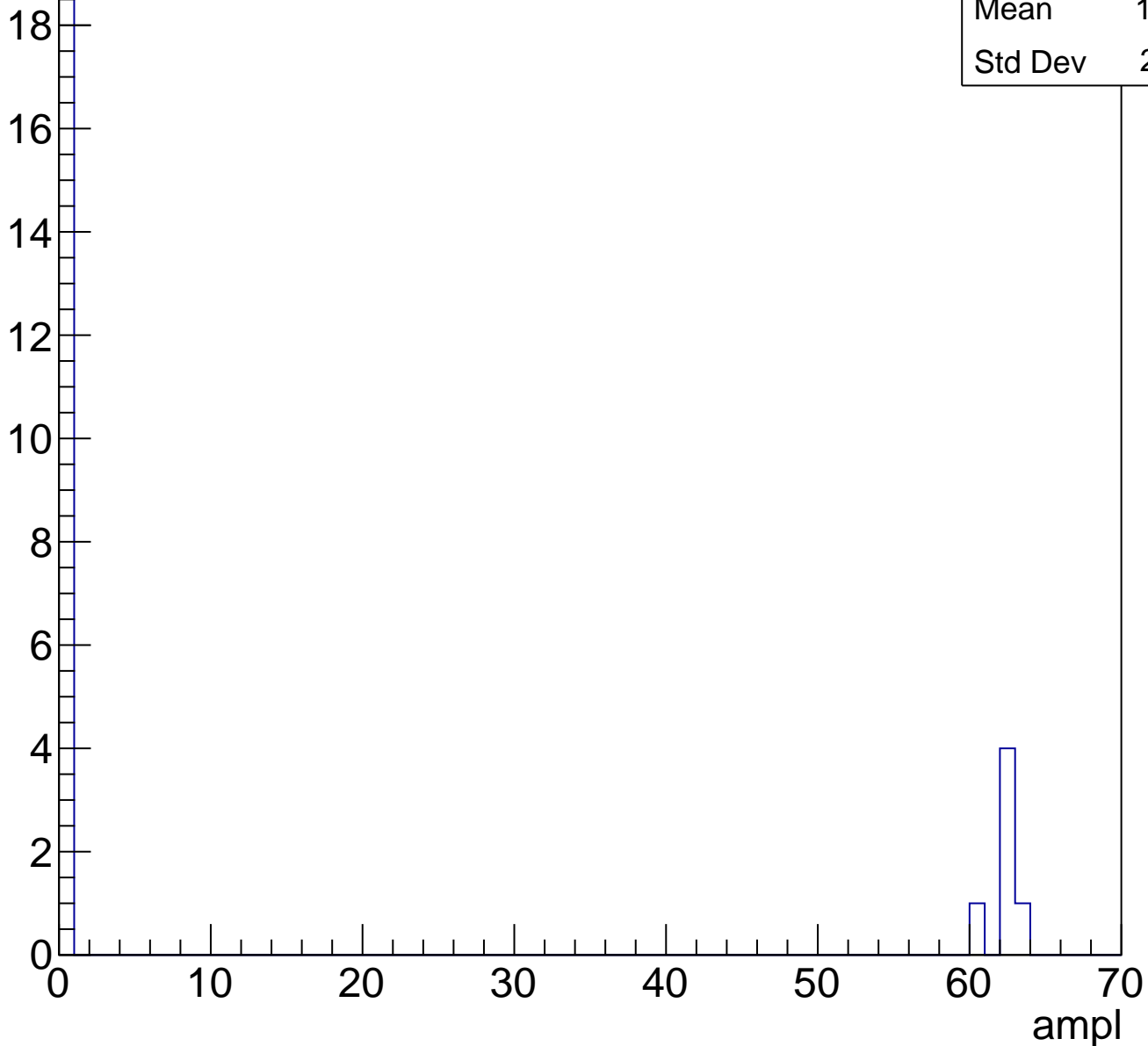


B1L103S, U1-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	25
Mean	14.84
Std Dev	26.41

Entry



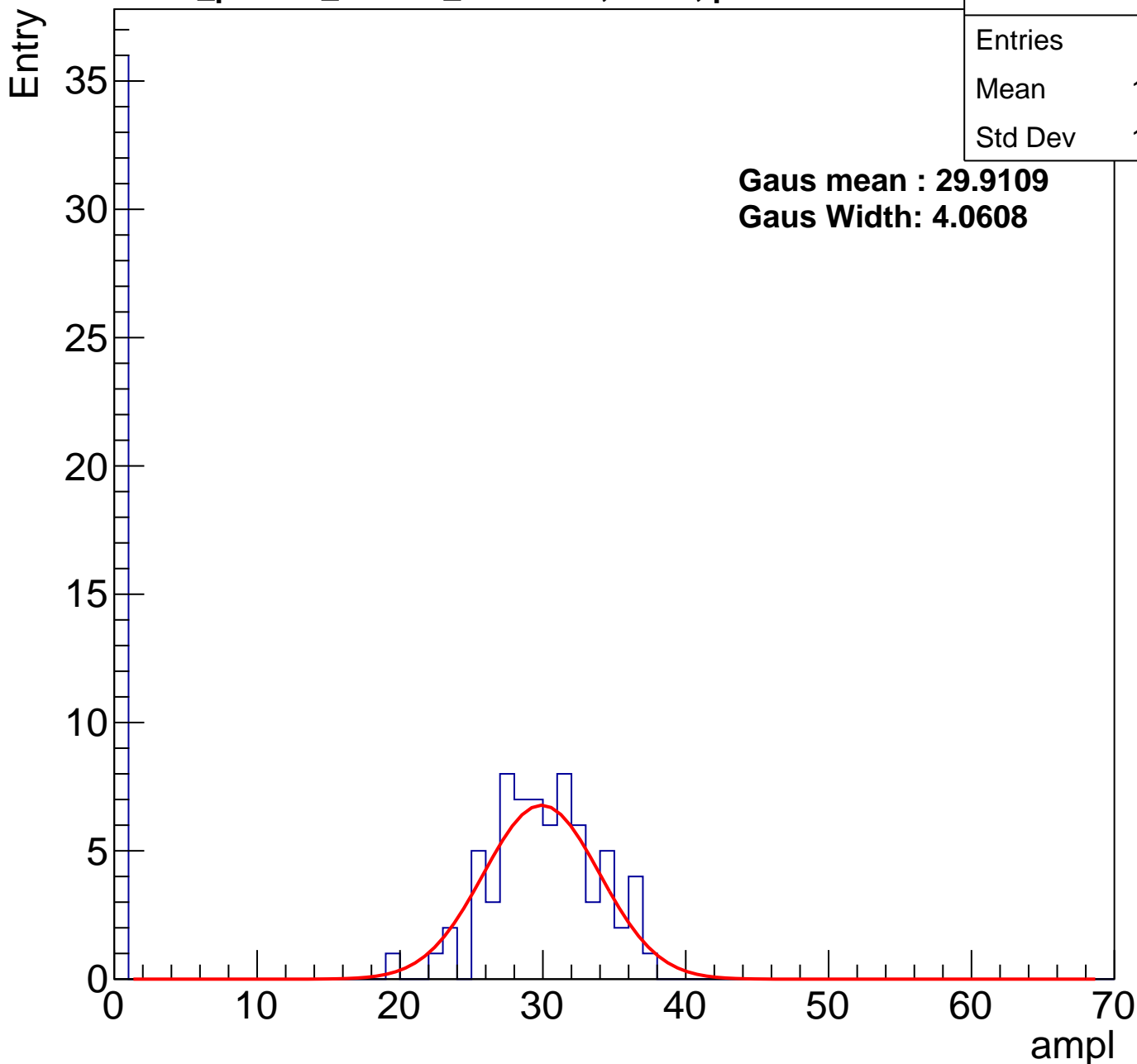
B1L103S, U1-ch65, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	19.48
Std Dev	14.38

Gaus mean : 29.9109

Gaus Width: 4.0608



B1L103S, U1-ch65, adc1

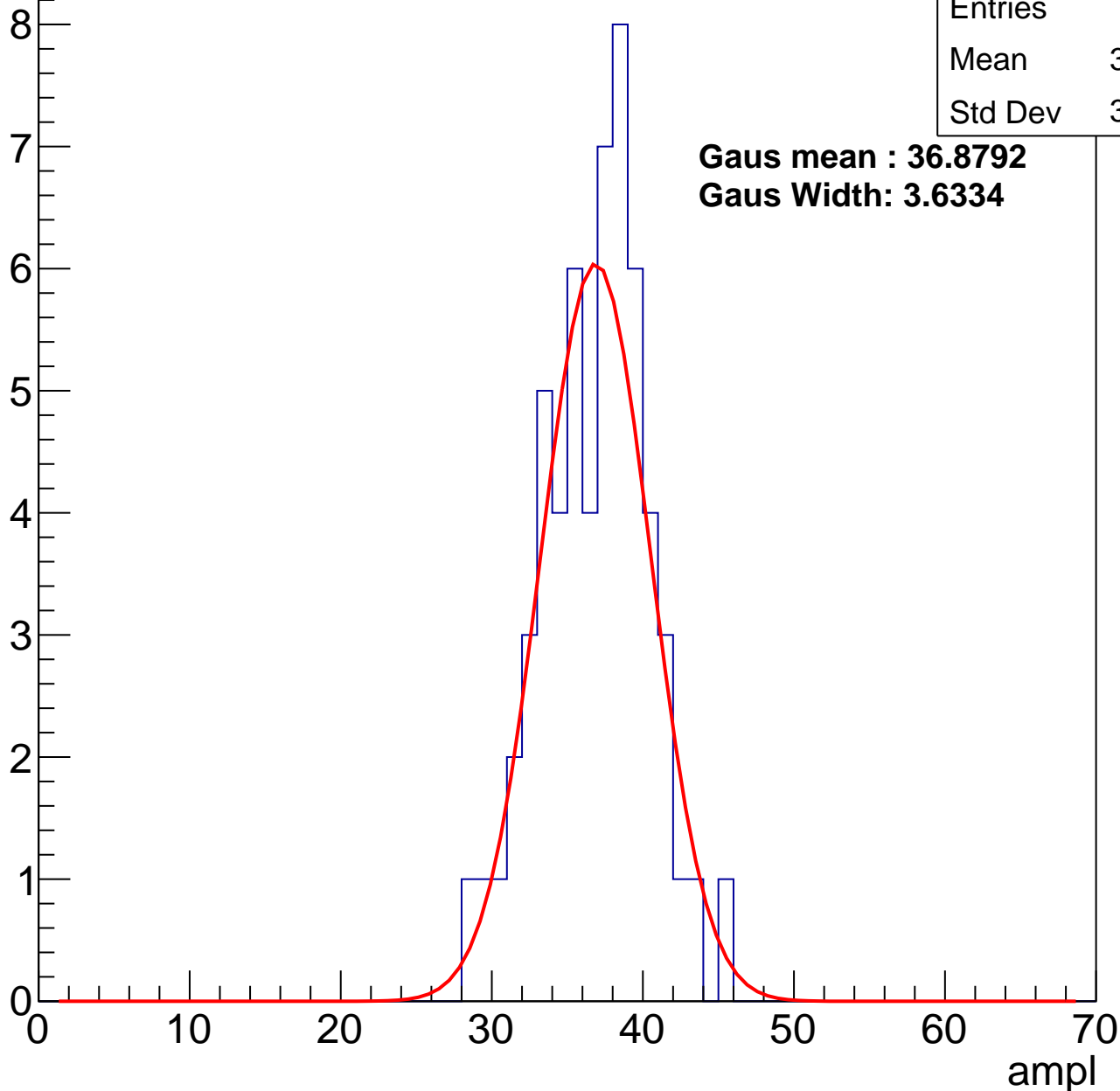
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	36.38
Std Dev	3.483

Gaus mean : 36.8792

Gaus Width: 3.6334



B1L103S, U1-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	32.9
Std Dev	17.12

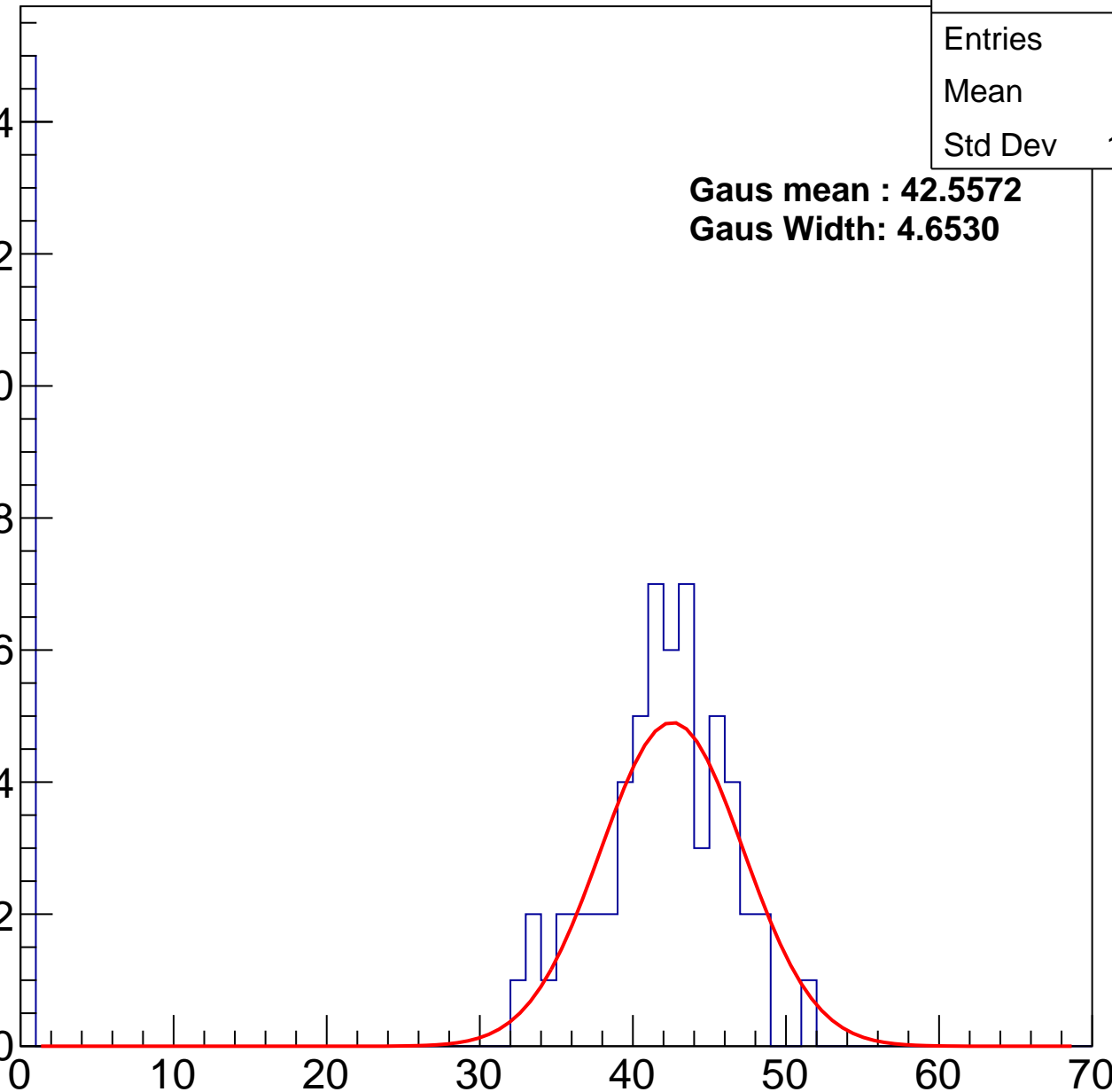
Gaus mean : 42.5572

Gaus Width: 4.6530

Entry

14
12
10
8
6
4
2
0

ampl

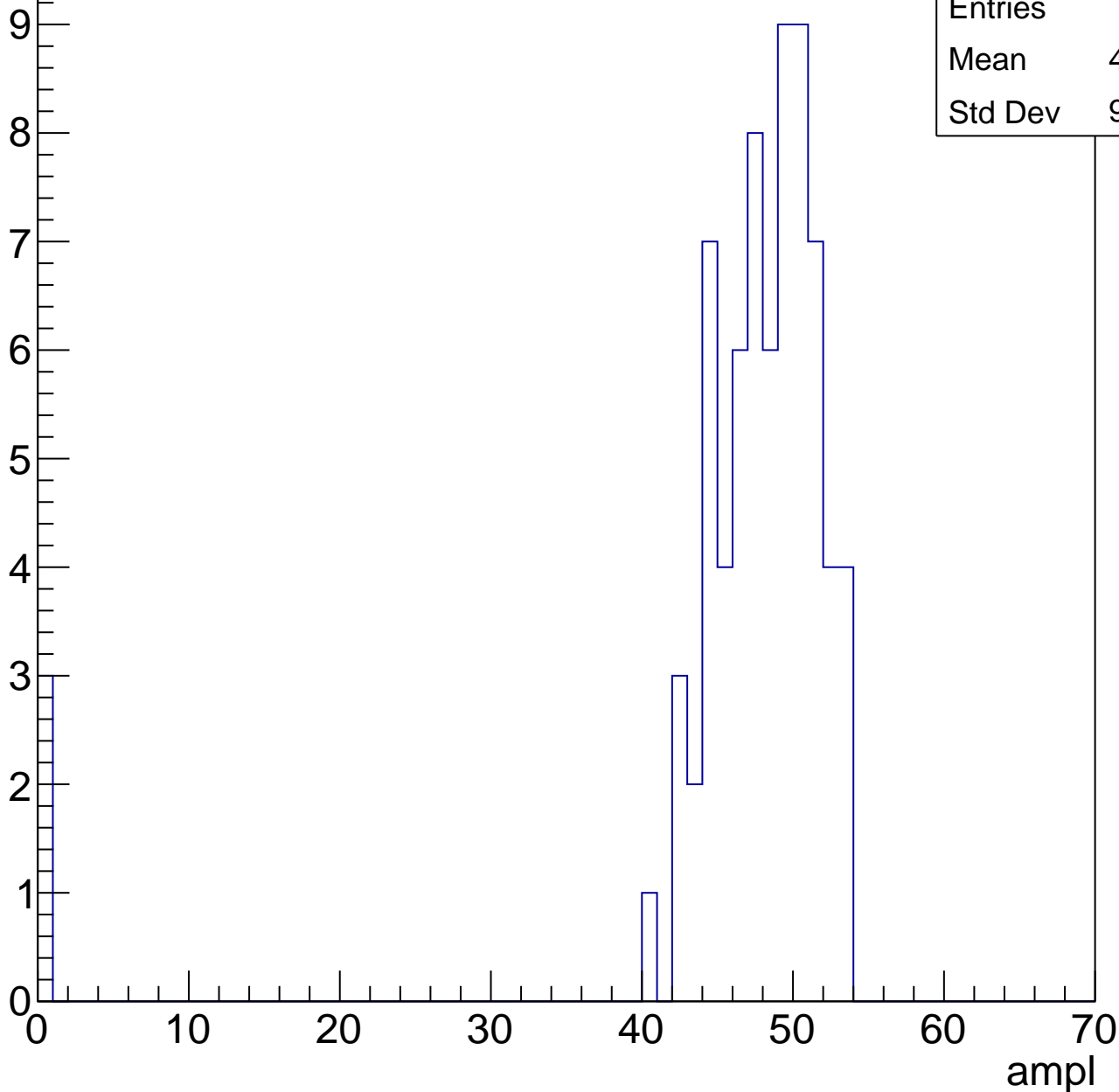


B1L103S, U1-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	45.86
Std Dev	9.968



B1L103S, U1-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	53
Mean	54.68
Std Dev	2.847

Entry

10

8

6

4

2

0

0

10

20

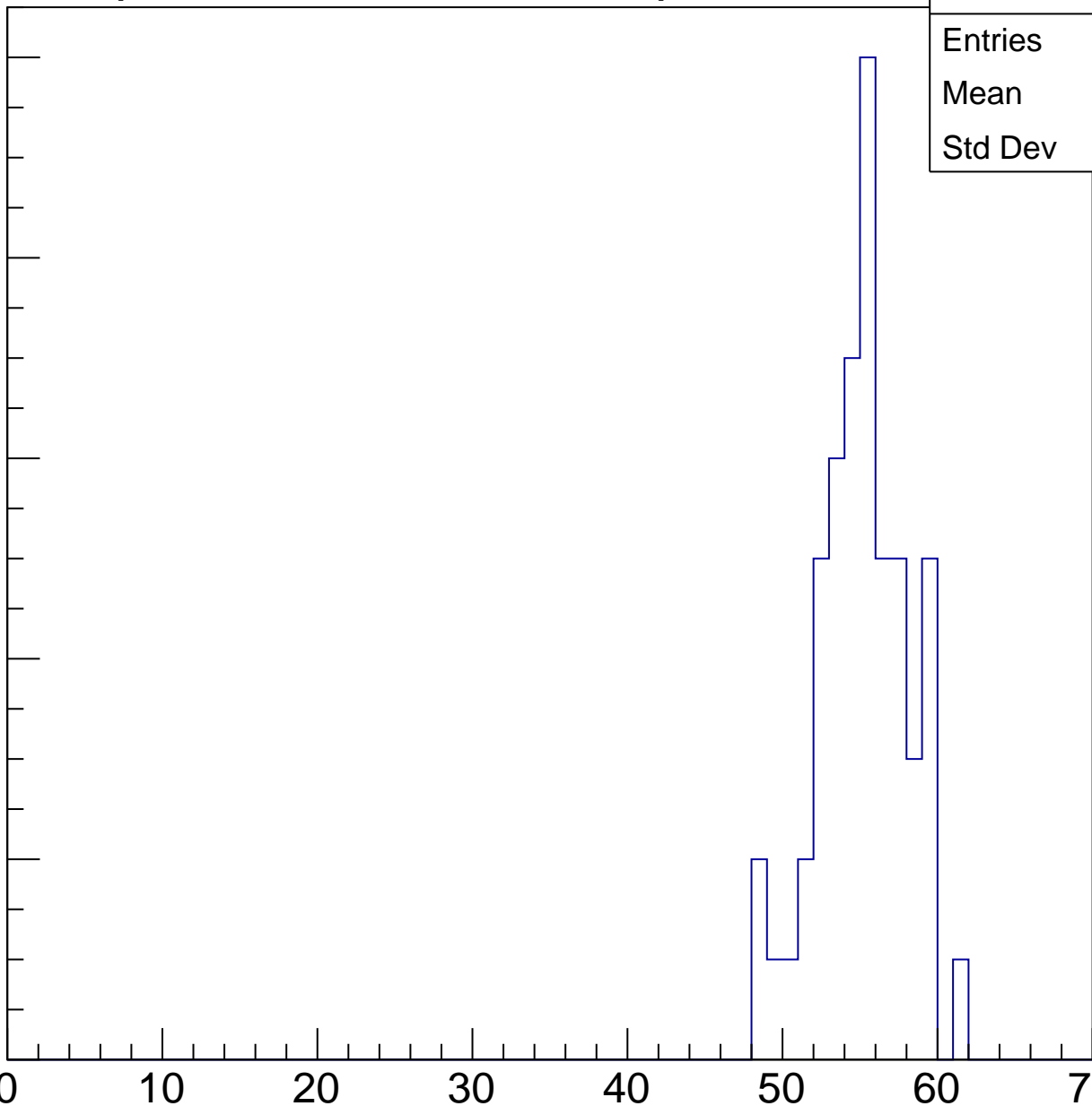
30

40

50

60

ampl

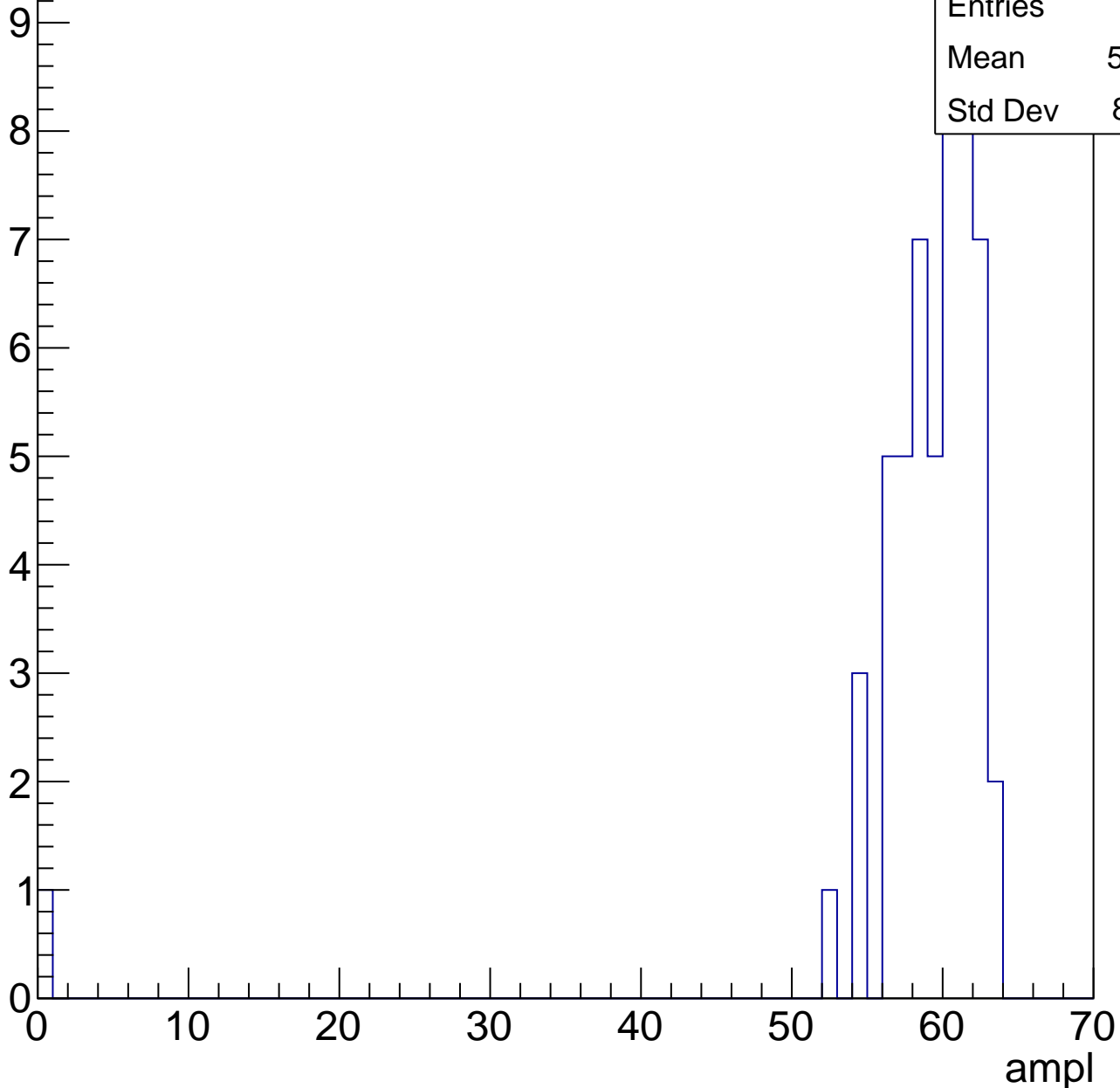


B1L103S, U1-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.94
Std Dev	8.341

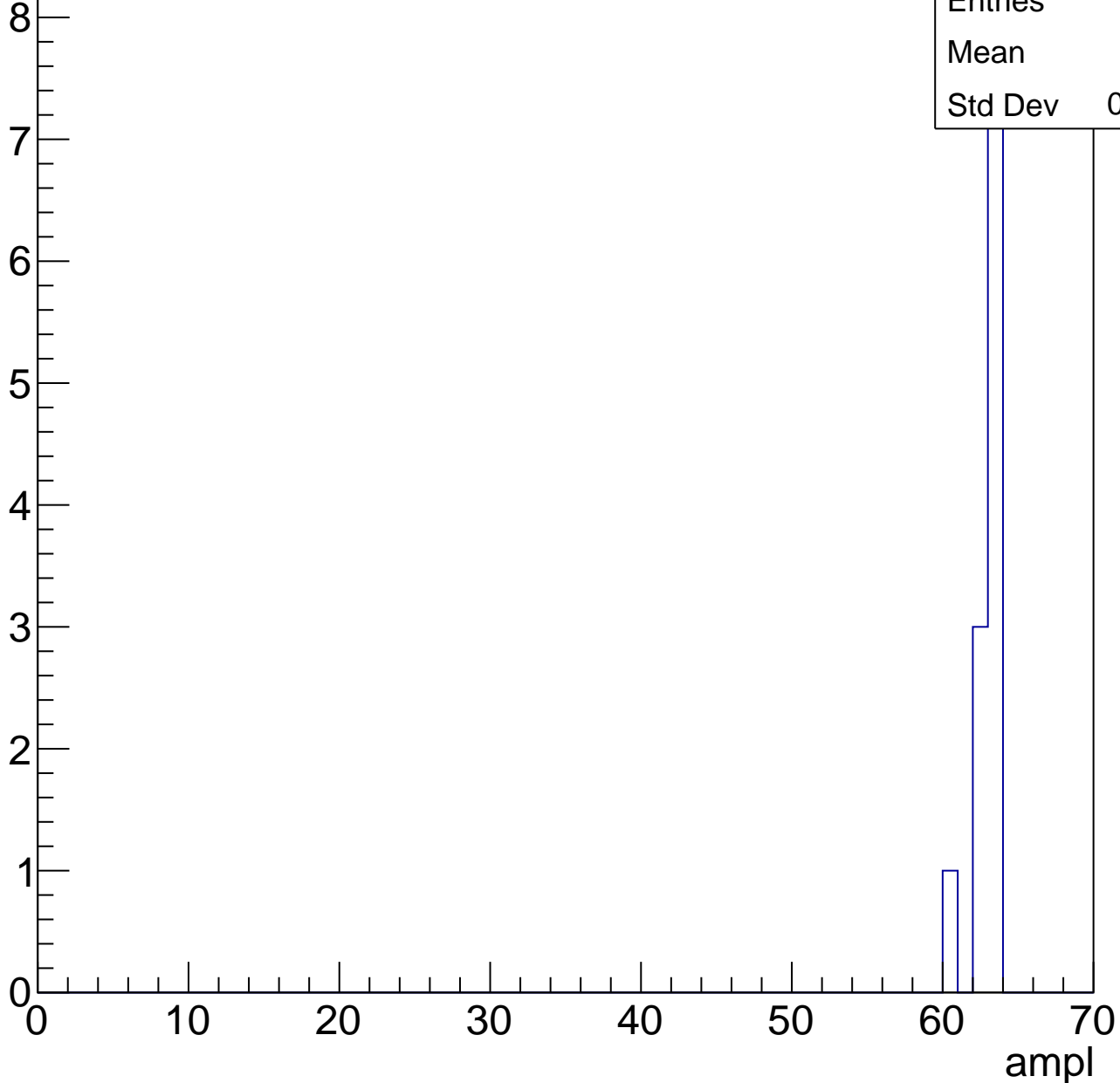


B1L103S, U1-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62.5
Std Dev	0.866



B1L103S, U1-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	22.46
Std Dev	10.56

Gaus mean : 27.6781

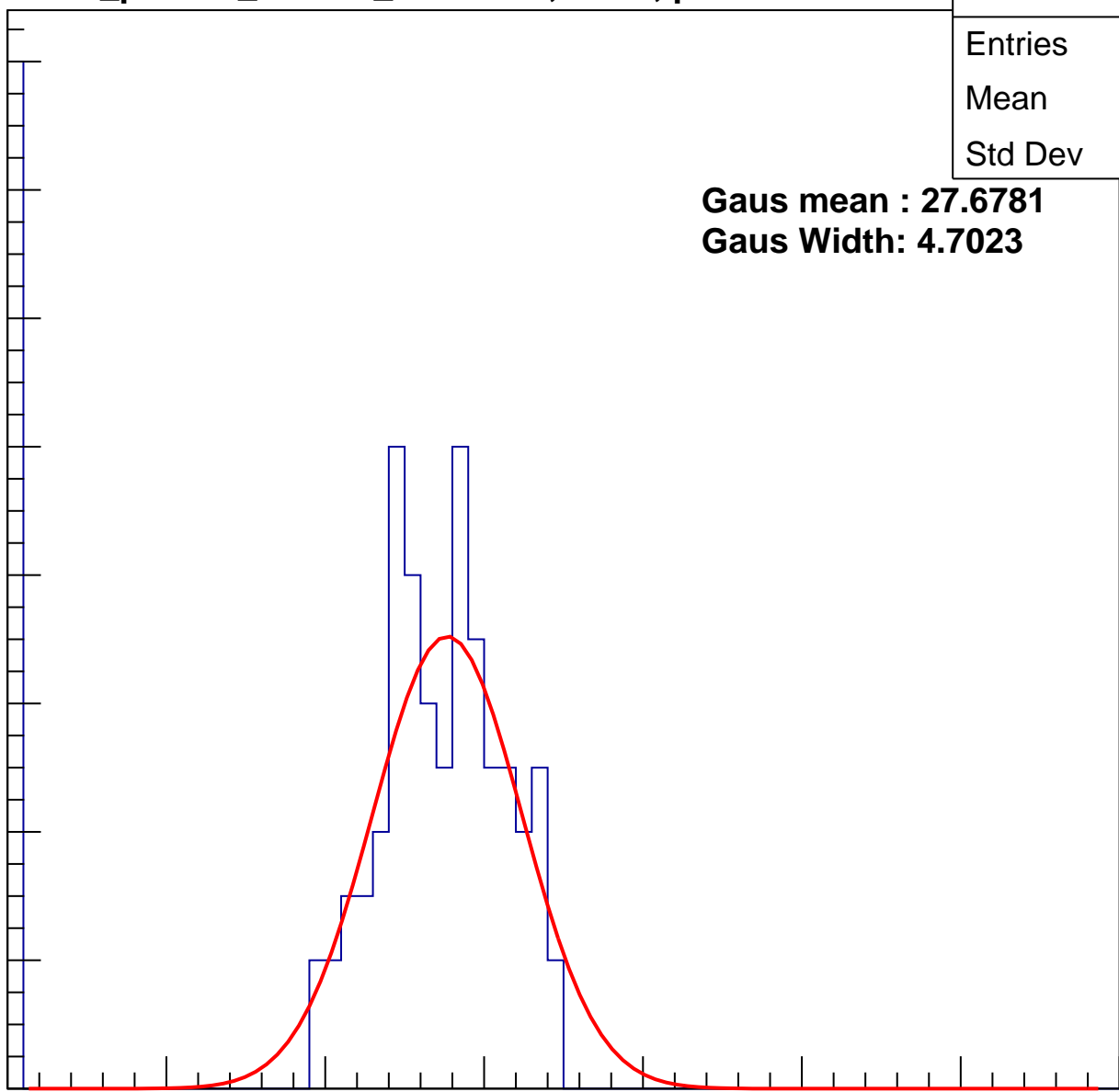
Gaus Width: 4.7023

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch66, adc1

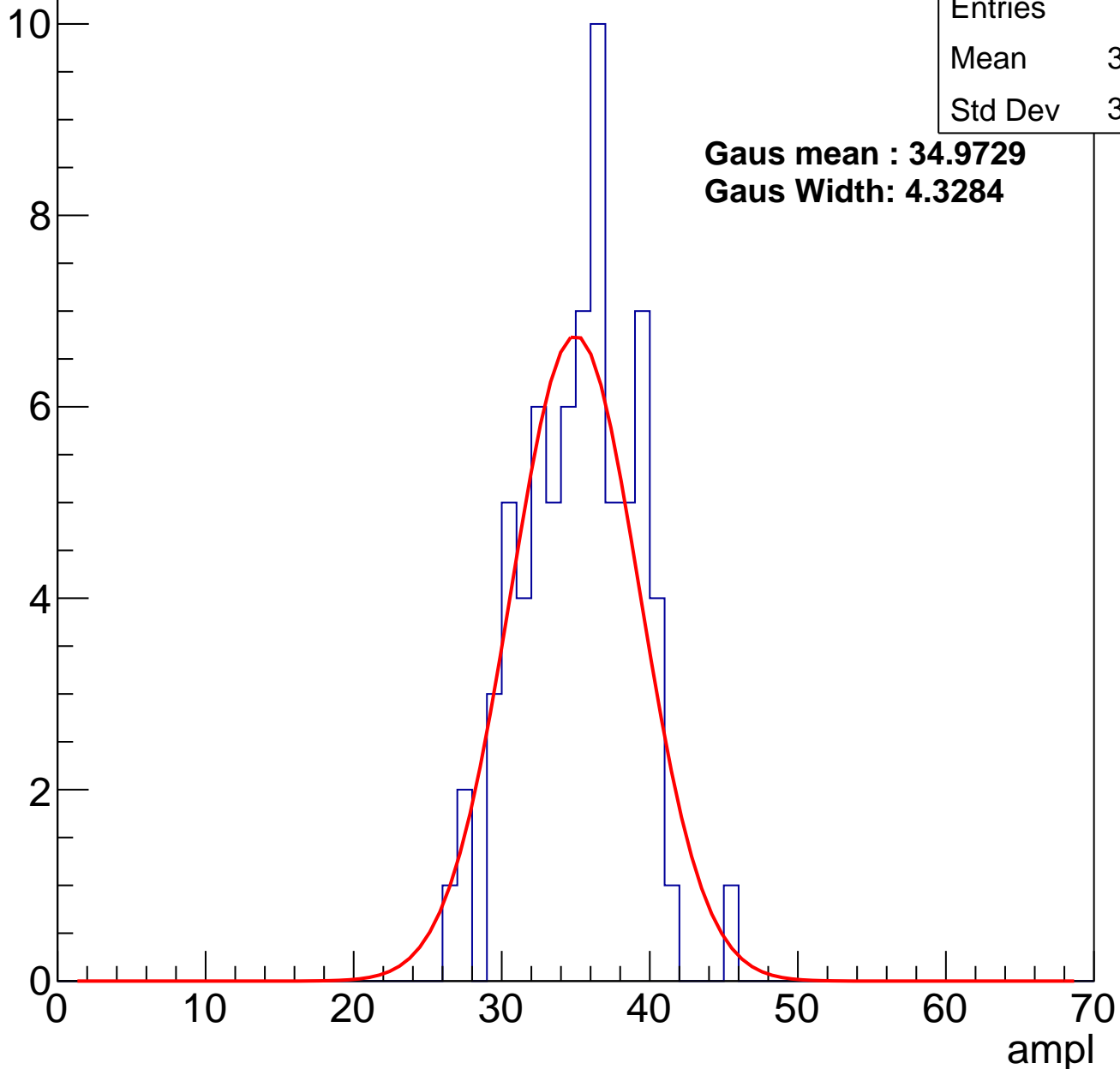
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.74
Std Dev	3.734

Gaus mean : 34.9729

Gaus Width: 4.3284

Entry



B1L103S, U1-ch66, adc2

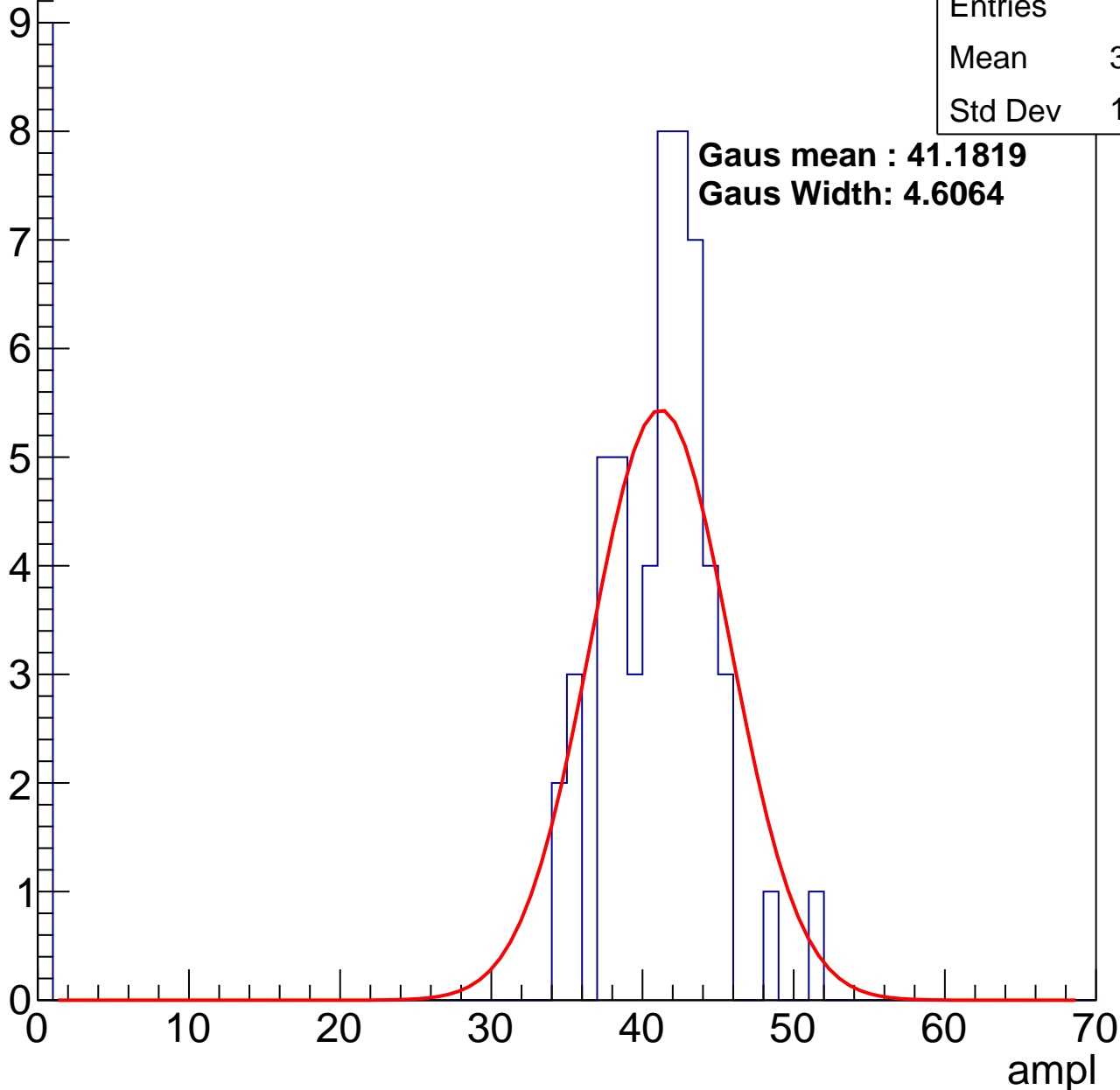
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.92
Std Dev	14.59

Gaus mean : 41.1819

Gaus Width: 4.6064



B1L103S, U1-ch66, adc3

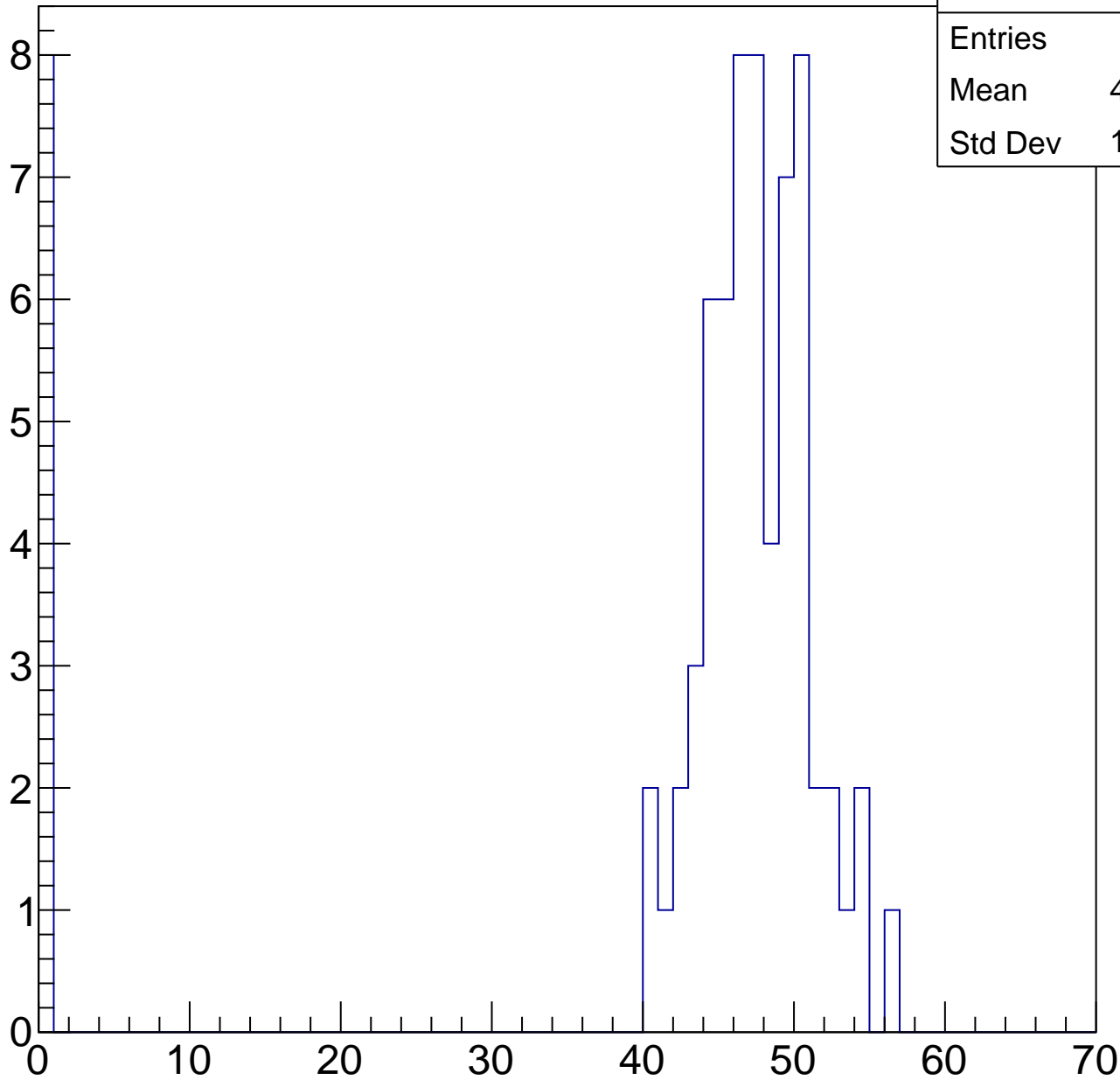
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	41.83
Std Dev	15.24

ampl

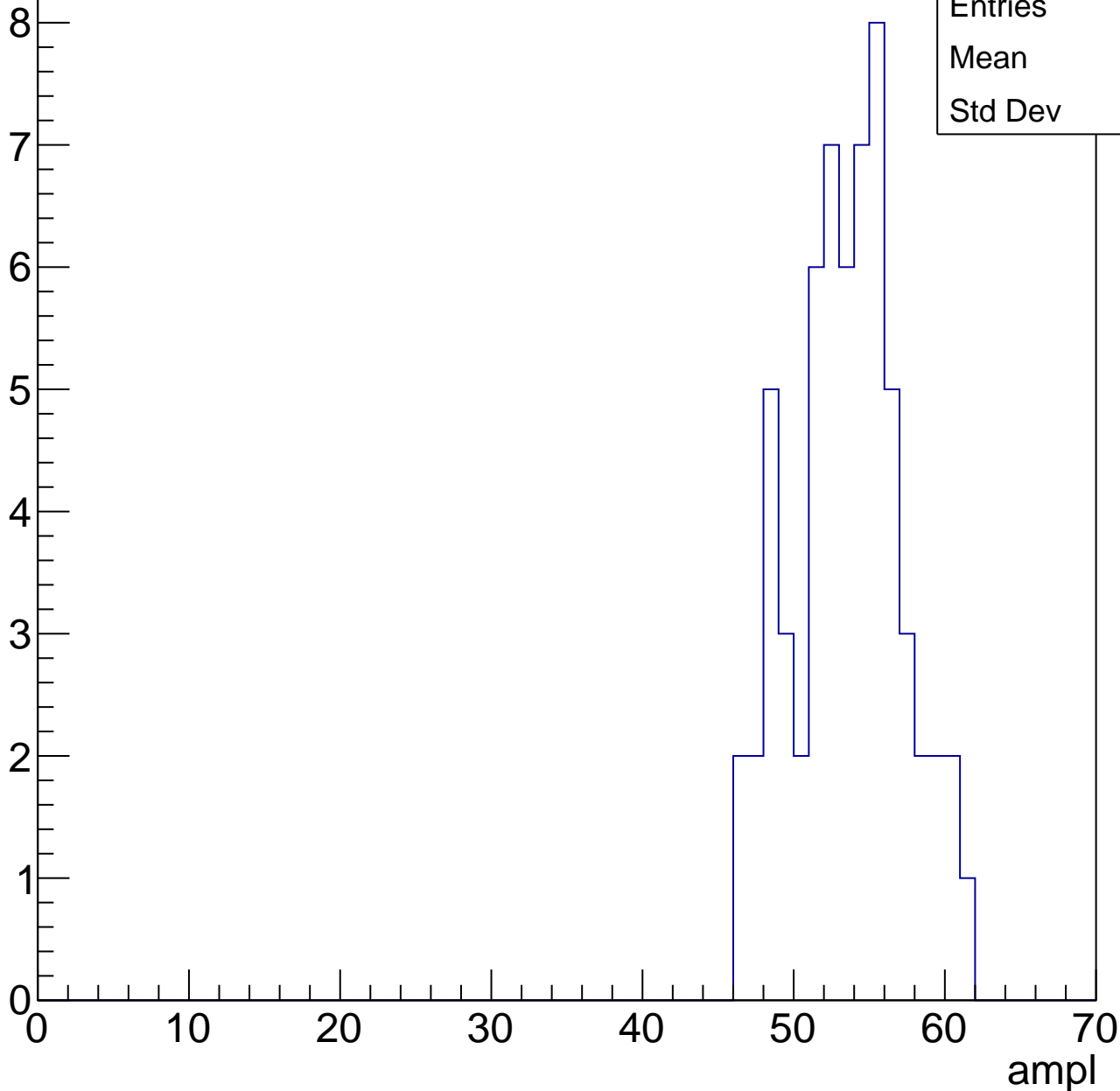


B1L103S, U1-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

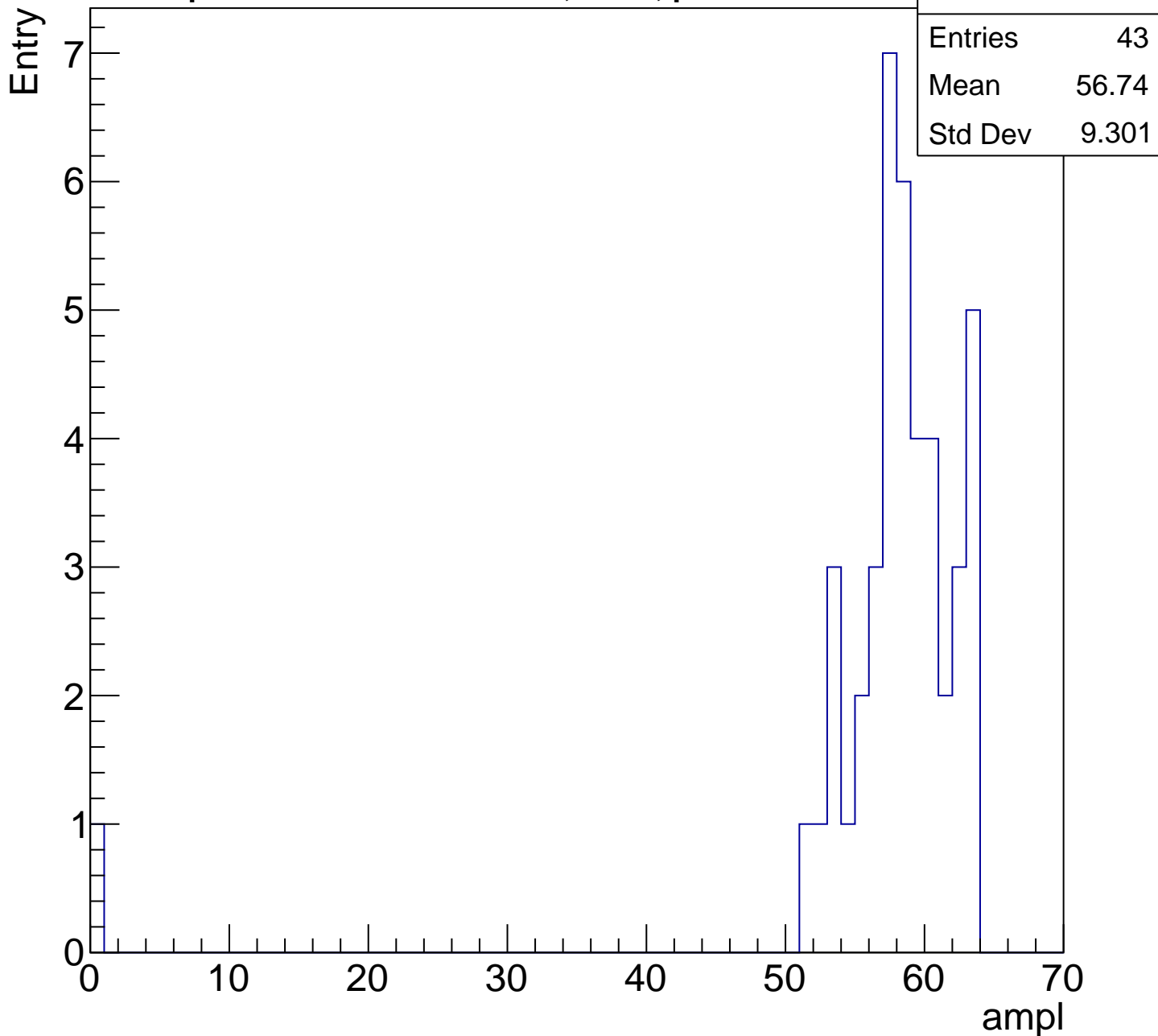
Entry

Entries	63
Mean	53.1
Std Dev	3.58



B1L103S, U1-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

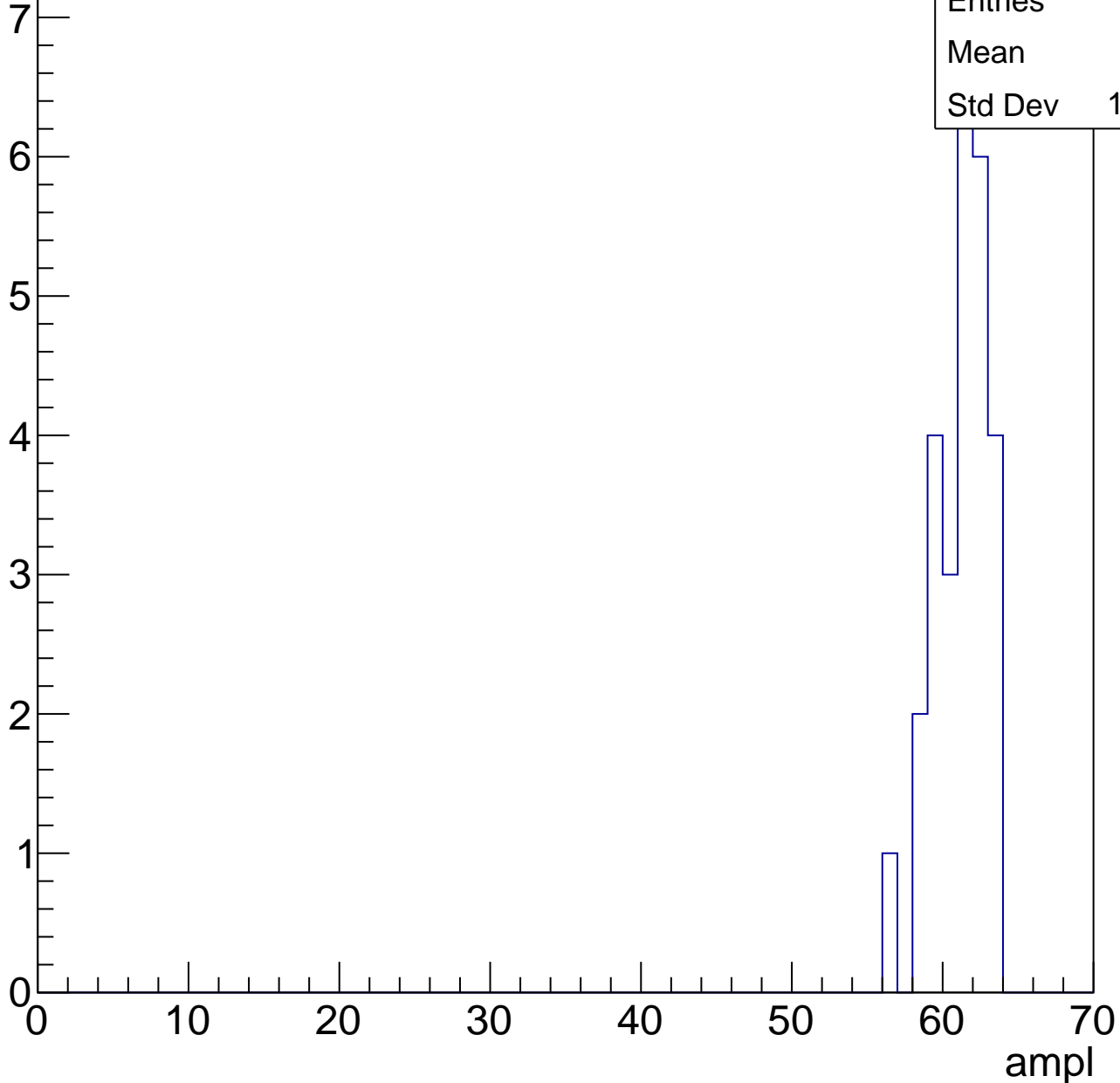


B1L103S, U1-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

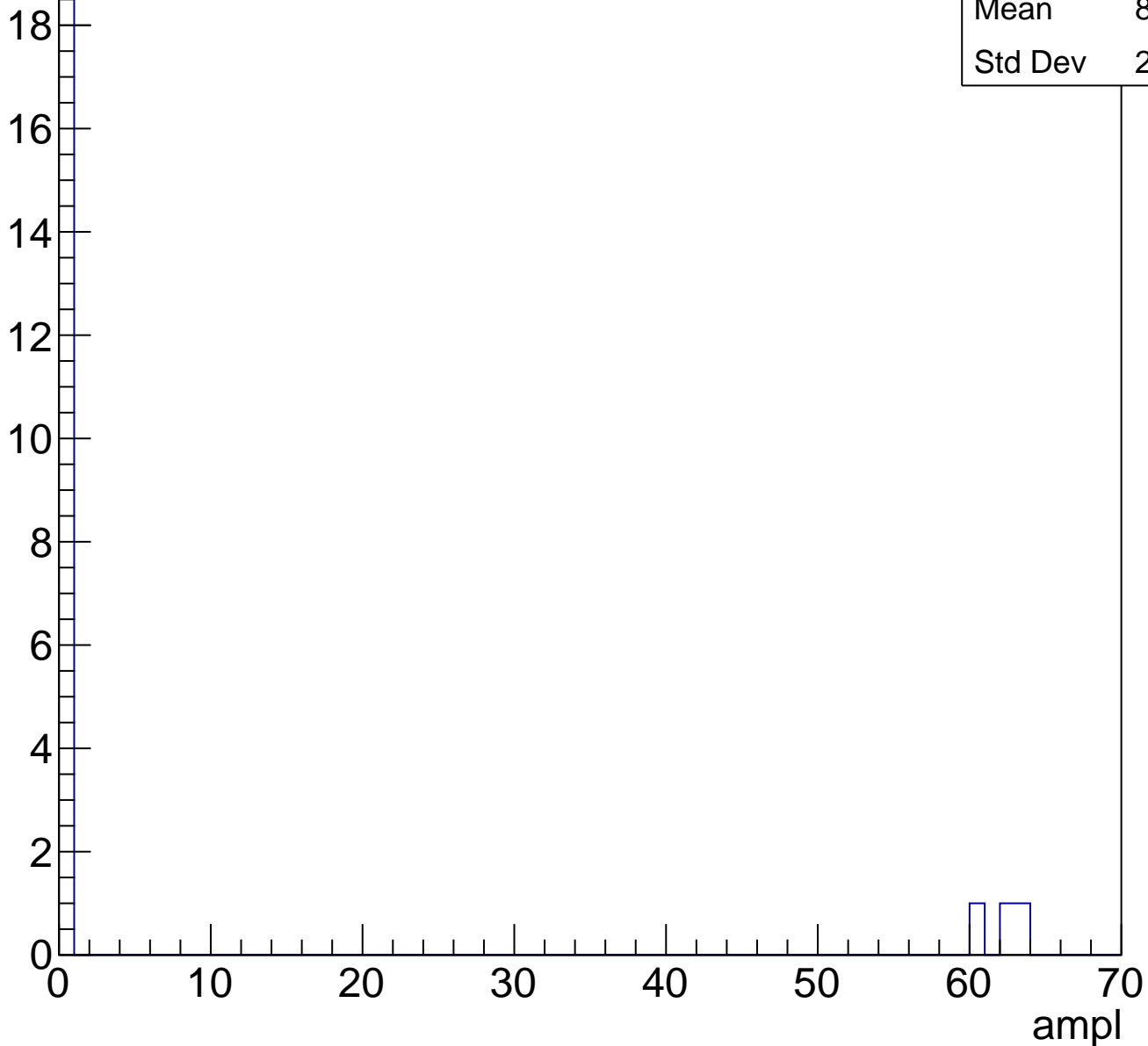
Entries	27
Mean	60.7
Std Dev	1.739



B1L103S, U1-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.409
Std Dev	21.17

B1L103S, U1-ch67, adc0

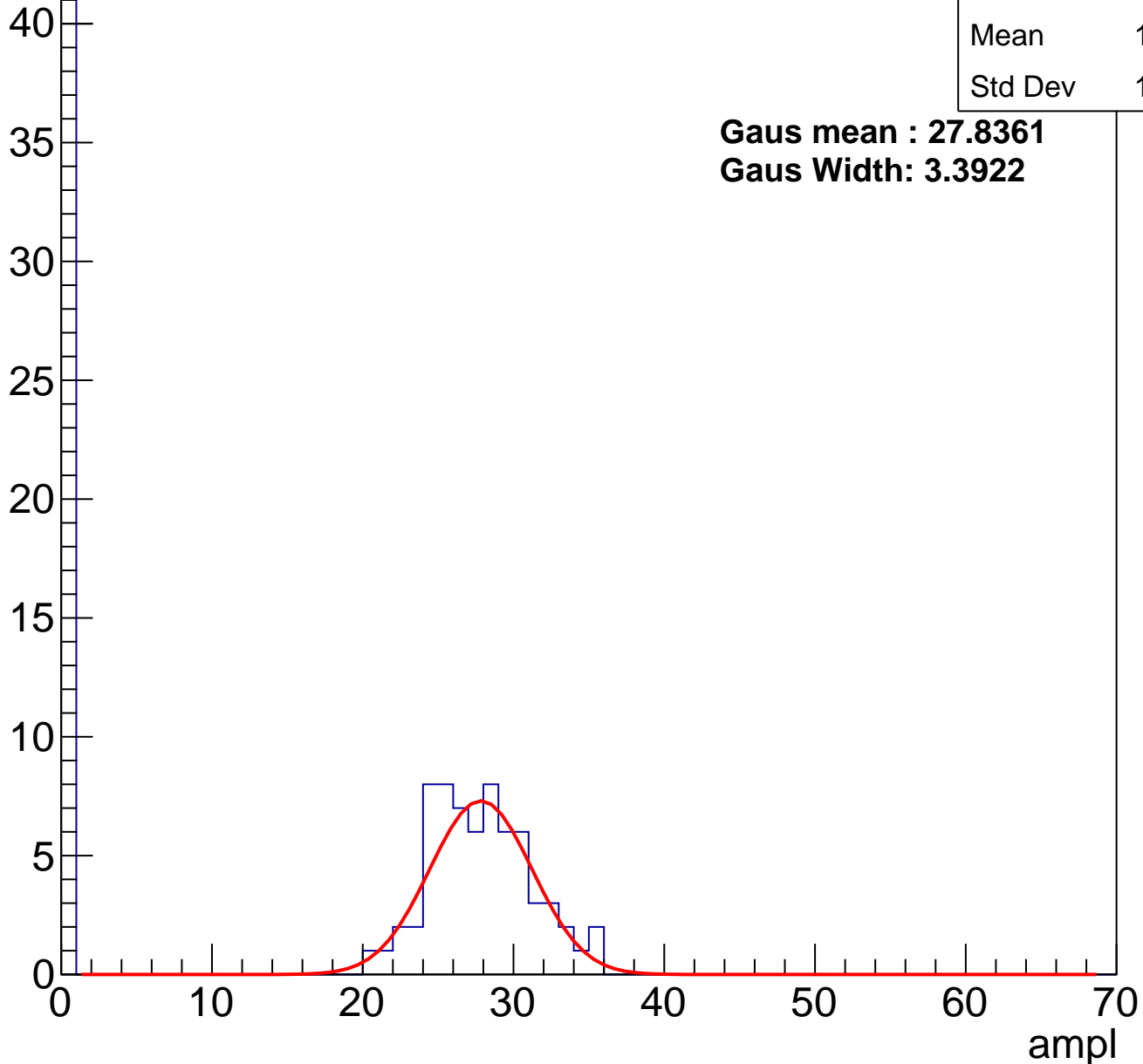
calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	16.86
Std Dev	13.54

Gaus mean : 27.8361

Gaus Width: 3.3922

Entry



B1L103S, U1-ch67, adc1

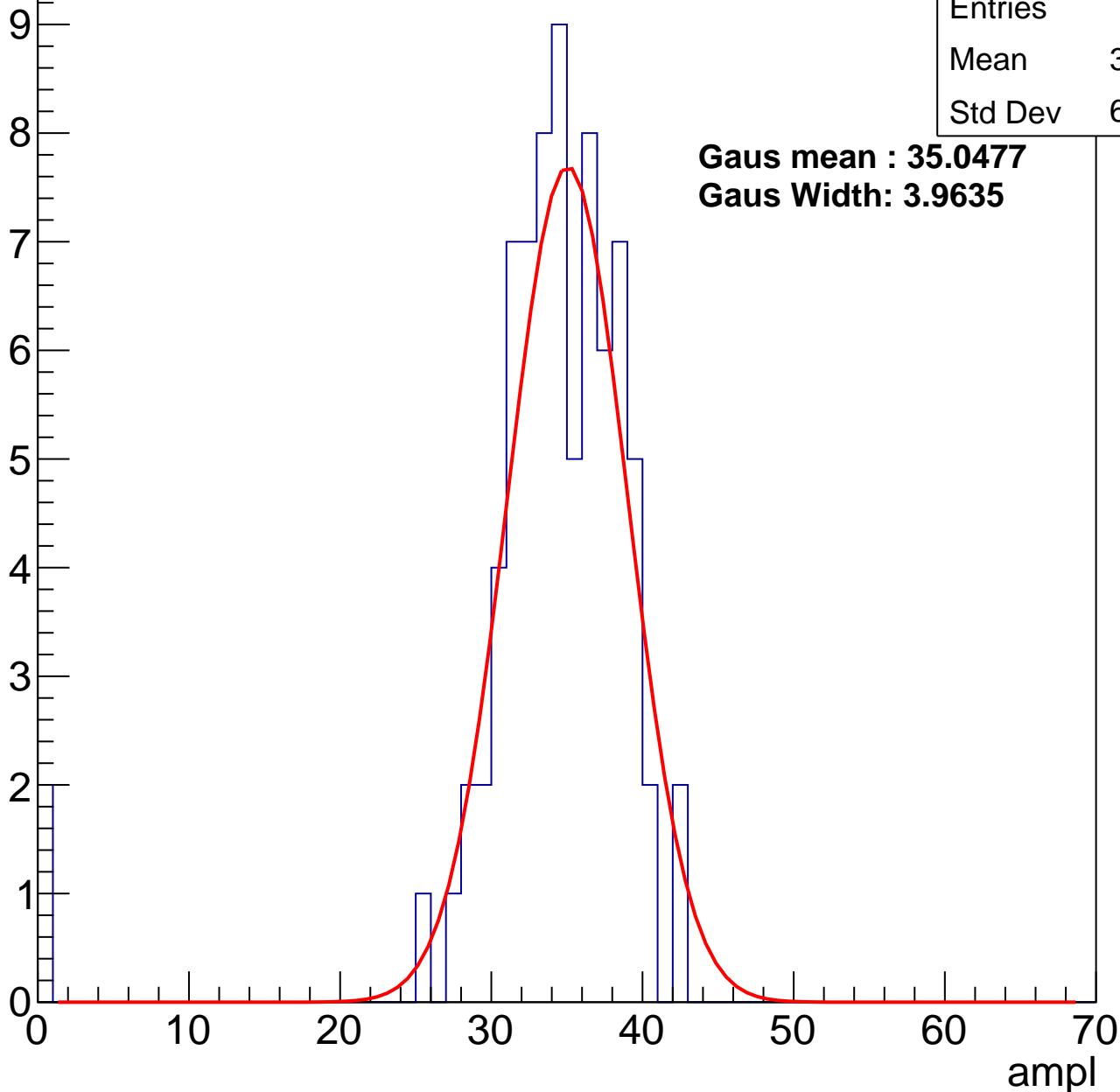
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.42
Std Dev	6.434

Gaus mean : 35.0477

Gaus Width: 3.9635



B1L103S, U1-ch67, adc2

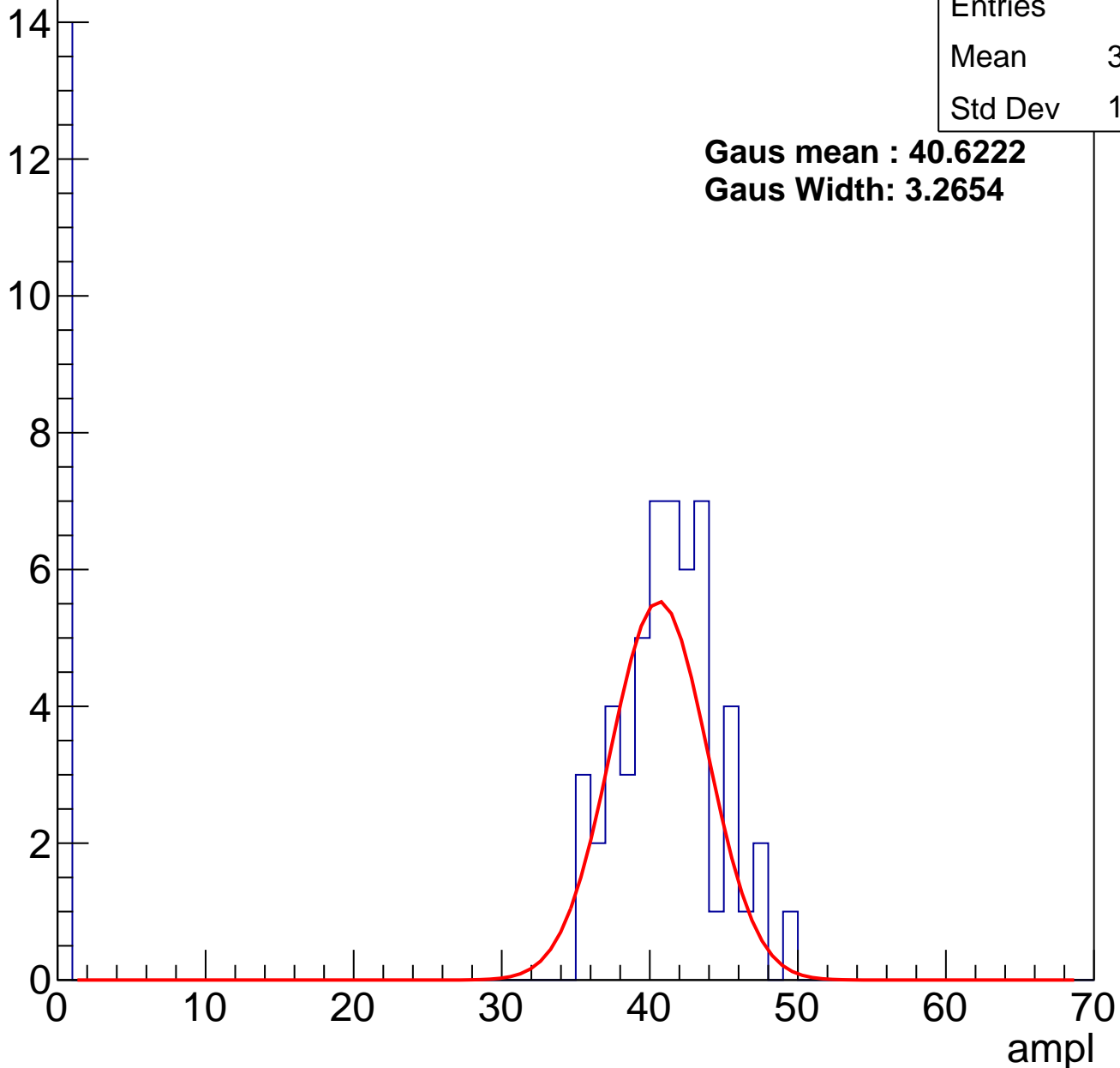
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	32.34
Std Dev	16.87

Gaus mean : 40.6222

Gaus Width: 3.2654

Entry

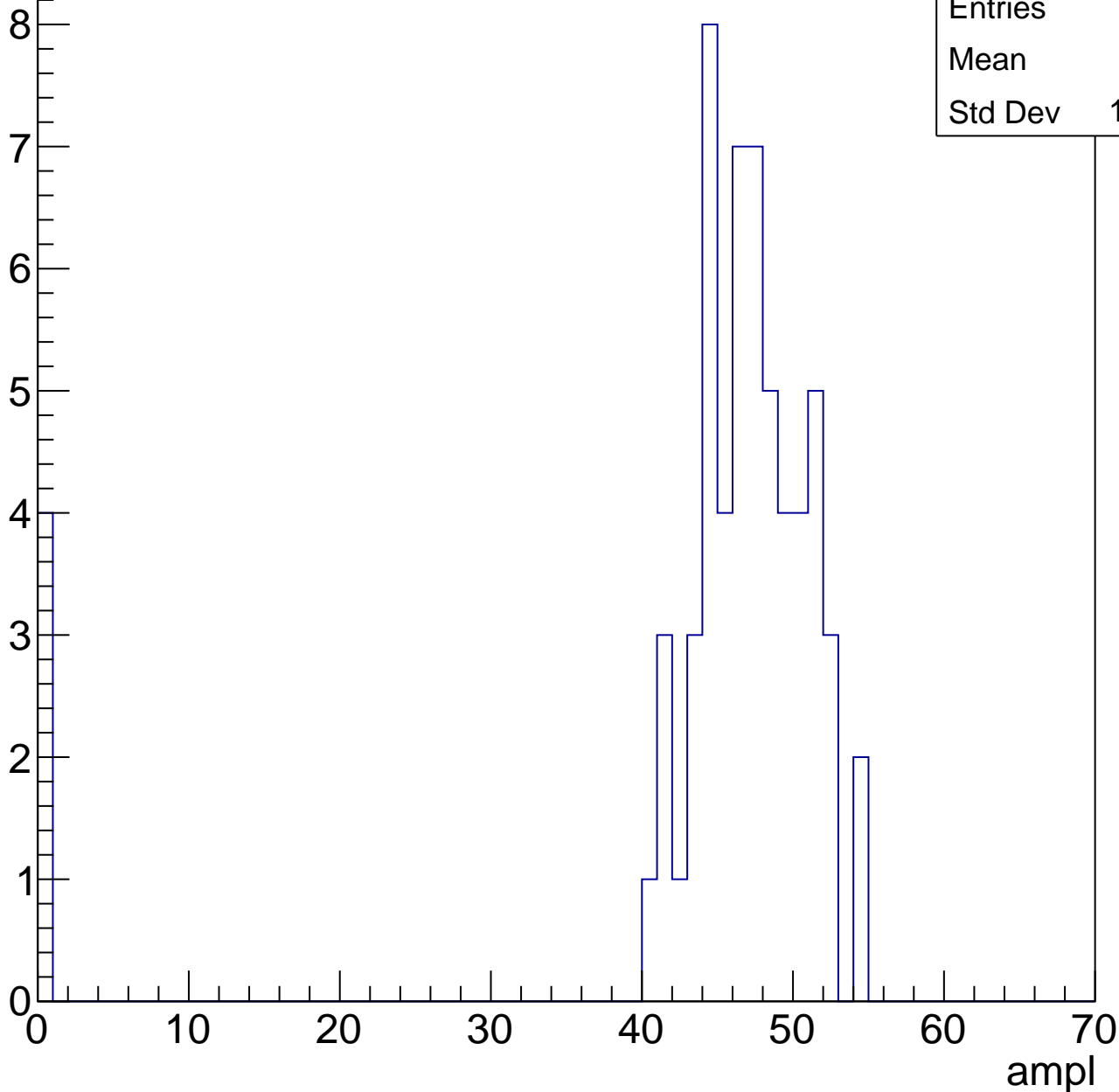


B1L103S, U1-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	43.8
Std Dev	12.04

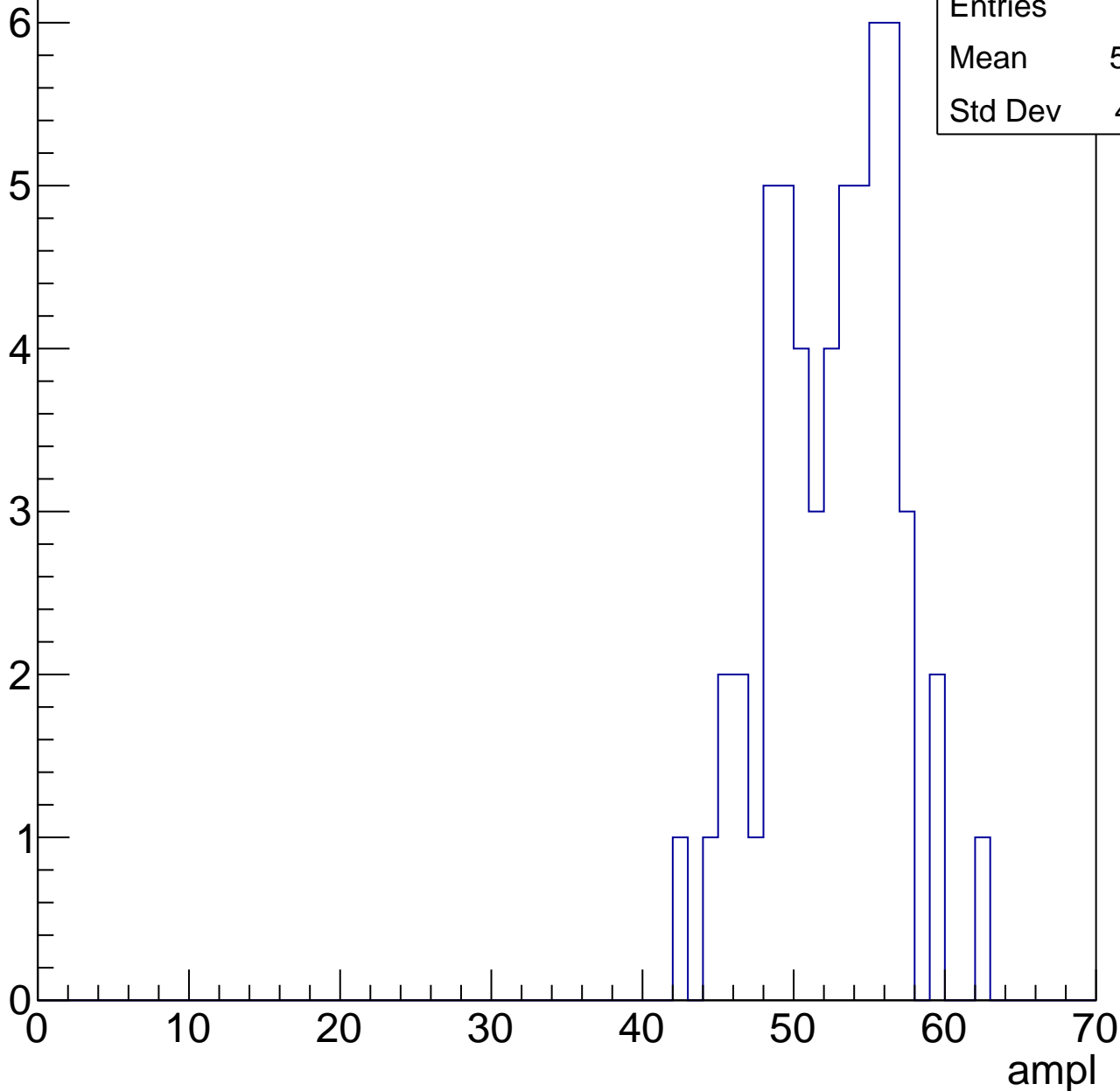


B1L103S, U1-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.02
Std Dev	4.121

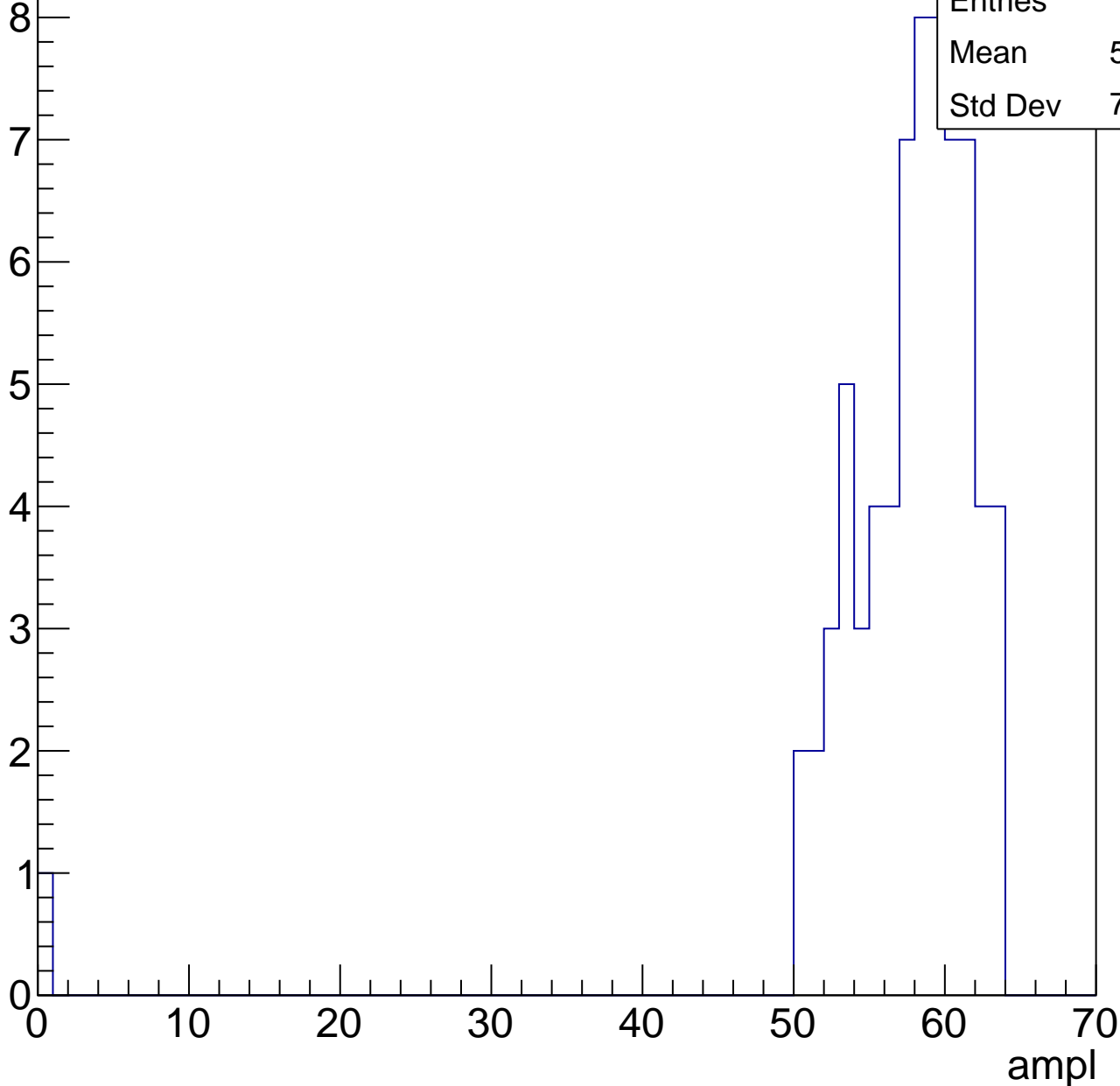


B1L103S, U1-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	56.68
Std Dev	7.683

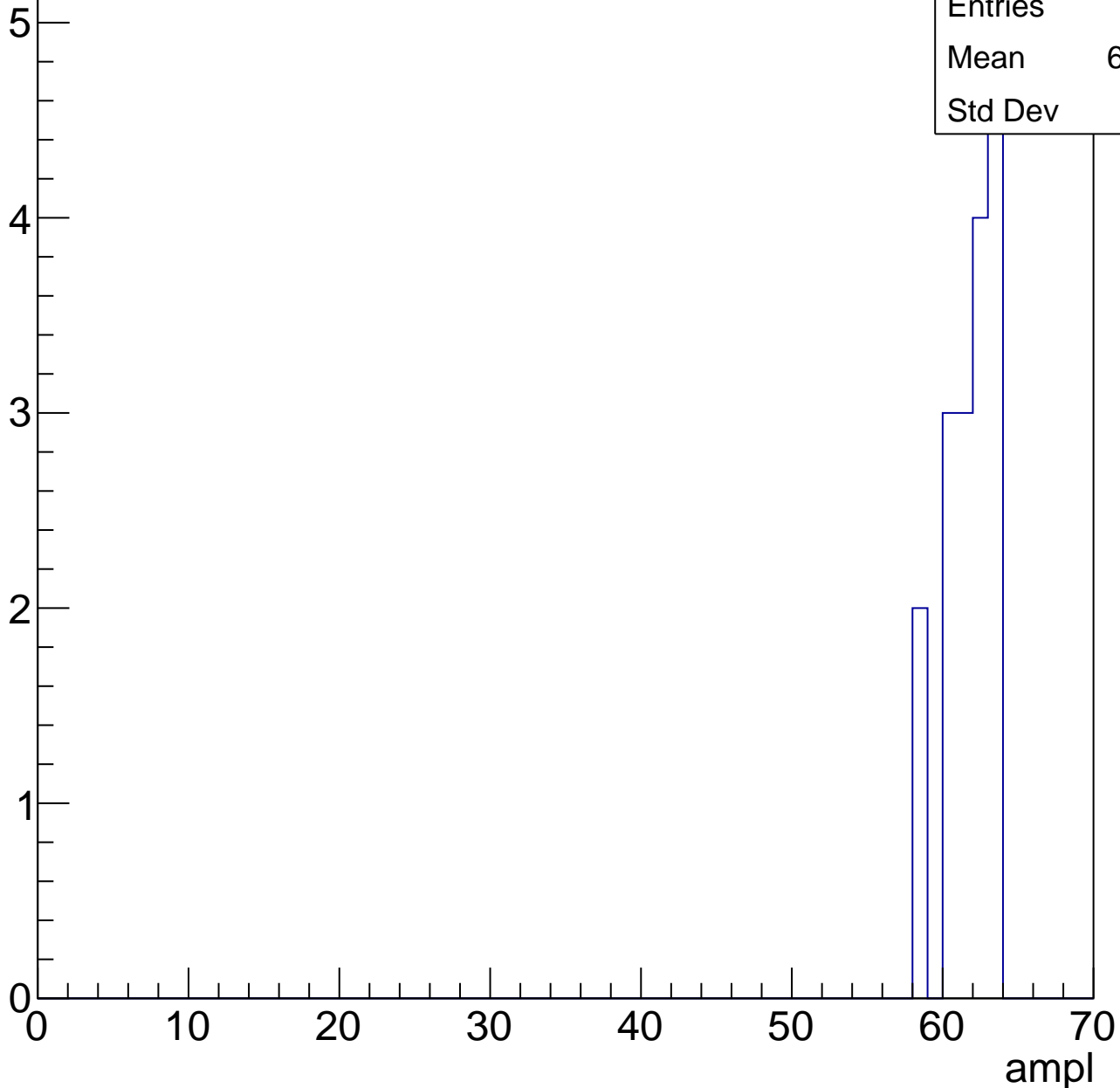


B1L103S, U1-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.29
Std Dev	1.6



B1L103S, U1-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U1-ch68, adc0

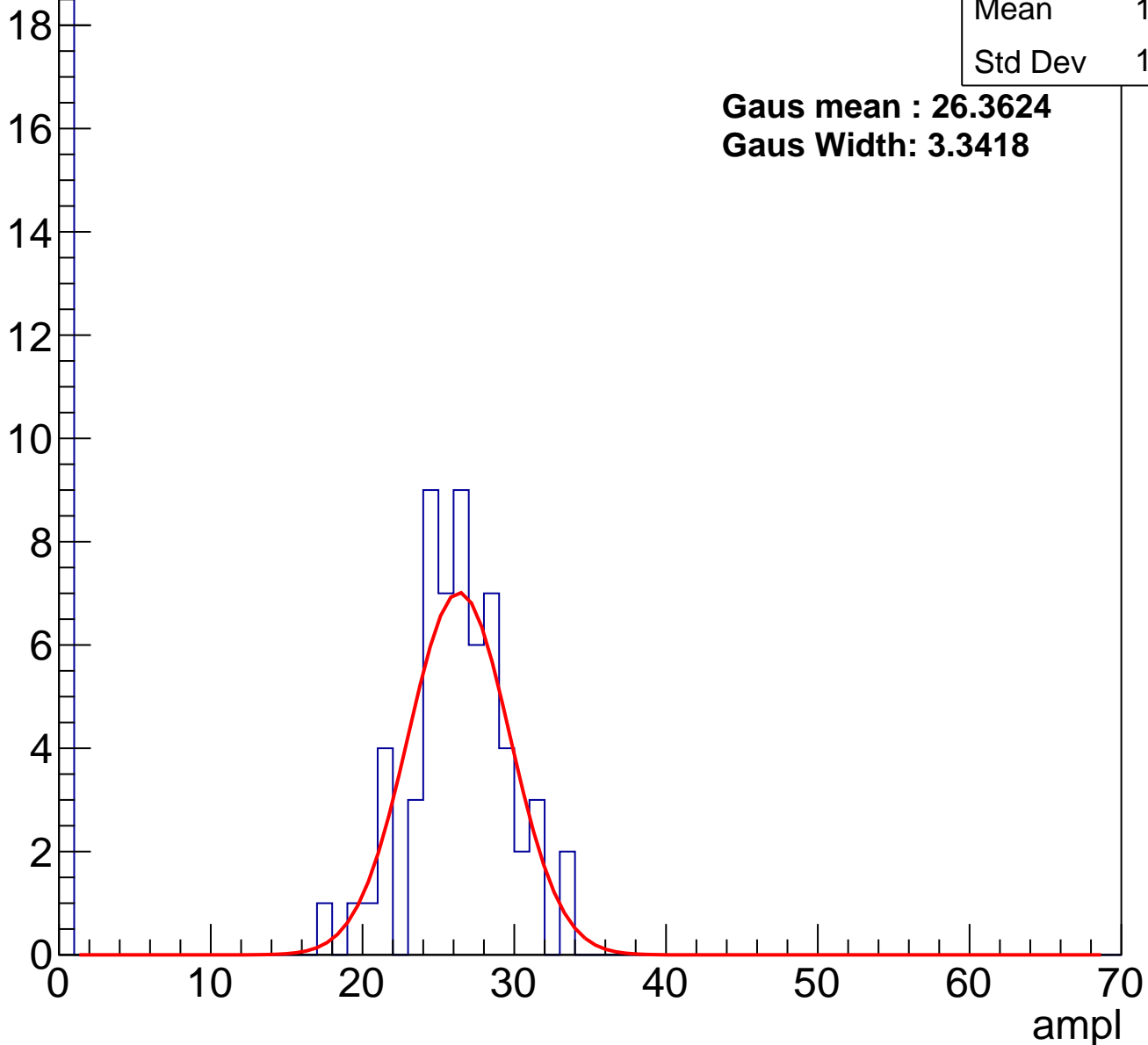
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	19.58
Std Dev	11.46

Gaus mean : 26.3624

Gaus Width: 3.3418

Entry



B1L103S, U1-ch68, adc1

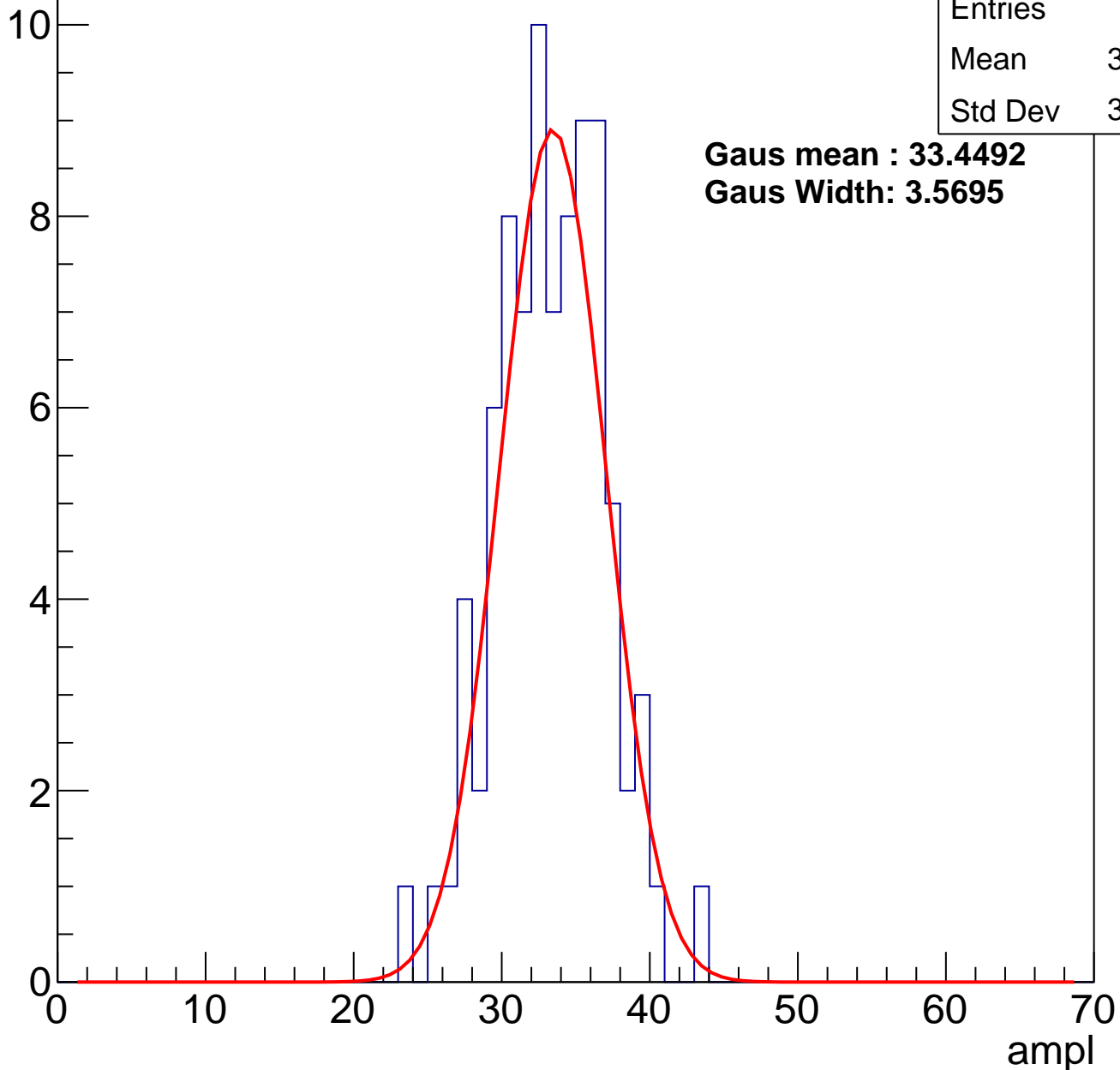
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.85
Std Dev	3.635

Gaus mean : 33.4492

Gaus Width: 3.5695

Entry



B1L103S, U1-ch68, adc2

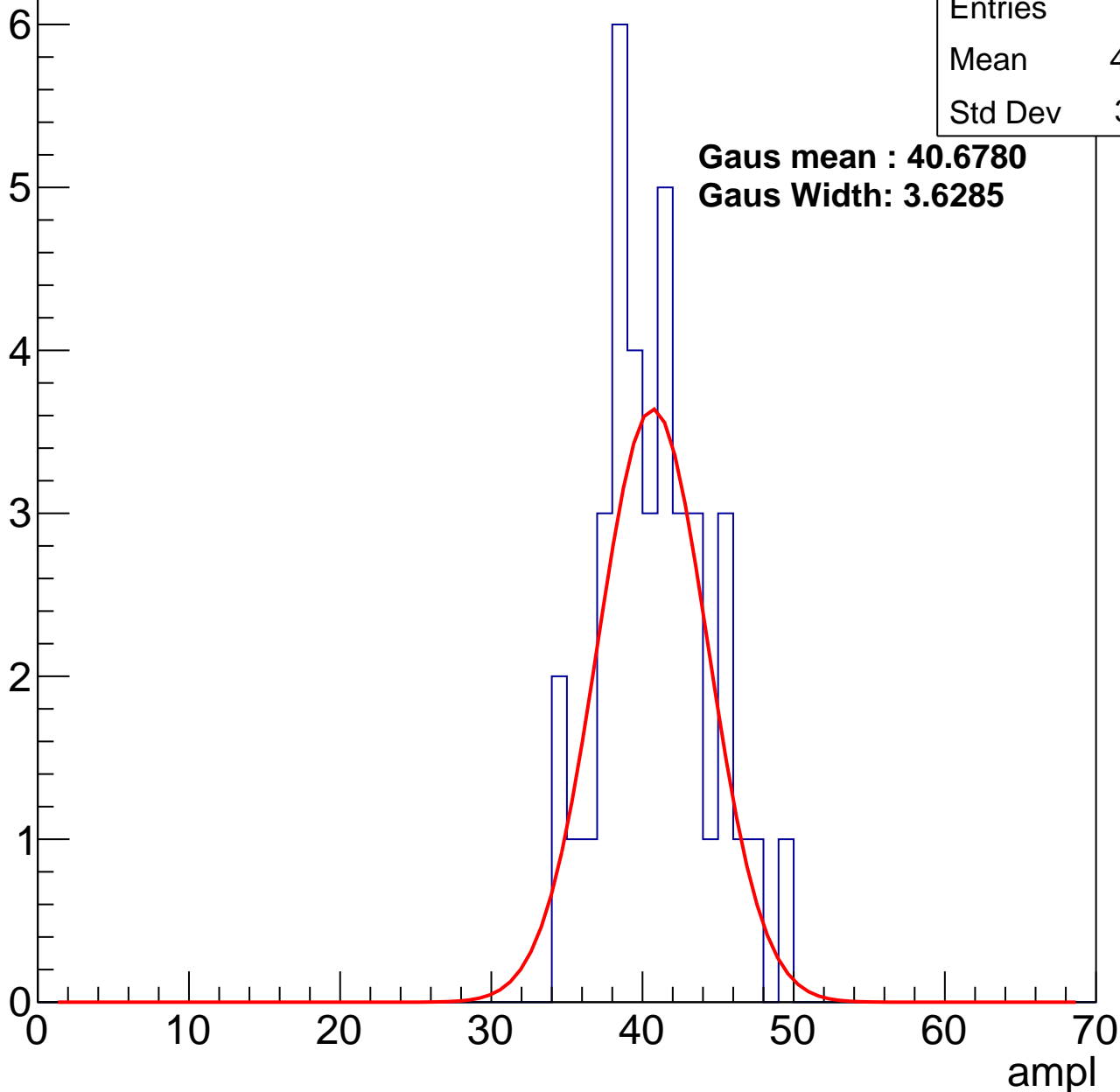
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	40.39
Std Dev	3.491

Gaus mean : 40.6780

Gaus Width: 3.6285



B1L103S, U1-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	38.4
Std Dev	16.04

Entry

12

10

8

6

4

2

0

0

10

20

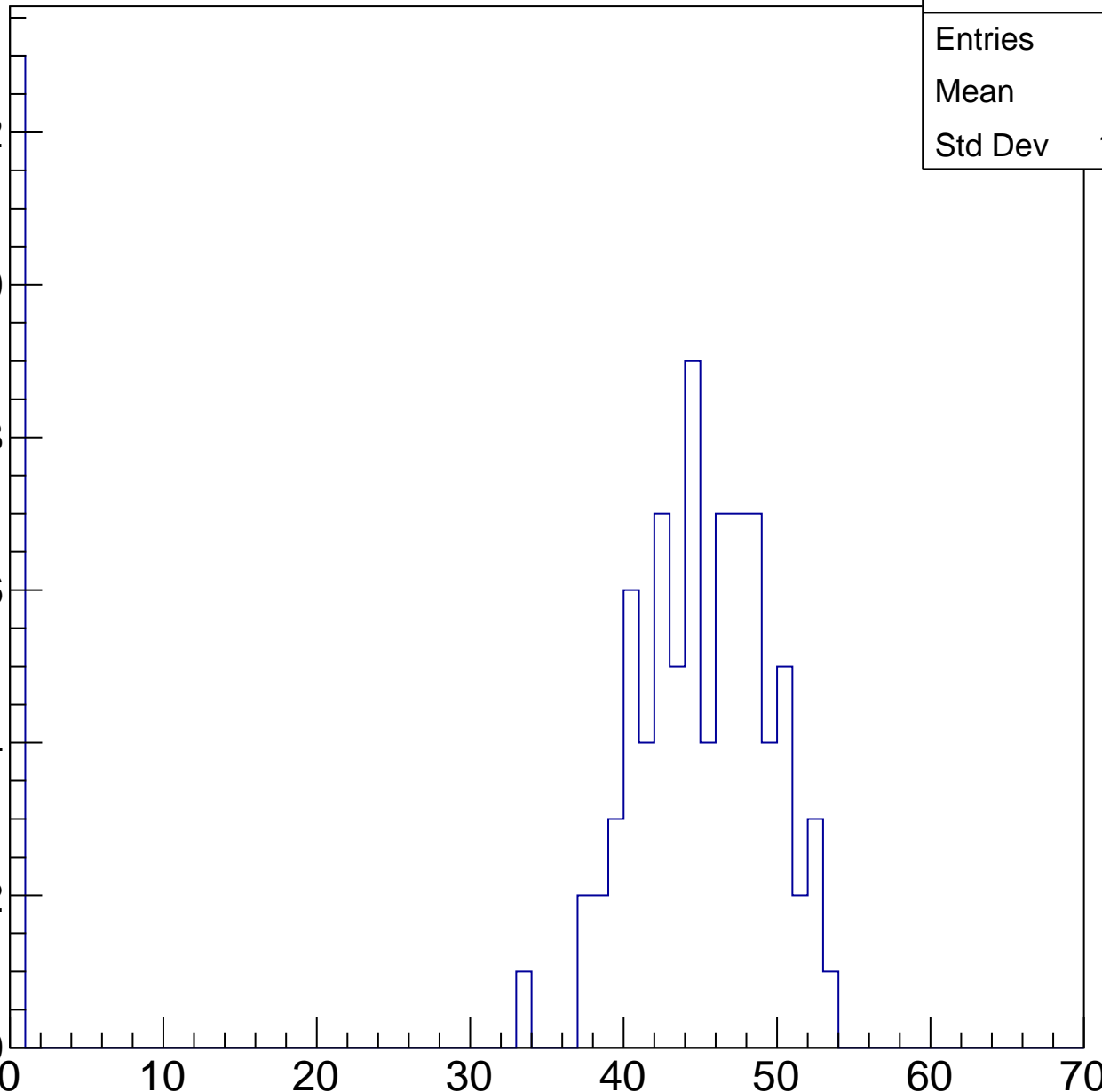
30

40

50

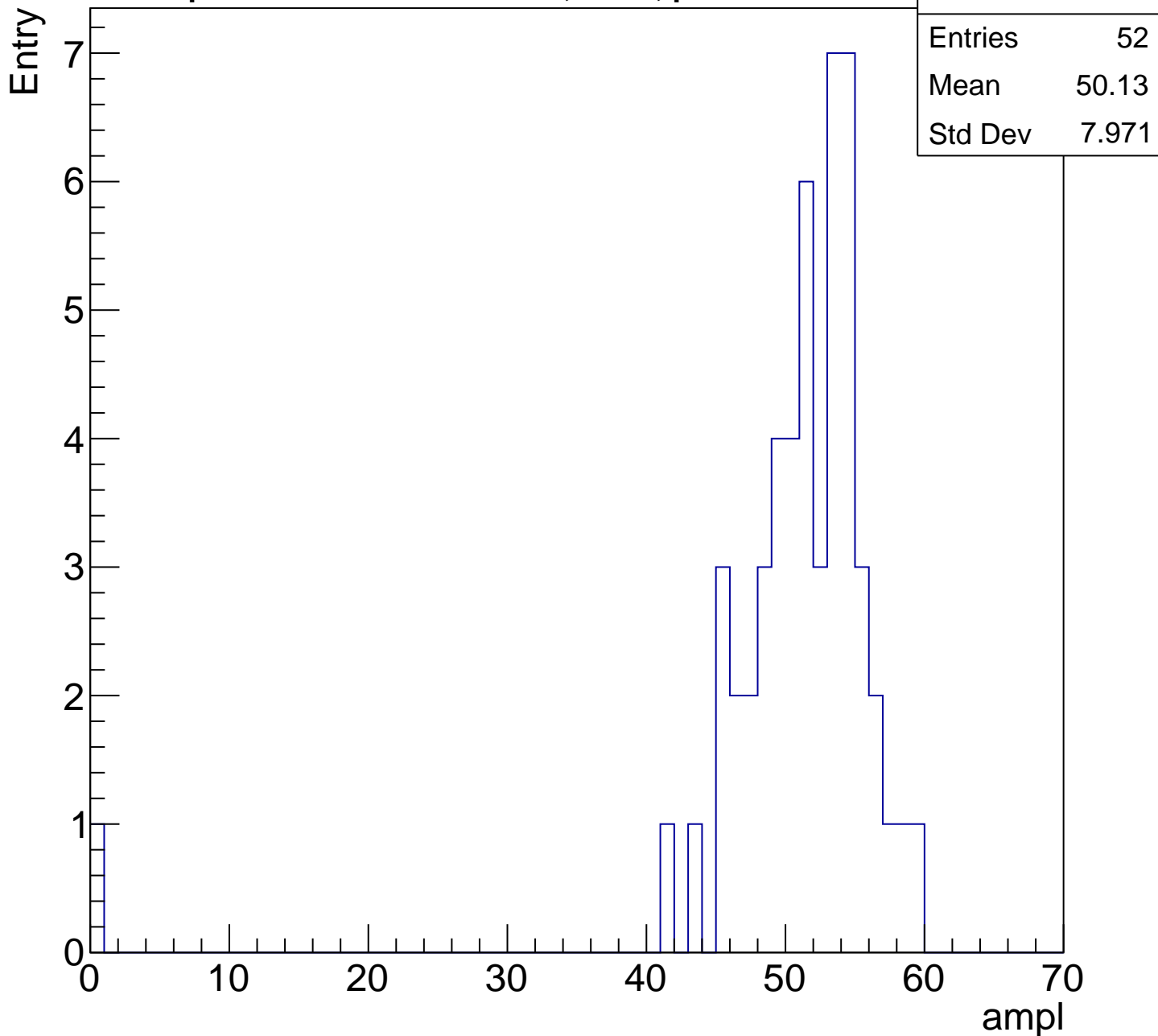
60

ampl



B1L103S, U1-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

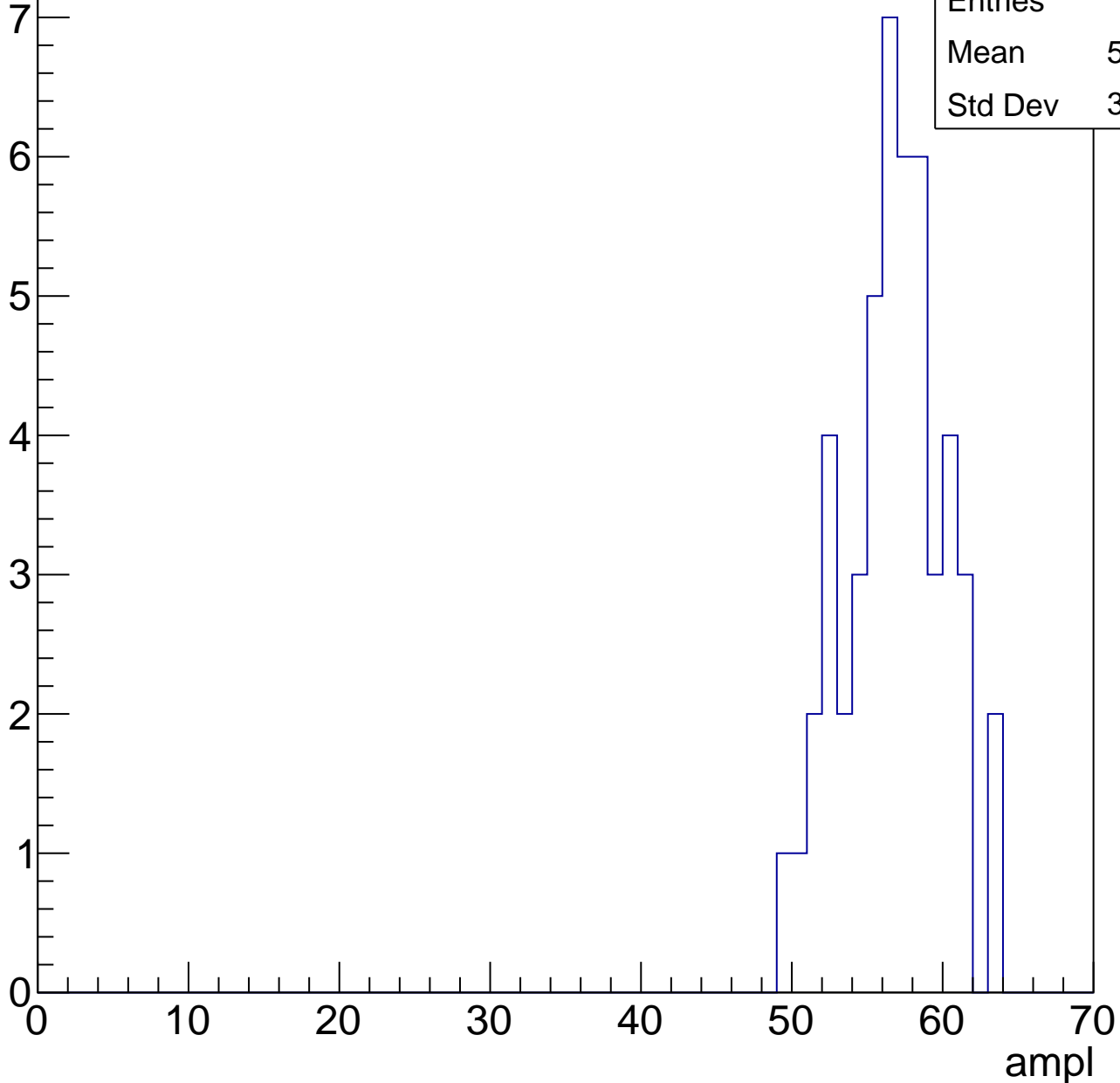


B1L103S, U1-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	56.33
Std Dev	3.266

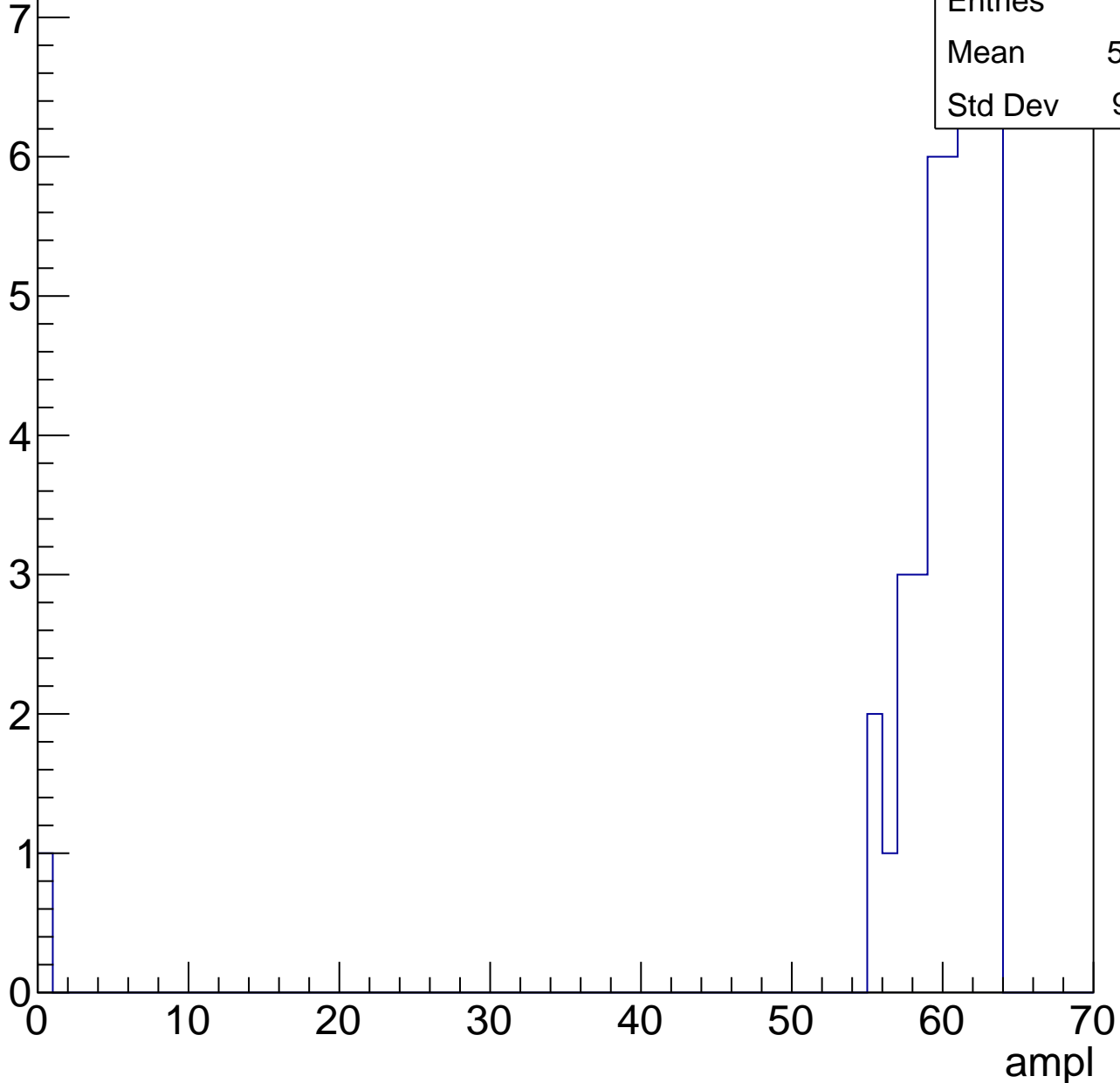


B1L103S, U1-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.77
Std Dev	9.331



B1L103S, U1-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	10.38
Std Dev	23.2

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



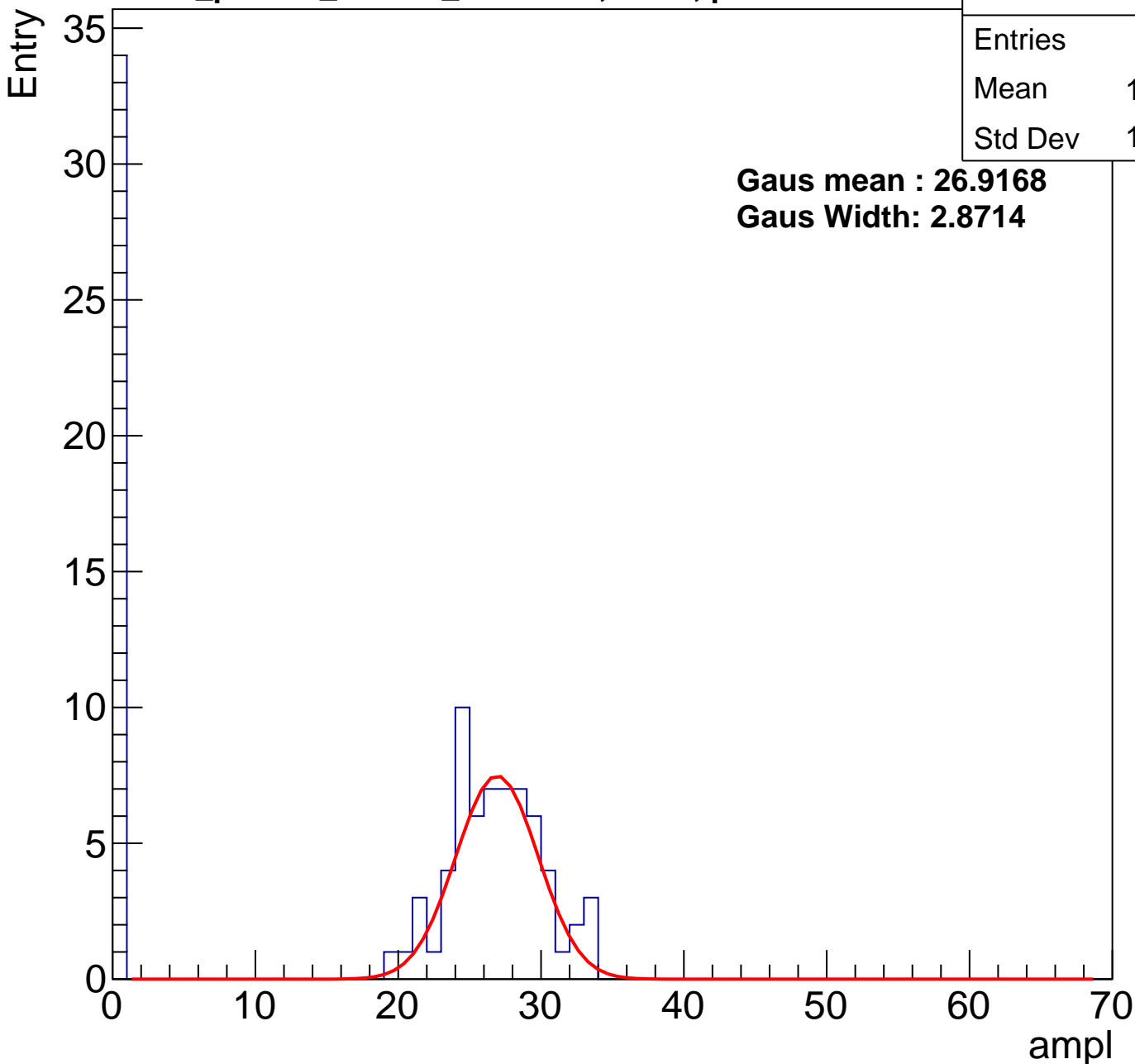
B1L103S, U1-ch69, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	17.12
Std Dev	12.84

Gaus mean : 26.9168

Gaus Width: 2.8714



B1L103S, U1-ch69, adc1

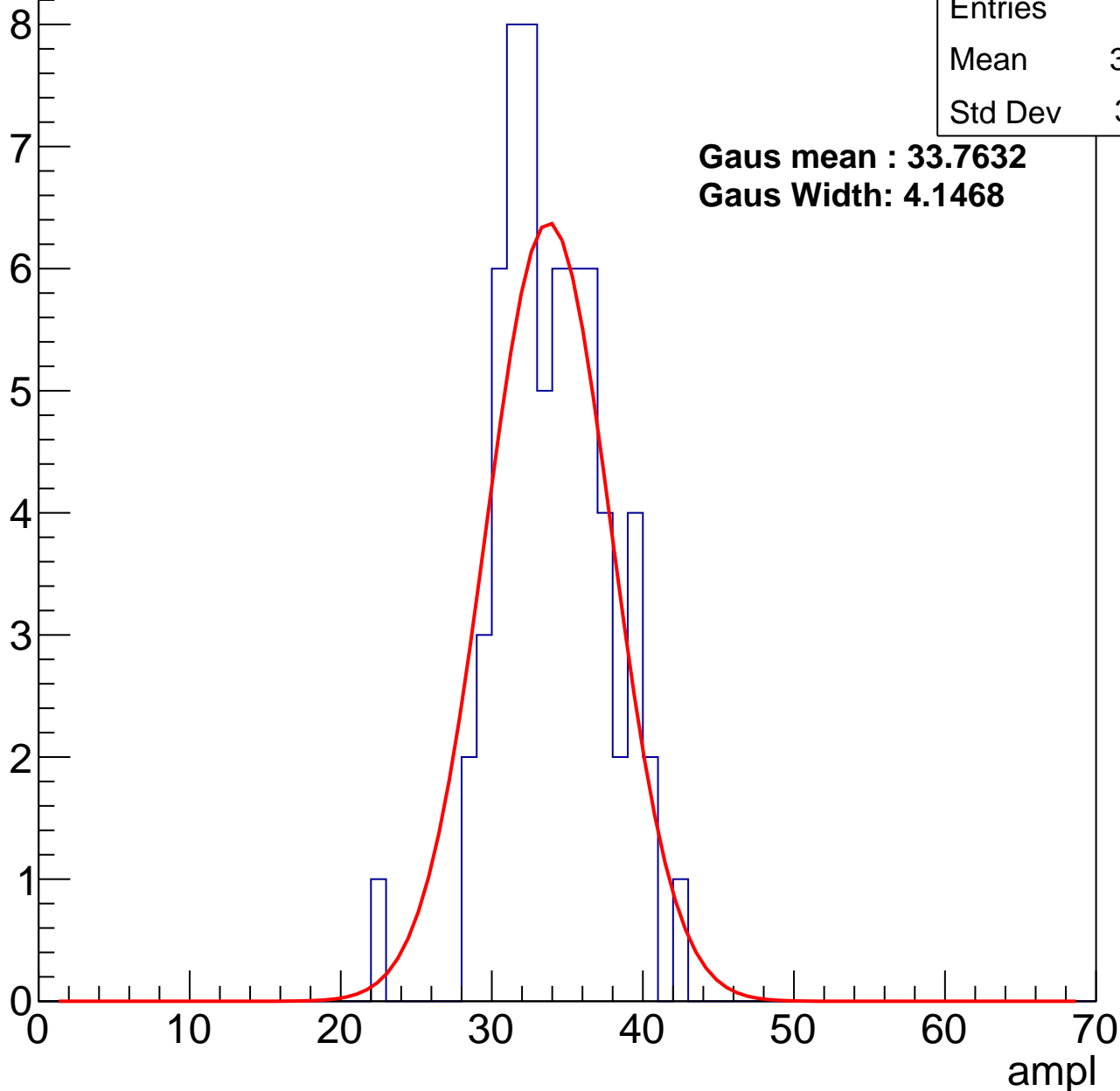
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.53
Std Dev	3.571

Gaus mean : 33.7632

Gaus Width: 4.1468



B1L103S, U1-ch69, adc2

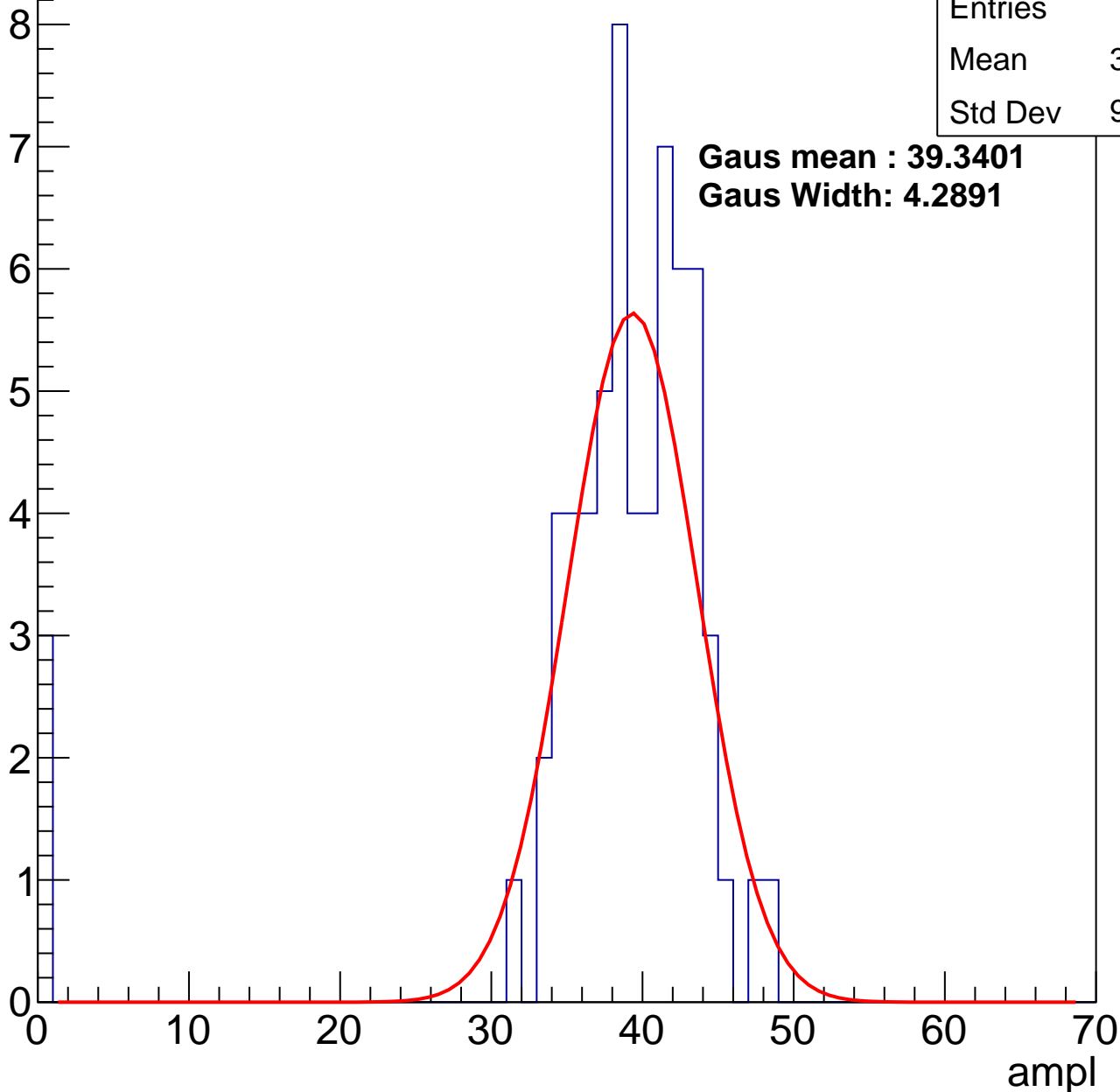
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.36
Std Dev	9.008

Gaus mean : 39.3401

Gaus Width: 4.2891



B1L103S, U1-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	38.17
Std Dev	16.84

Entry

10

8

6

4

2

0

0

10

20

30

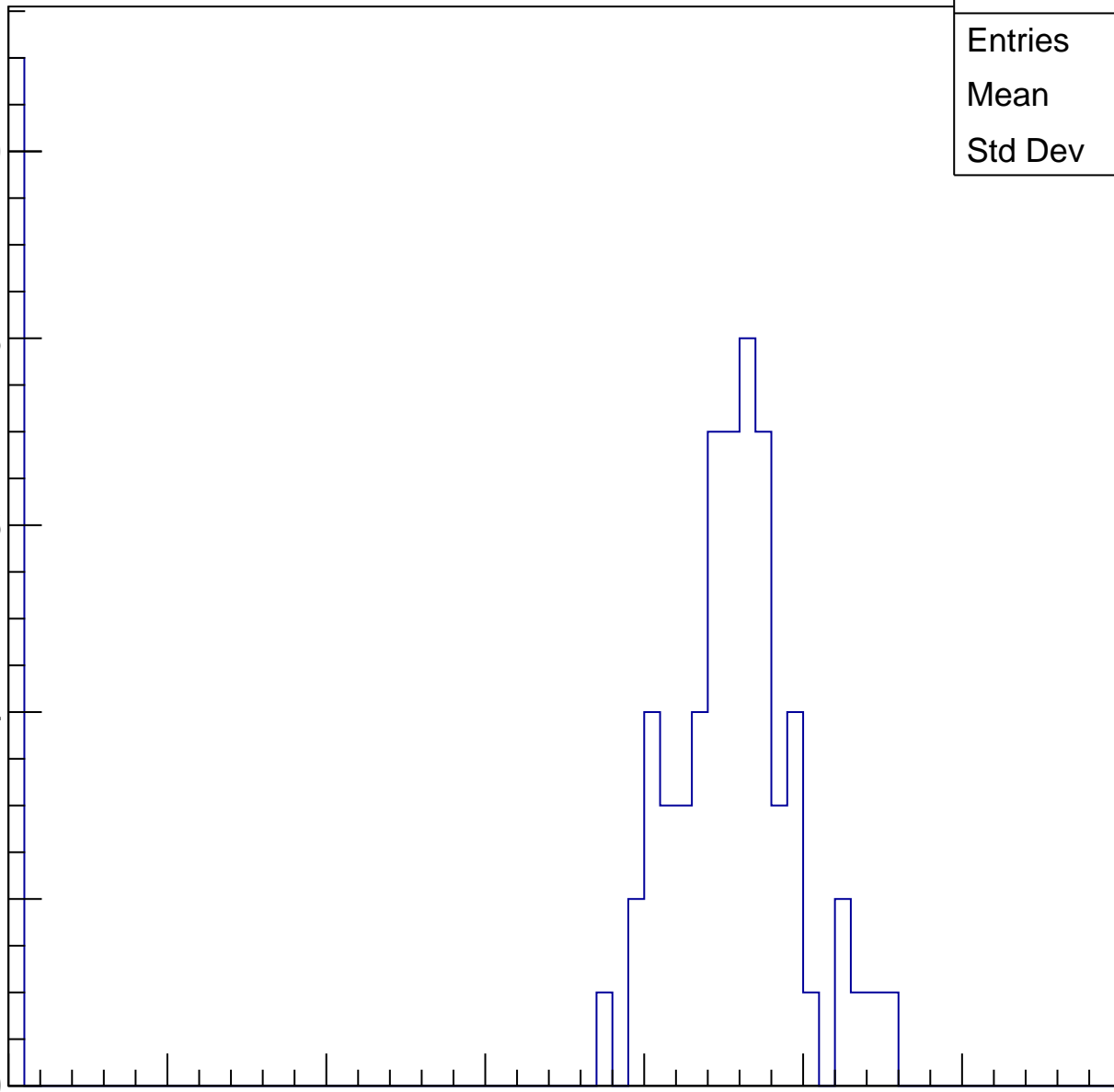
40

50

60

70

ampl

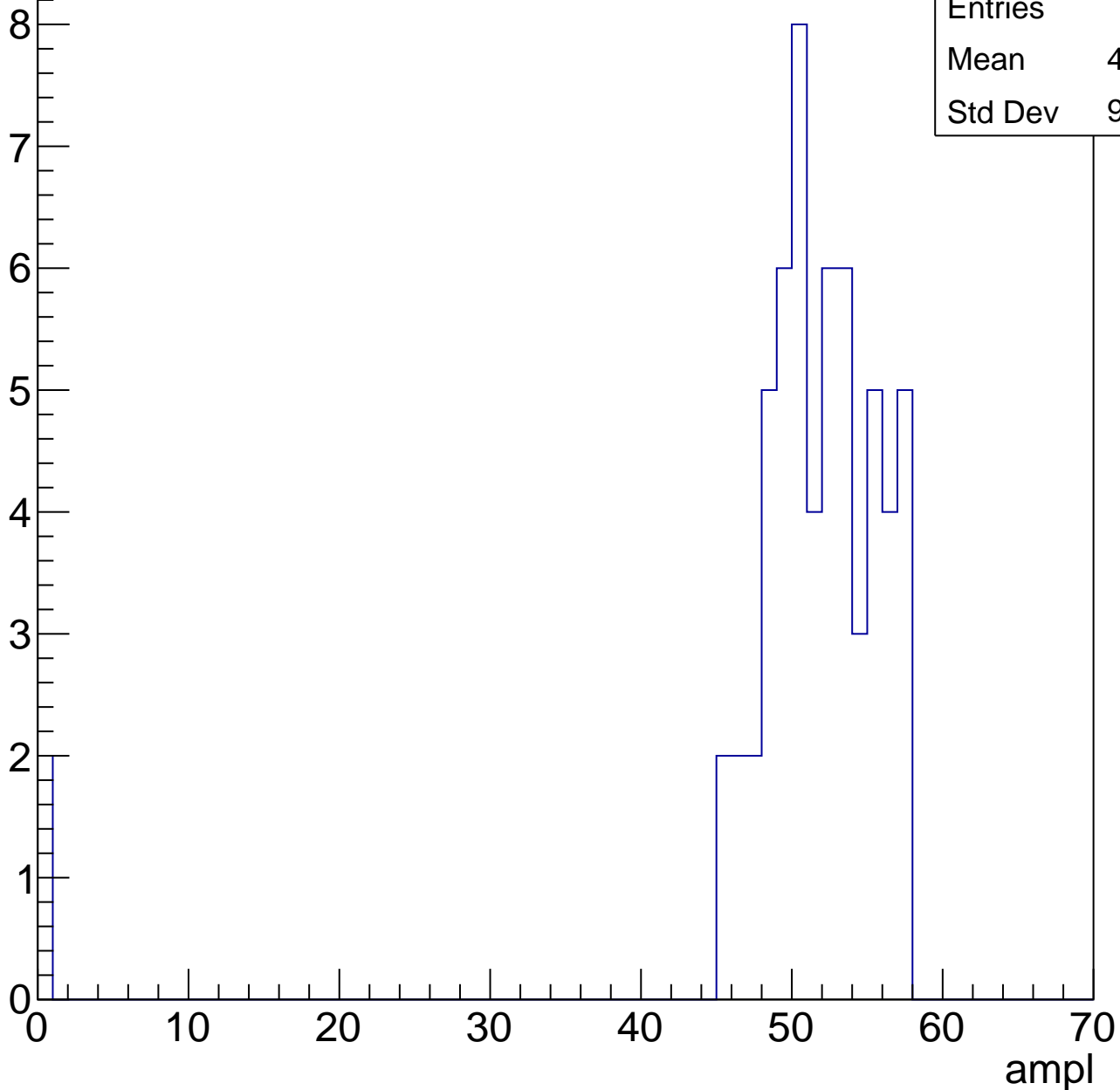


B1L103S, U1-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

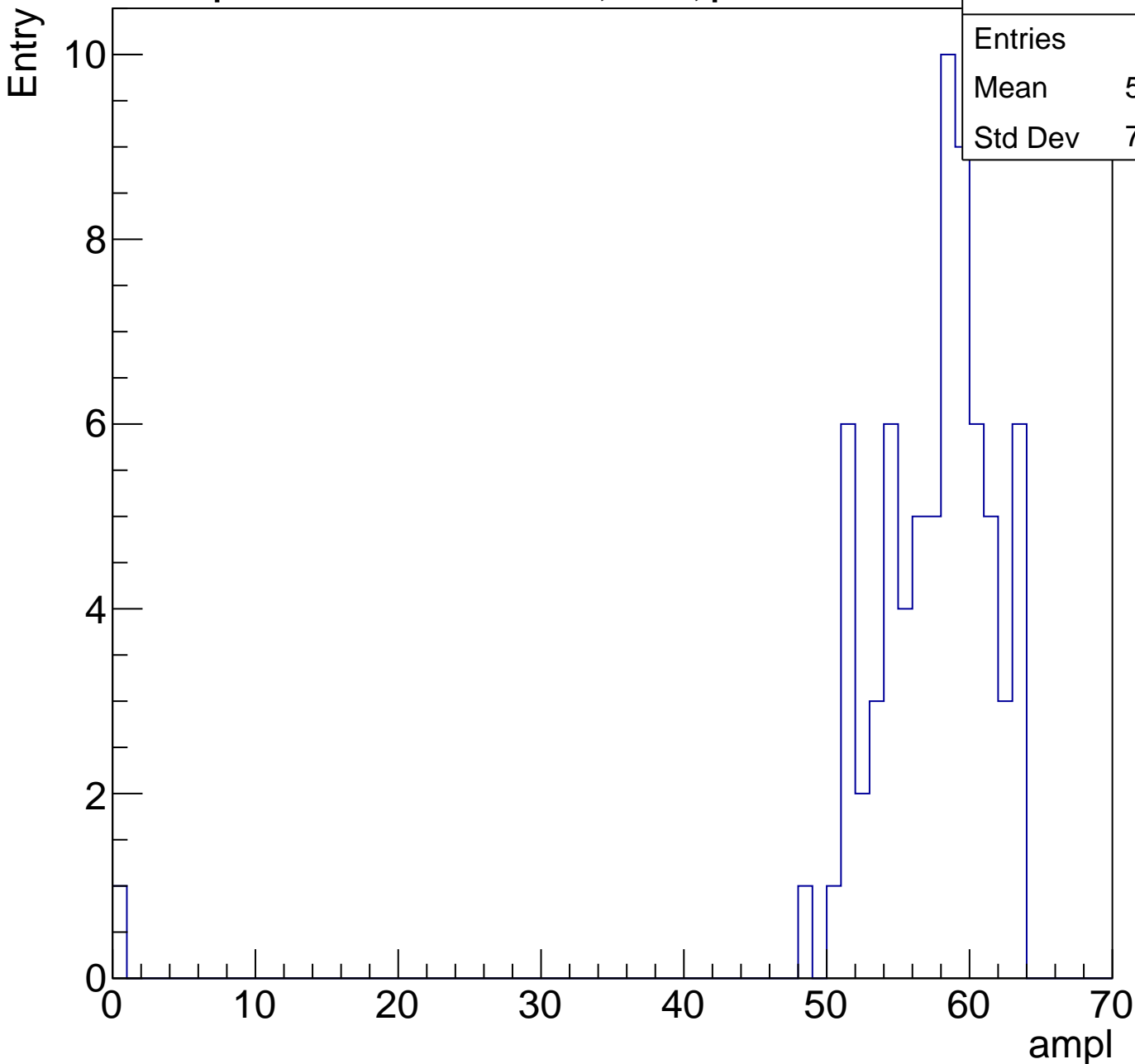
Entries	60
Mean	49.83
Std Dev	9.805



B1L103S, U1-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

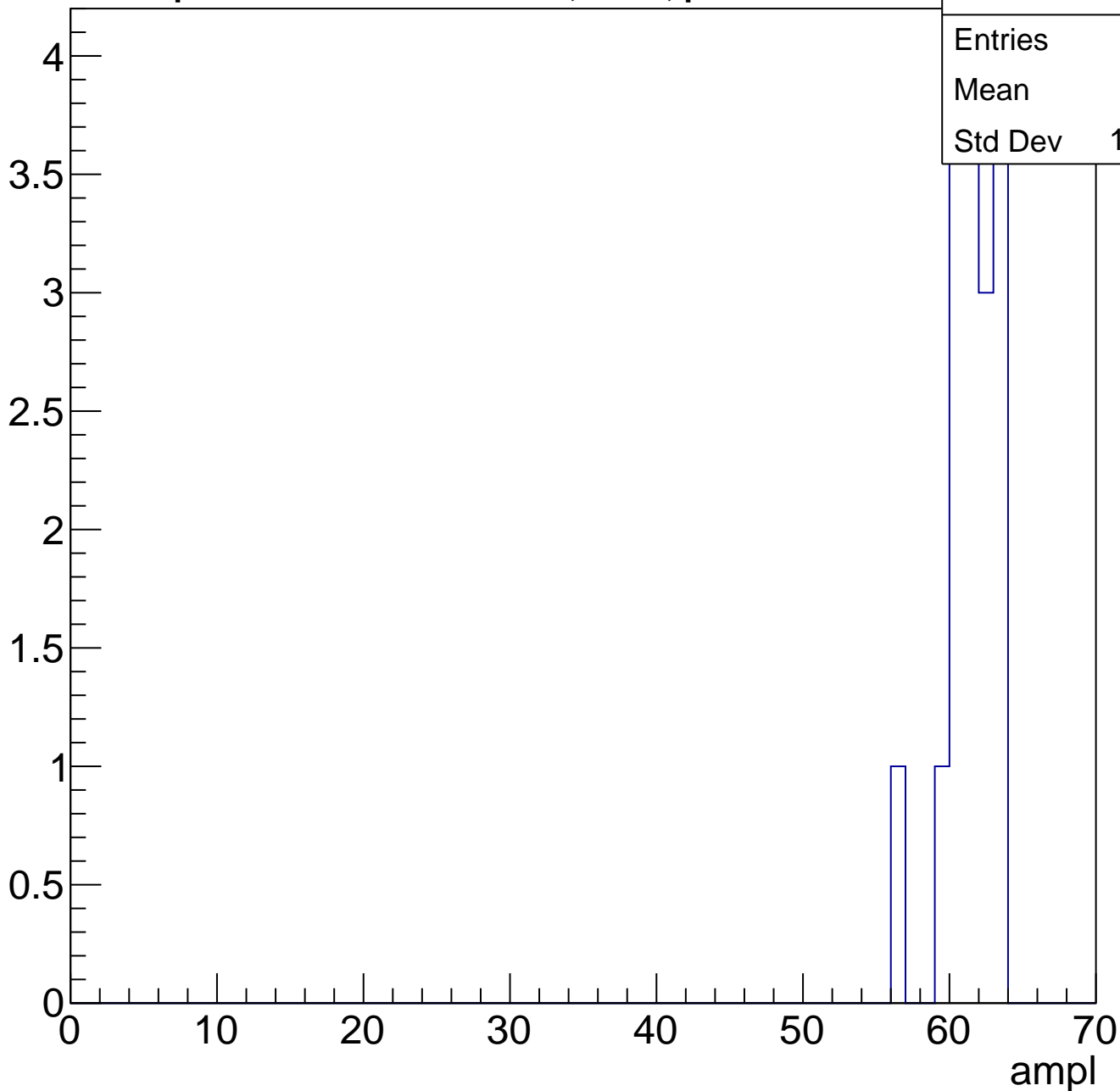
Entries	73
Mean	56.38
Std Dev	7.595



B1L103S, U1-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

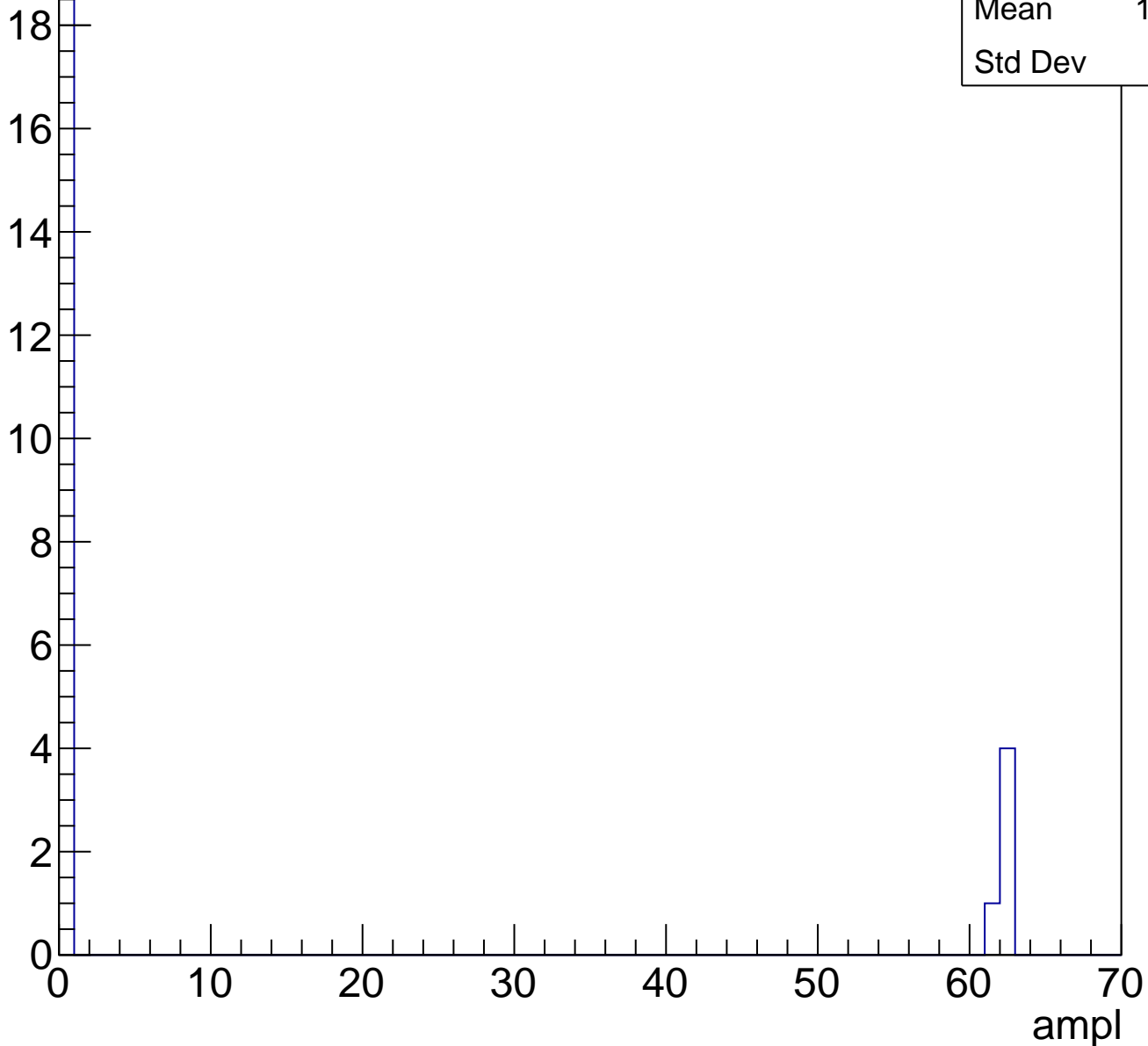


B1L103S, U1-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.88
Std Dev	25.1

Entry



B1L103S, U1-ch70, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	20.91
Std Dev	10.86

Gaus mean : 26.6112

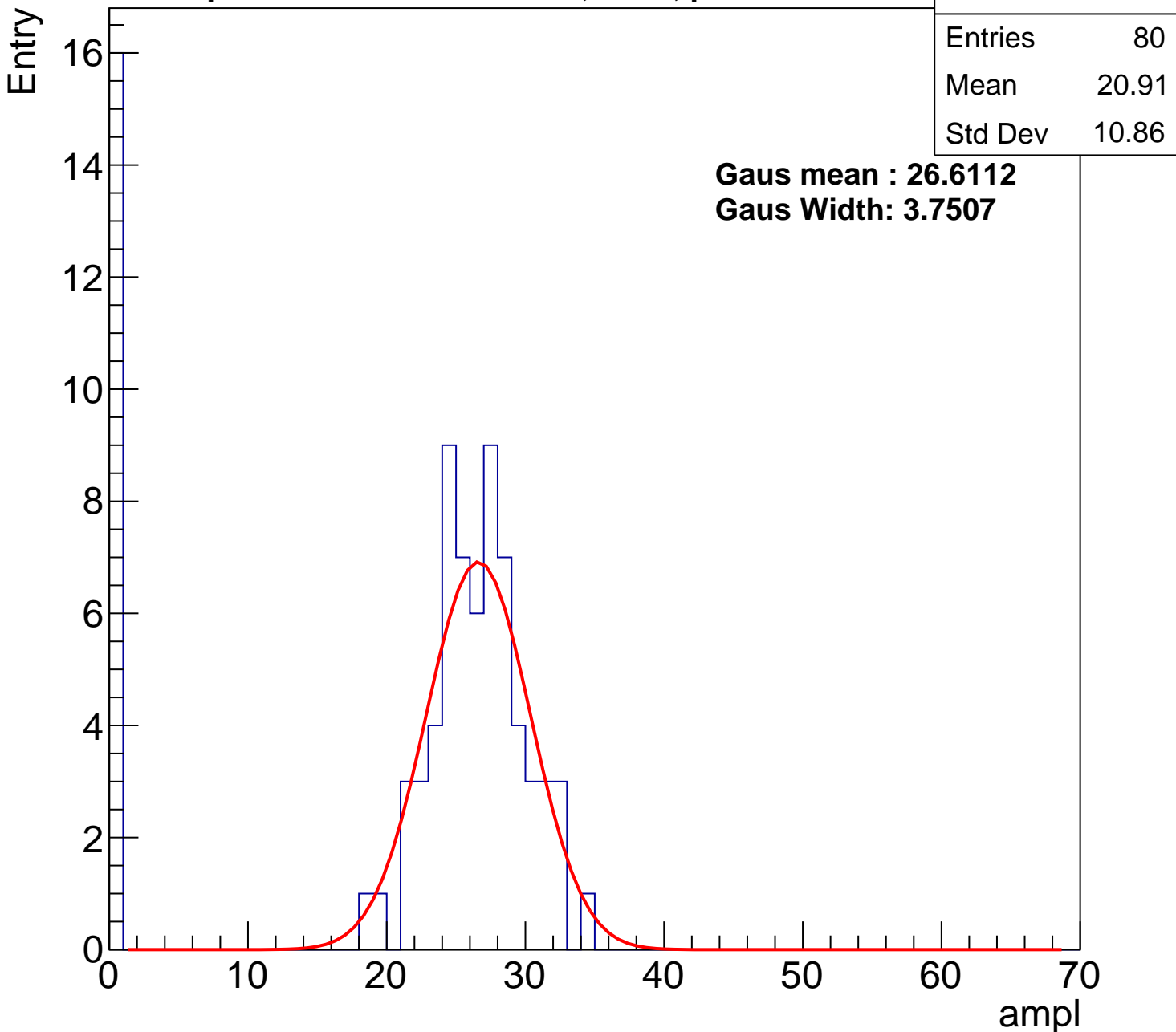
Gaus Width: 3.7507

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch70, adc1

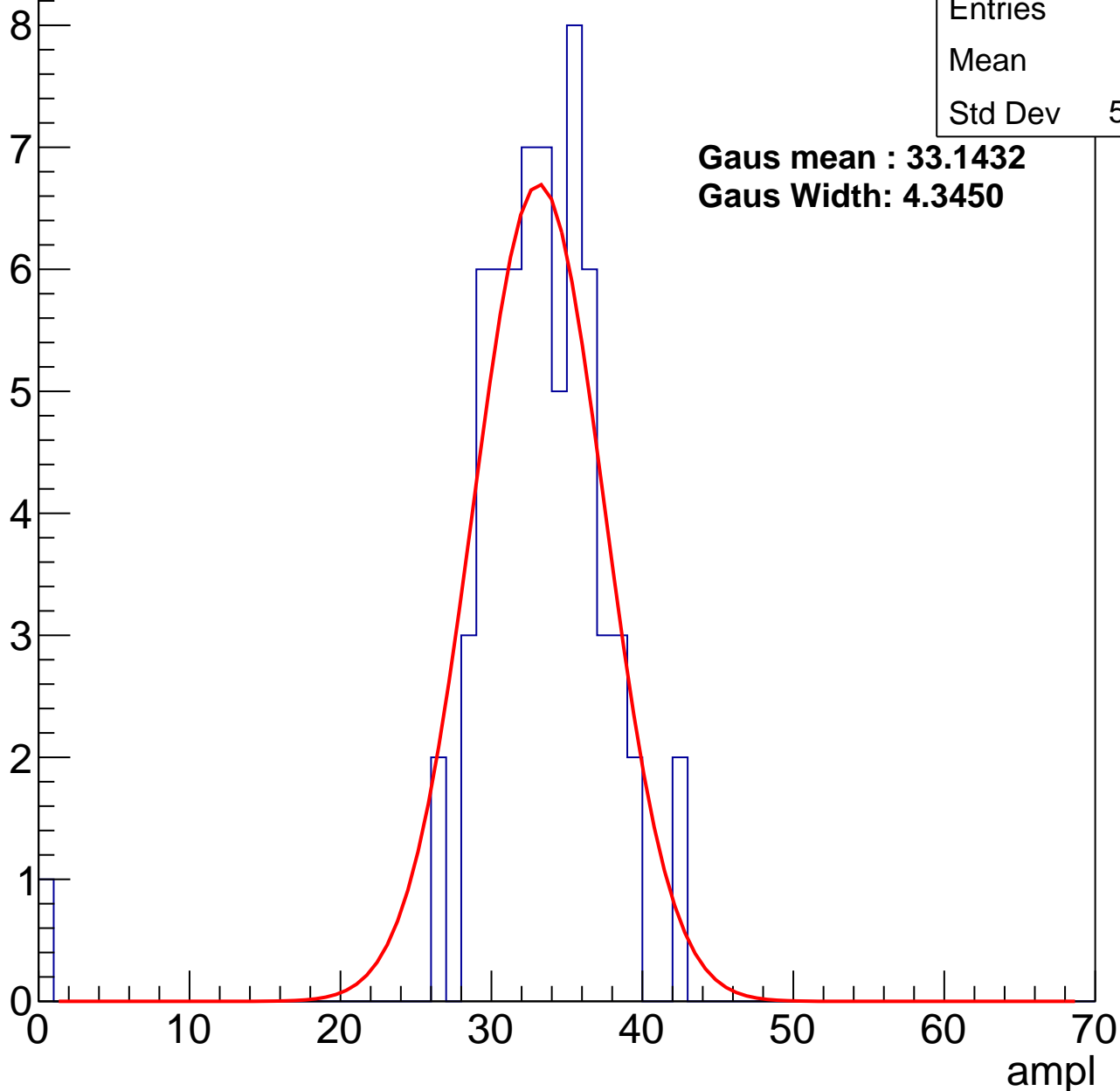
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.6
Std Dev	5.297

Gaus mean : 33.1432

Gaus Width: 4.3450



B1L103S, U1-ch70, adc2

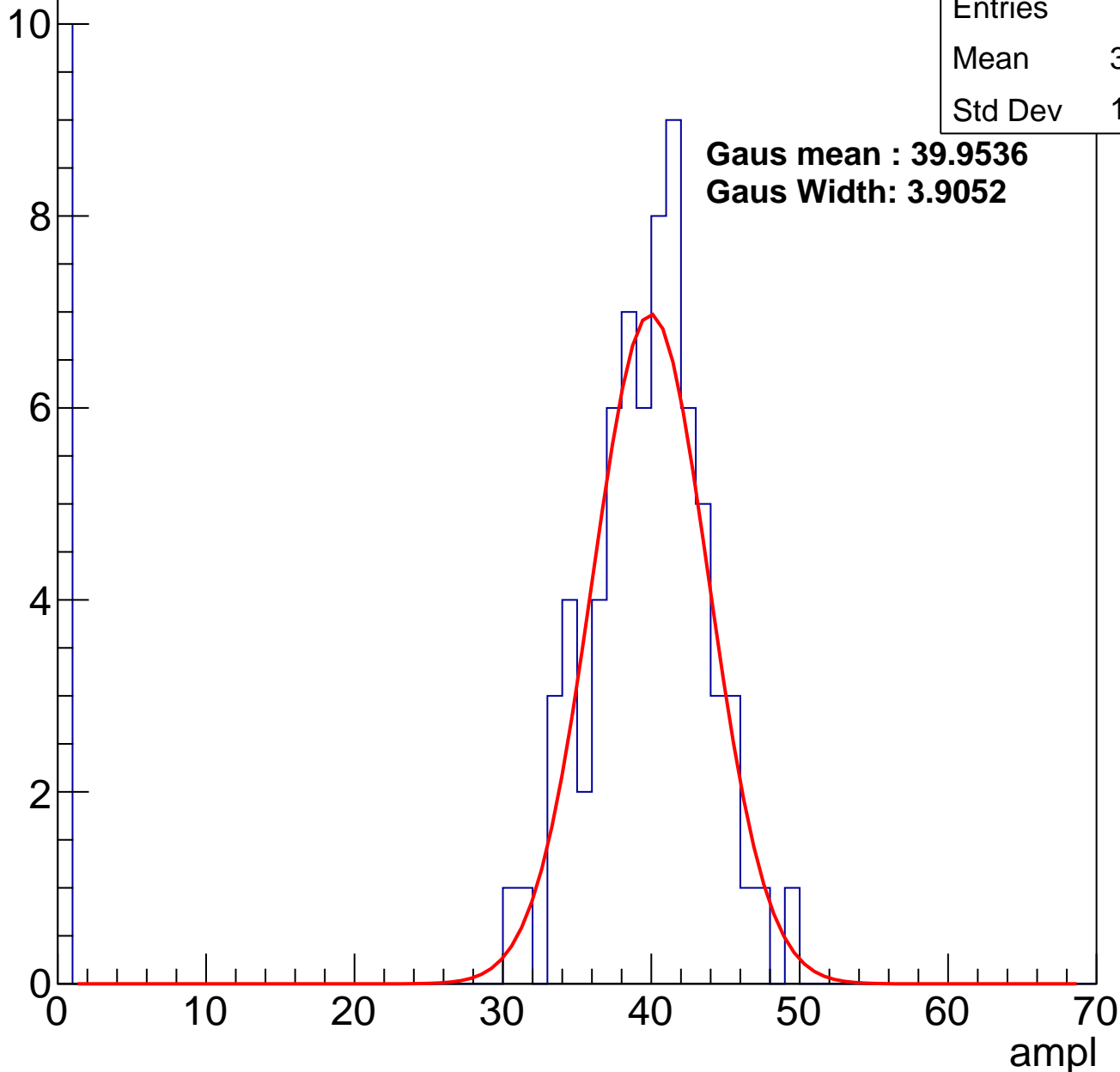
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	34.53
Std Dev	13.43

Gaus mean : 39.9536

Gaus Width: 3.9052

Entry

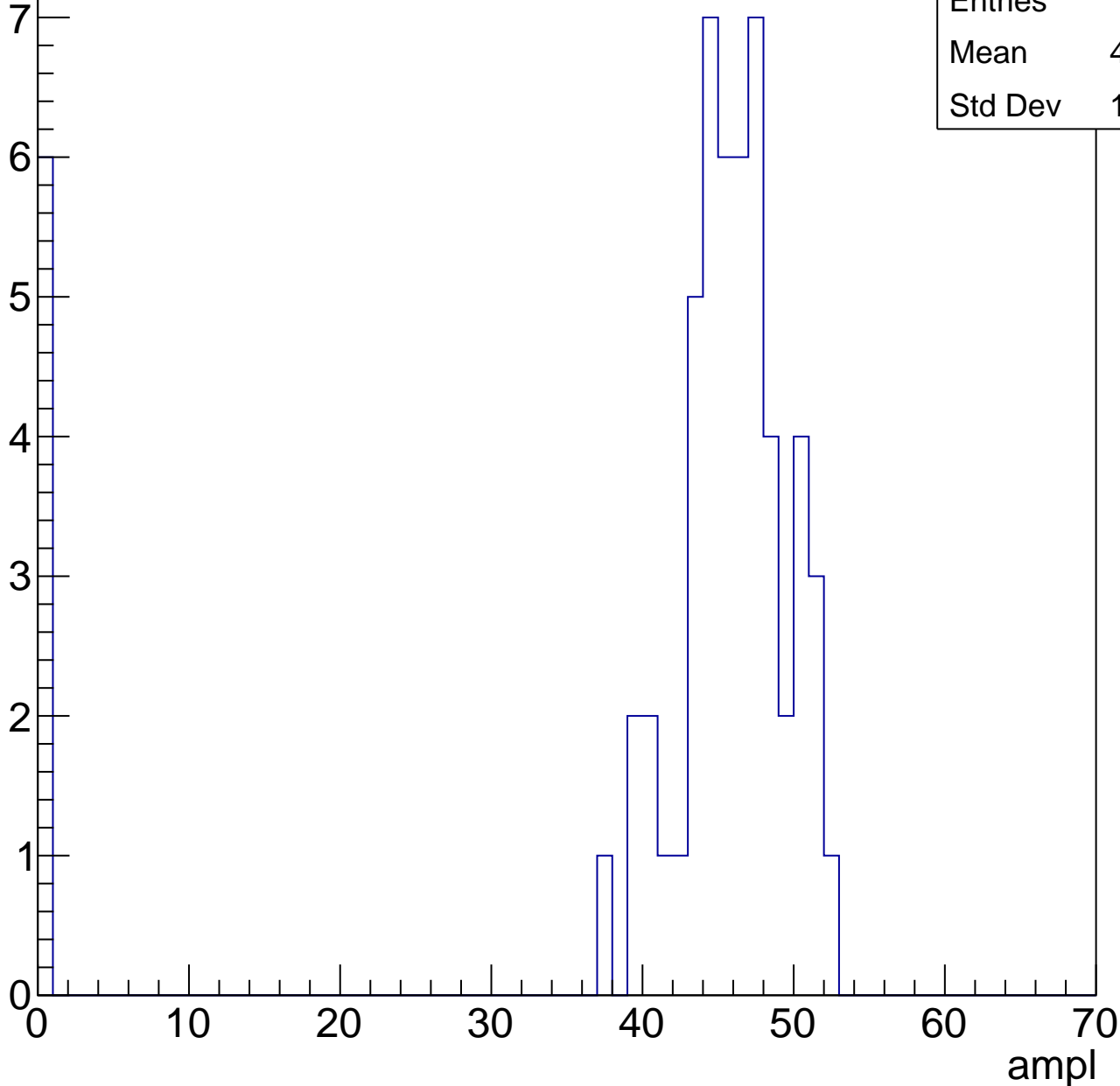


B1L103S, U1-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.88
Std Dev	14.24

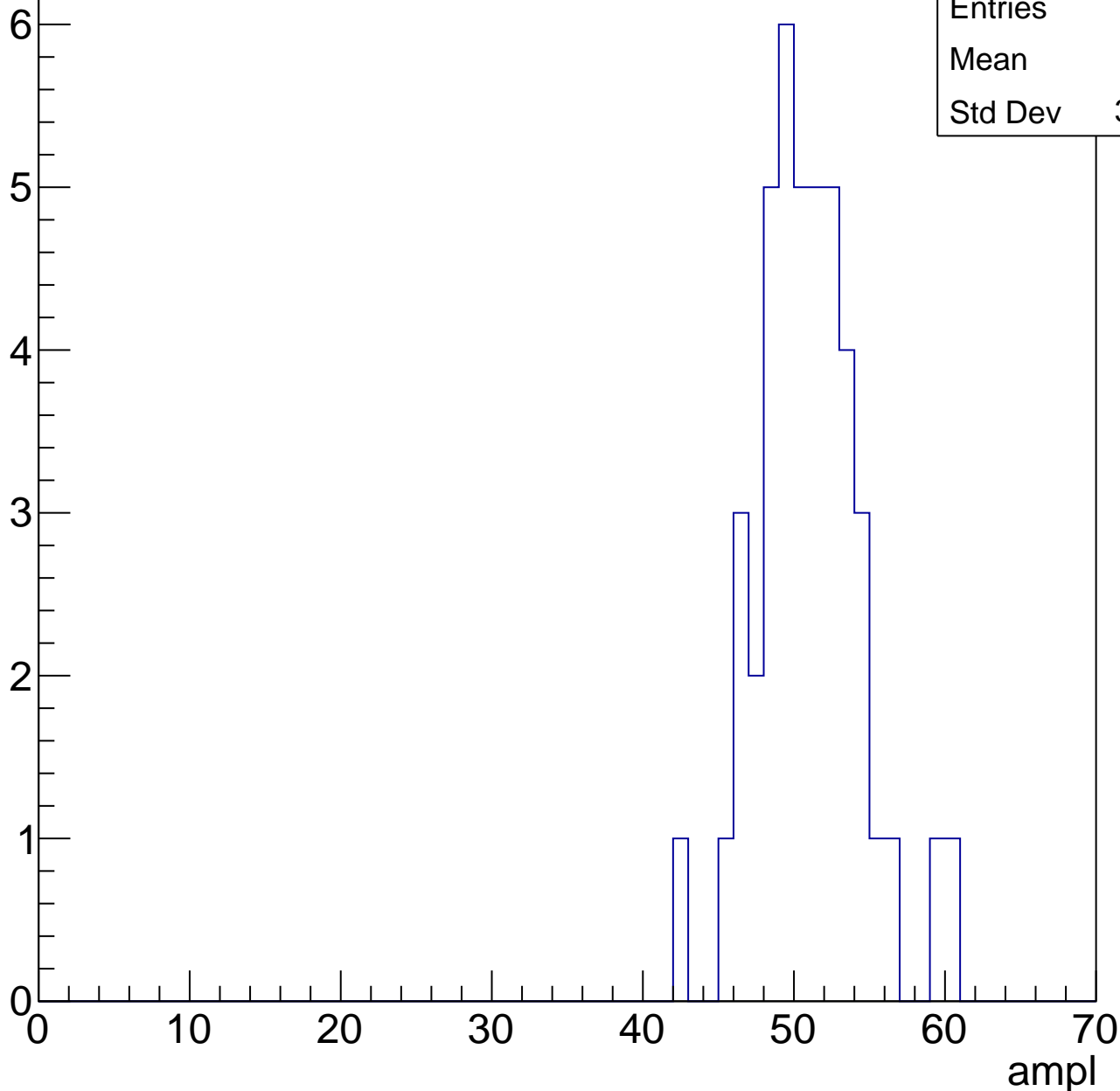


B1L103S, U1-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

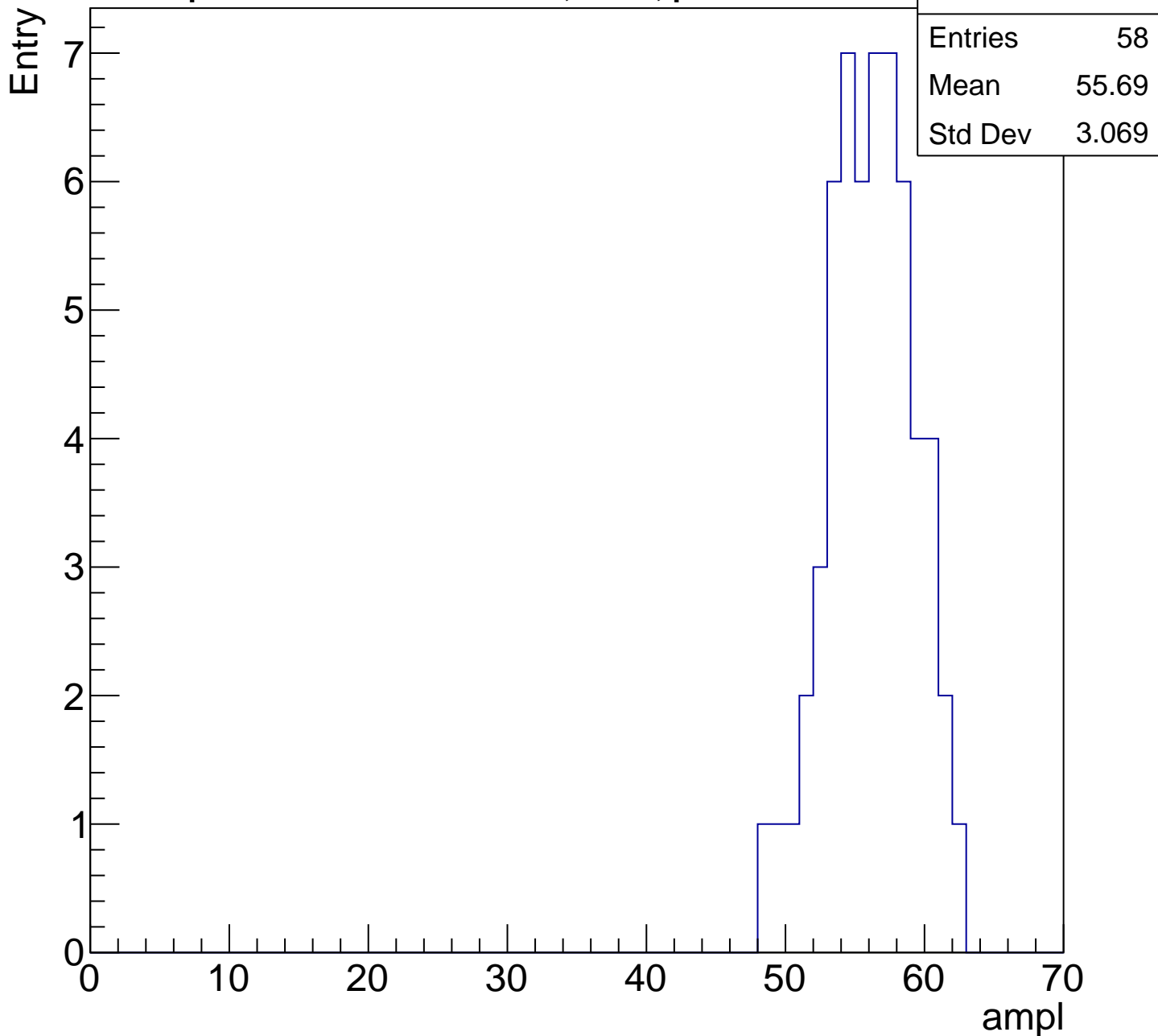
Entry

Entries	44
Mean	50.5
Std Dev	3.441



B1L103S, U1-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

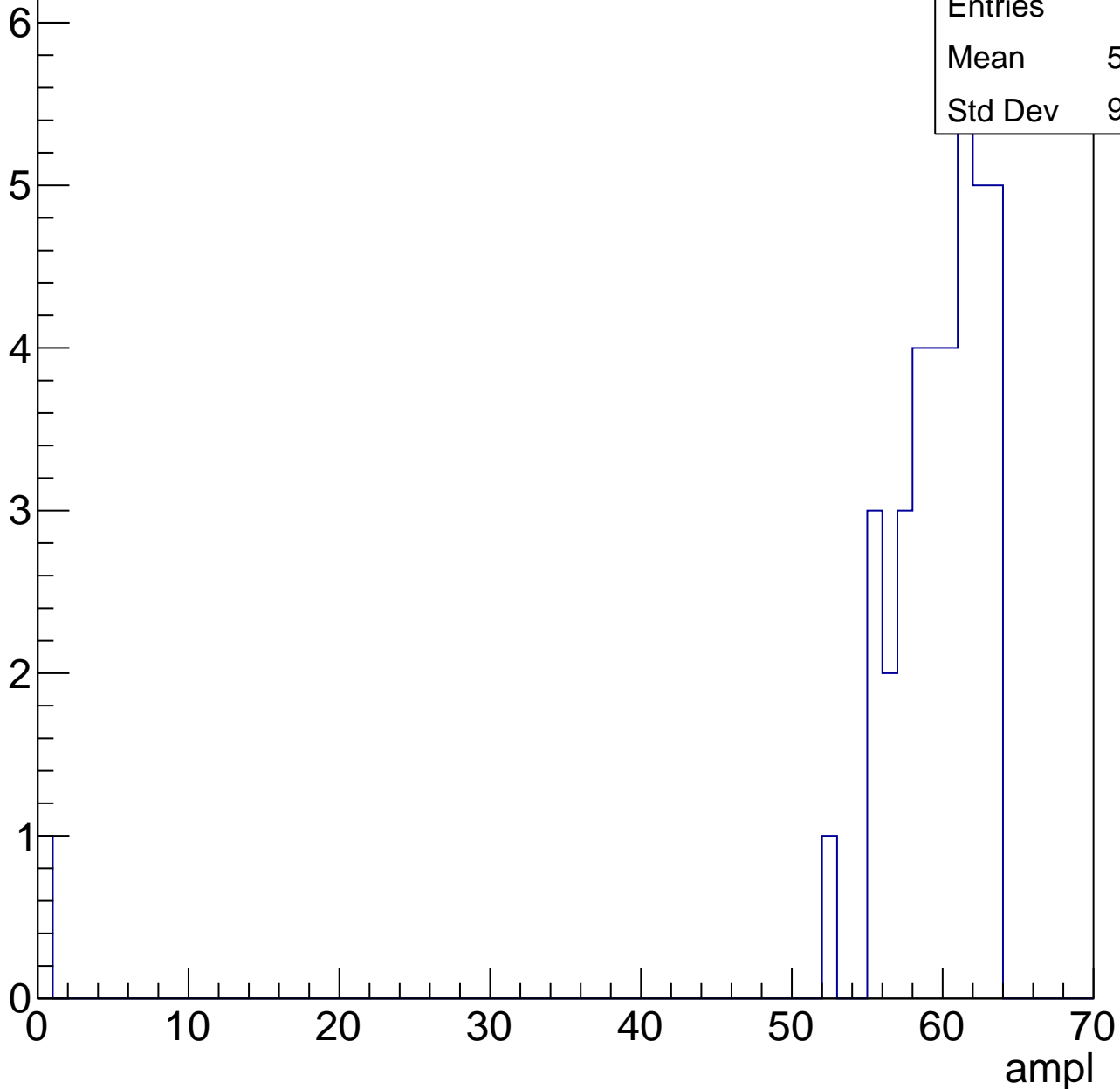


B1L103S, U1-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	57.87
Std Dev	9.889

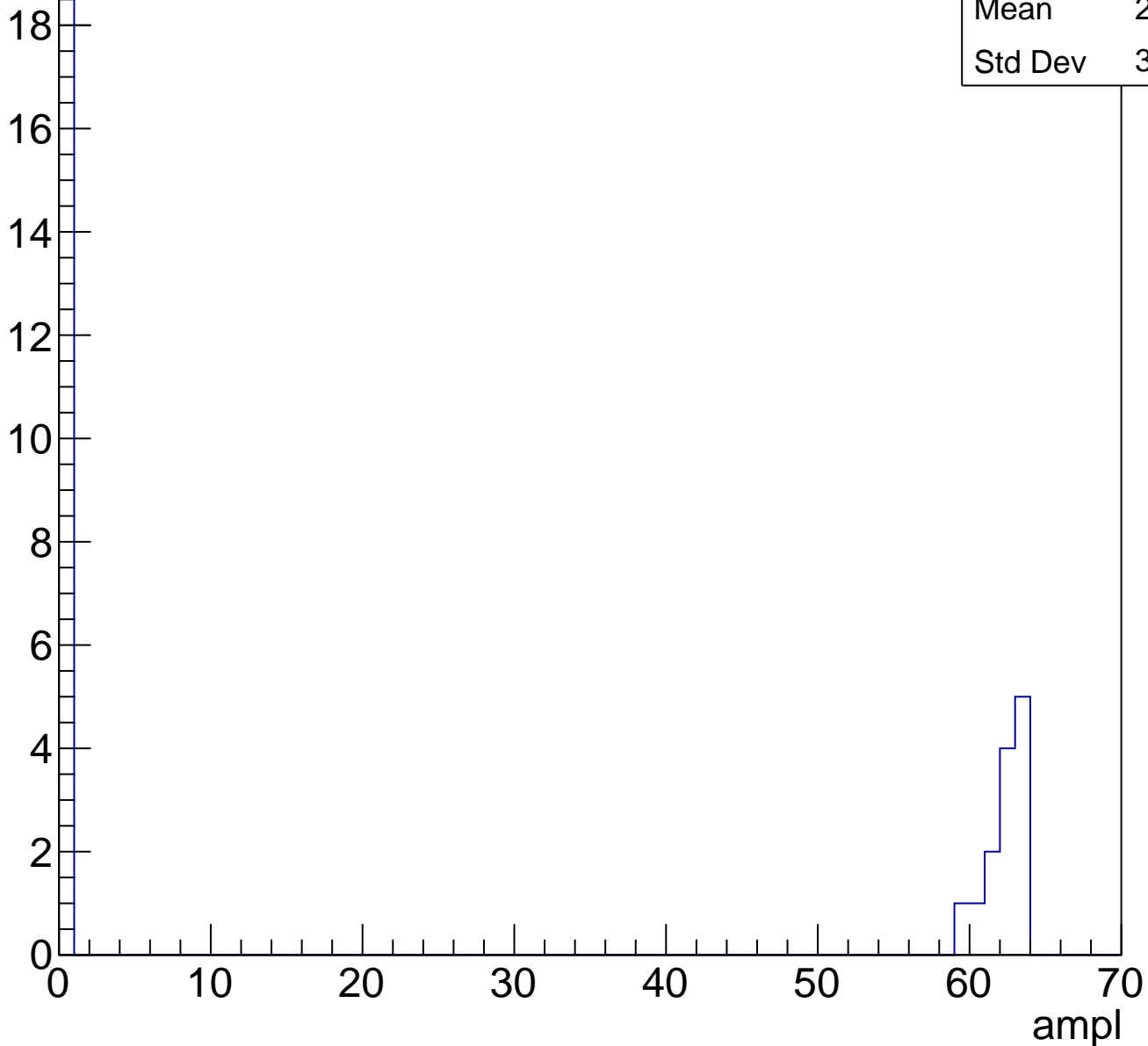


B1L103S, U1-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	32
Mean	25.12
Std Dev	30.38

Entry



B1L103S, U1-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	19
Std Dev	12.83

Gaus mean : 27.8086

Gaus Width: 3.5939

Entry

30

25

20

15

10

5

0

0

10

20

30

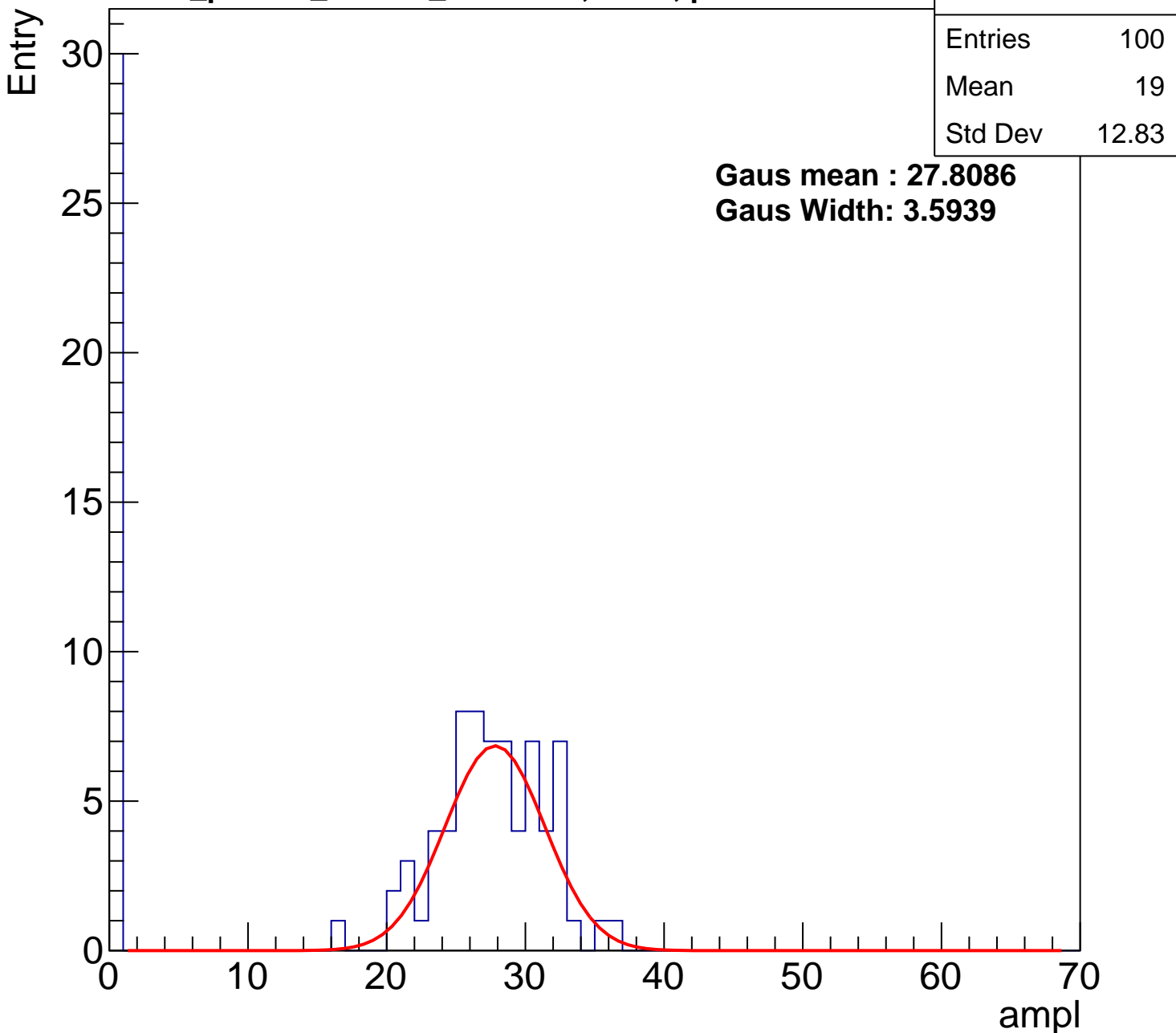
40

50

60

70

ampl



B1L103S, U1-ch71, adc1

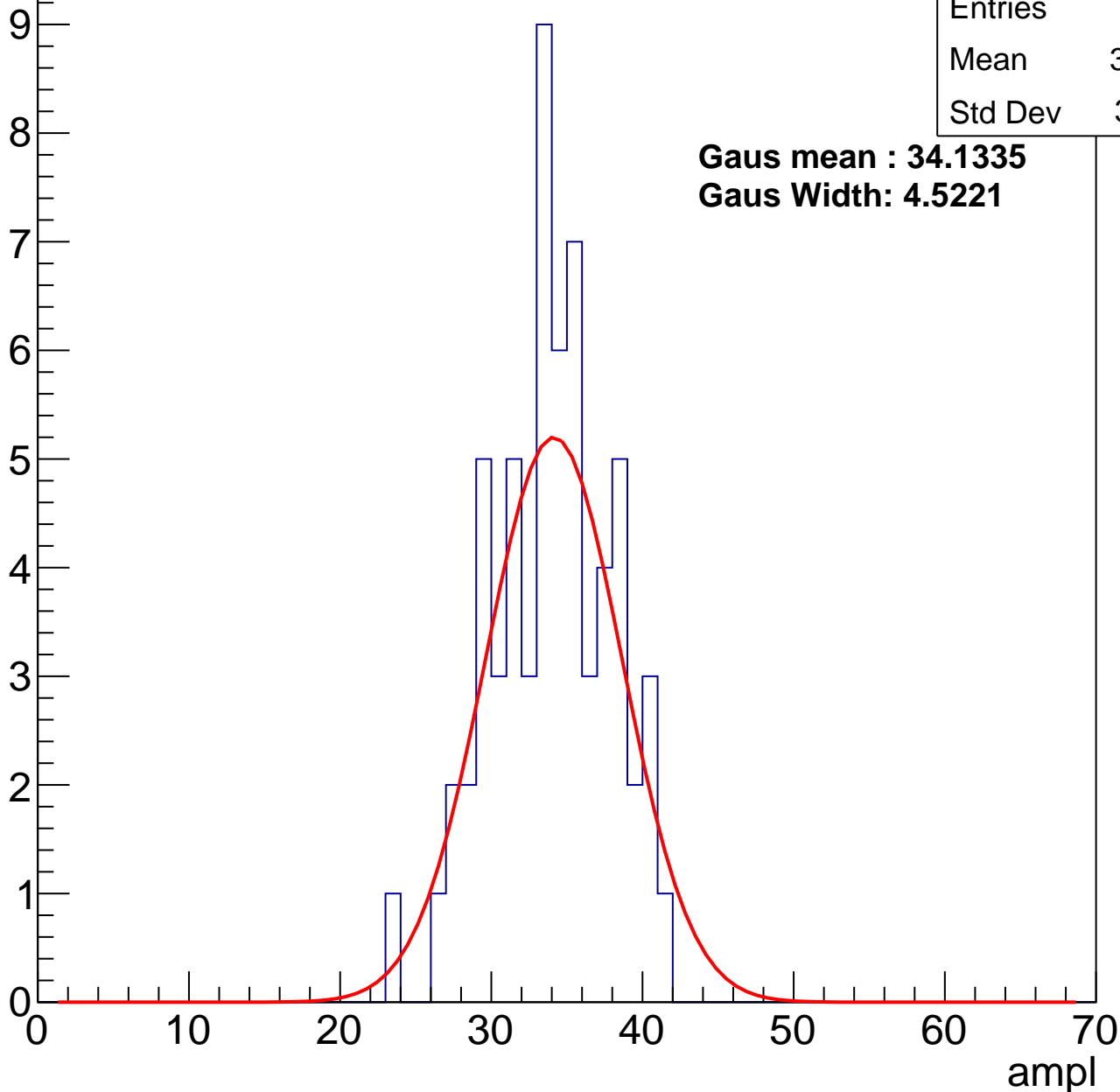
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	33.48
Std Dev	3.851

Gaus mean : 34.1335

Gaus Width: 4.5221



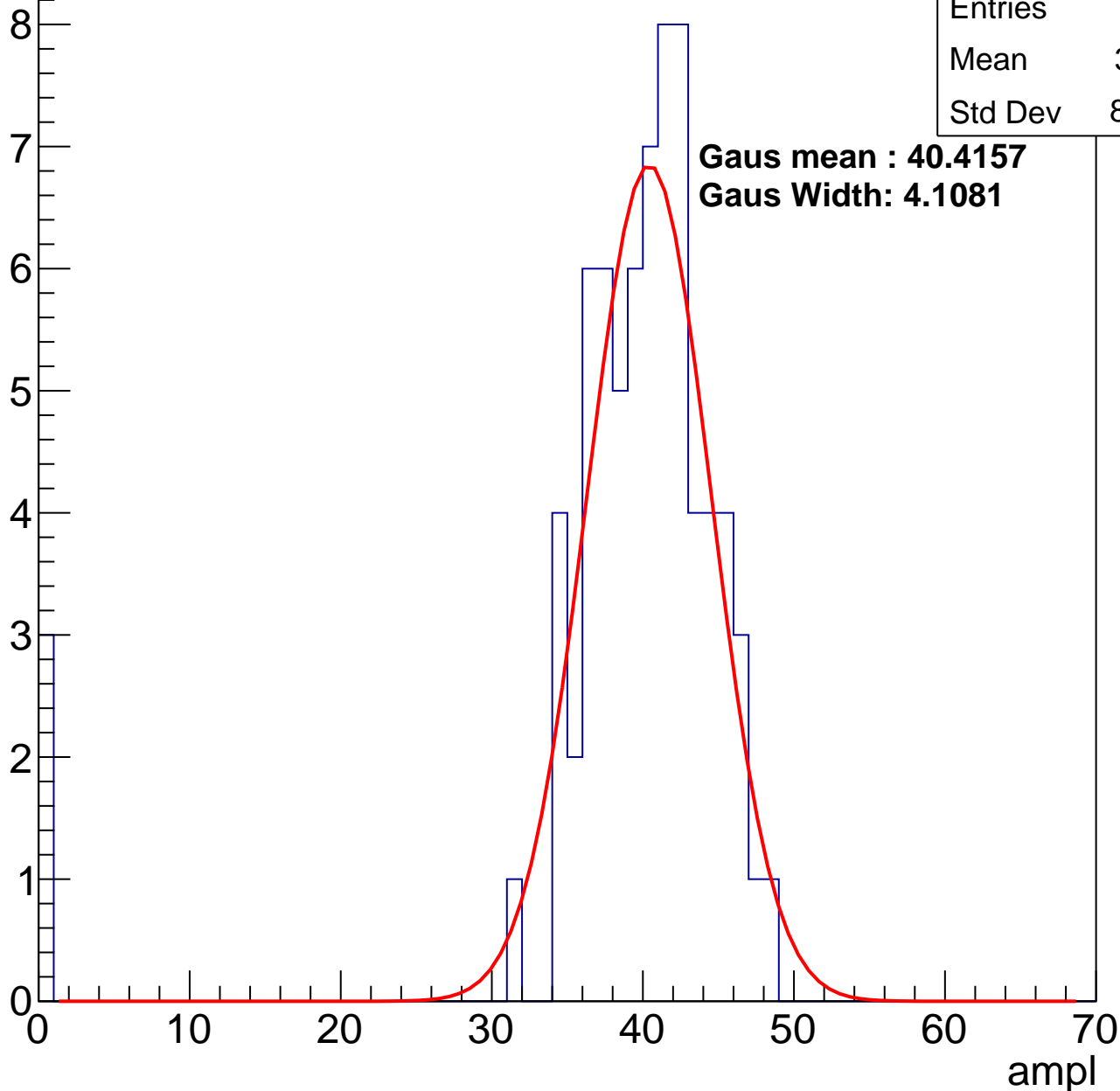
B1L103S, U1-ch71, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.41
Std Dev	8.702

Gaus mean : 40.4157
Gaus Width: 4.1081

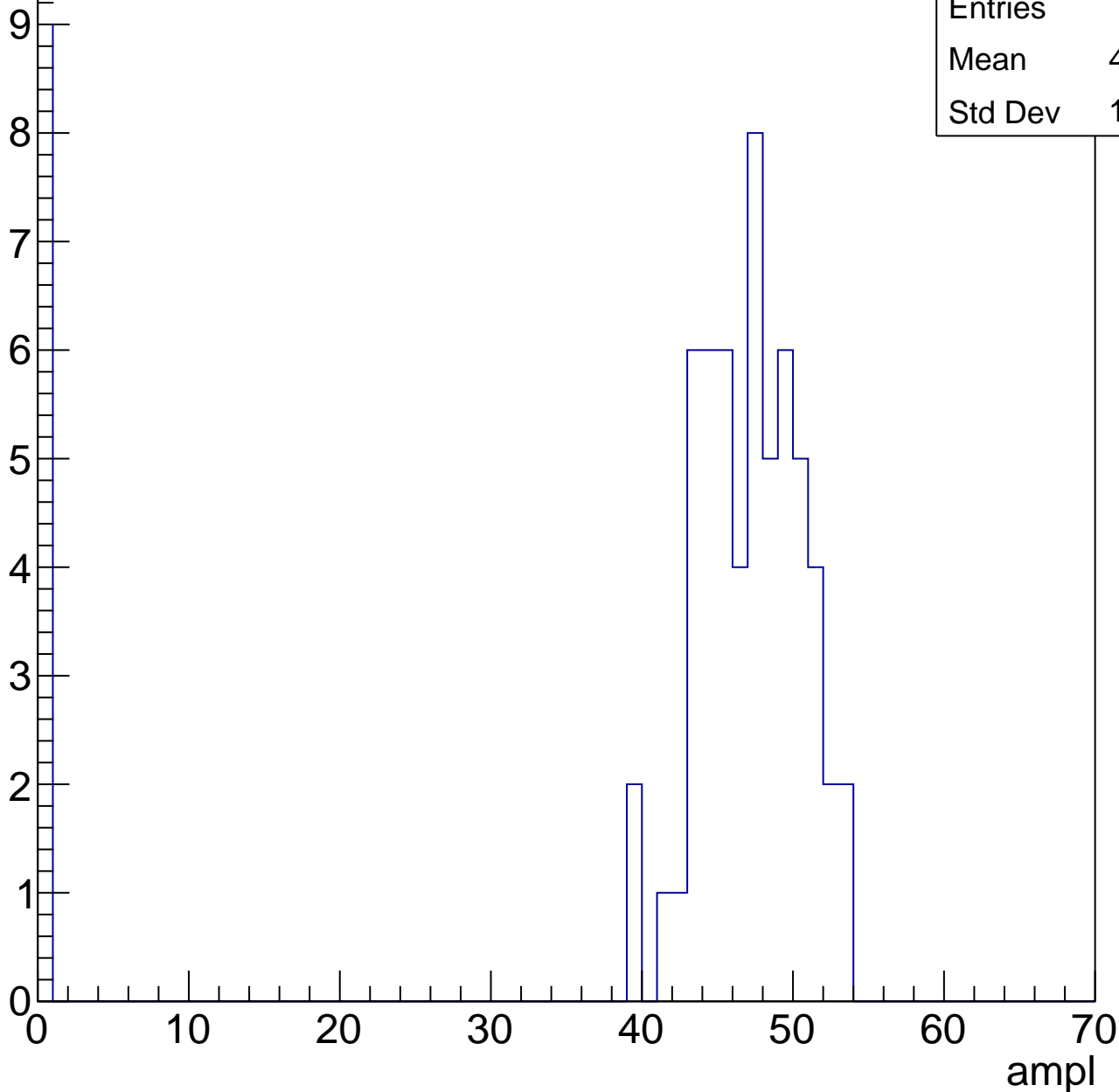


B1L103S, U1-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

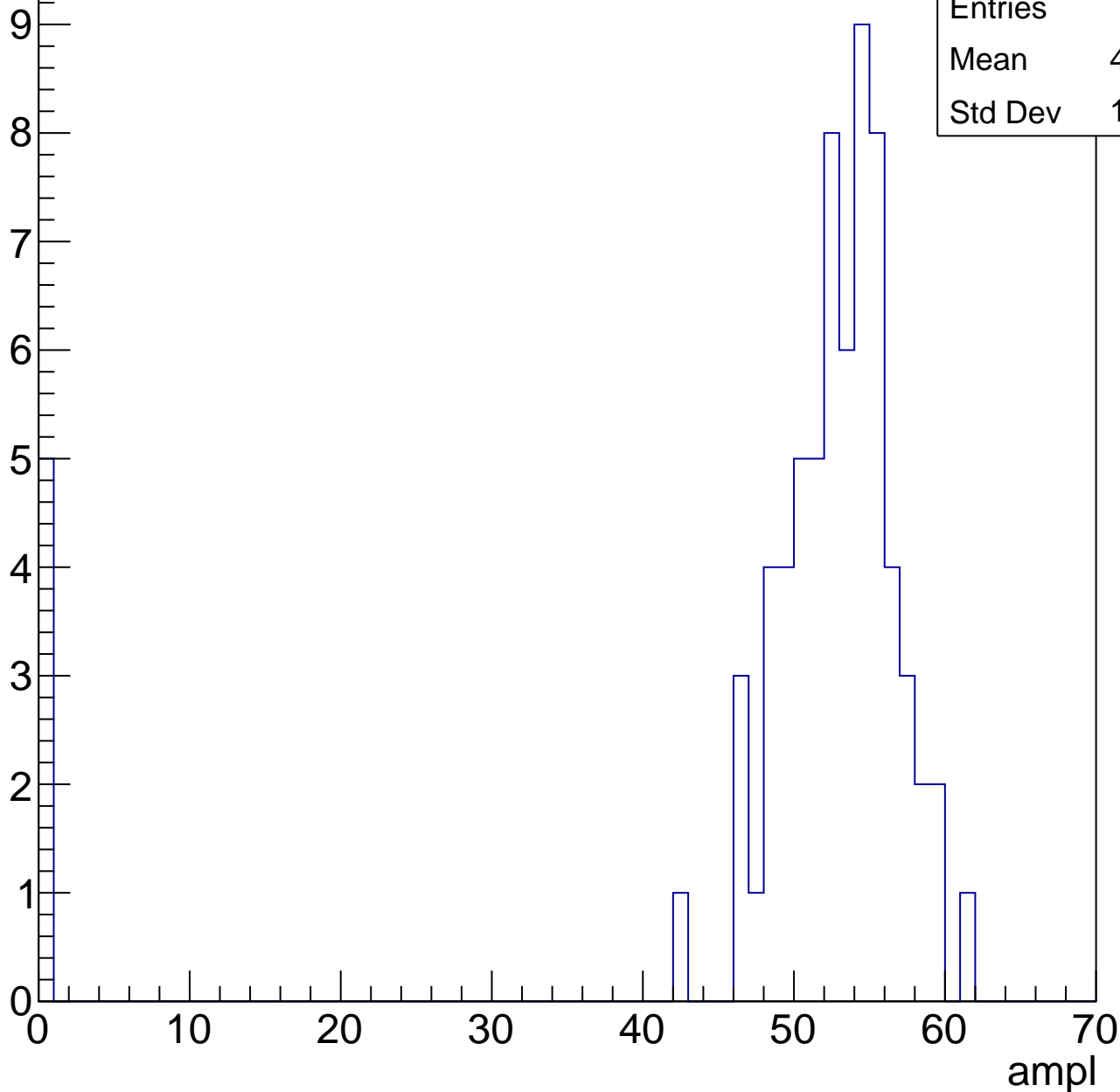
Entries	67
Mean	40.46
Std Dev	16.23



B1L103S, U1-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch71, adc5

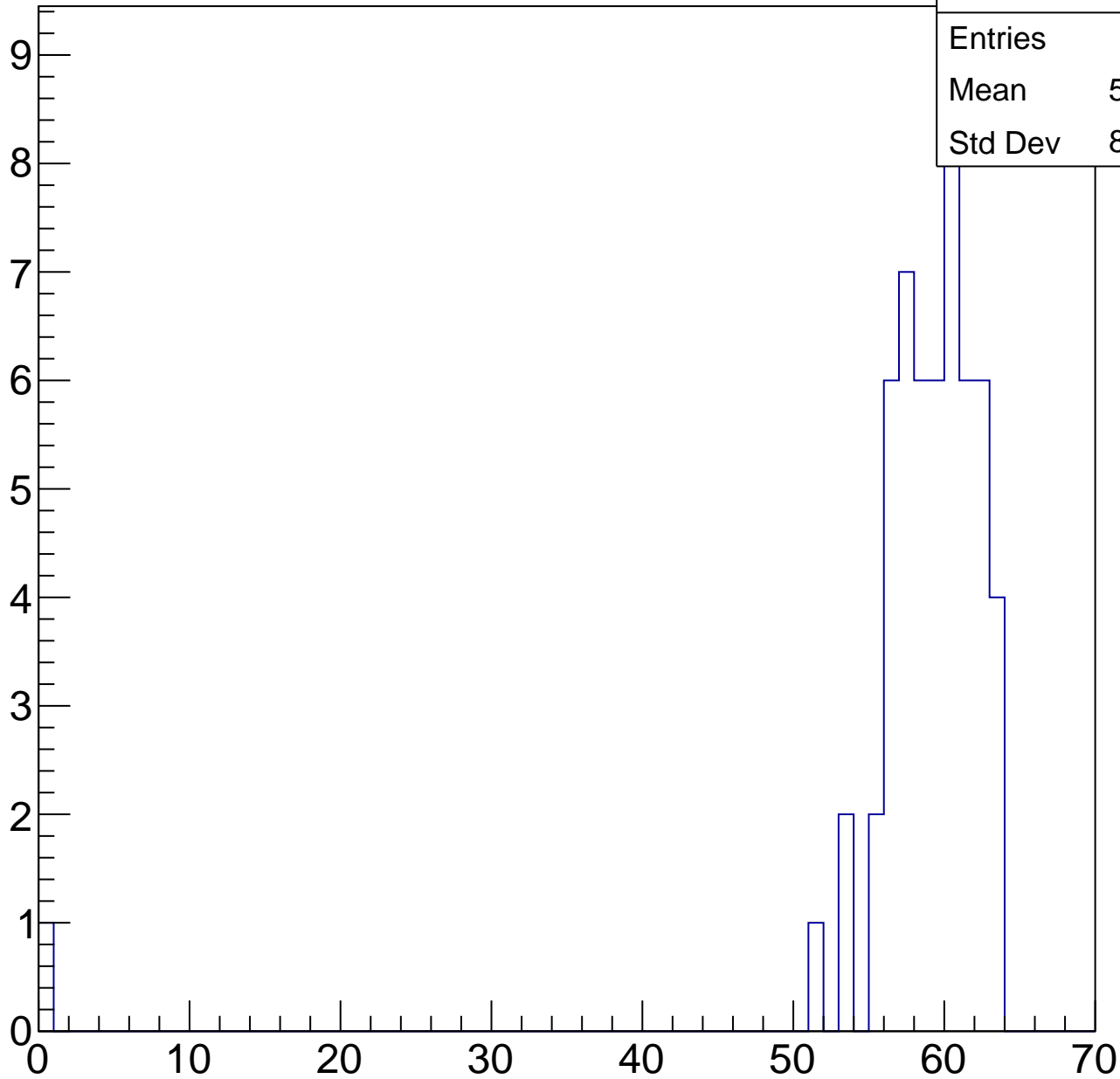
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	57.75
Std Dev	8.238

ampl

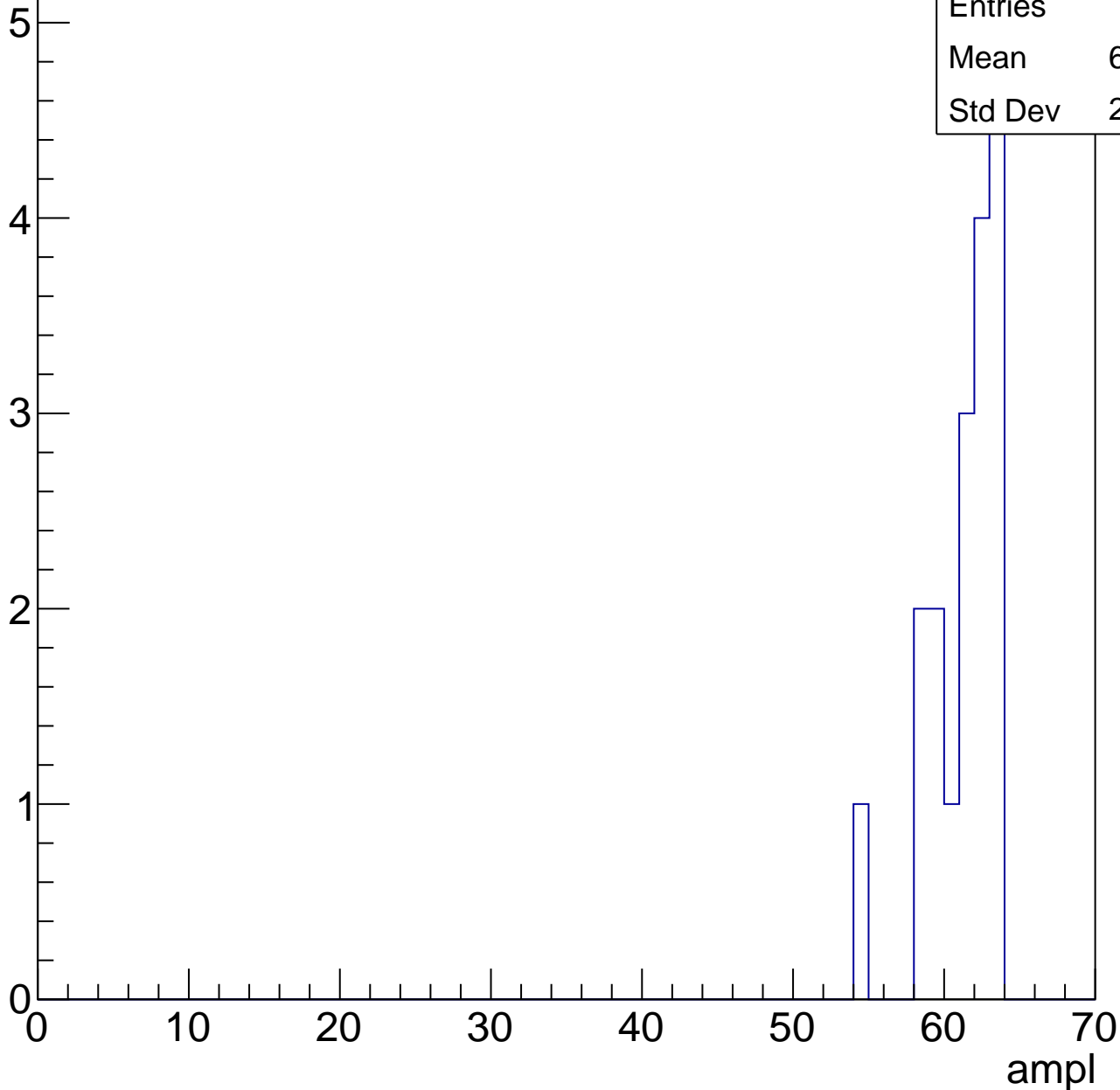


B1L103S, U1-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	60.78
Std Dev	2.347



B1L103S, U1-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



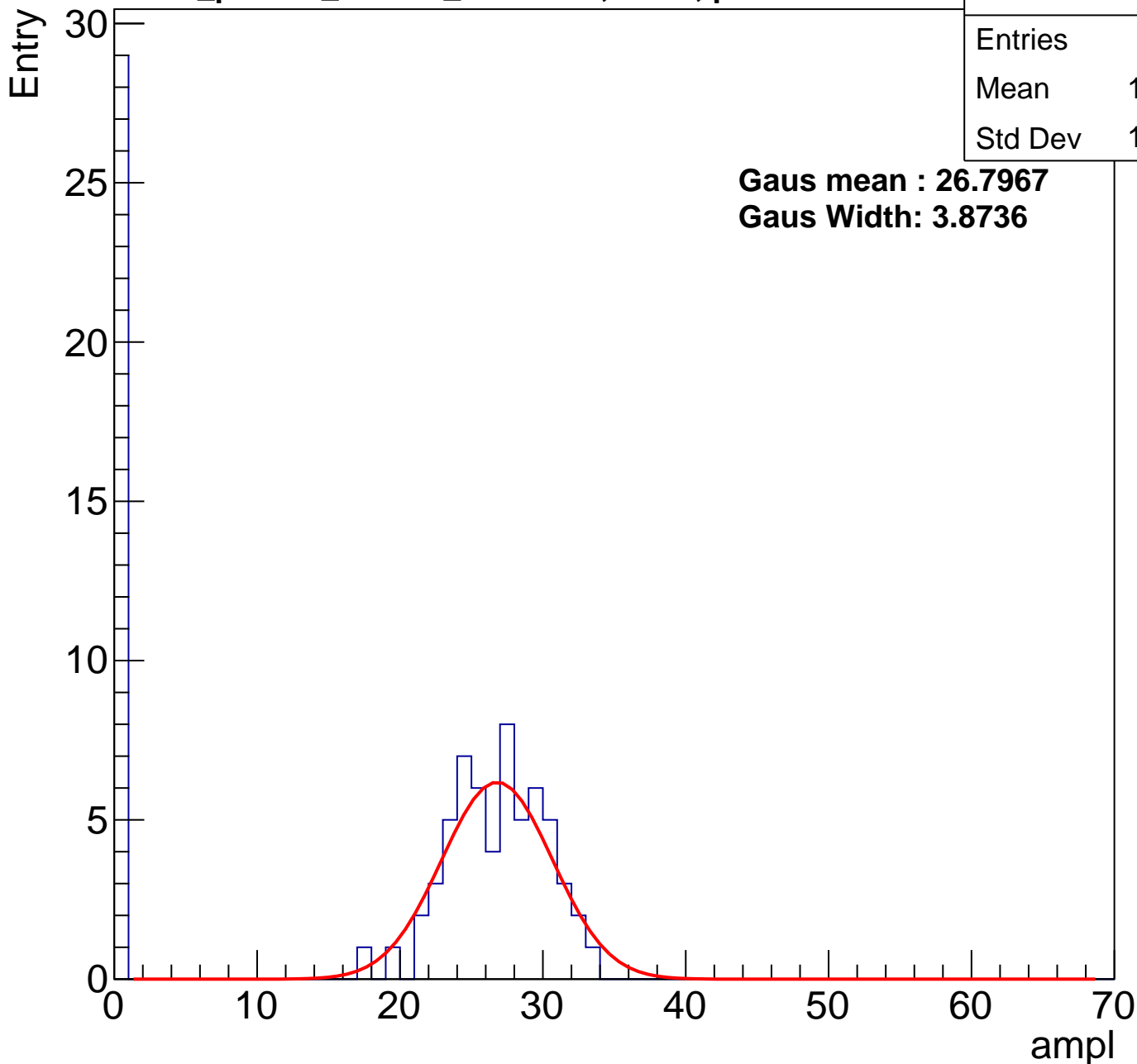
B1L103S, U1-ch72, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	17.62
Std Dev	12.66

Gaus mean : 26.7967

Gaus Width: 3.8736



B1L103S, U1-ch72, adc1

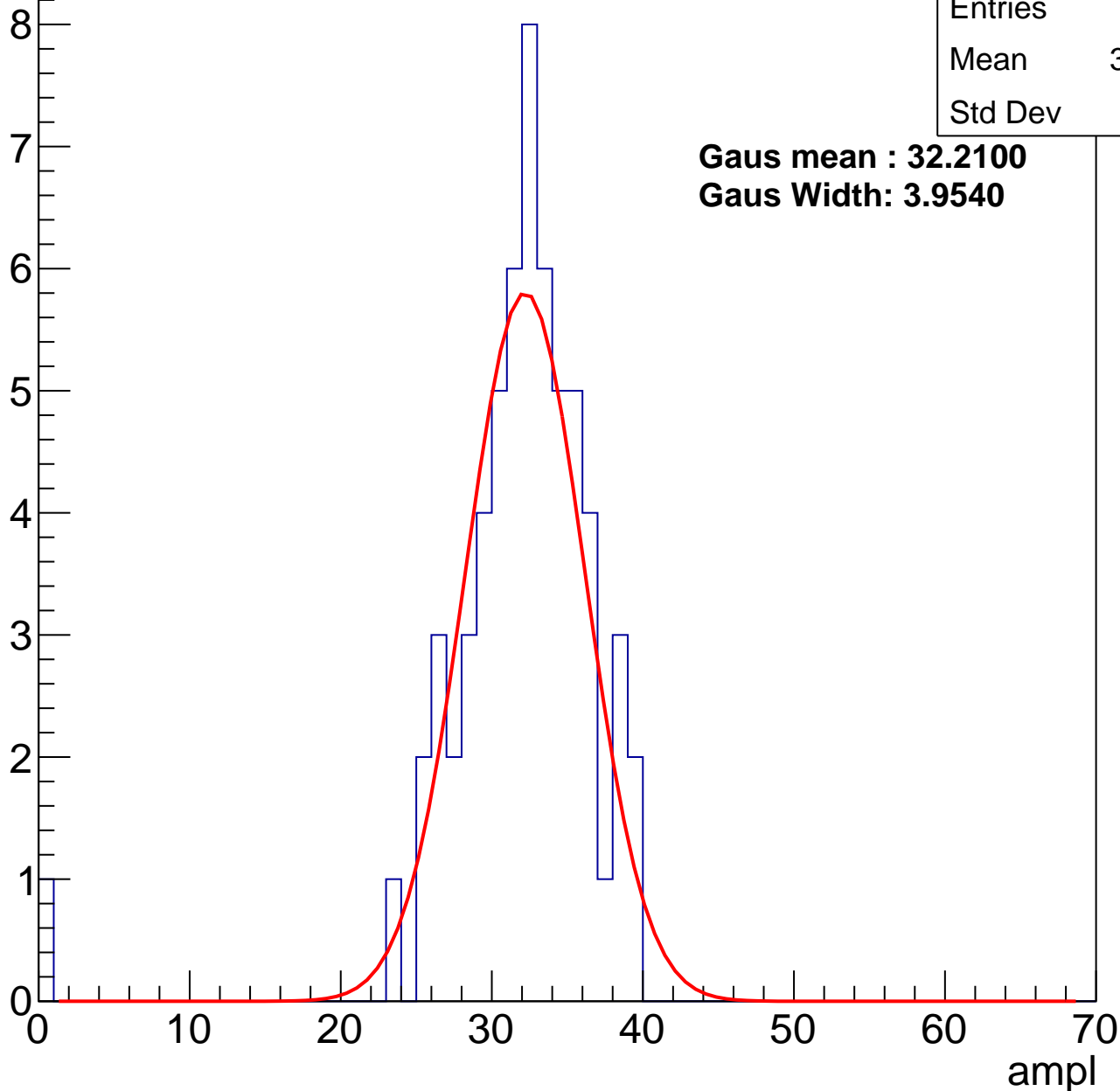
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	31.36
Std Dev	5.45

Gaus mean : 32.2100

Gaus Width: 3.9540



B1L103S, U1-ch72, adc2

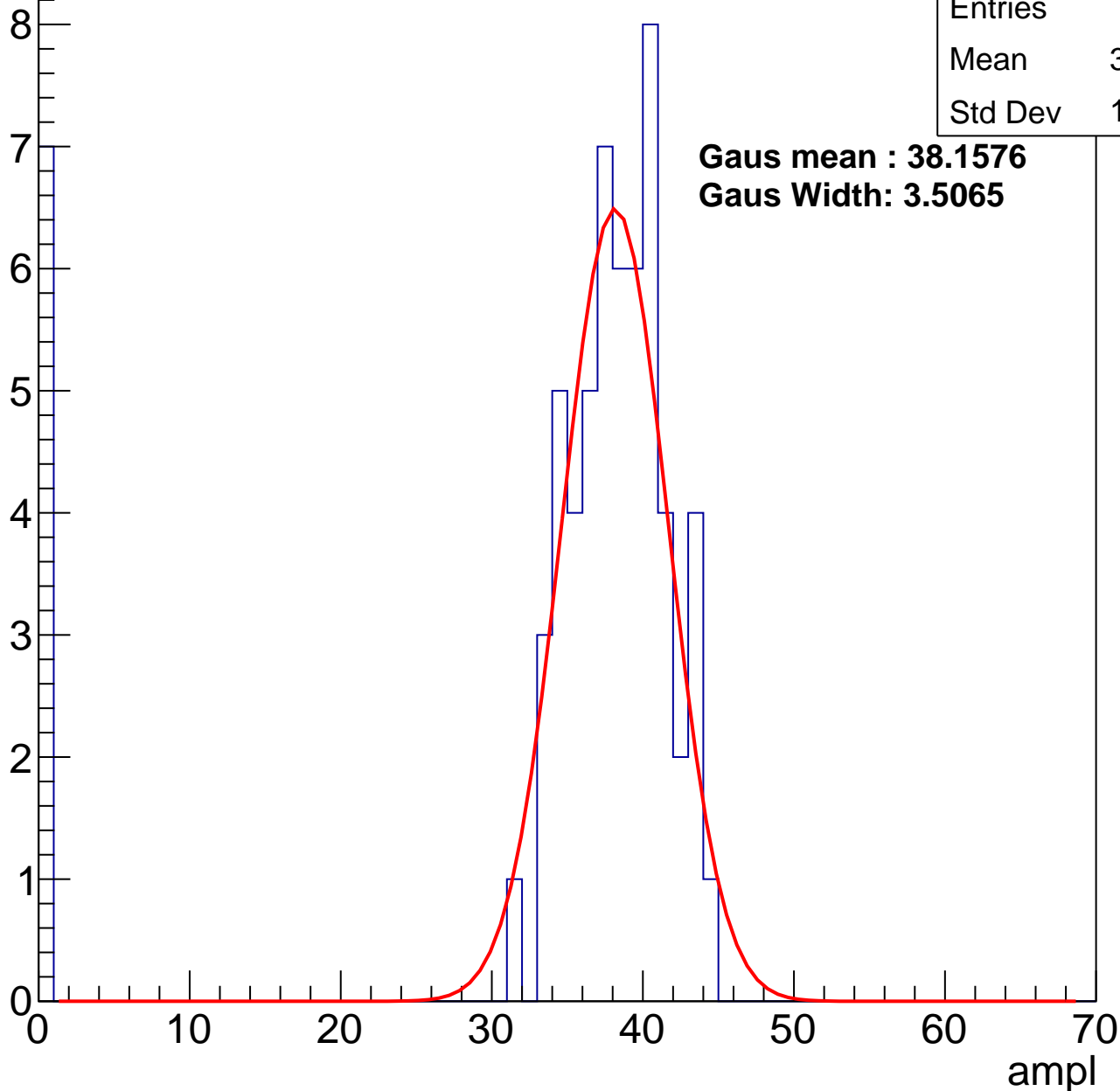
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.73
Std Dev	12.26

Gaus mean : 38.1576

Gaus Width: 3.5065

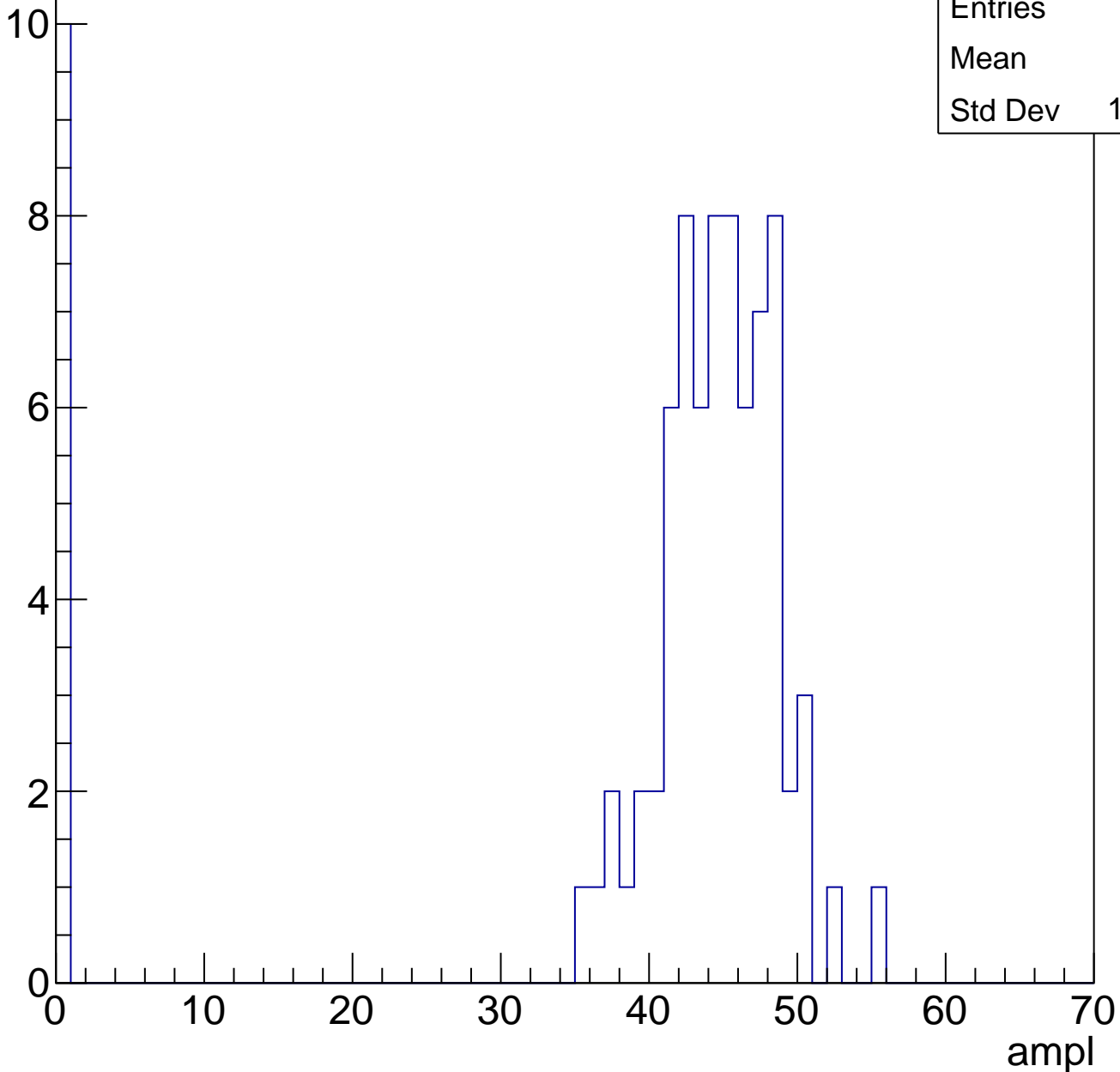


B1L103S, U1-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	39
Std Dev	14.85

Entry

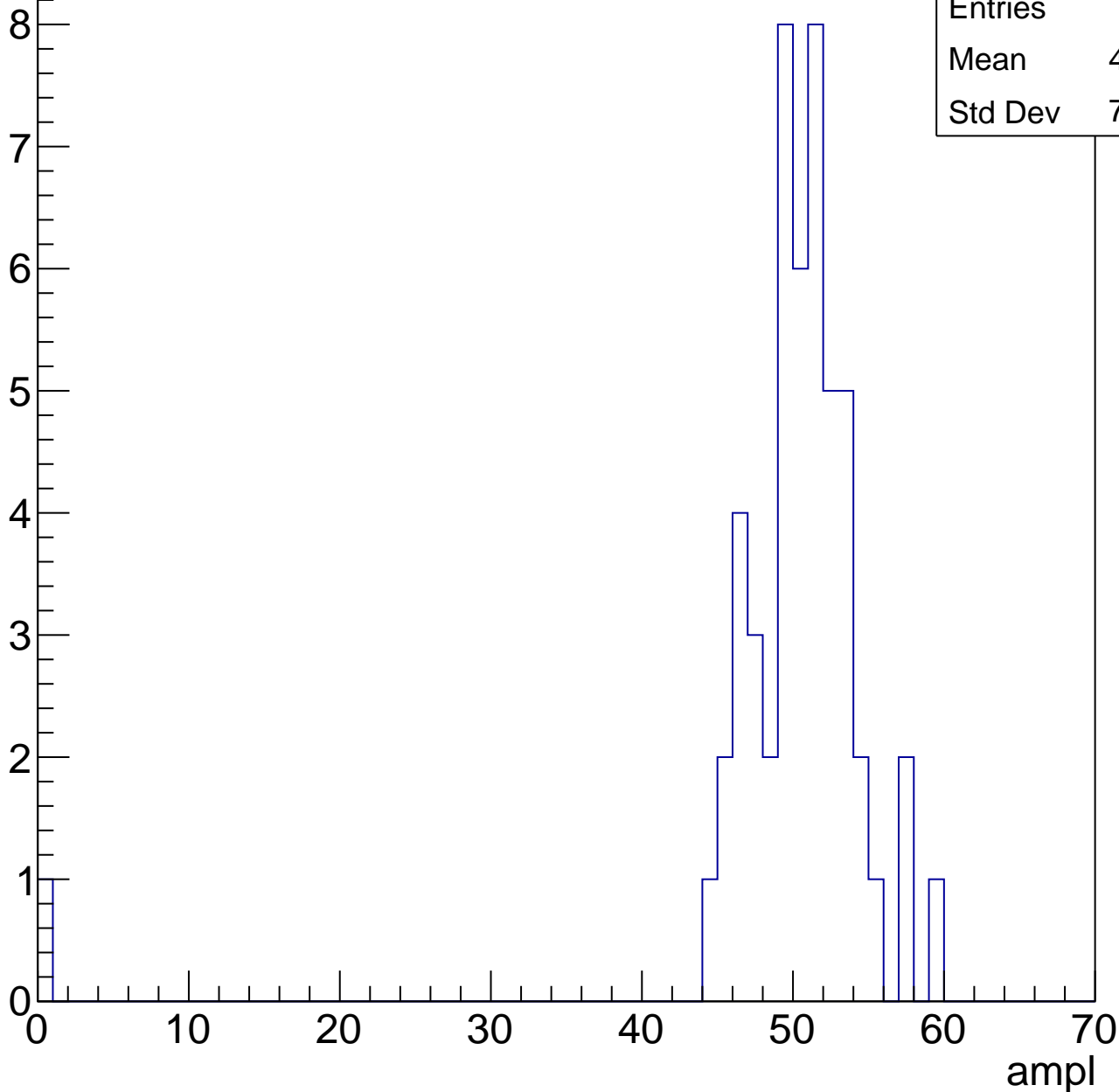


B1L103S, U1-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

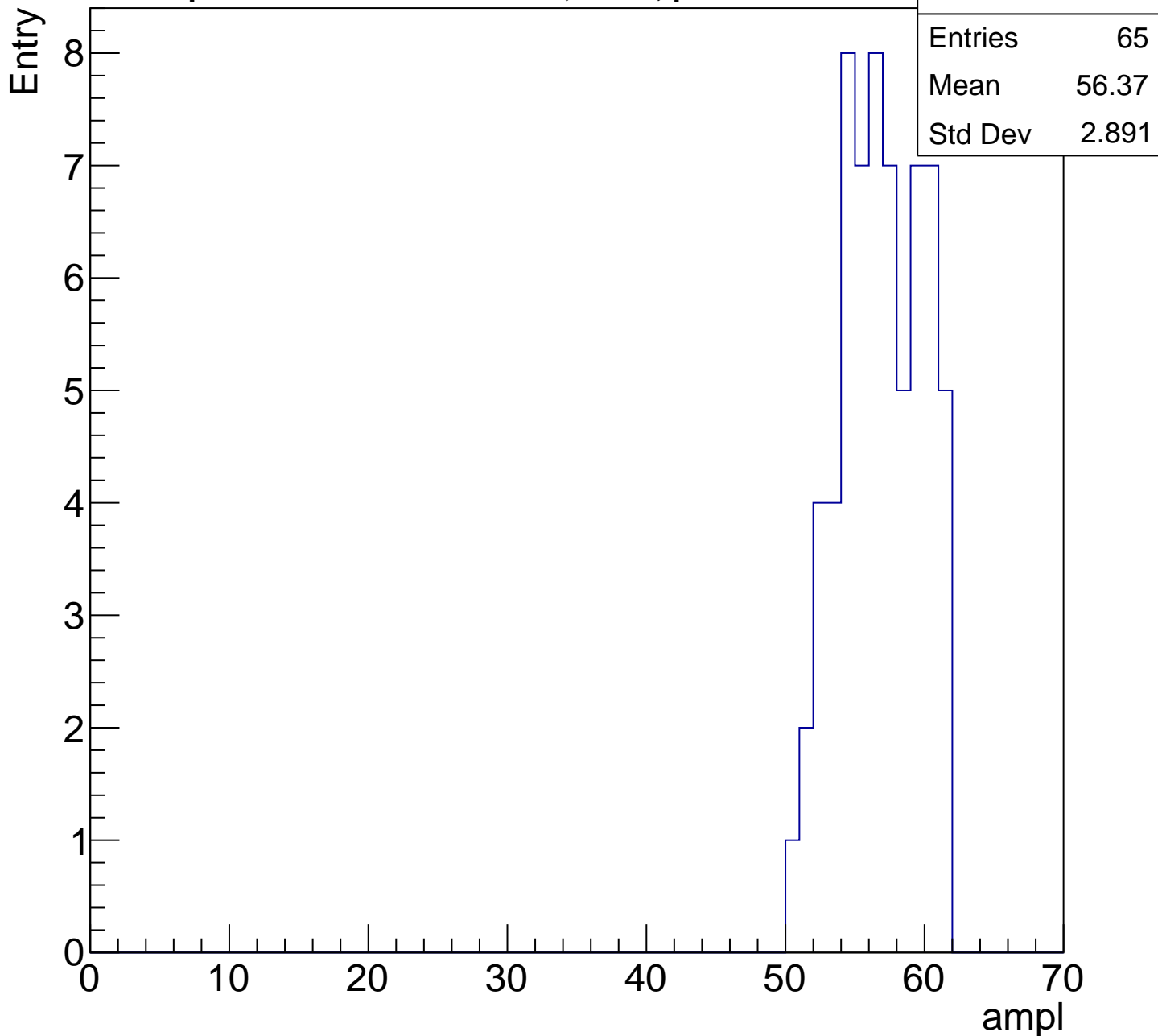
Entry

Entries	51
Mean	49.33
Std Dev	7.643



B1L103S, U1-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

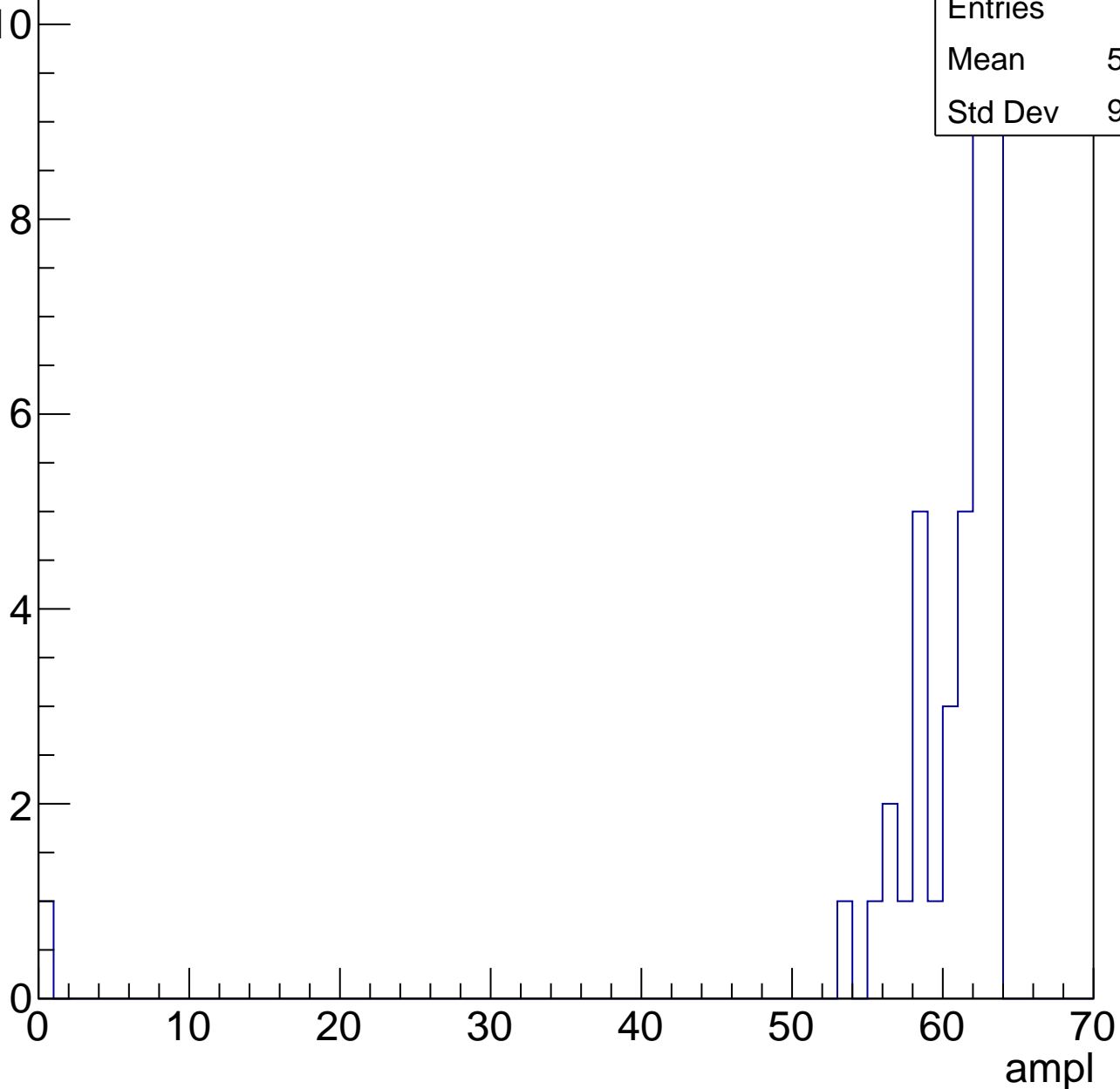


B1L103S, U1-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

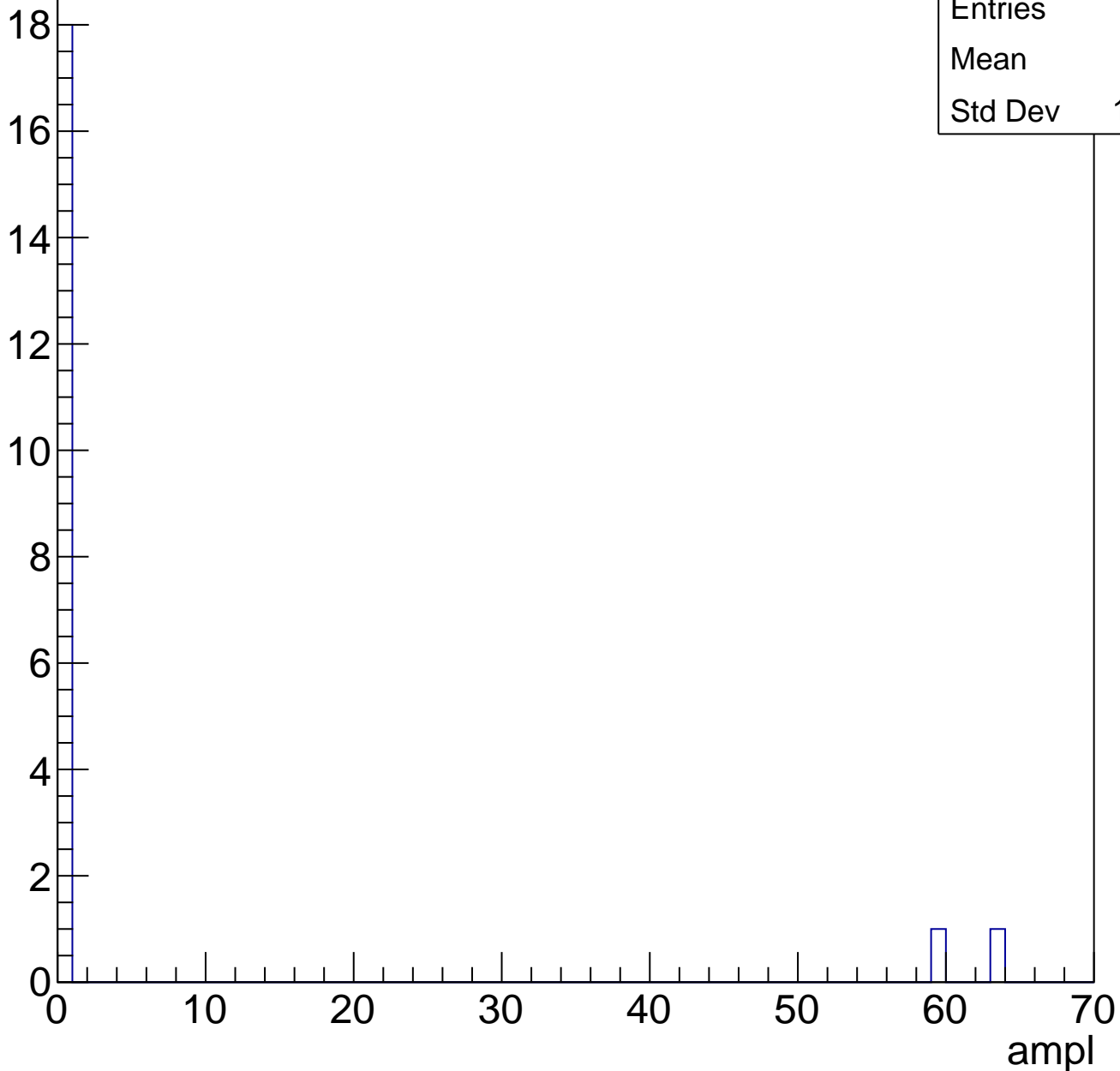
Entries	39
Mean	58.92
Std Dev	9.893



B1L103S, U1-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	6.1
Std Dev	18.31

B1L103S, U1-ch73, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	22.2
Std Dev	11.37

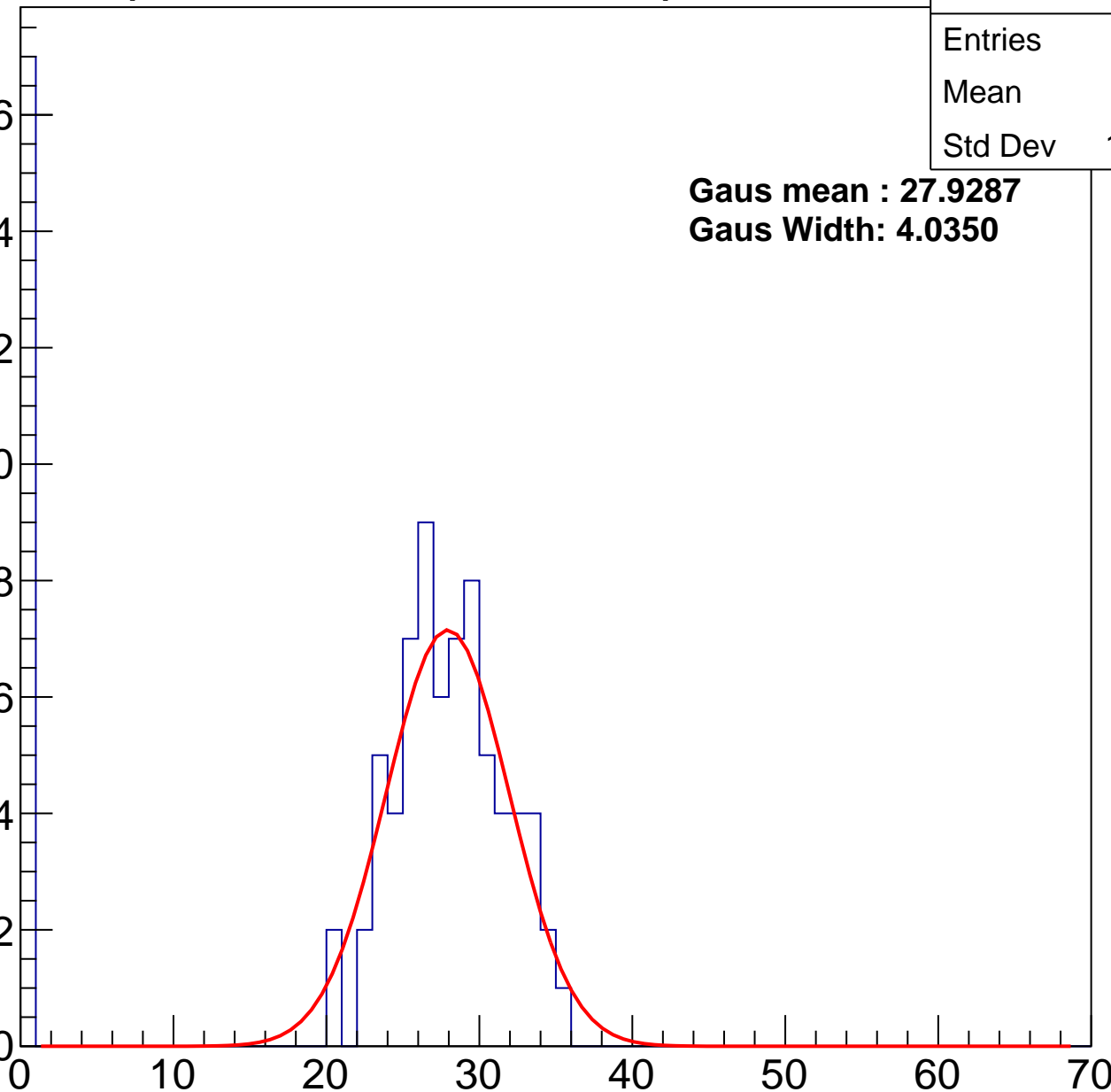
Gaus mean : 27.9287

Gaus Width: 4.0350

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U1-ch73, adc1

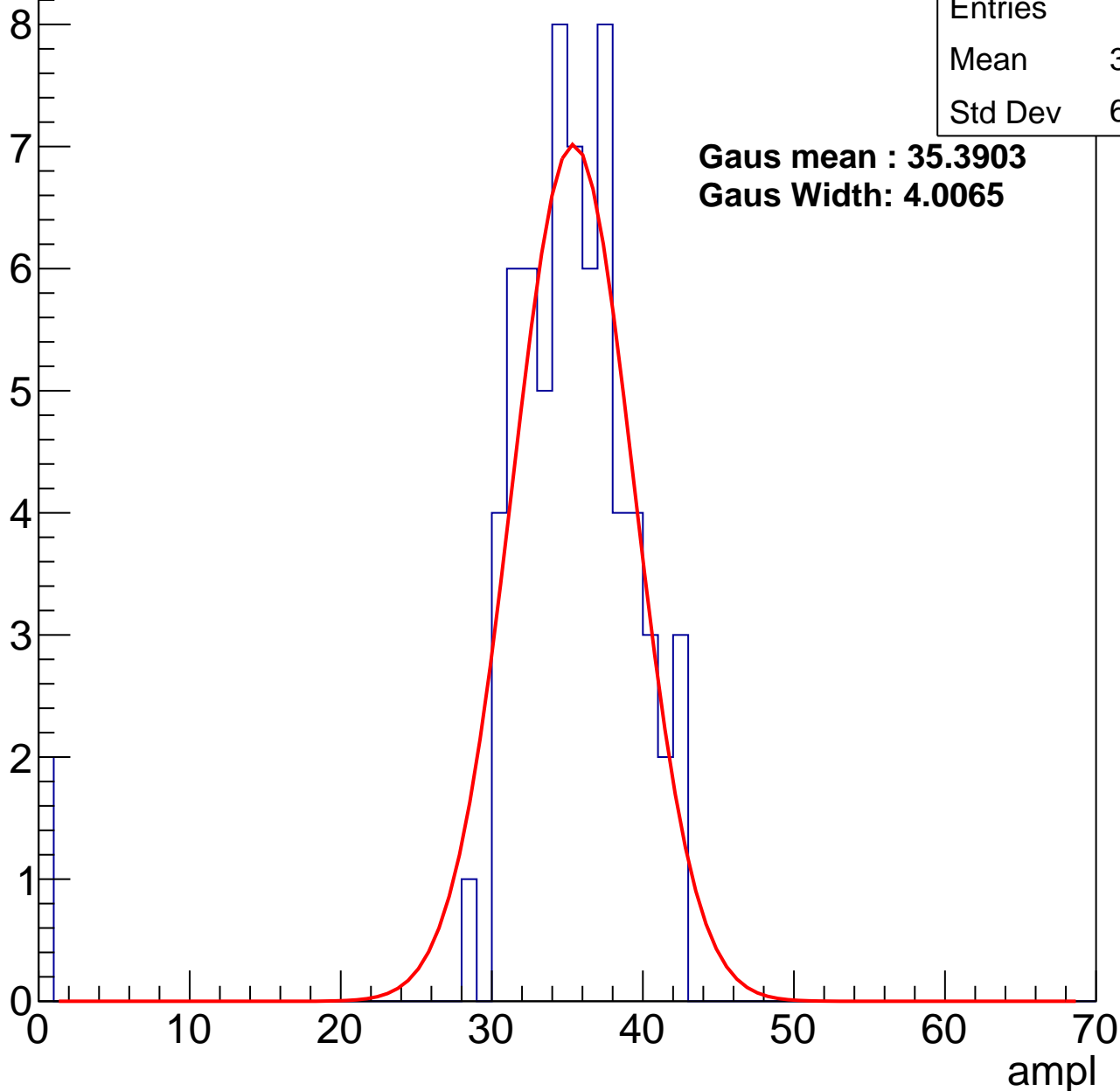
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	34.14
Std Dev	6.766

Gaus mean : 35.3903

Gaus Width: 4.0065



B1L103S, U1-ch73, adc2

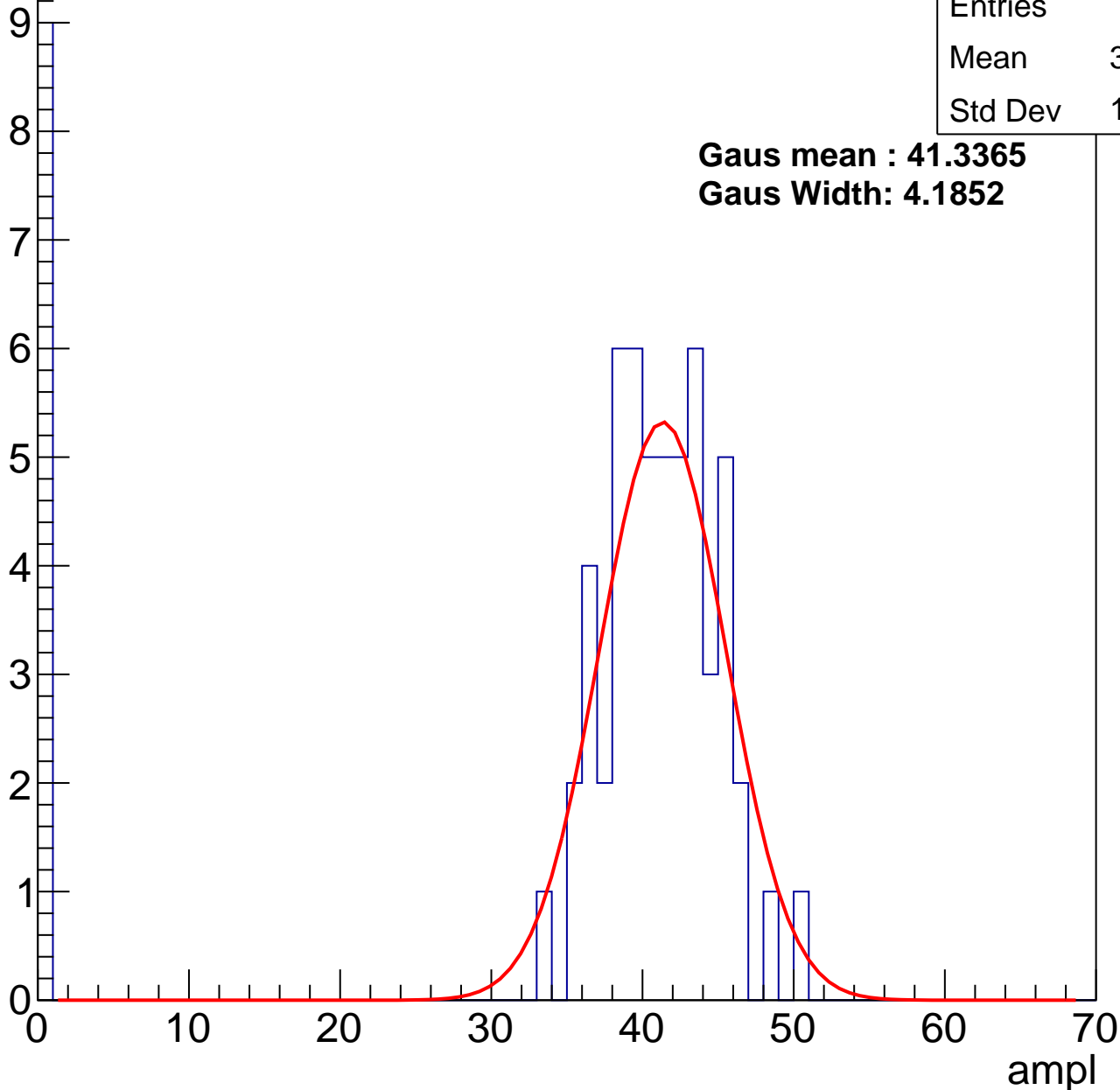
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.97
Std Dev	14.64

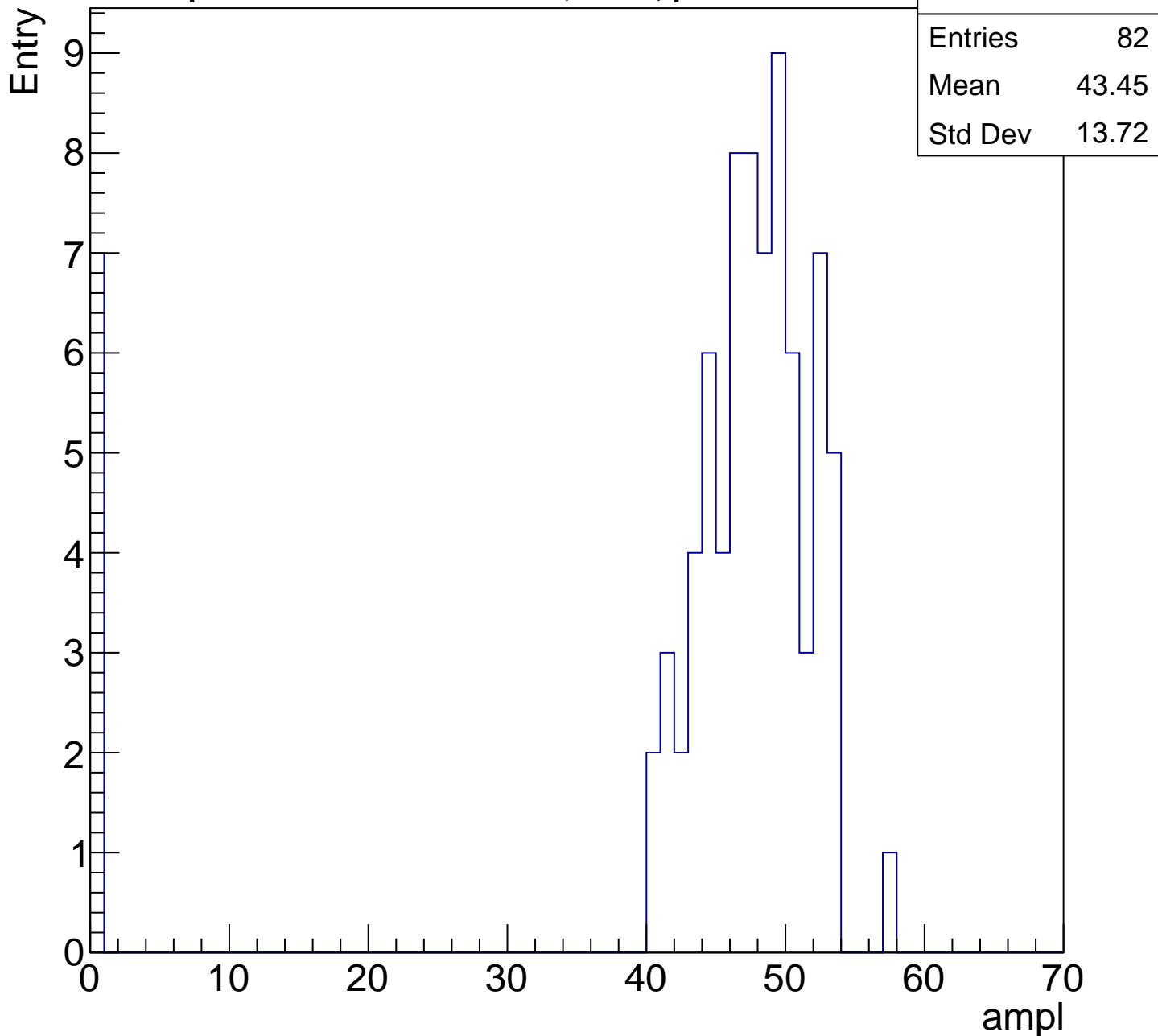
Gaus mean : 41.3365

Gaus Width: 4.1852



B1L103S, U1-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

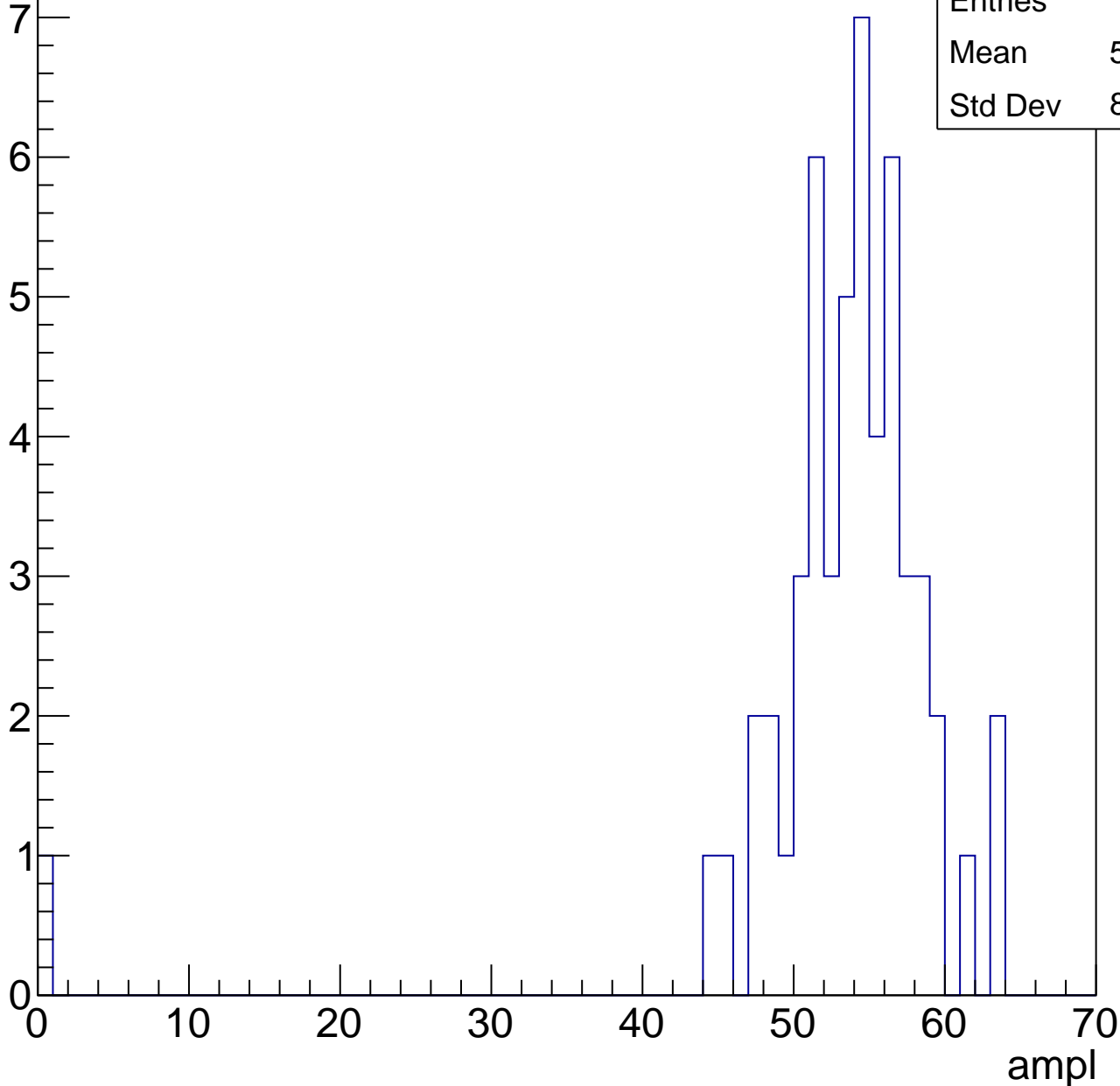


B1L103S, U1-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	52.62
Std Dev	8.326

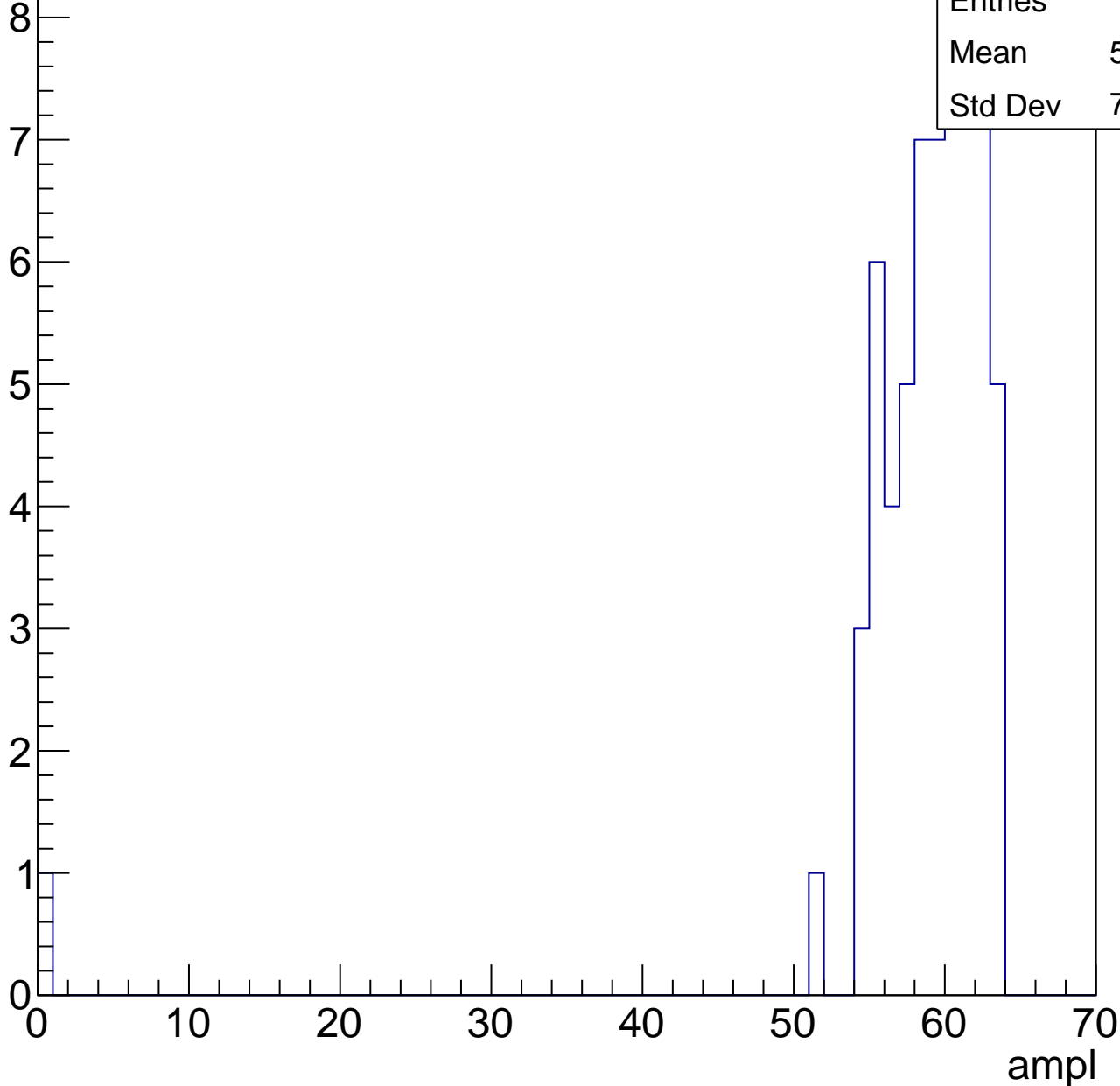


B1L103S, U1-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

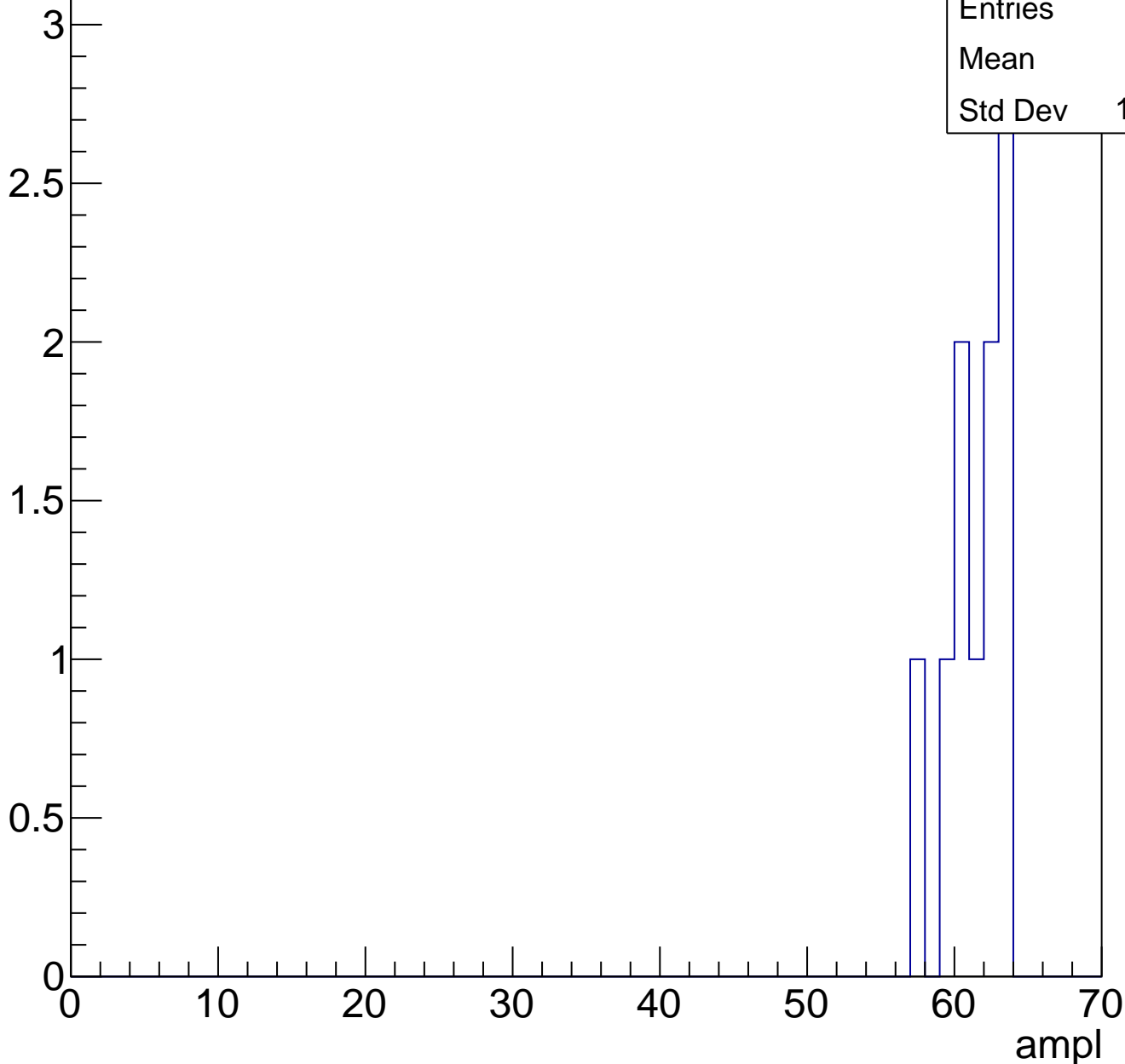
Entries	63
Mean	57.94
Std Dev	7.868



B1L103S, U1-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

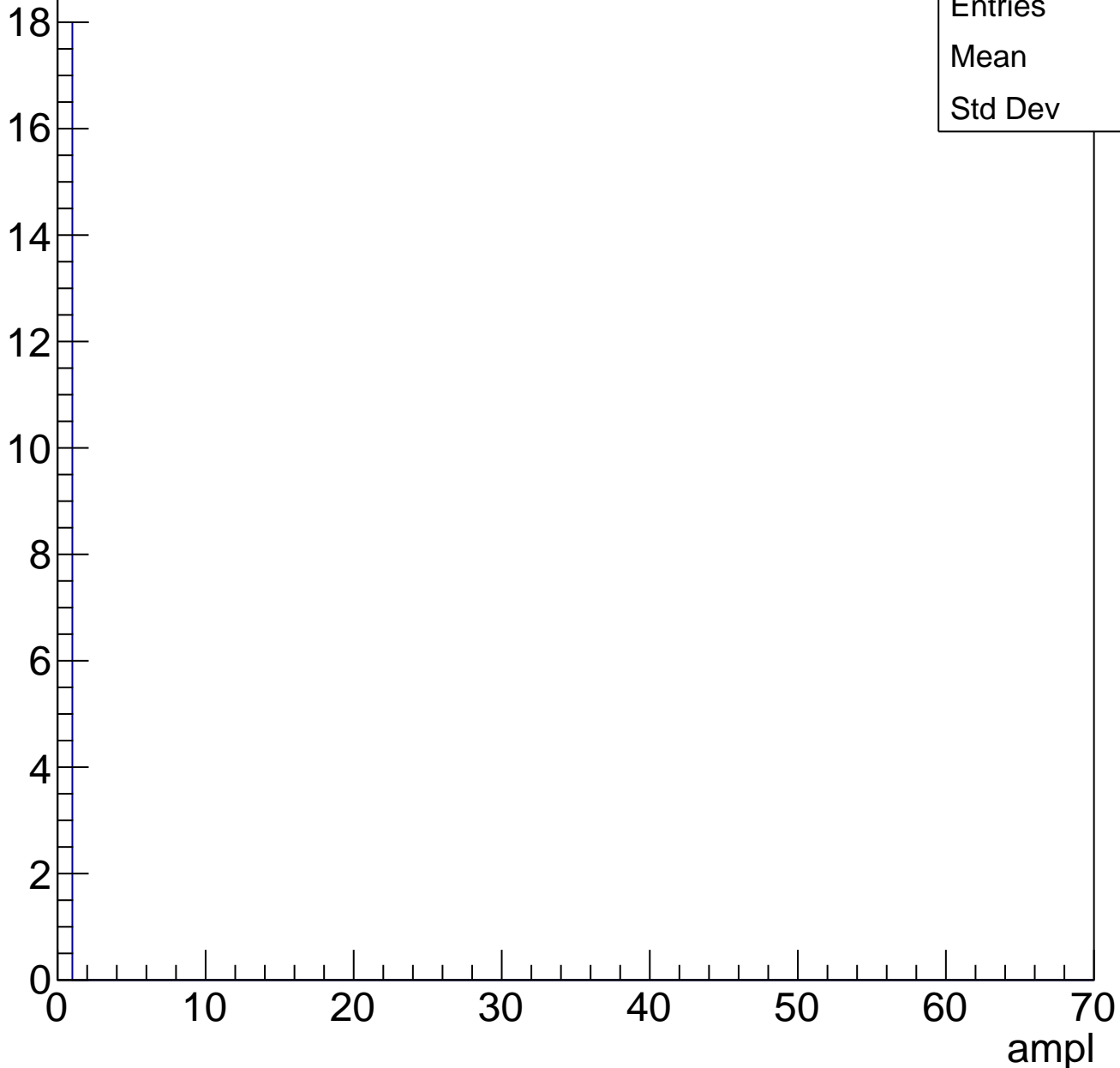
Entry



B1L103S, U1-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U1-ch74, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	16.63
Std Dev	12.79

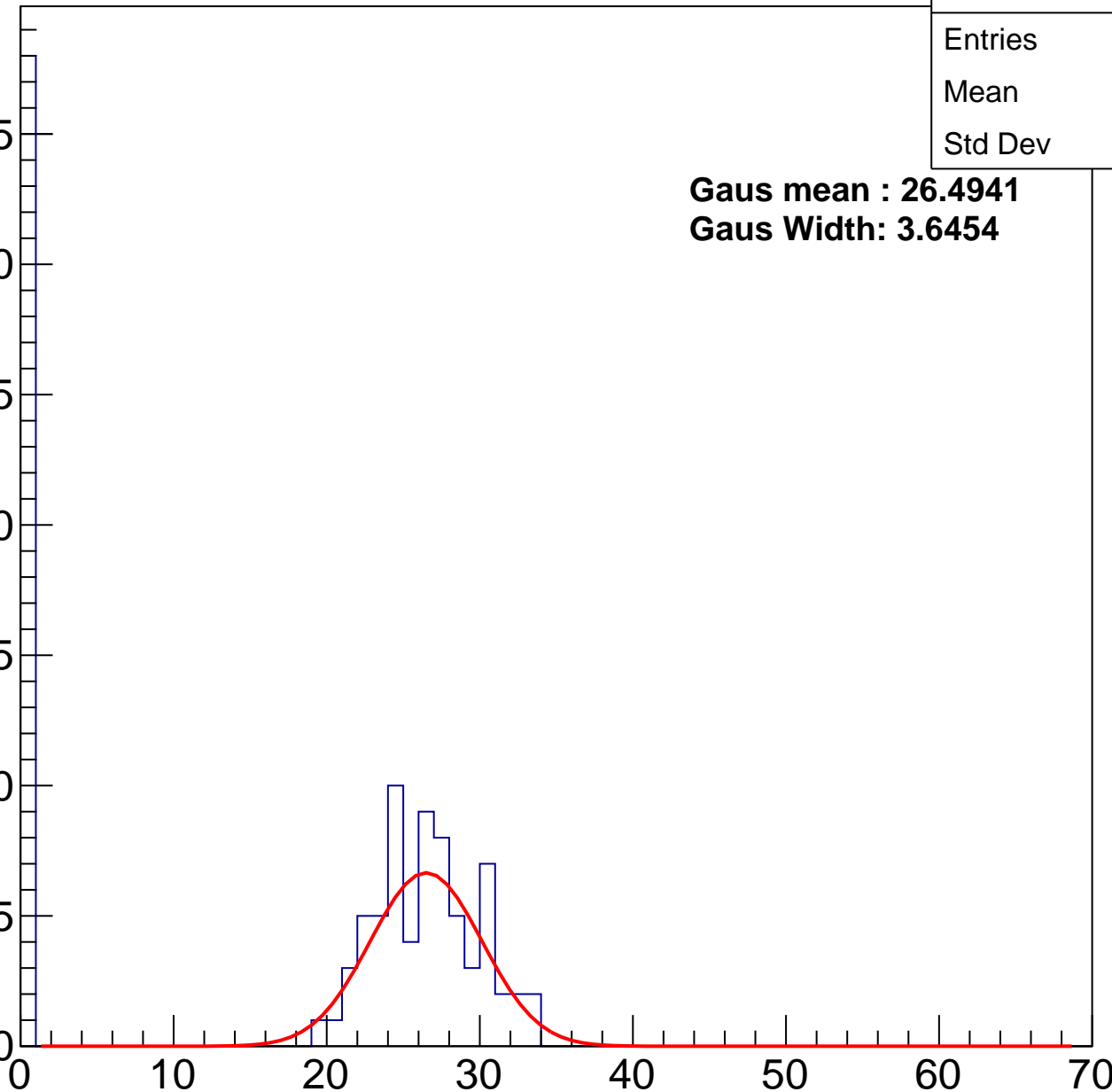
Gaus mean : 26.4941

Gaus Width: 3.6454

Entry

35
30
25
20
15
10
5
0

ampl



B1L103S, U1-ch74, adc1

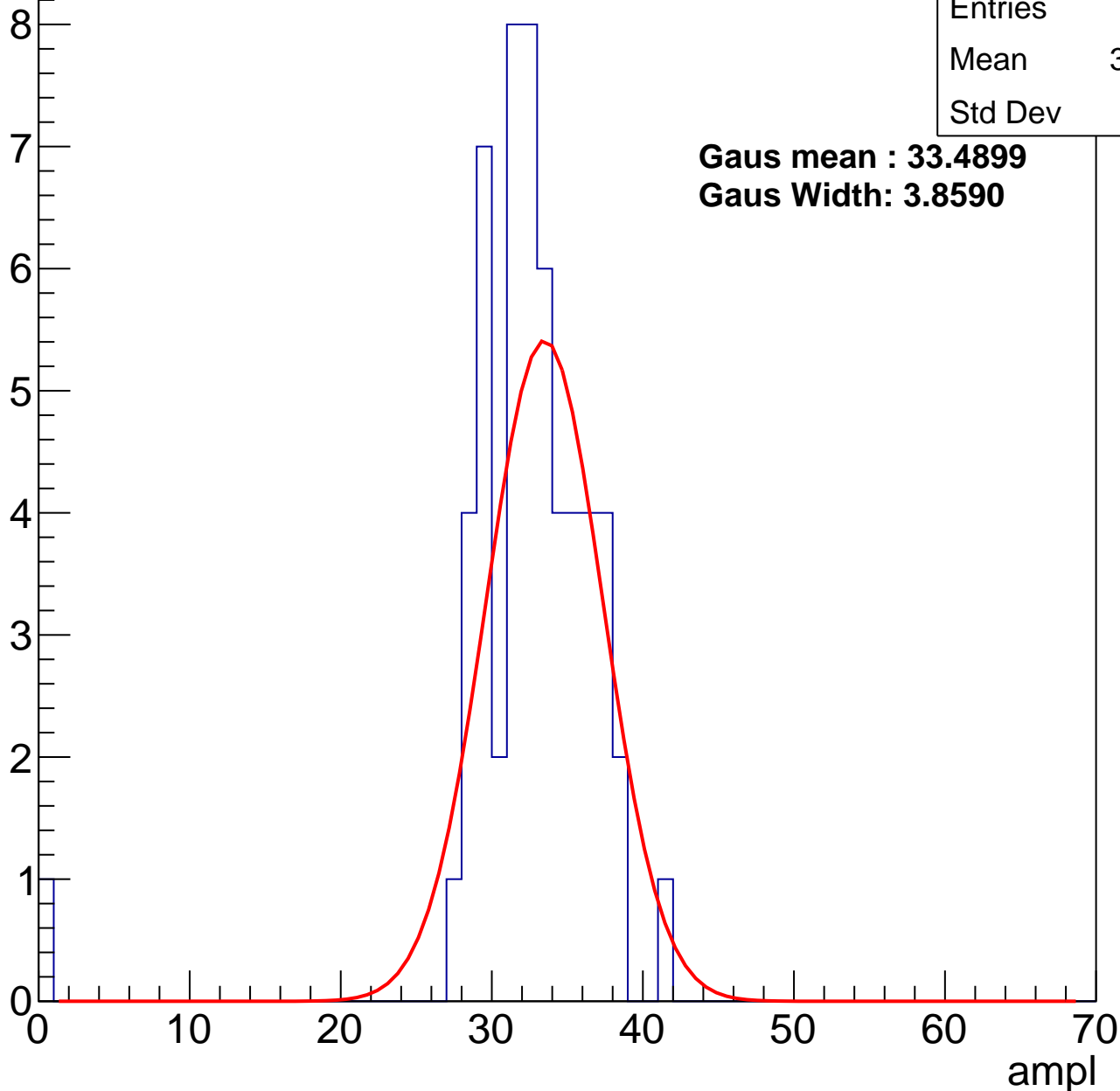
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	31.95
Std Dev	5.29

Gaus mean : 33.4899

Gaus Width: 3.8590



B1L103S, U1-ch74, adc2

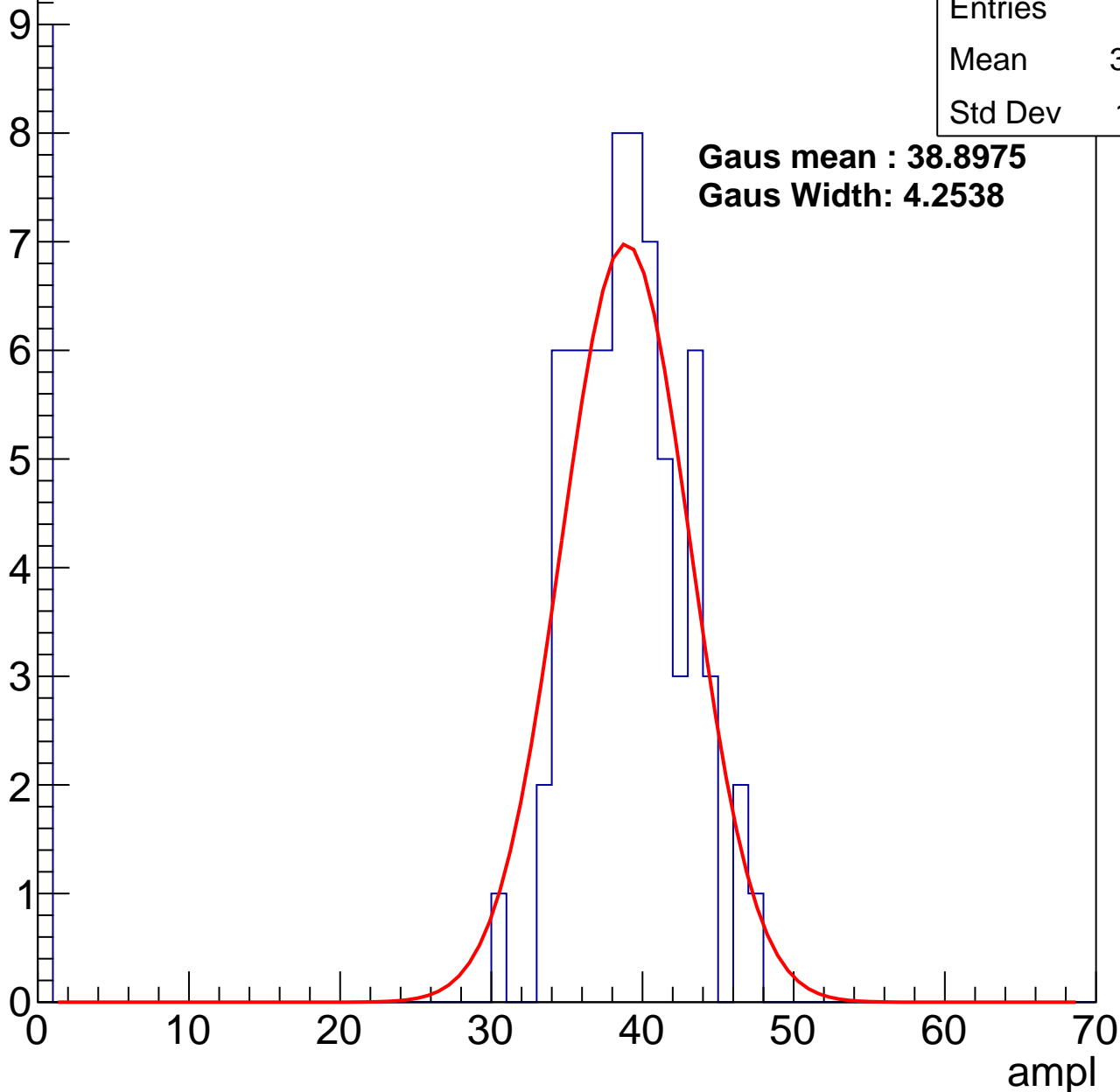
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	34.23
Std Dev	12.71

Gaus mean : 38.8975

Gaus Width: 4.2538

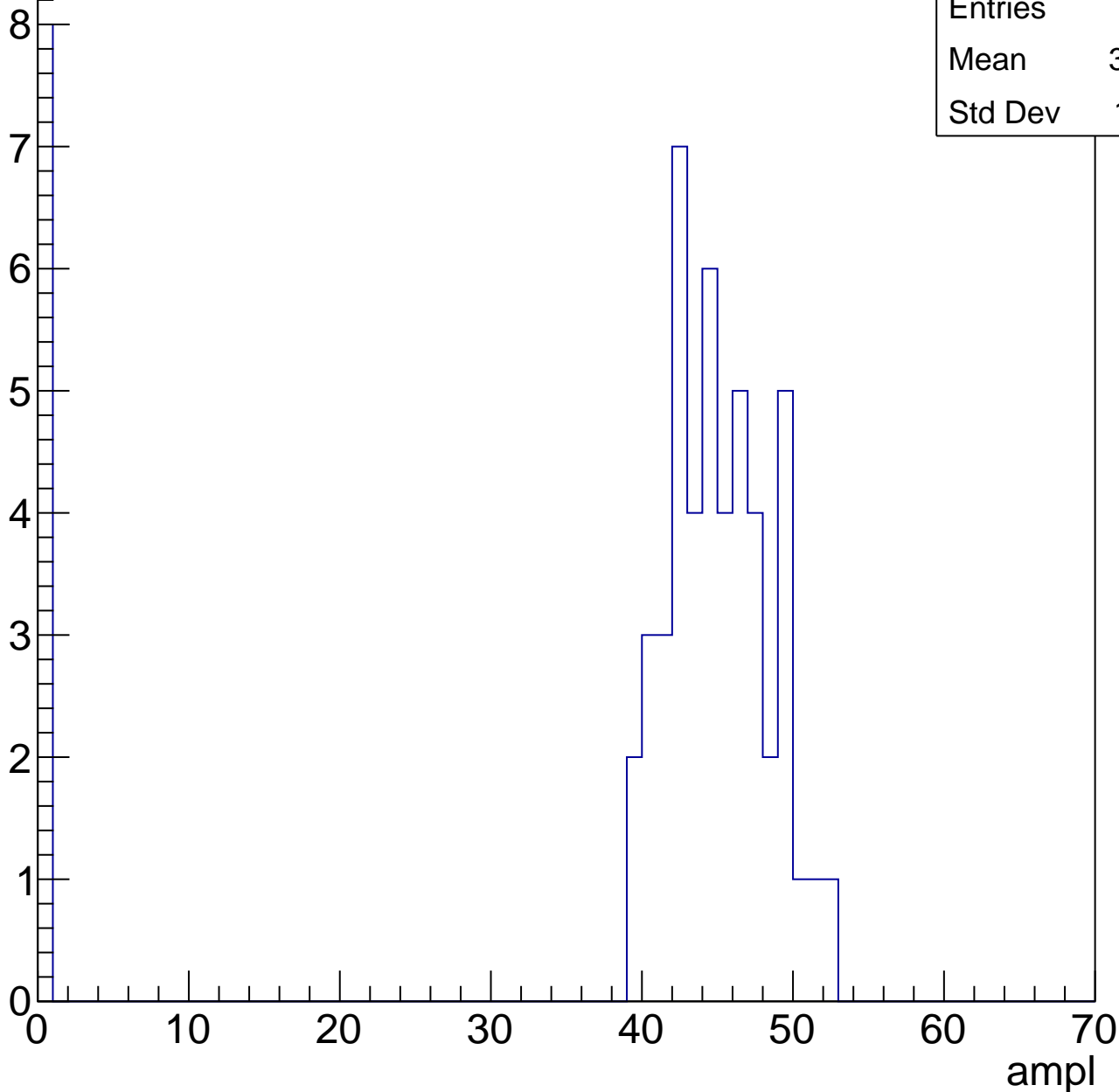


B1L103S, U1-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

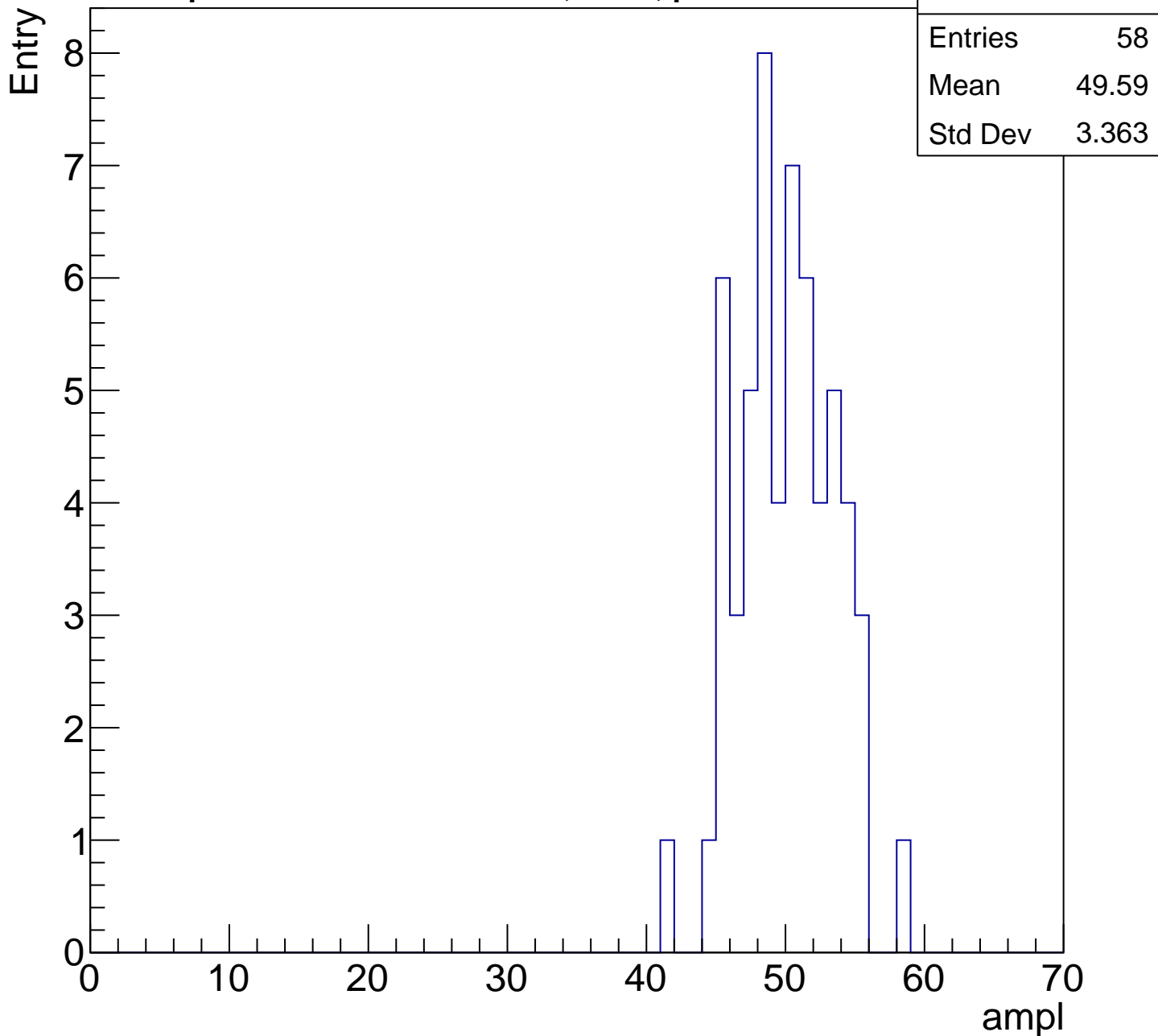
Entry

Entries	56
Mean	38.27
Std Dev	15.91



B1L103S, U1-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

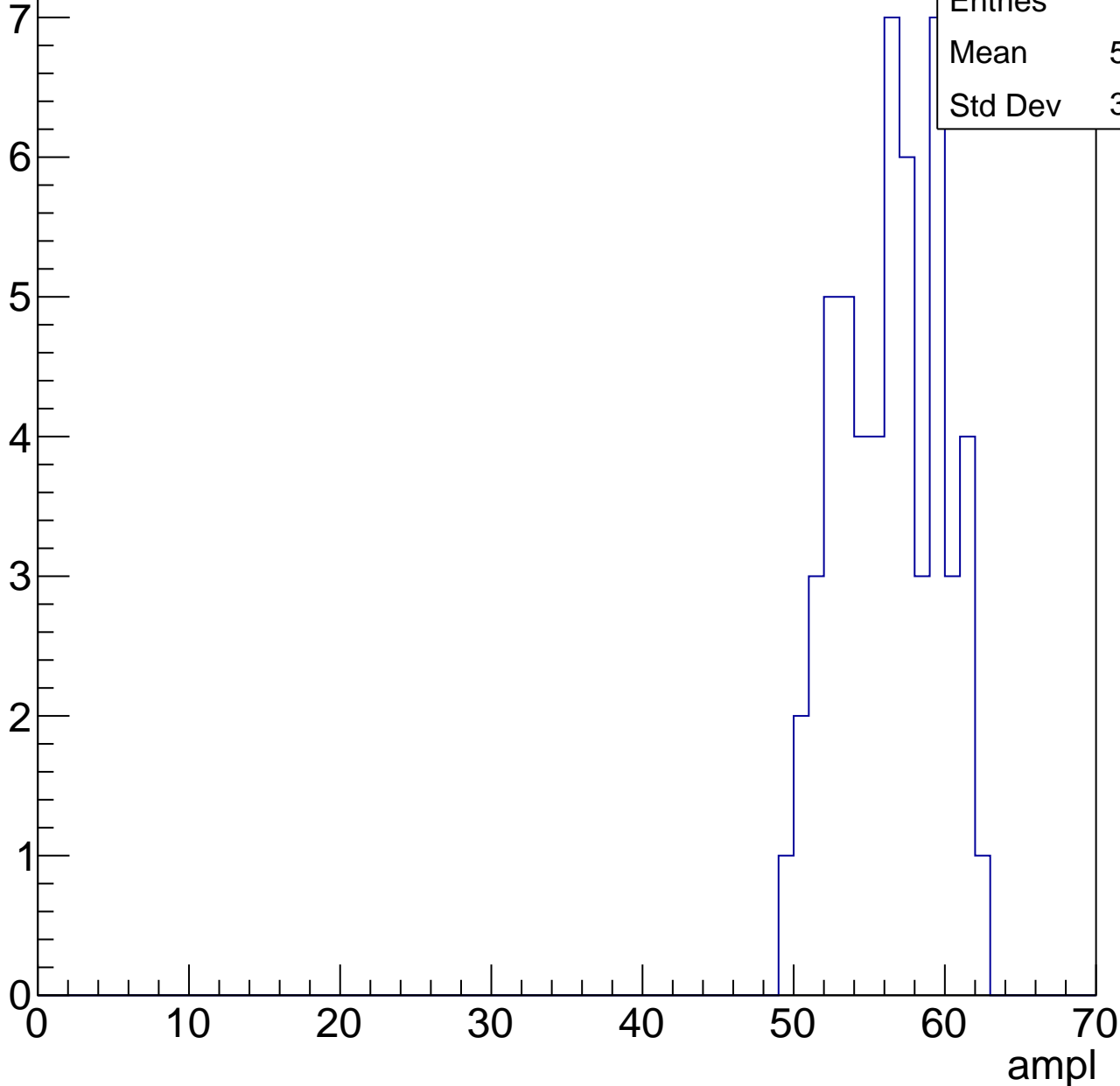


B1L103S, U1-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.82
Std Dev	3.314

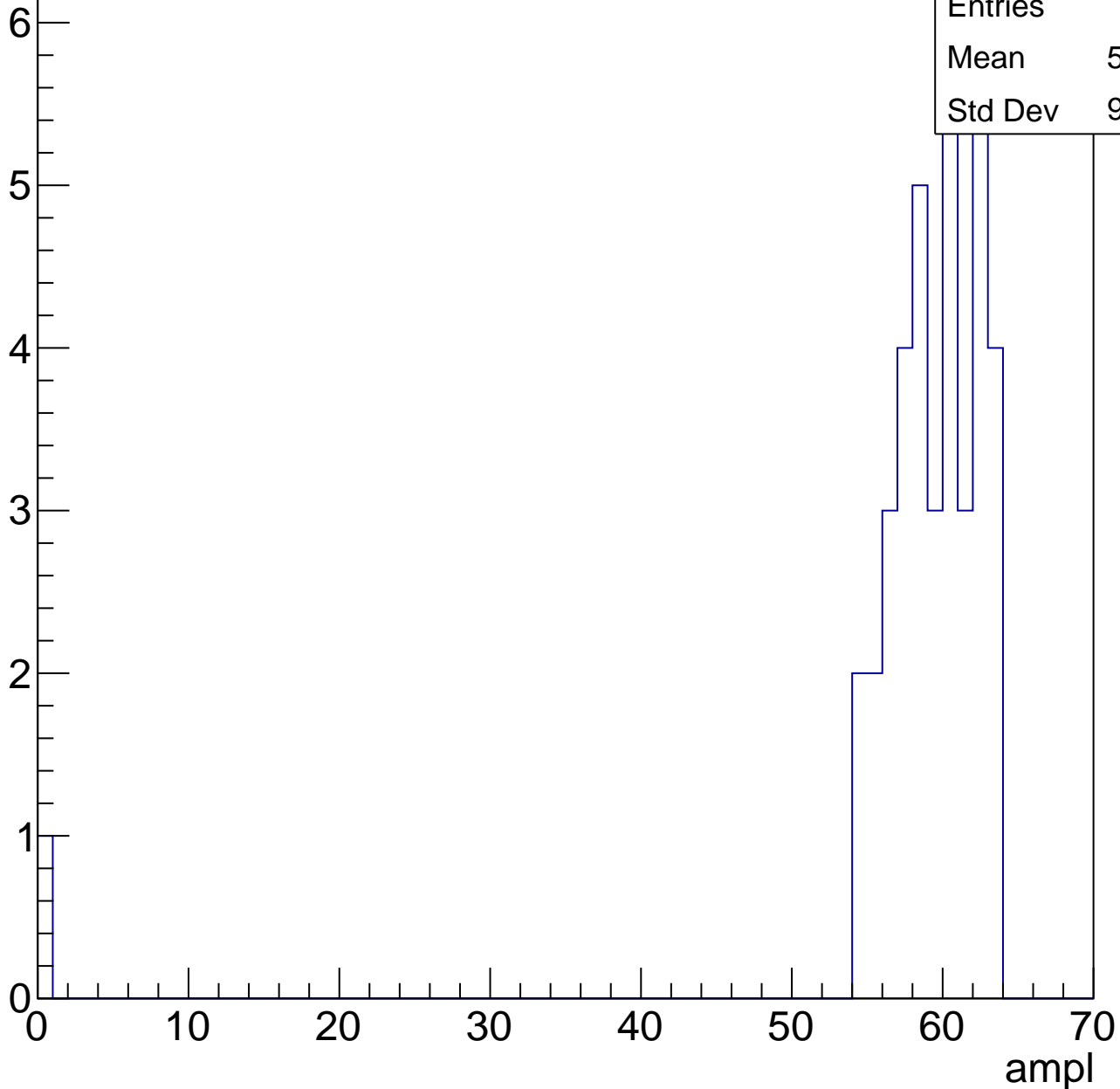


B1L103S, U1-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	57.64
Std Dev	9.707

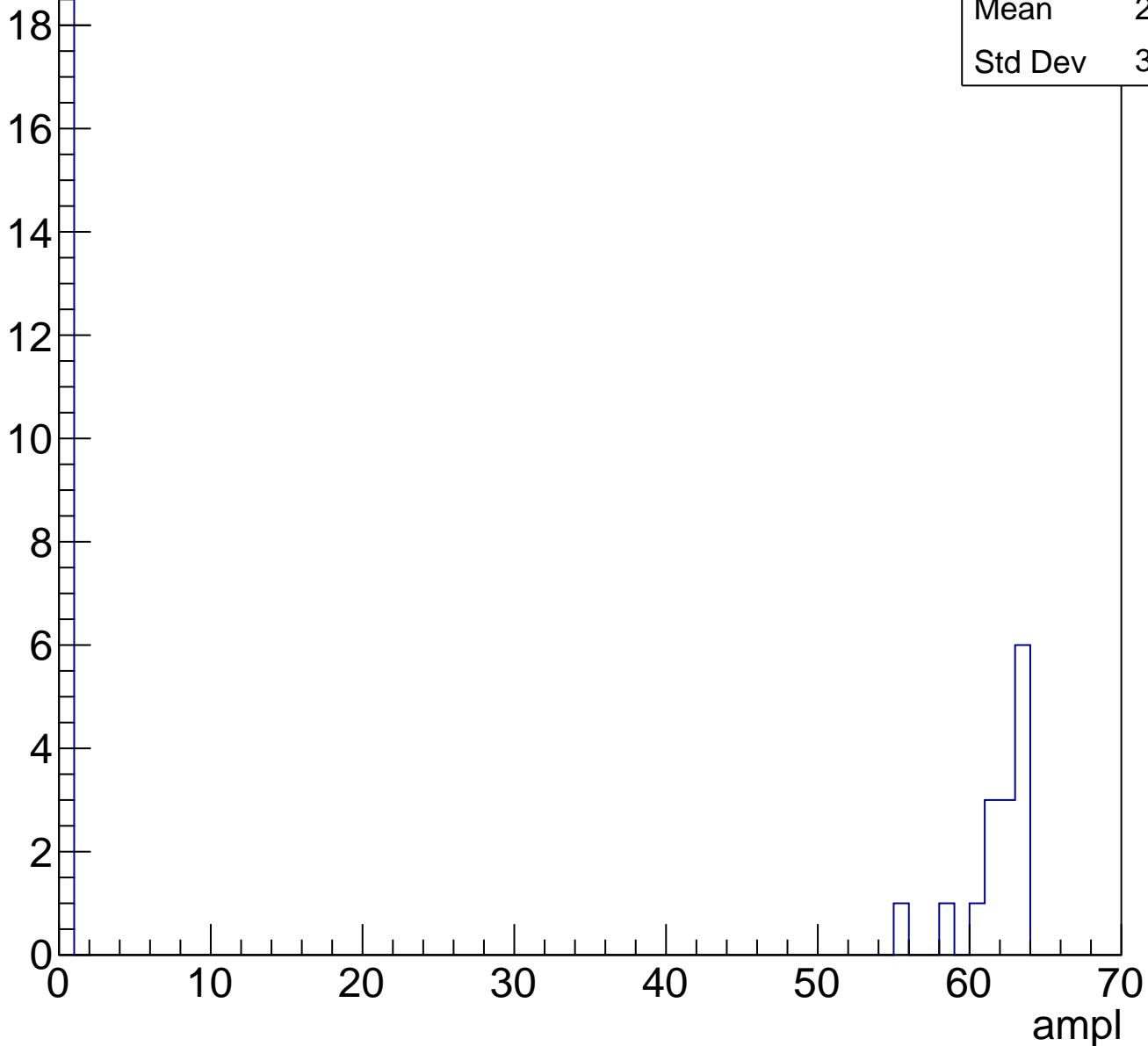


B1L103S, U1-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	34
Mean	27.06
Std Dev	30.49

Entry



B1L103S, U1-ch75, adc0

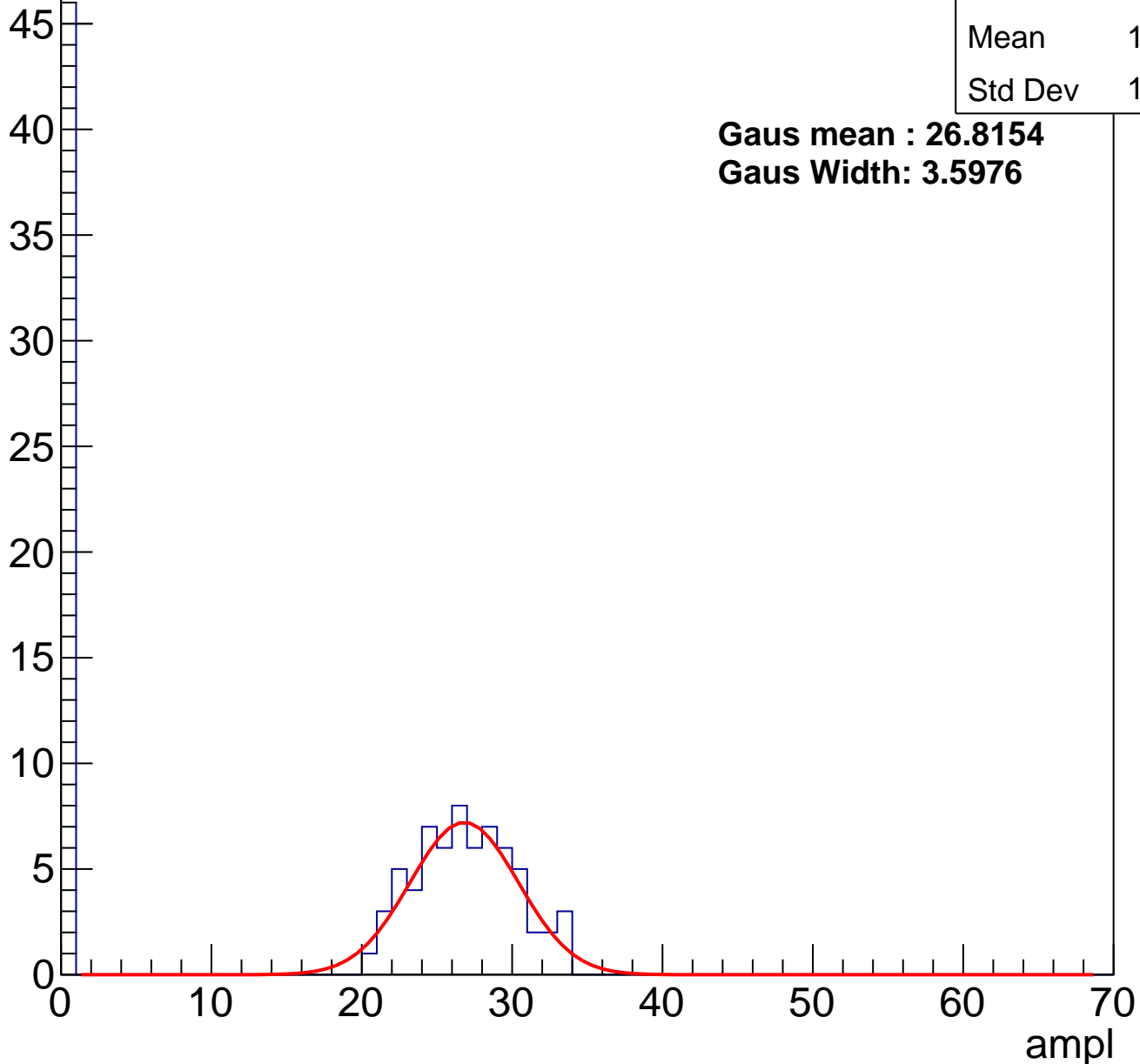
calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	15.48
Std Dev	13.26

Gaus mean : 26.8154

Gaus Width: 3.5976

Entry



B1L103S, U1-ch75, adc1

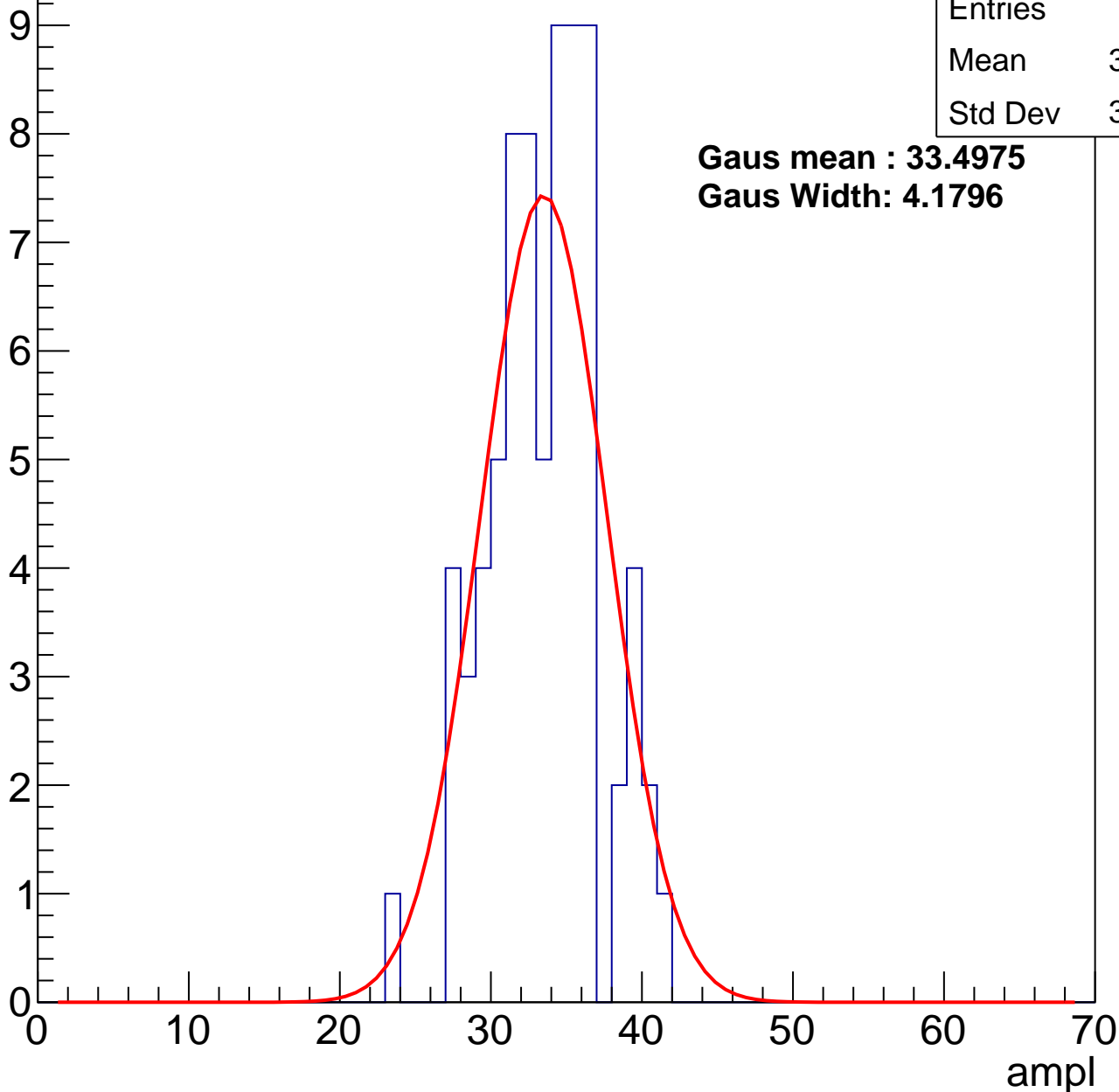
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	33.08
Std Dev	3.582

Gaus mean : 33.4975

Gaus Width: 4.1796



B1L103S, U1-ch75, adc2

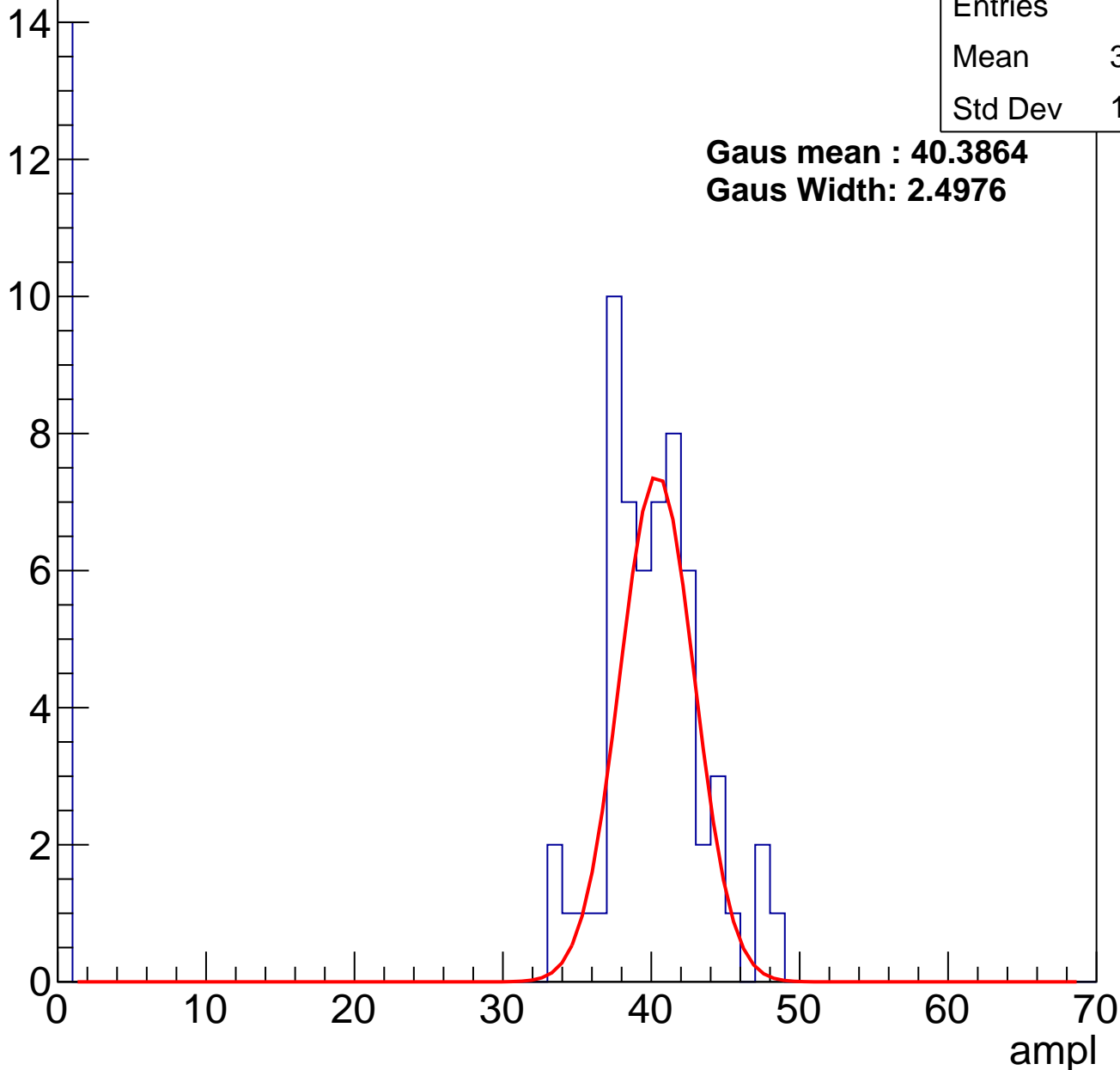
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	32.03
Std Dev	15.99

Gaus mean : 40.3864

Gaus Width: 2.4976

Entry

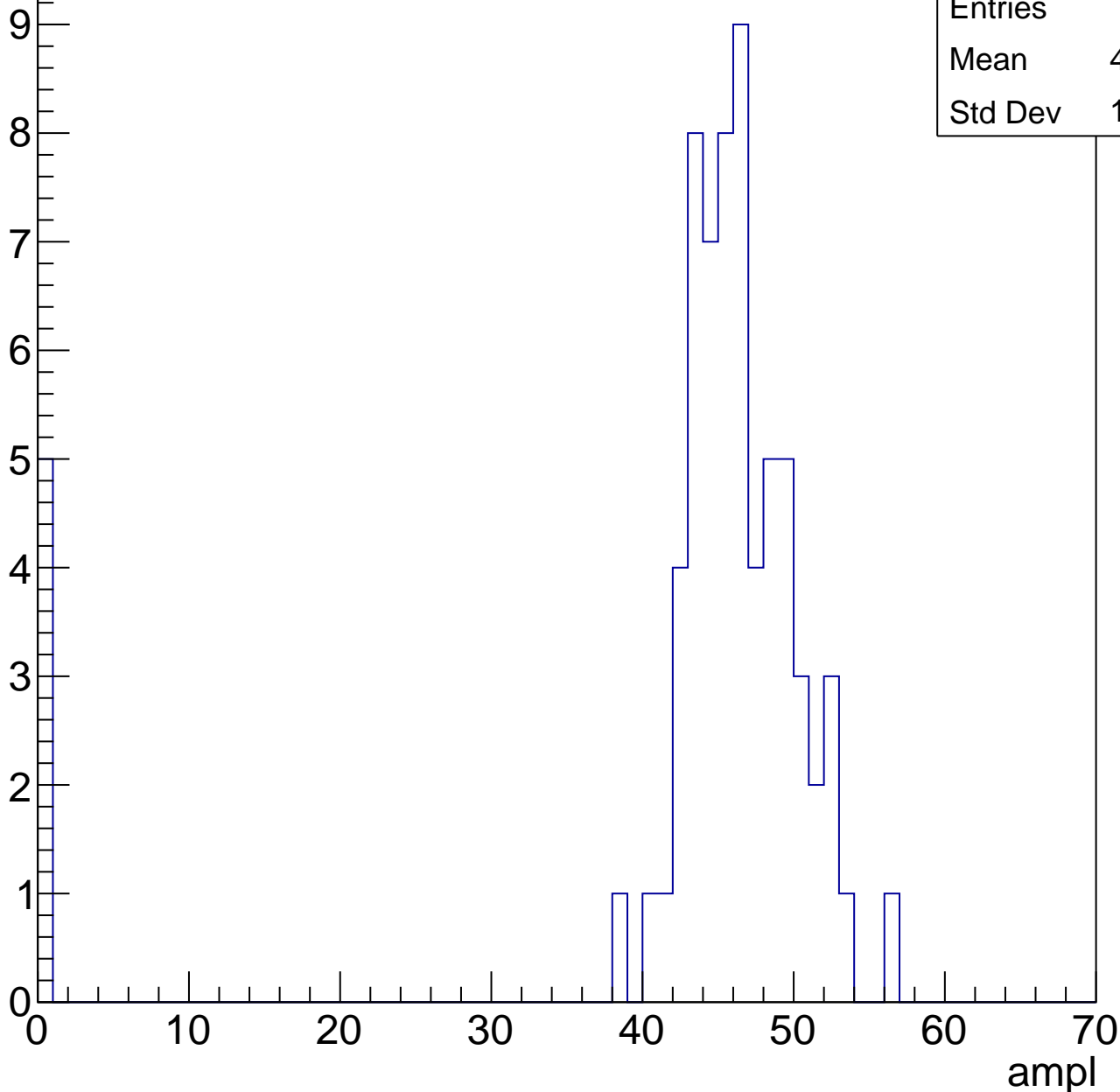


B1L103S, U1-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	42.69
Std Dev	12.46

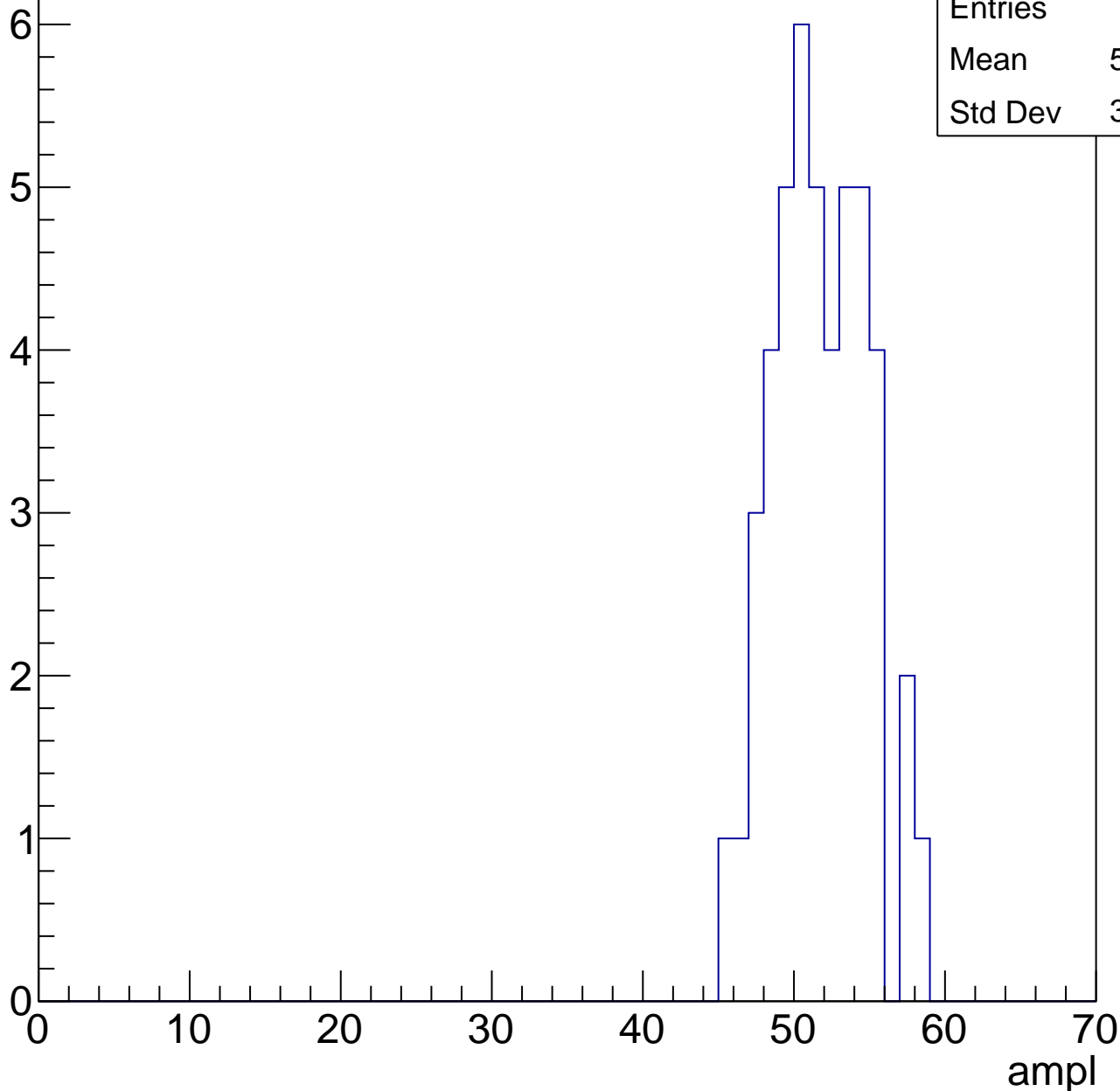


B1L103S, U1-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	51.28
Std Dev	3.026

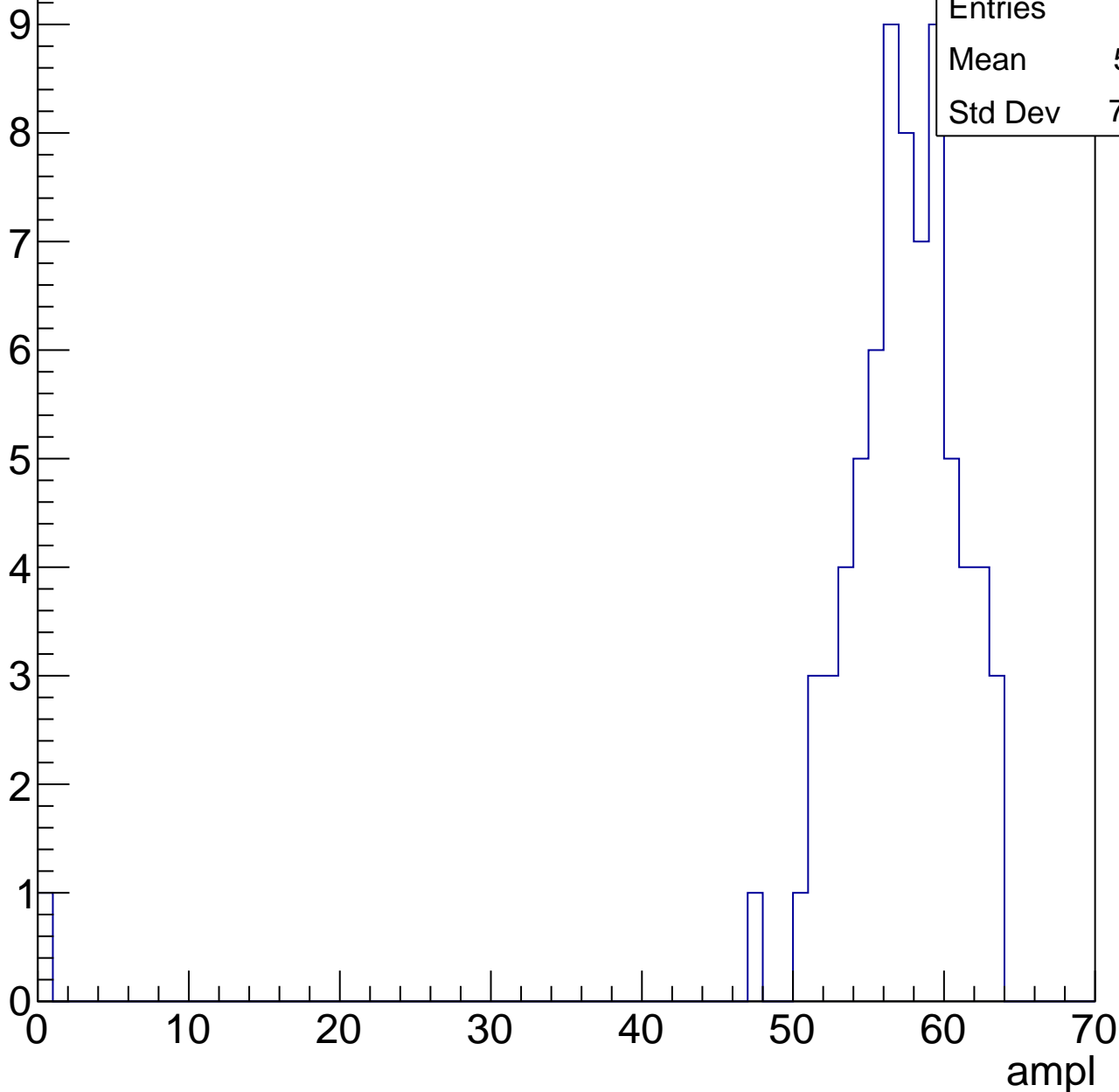


B1L103S, U1-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	56.11
Std Dev	7.426

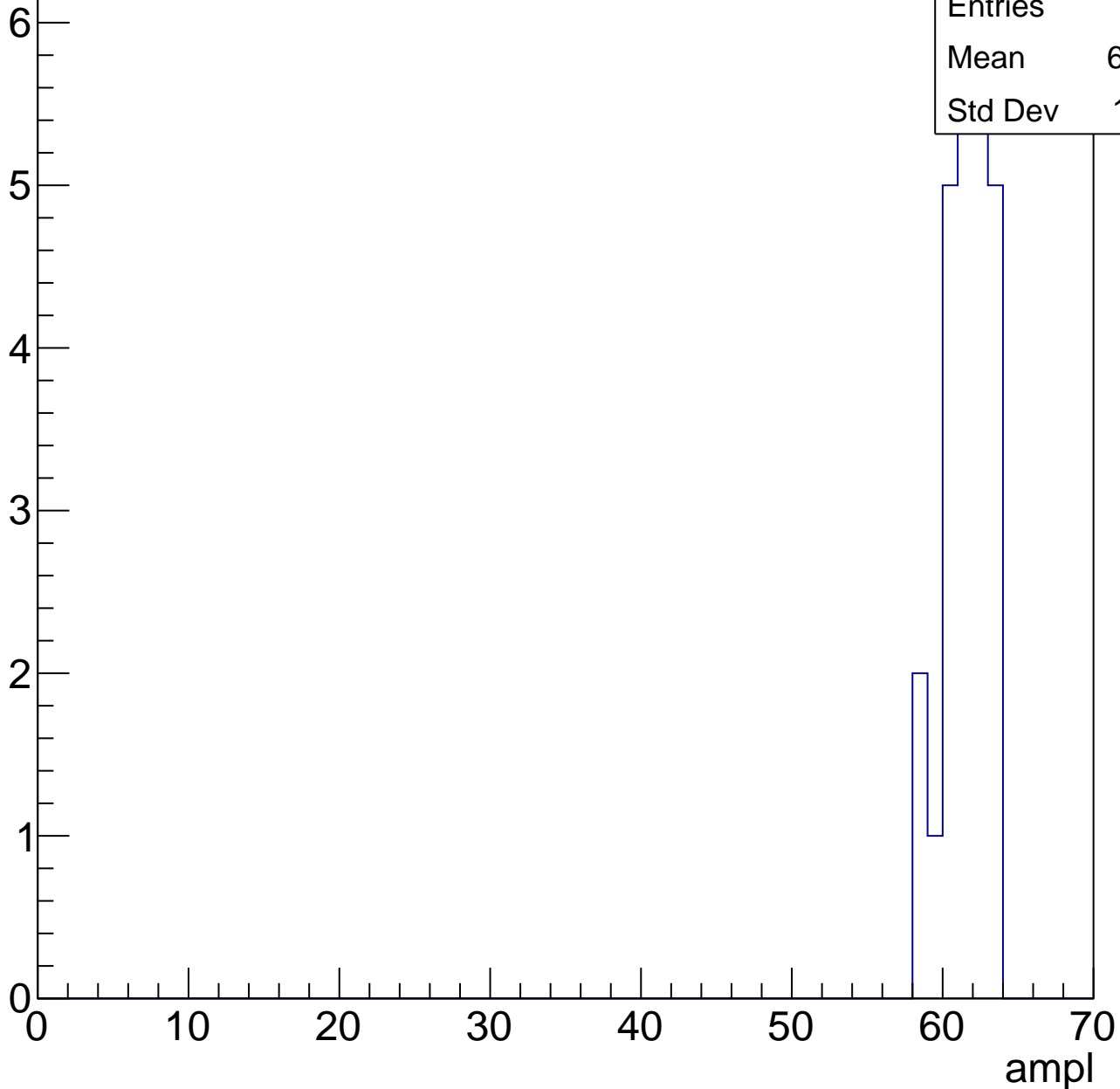


B1L103S, U1-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	61.12
Std Dev	1.451



B1L103S, U1-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch76, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	17.78
Std Dev	12.58

Gaus mean : 26.3574

Gaus Width: 4.2372

Entry

25

20

15

10

5

0

0

10

20

30

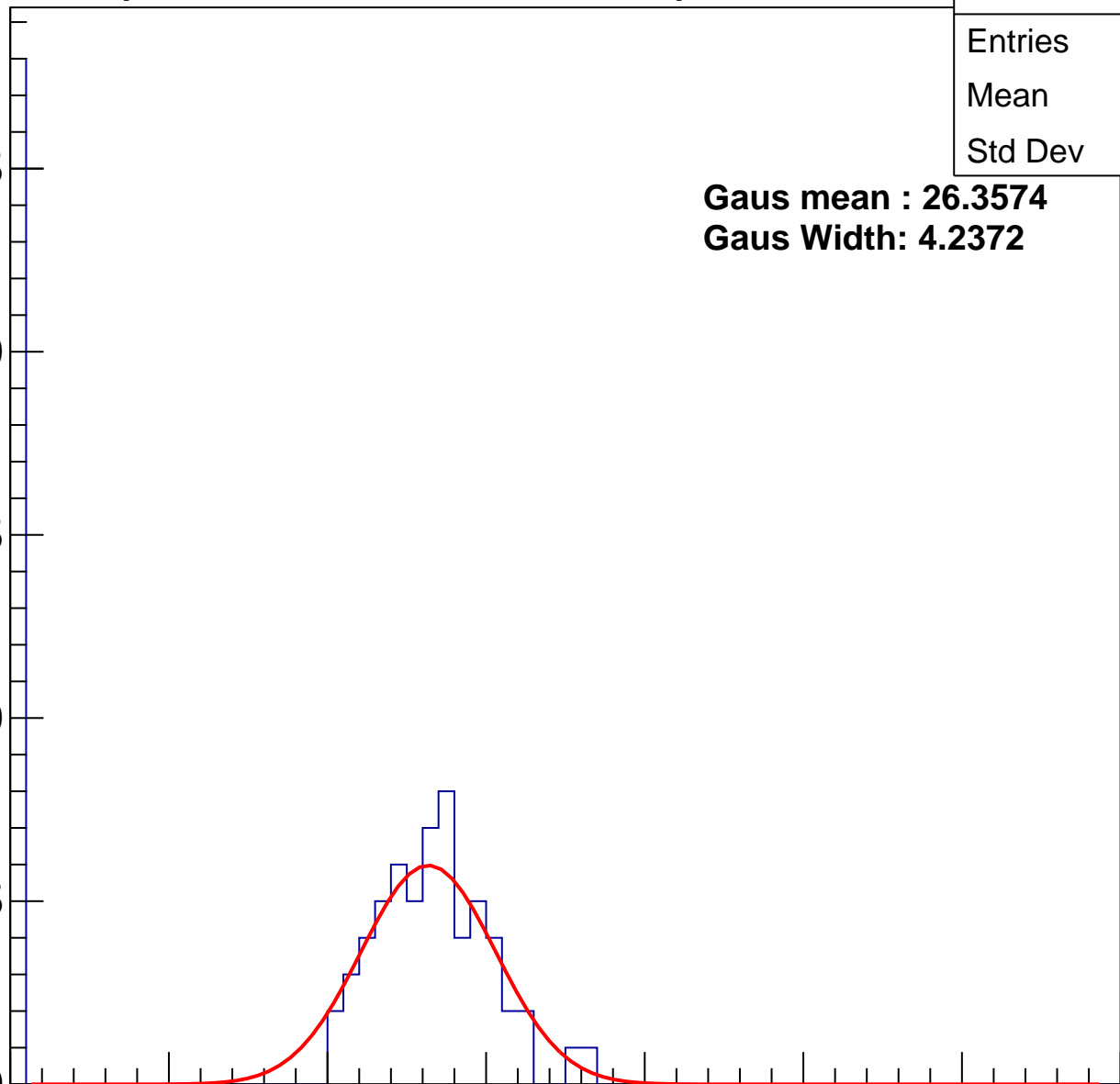
40

50

60

70

ampl



B1L103S, U1-ch76, adc1

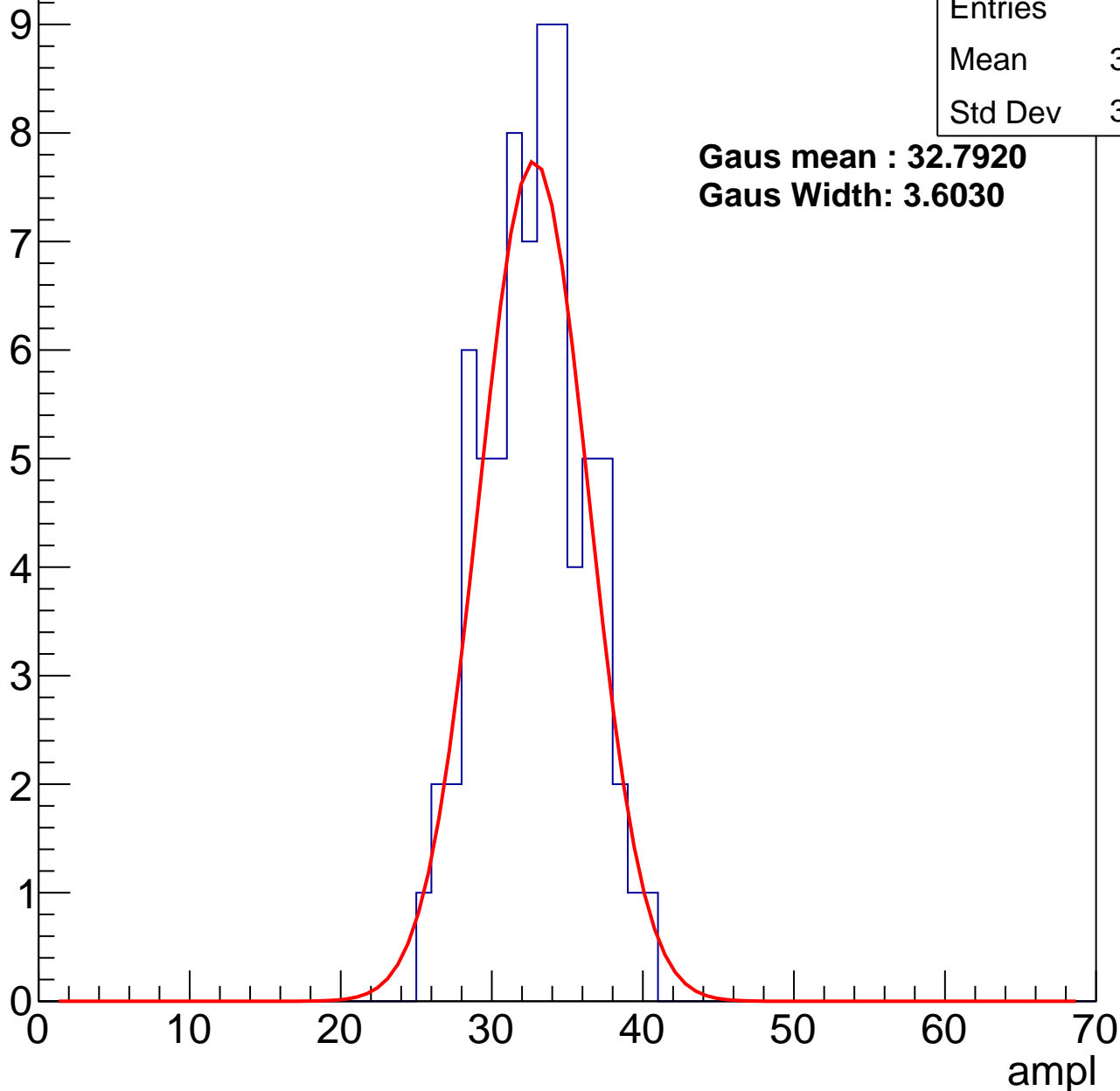
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	32.35
Std Dev	3.338

Gaus mean : 32.7920

Gaus Width: 3.6030



B1L103S, U1-ch76, adc2

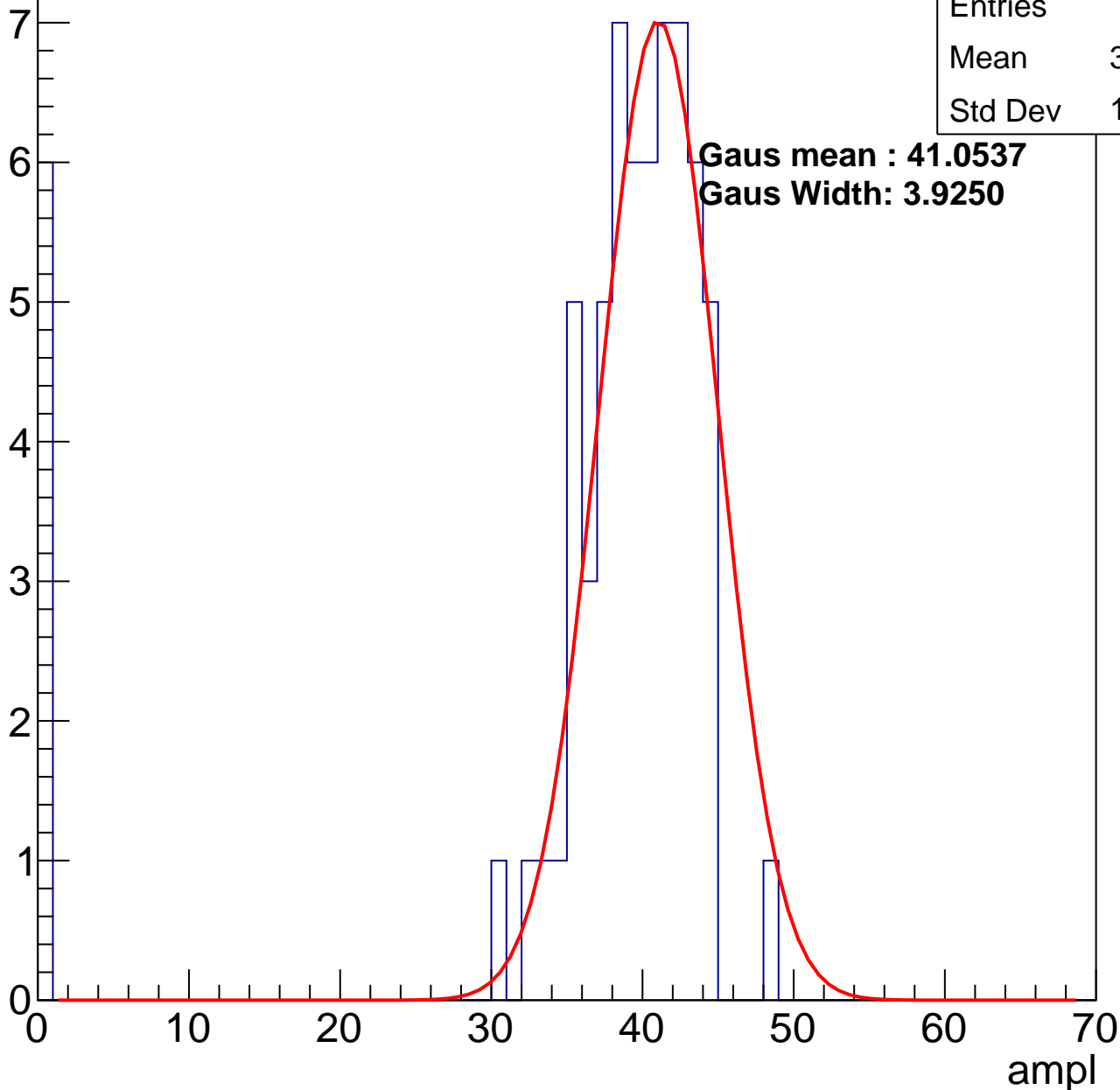
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.94
Std Dev	11.64

Gaus mean : 41.0537

Gaus Width: 3.9250

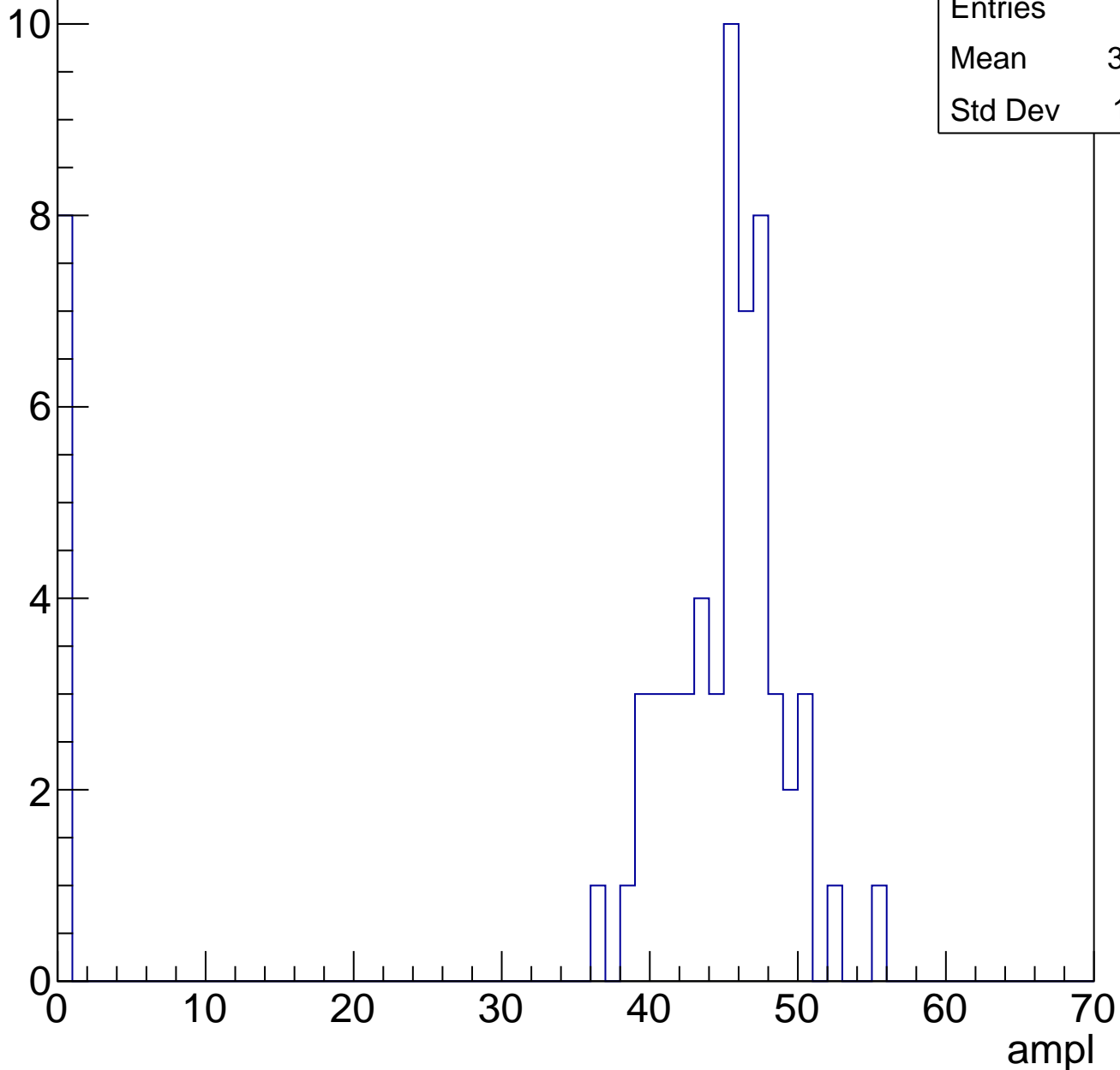


B1L103S, U1-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	39.23
Std Dev	15.21

Entry

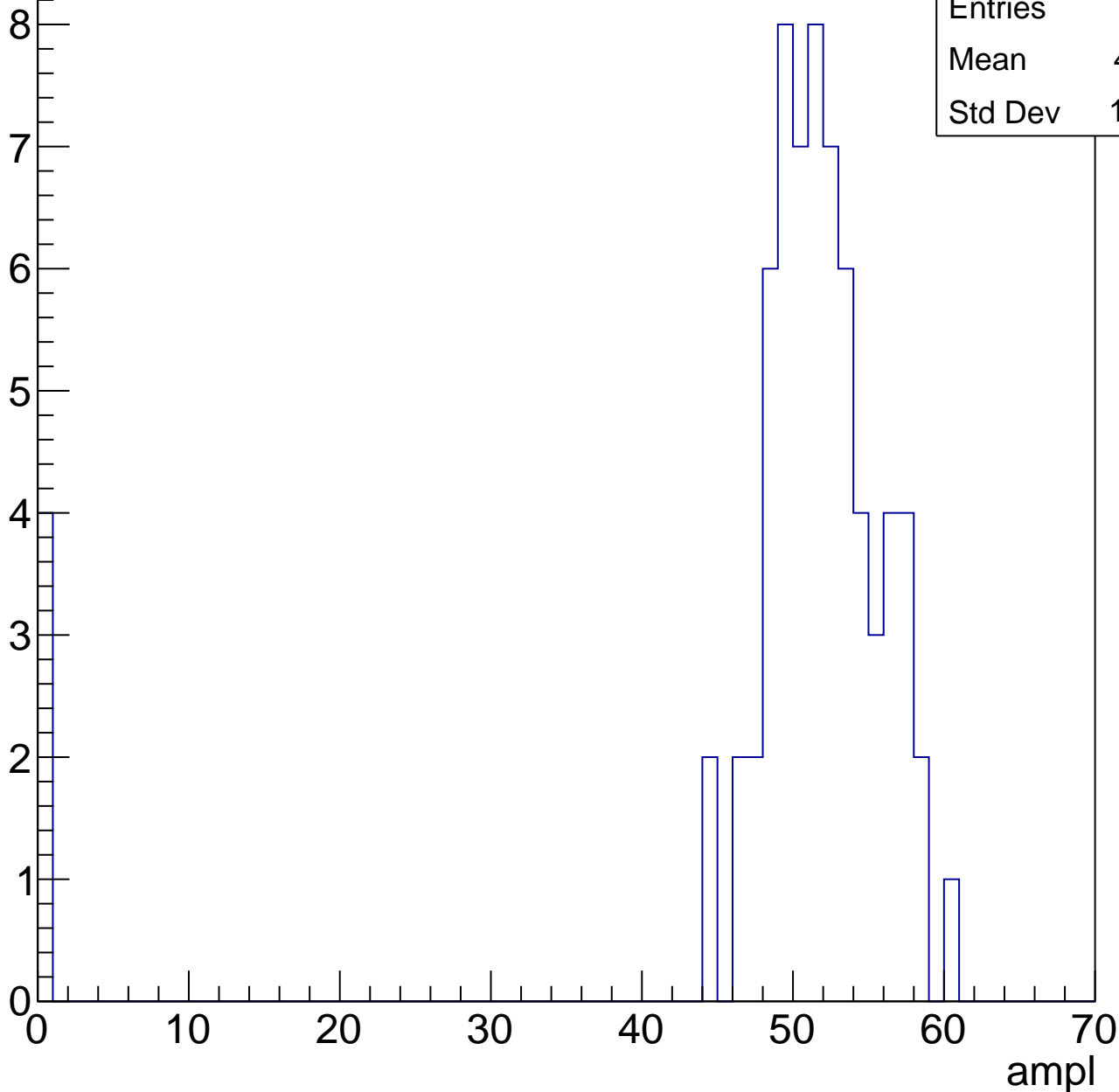


B1L103S, U1-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

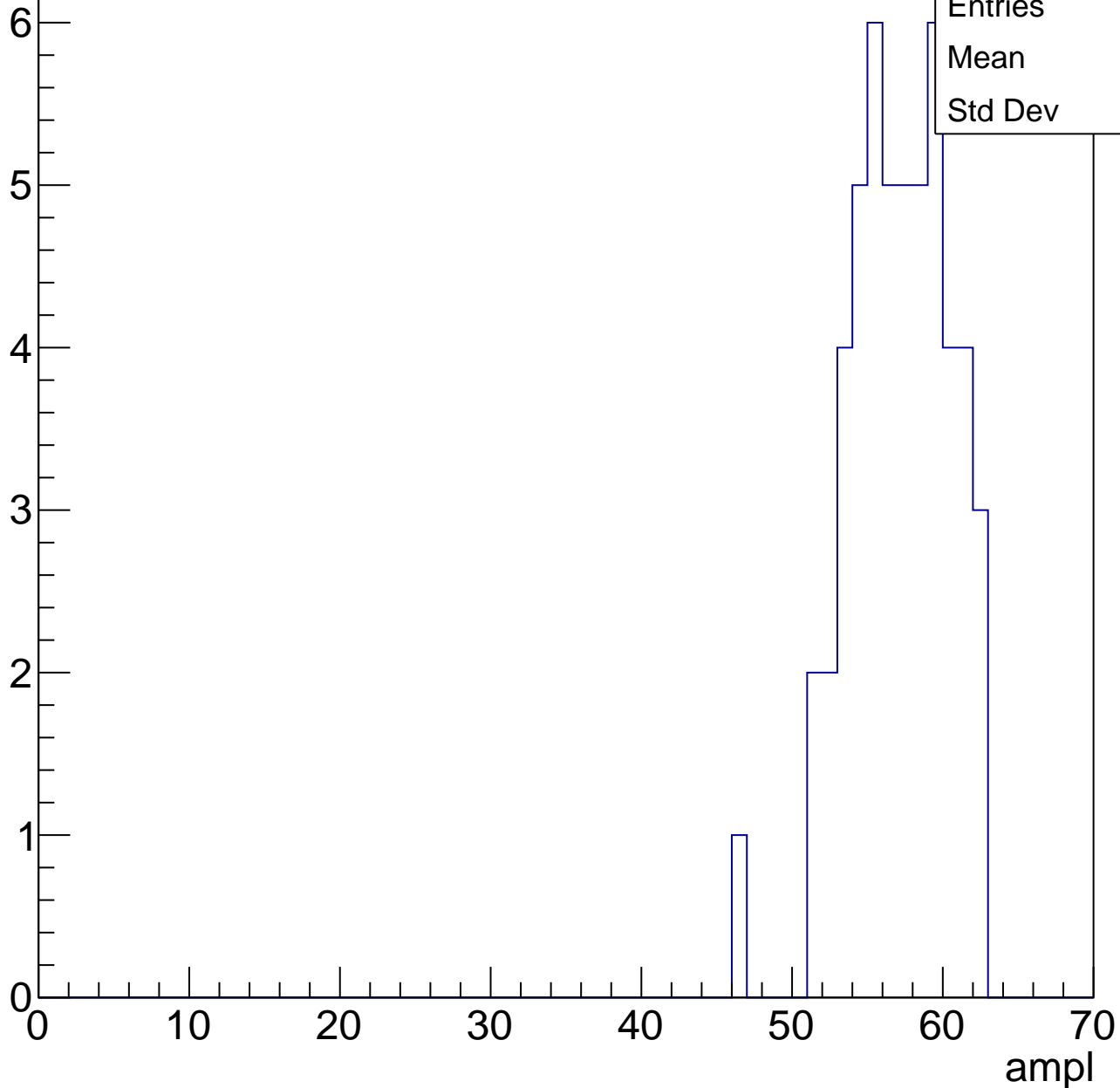
Entries	70
Mean	48.61
Std Dev	12.43



B1L103S, U1-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

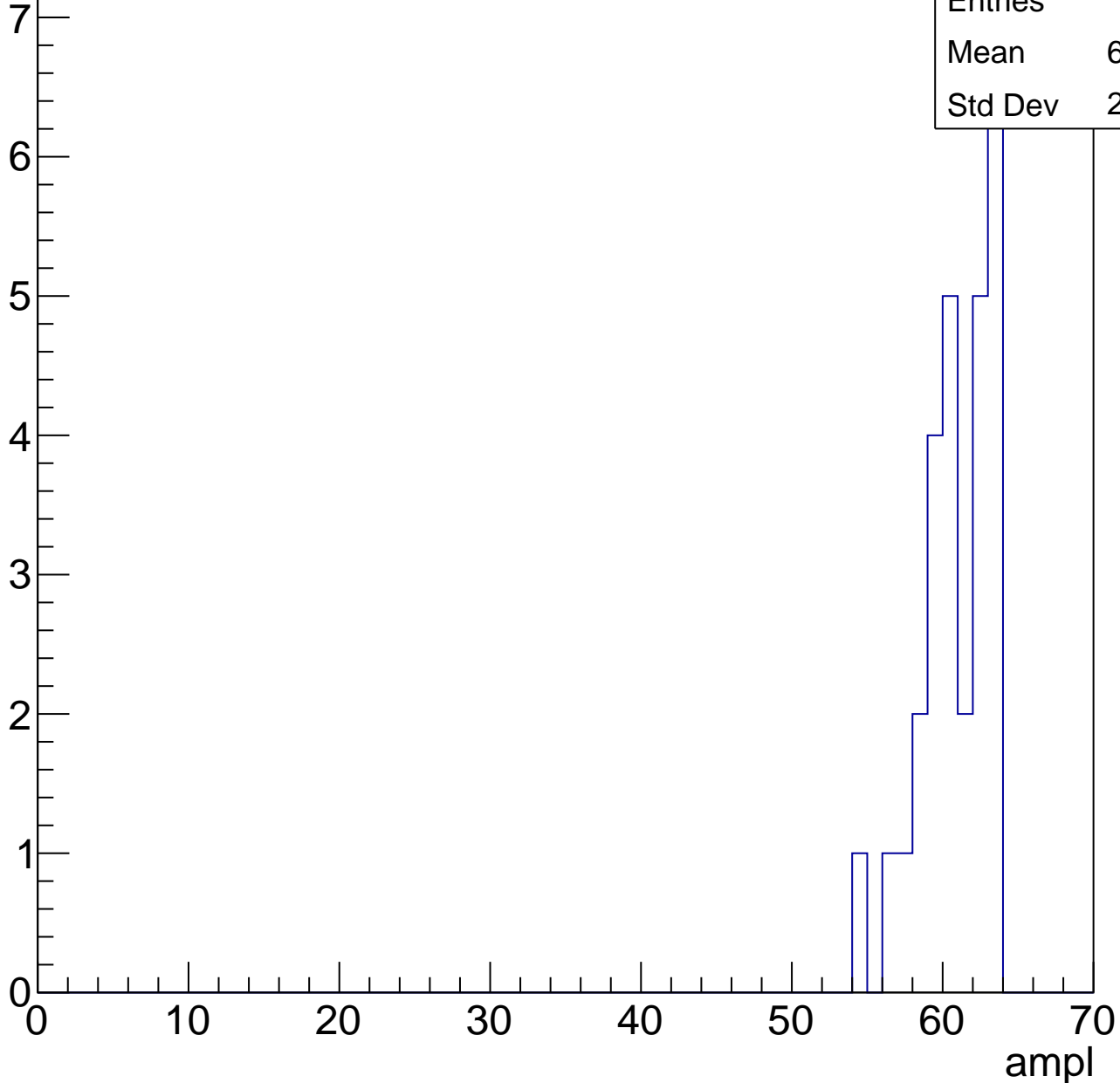


B1L103S, U1-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	60.43
Std Dev	2.336



B1L103S, U1-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	30
Mean	20.5
Std Dev	29

ampl

0 10 20 30 40 50 60 70

B1L103S, U1-ch77, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	21.67
Std Dev	12.69

Gaus mean : 29.0125

Gaus Width: 4.0819

Entry

25

20

15

10

5

0

0

10

20

30

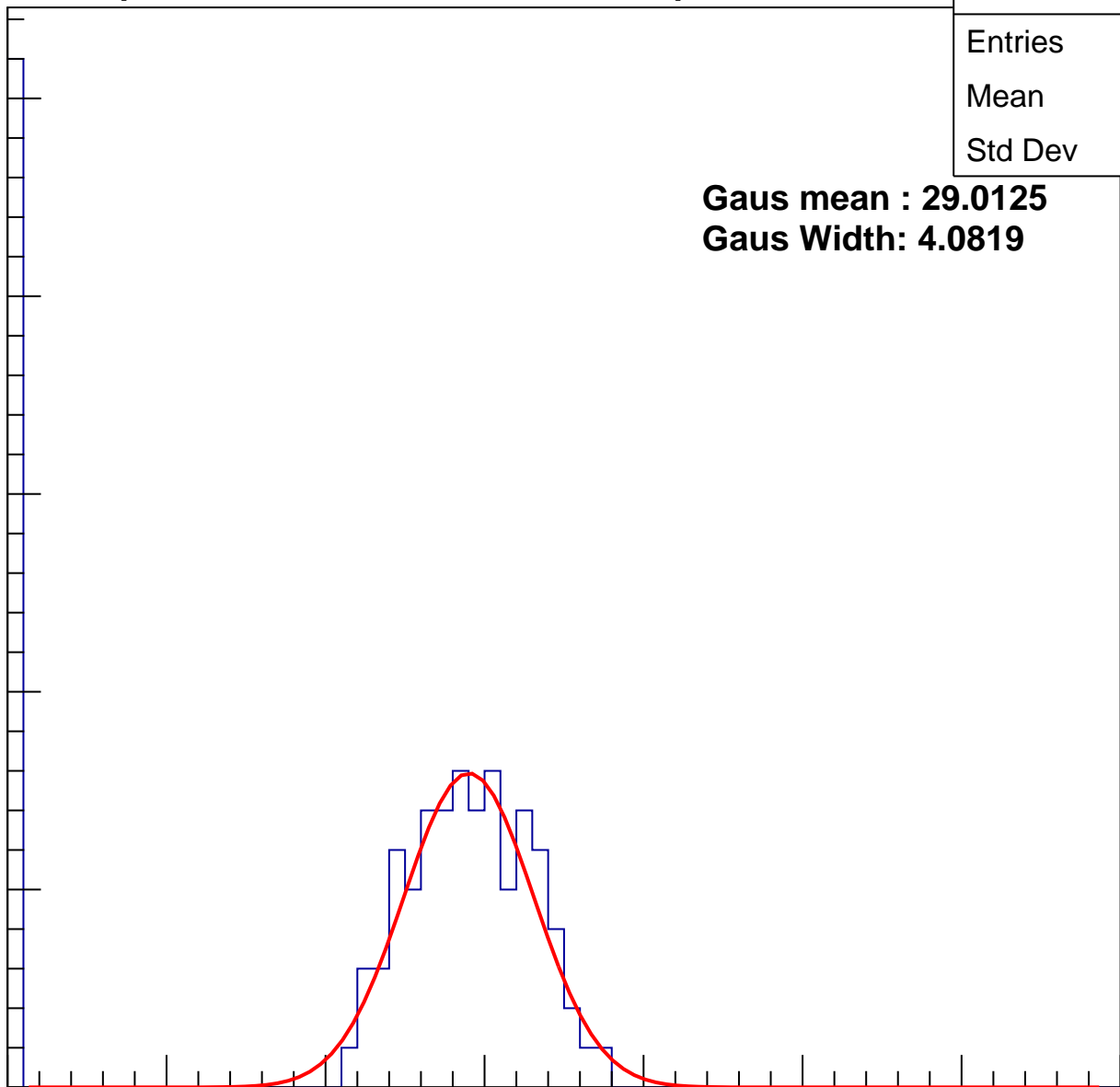
40

50

60

70

ampl



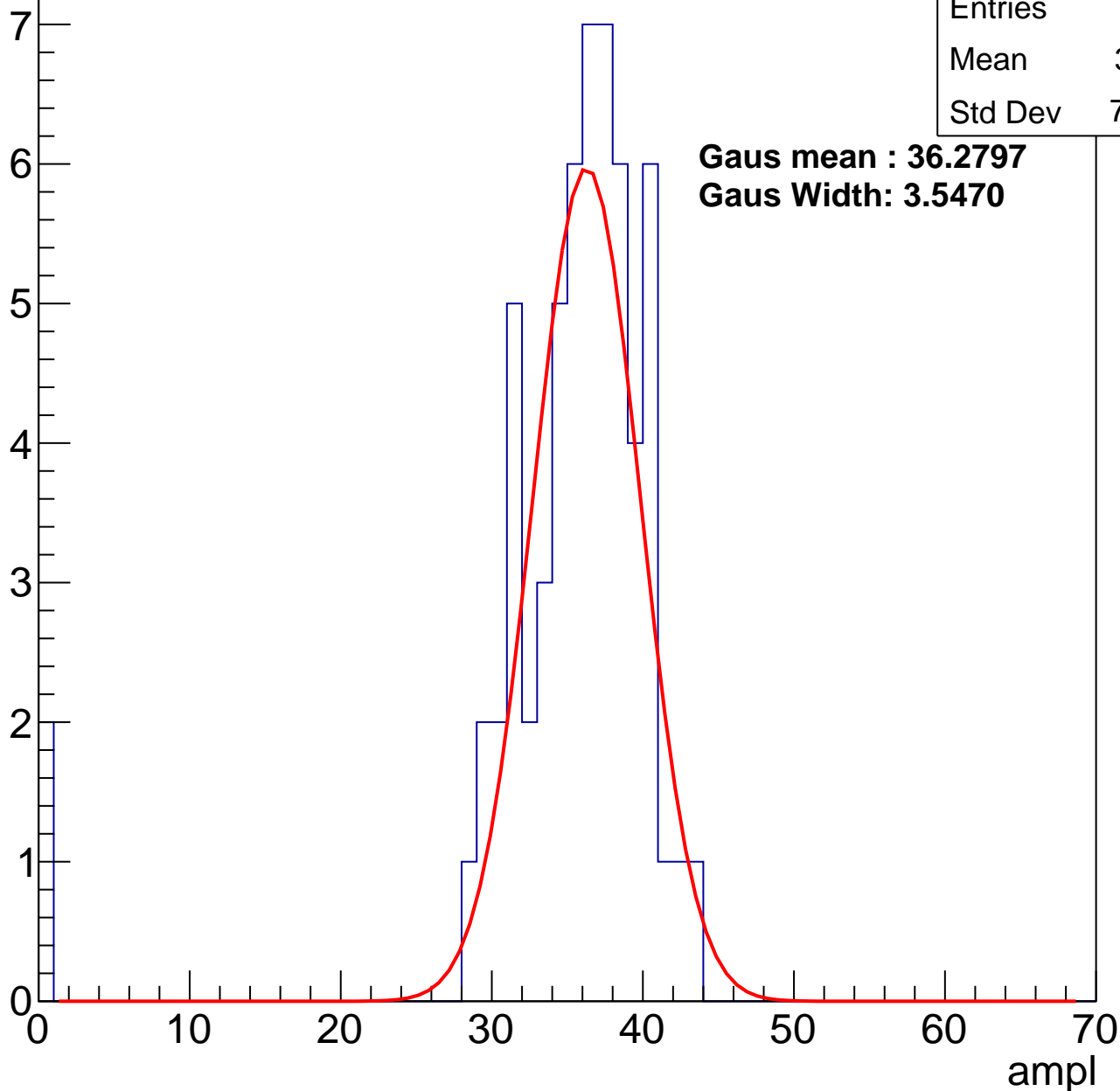
B1L103S, U1-ch77, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.51
Std Dev	7.213

Gaus mean : 36.2797
Gaus Width: 3.5470



B1L103S, U1-ch77, adc2

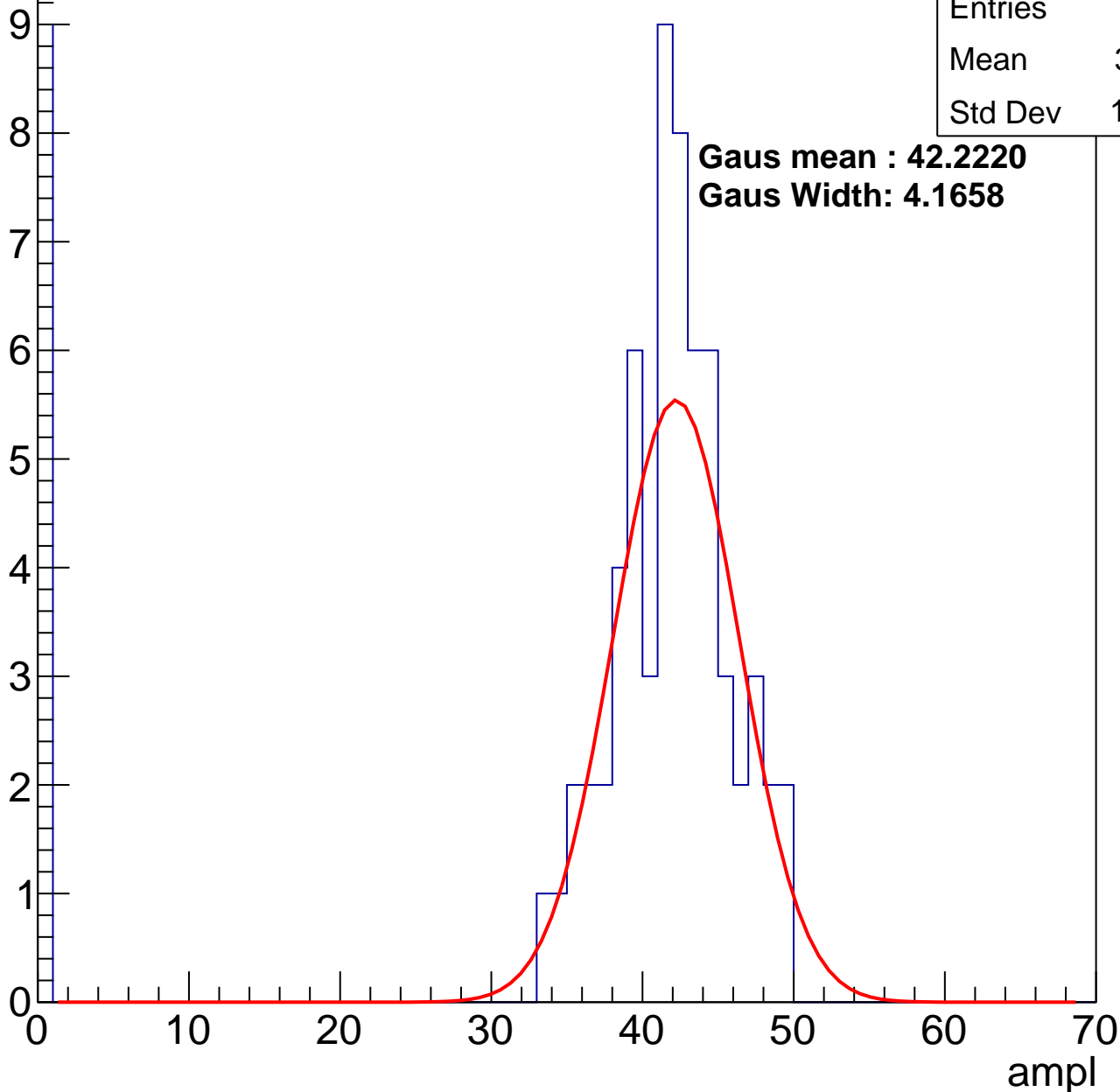
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	36.31
Std Dev	14.25

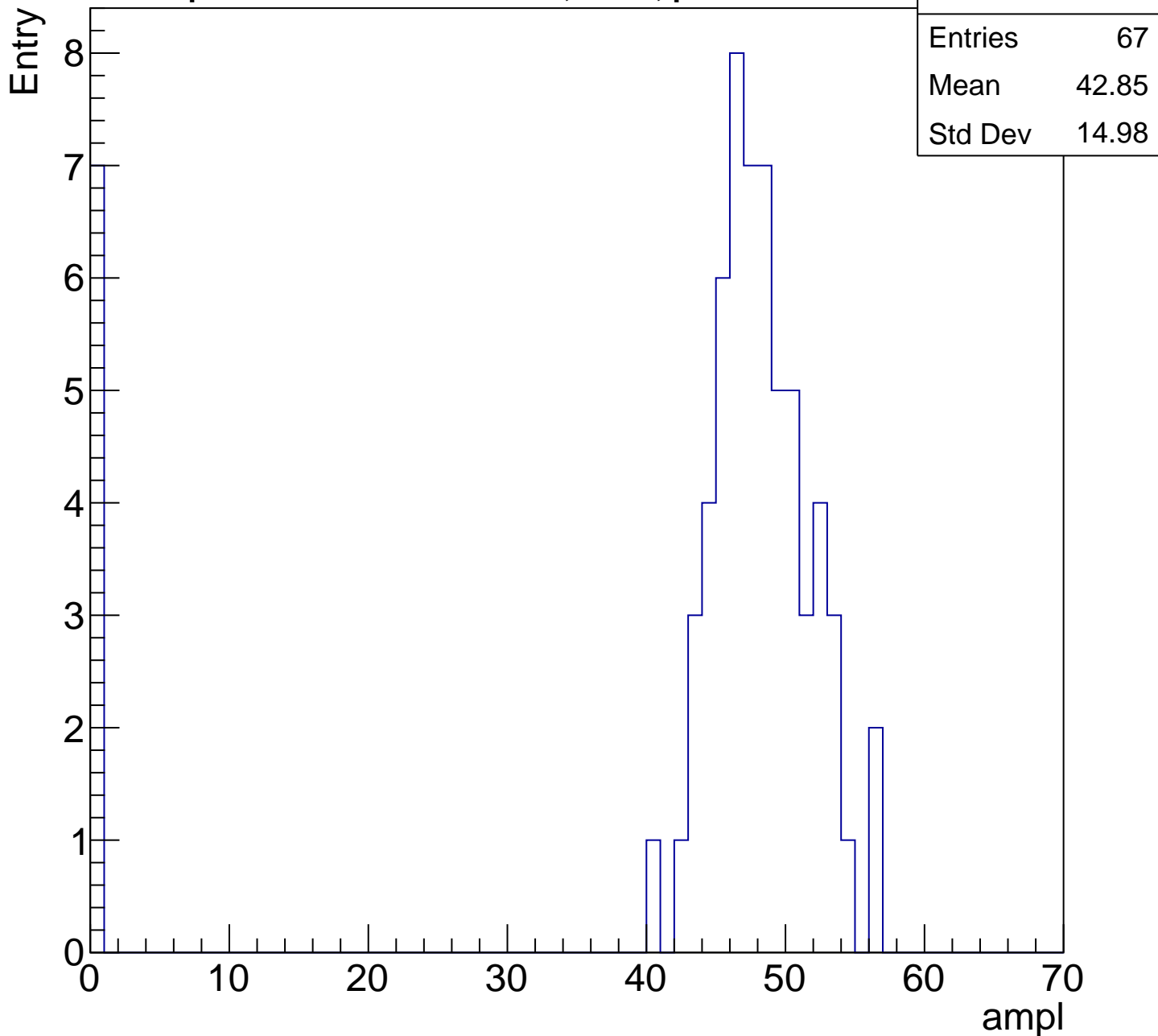
Gaus mean : 42.2220

Gaus Width: 4.1658



B1L103S, U1-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

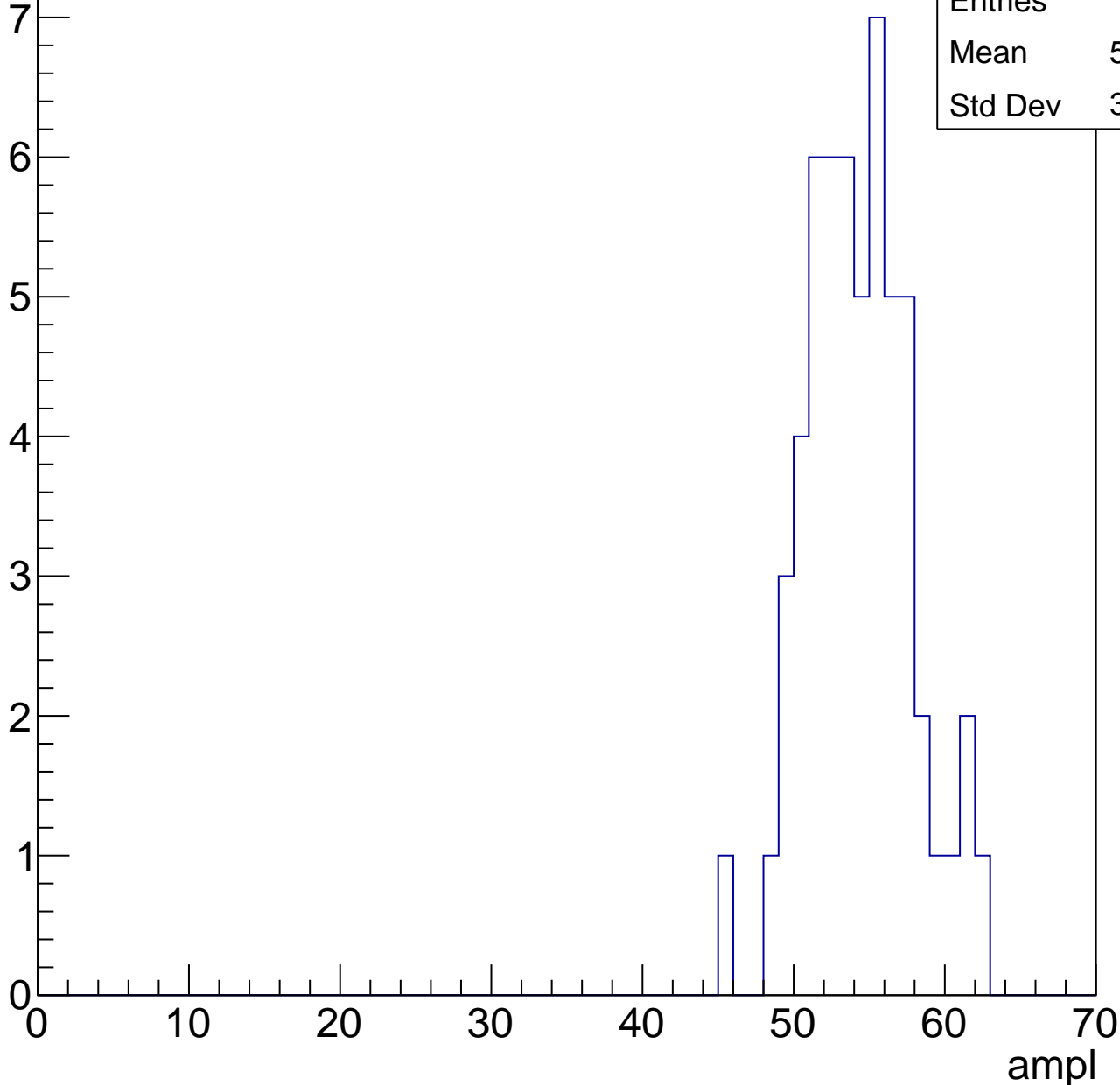


B1L103S, U1-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.84
Std Dev	3.447



B1L103S, U1-ch77, adc5

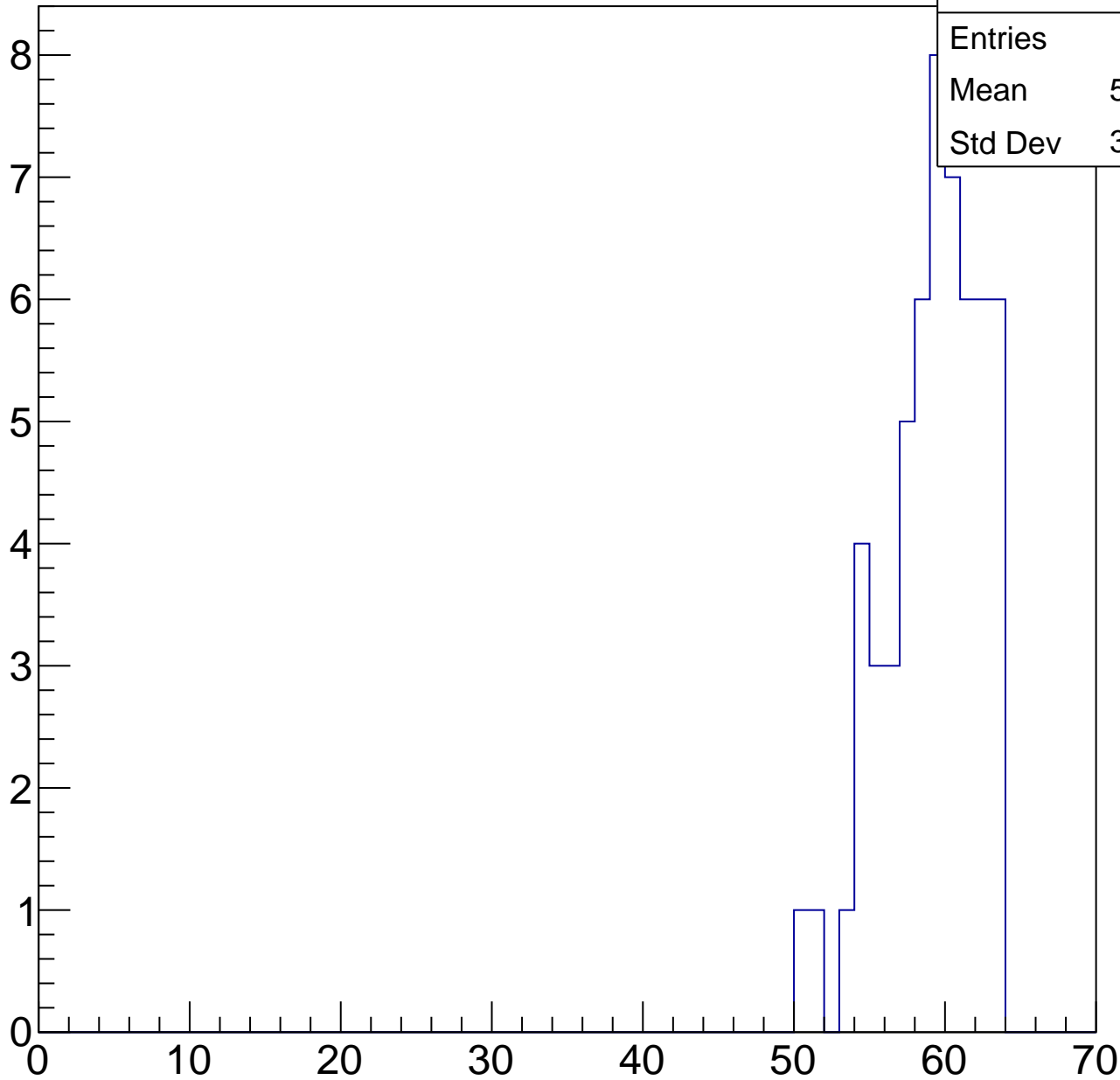
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.67
Std Dev	3.125

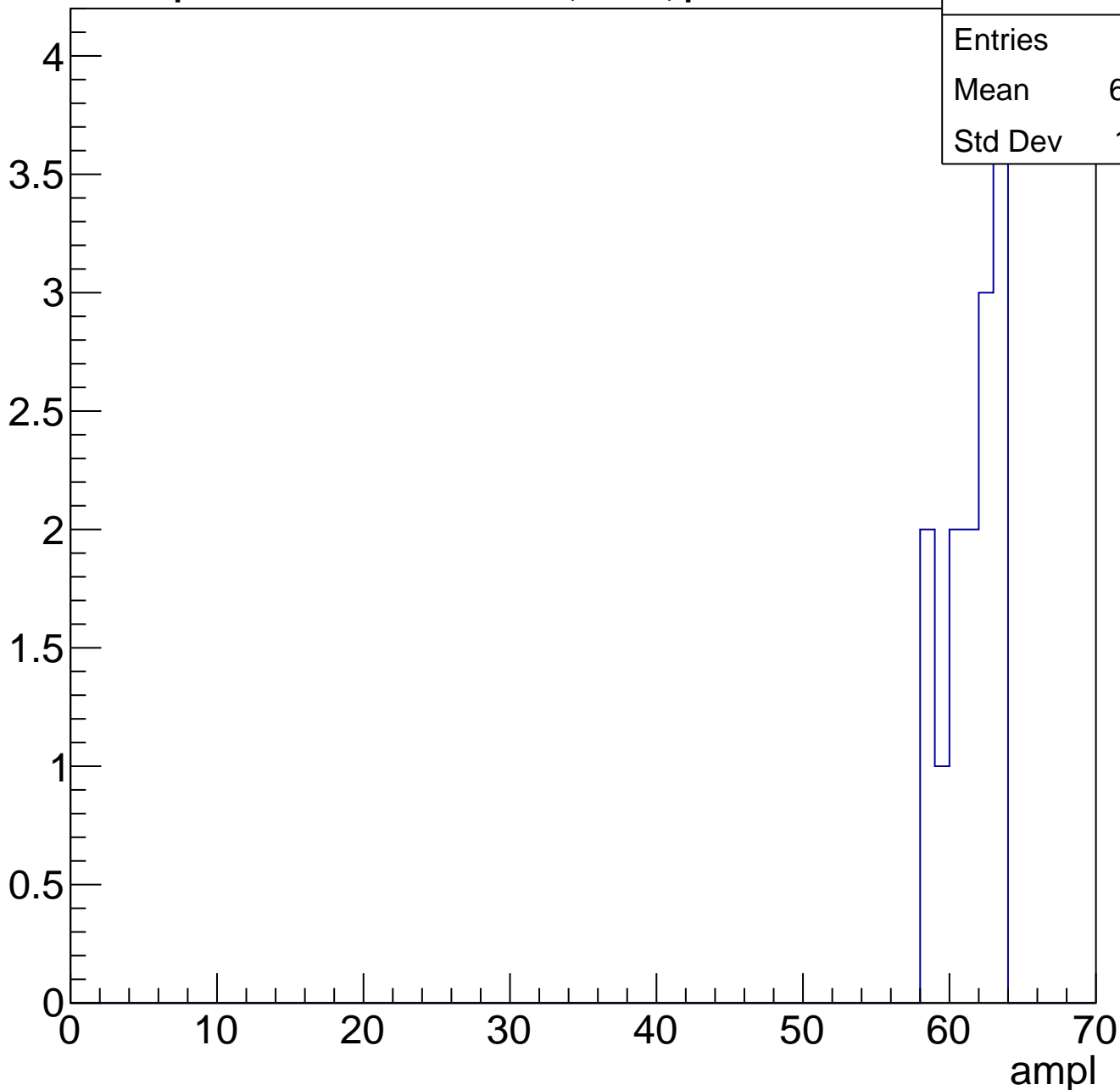
ampl



B1L103S, U1-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	14
Mean	61.07
Std Dev	1.751

B1L103S, U1-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U1-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.22
Std Dev	10.17

Gaus mean : 27.0413

Gaus Width: 3.4157

Entry

12

10

8

6

4

2

0

0

10

20

30

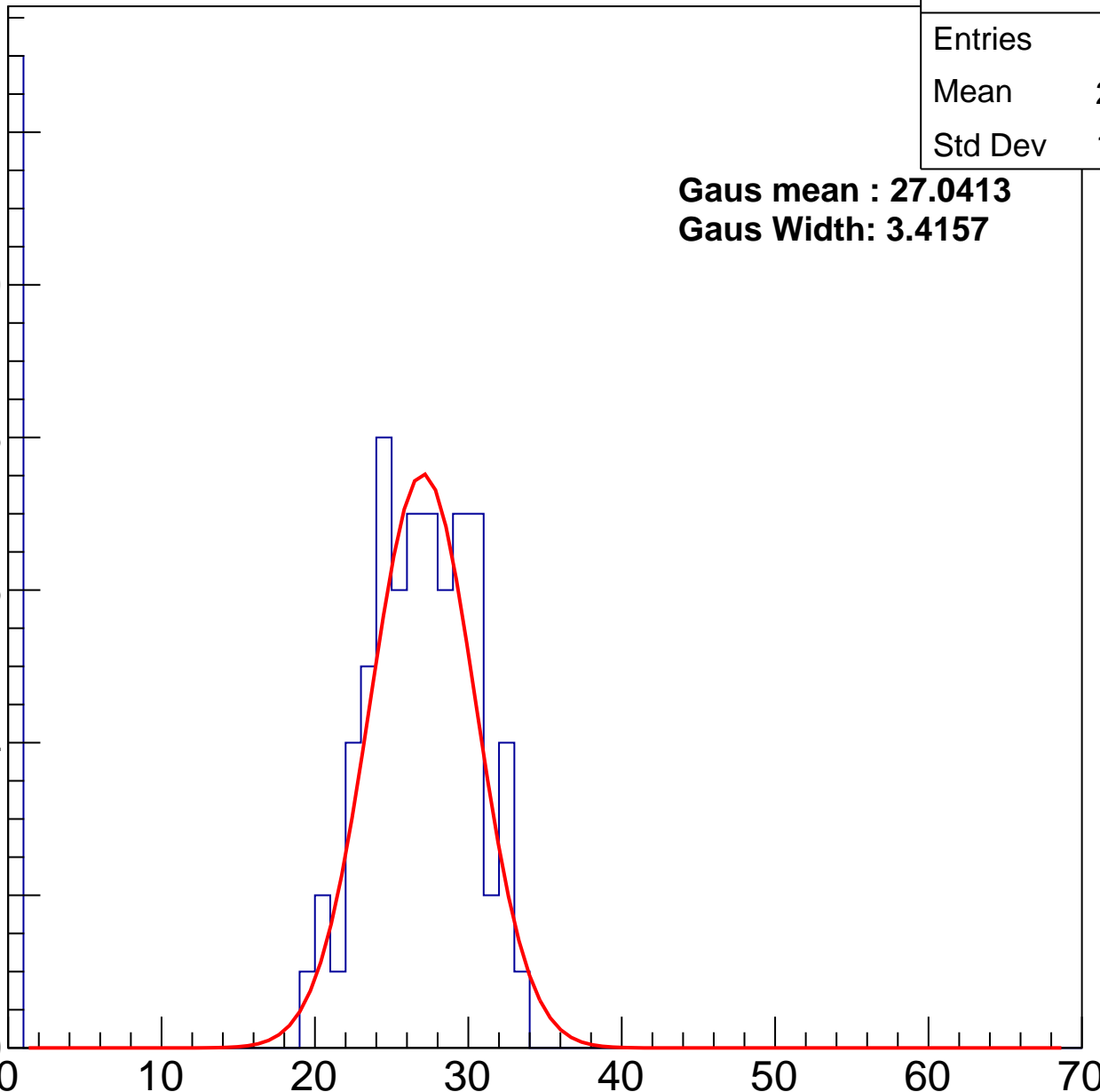
40

50

60

70

ampl



B1L103S, U1-ch78, adc1

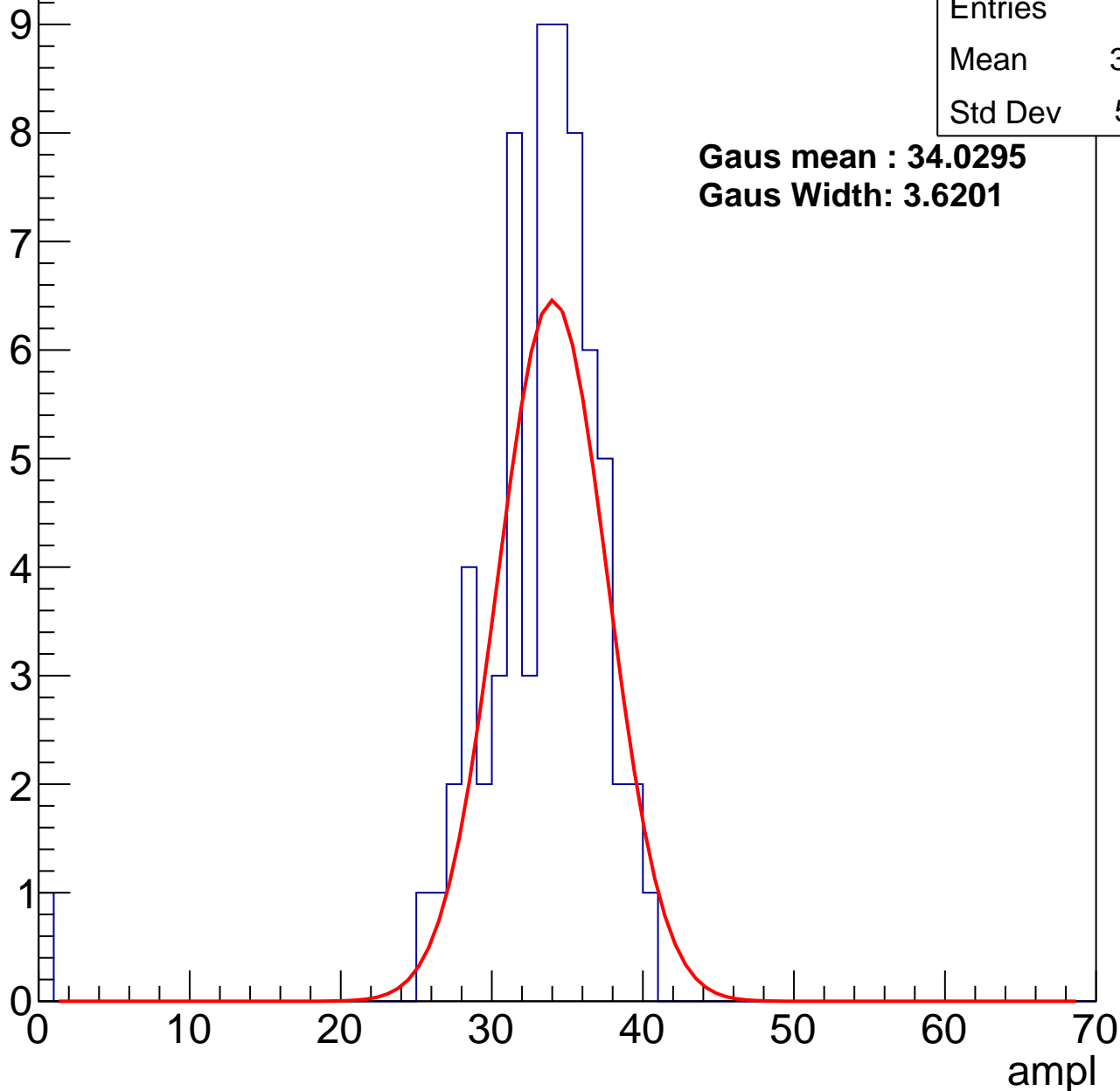
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.64
Std Dev	5.191

Gaus mean : 34.0295

Gaus Width: 3.6201



B1L103S, U1-ch78, adc2

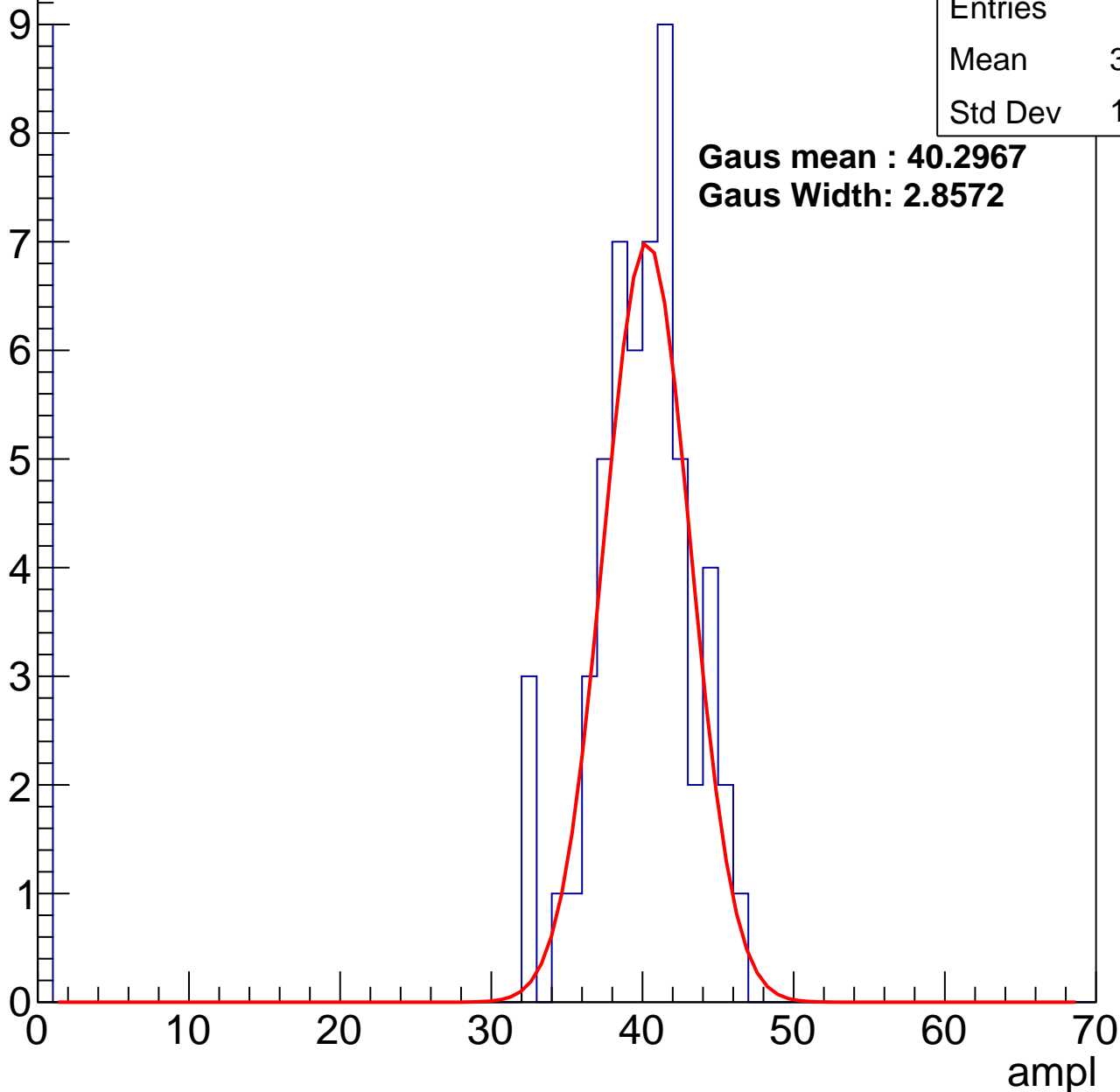
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.08
Std Dev	13.98

Gaus mean : 40.2967

Gaus Width: 2.8572

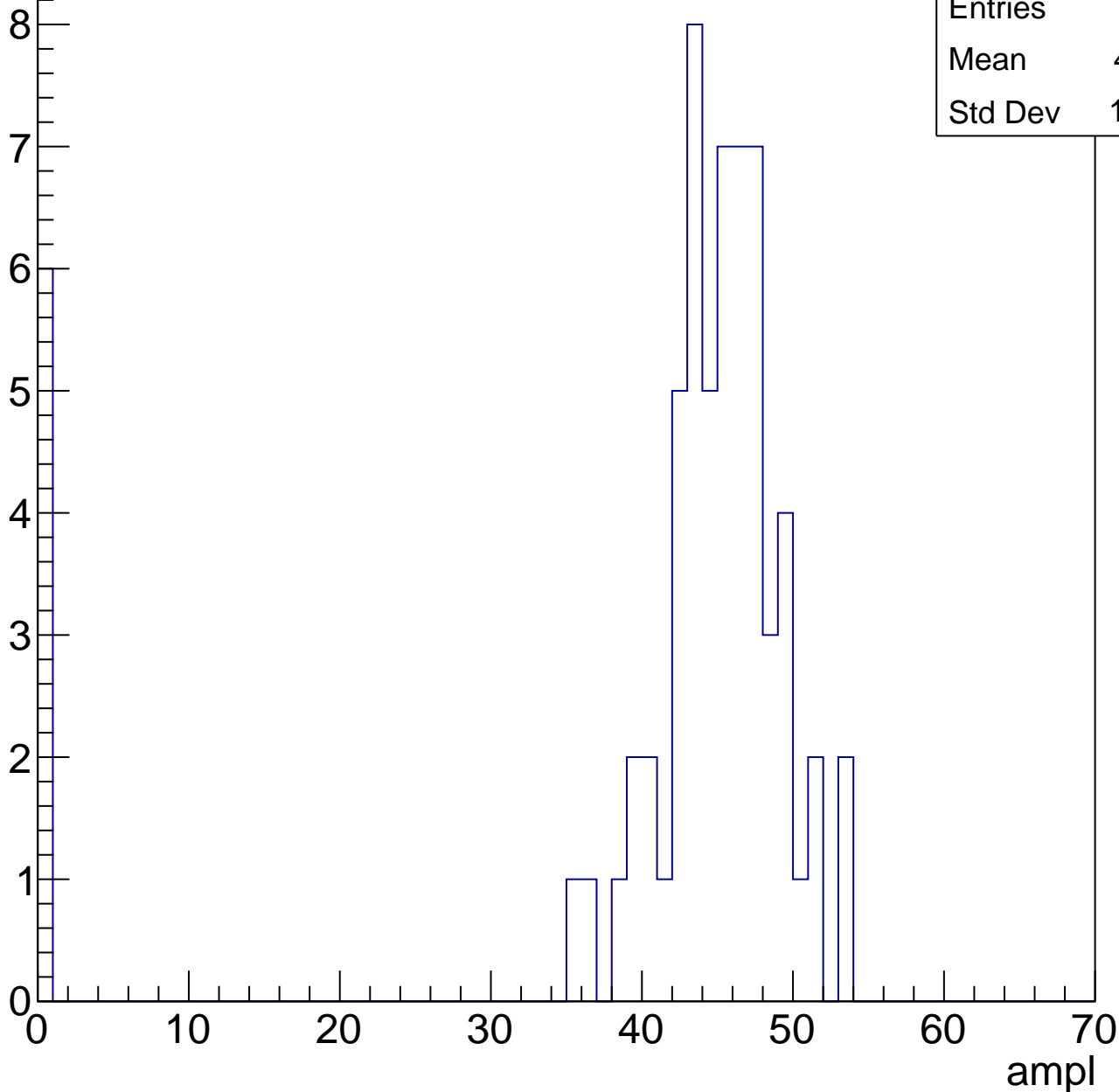


B1L103S, U1-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.71
Std Dev	13.45

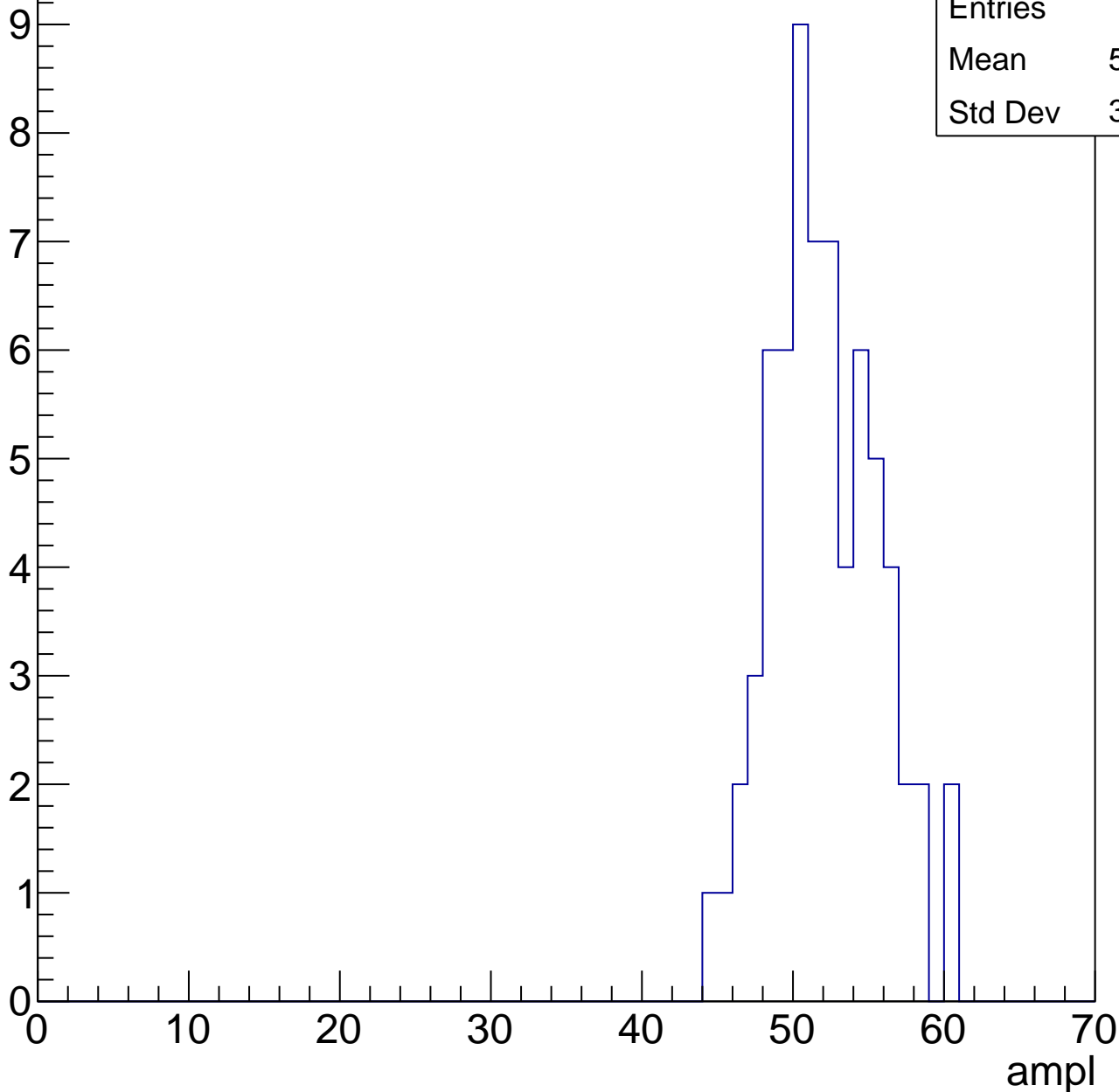


B1L103S, U1-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

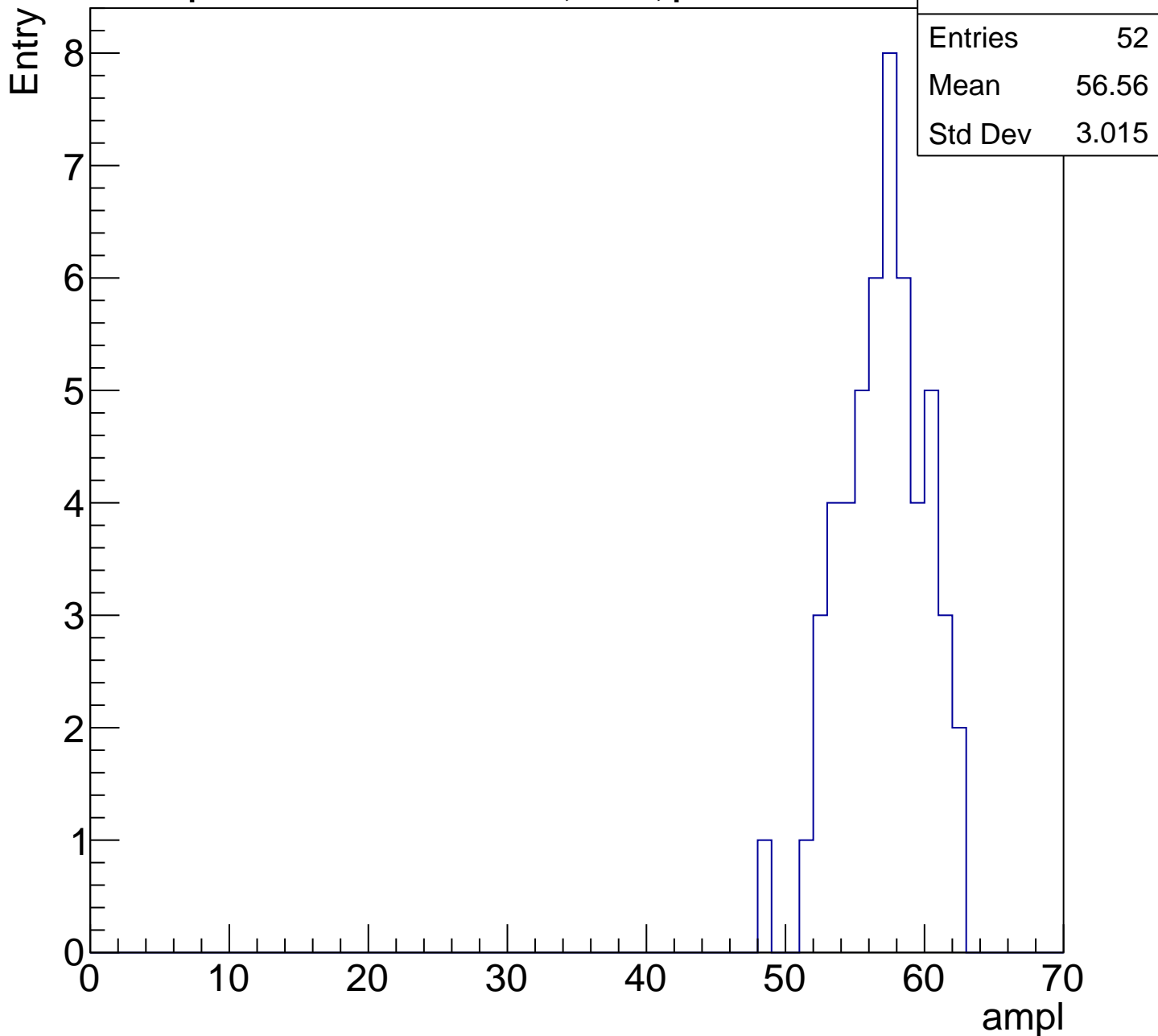
Entry

Entries	67
Mean	51.64
Std Dev	3.523



B1L103S, U1-ch78, adc5

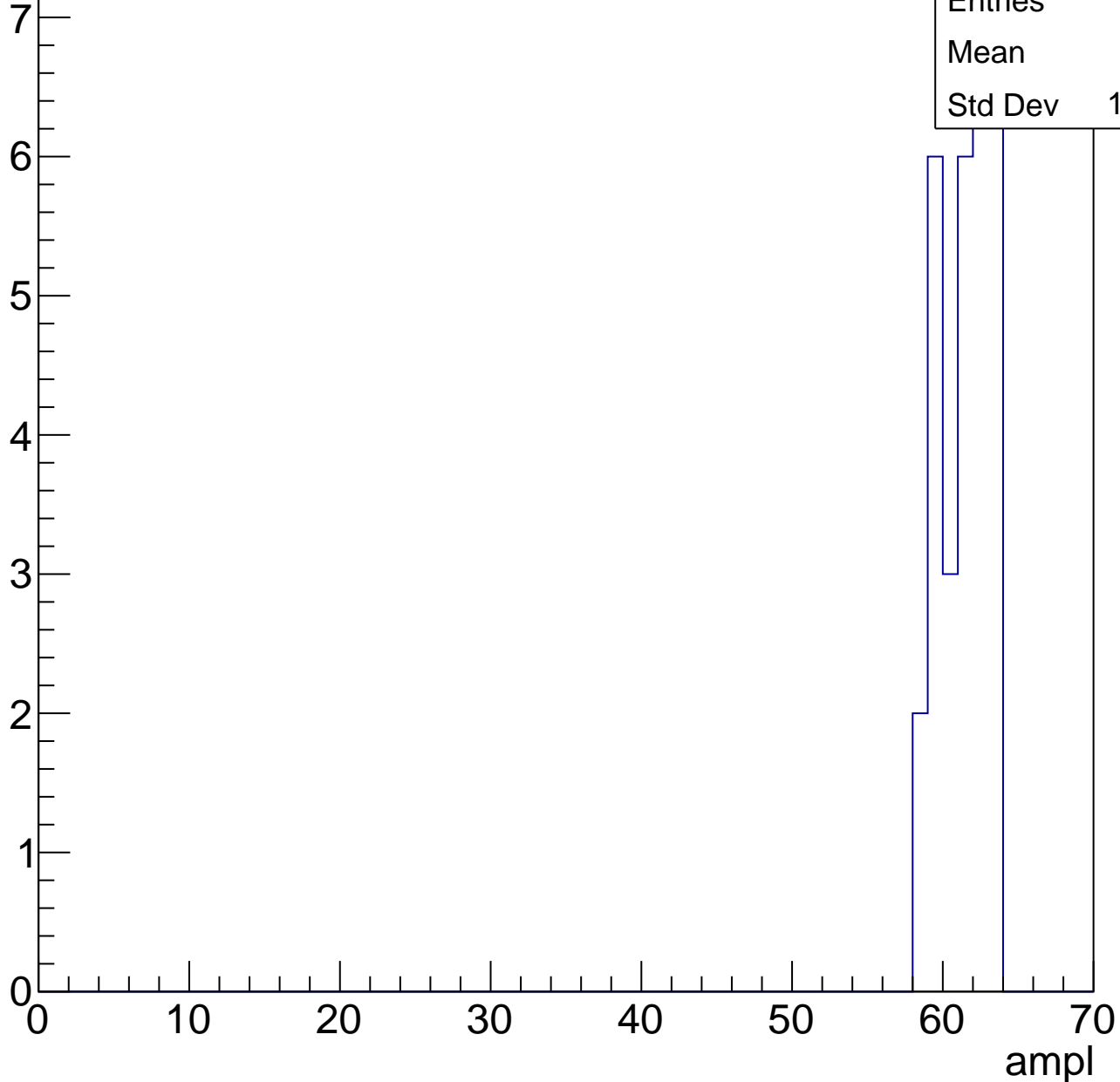
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

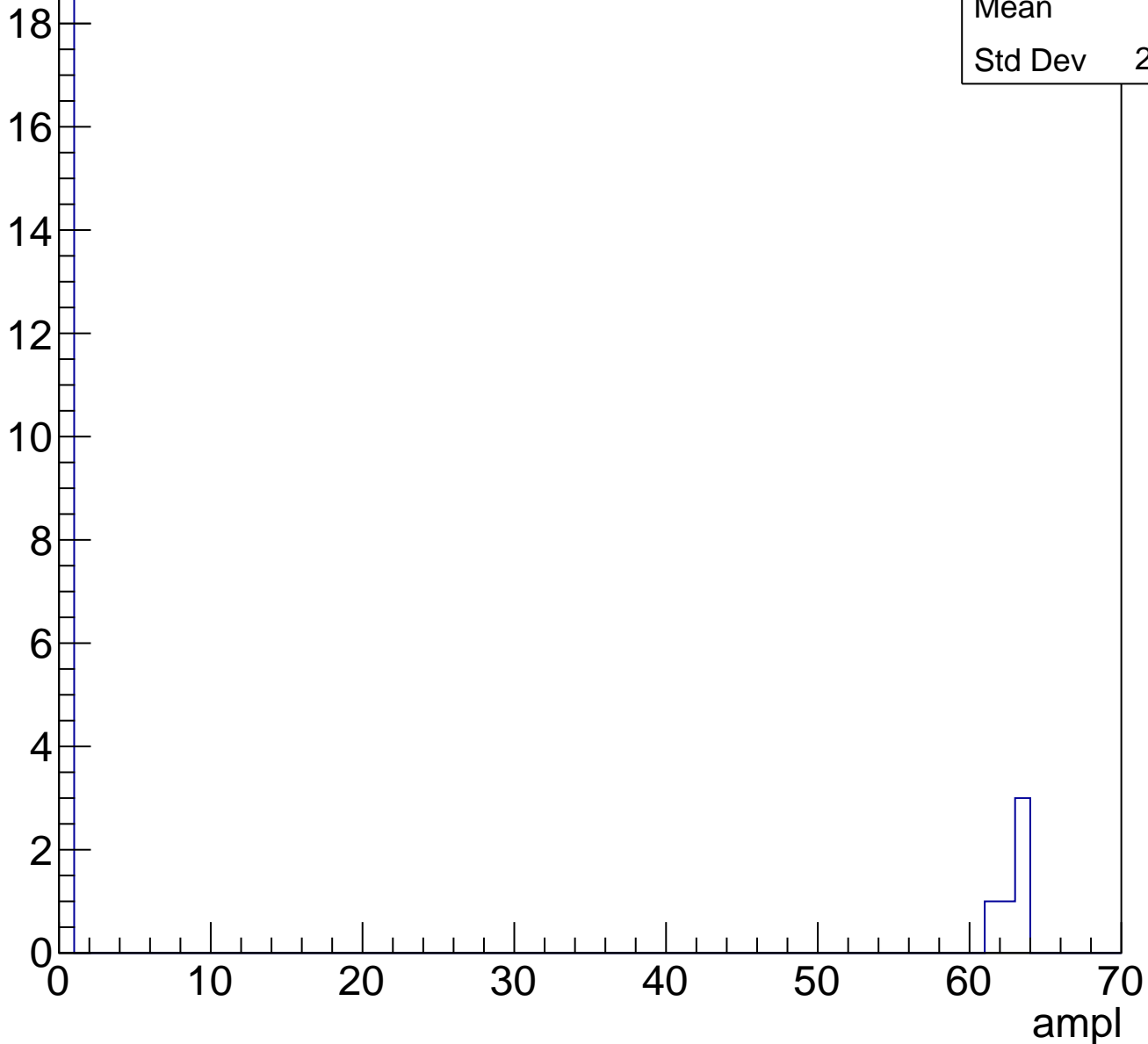


B1L103S, U1-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	13
Std Dev	25.34

Entry



B1L103S, U1-ch79, adc0

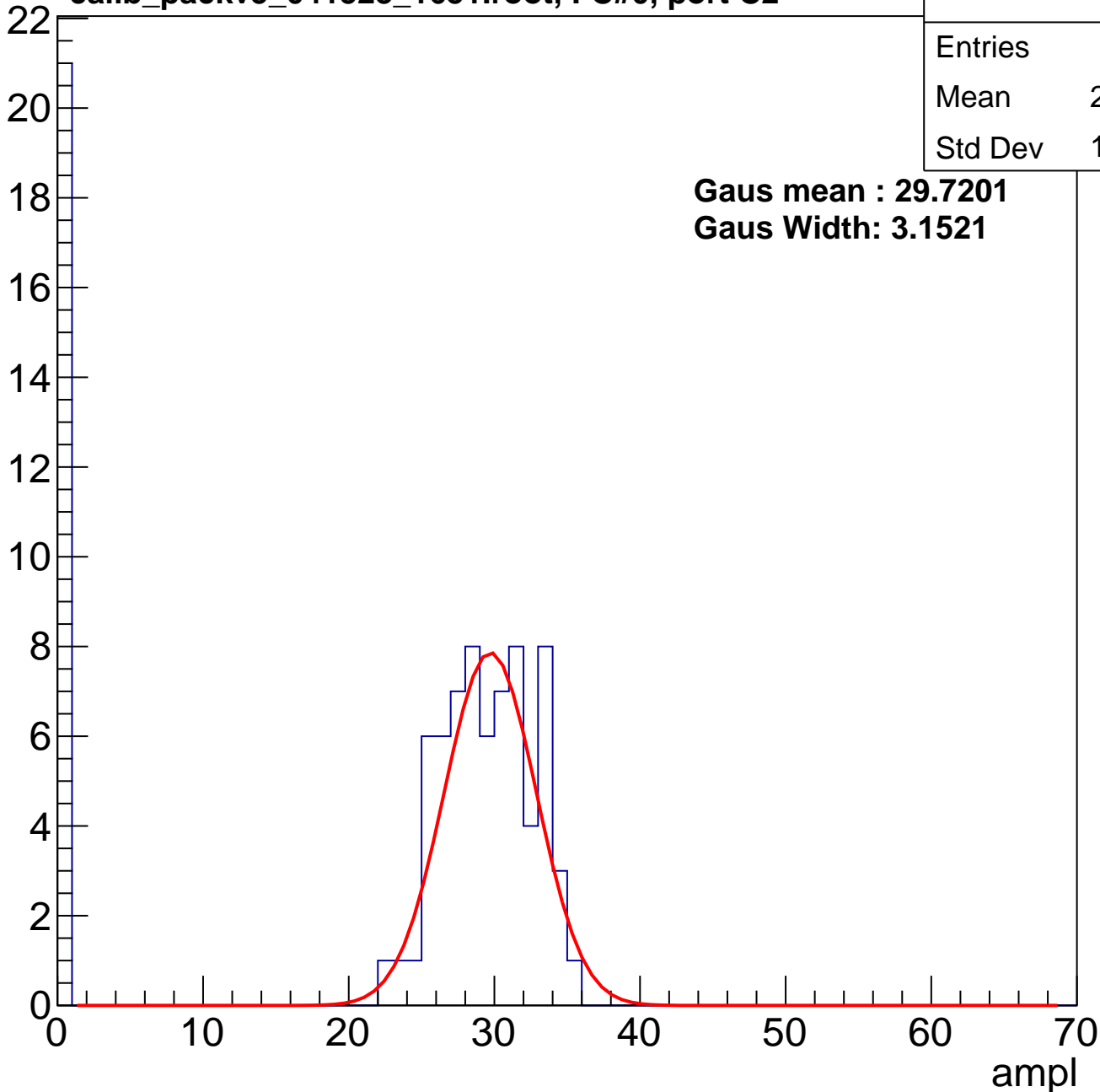
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	22.15
Std Dev	12.68

Gaus mean : 29.7201

Gaus Width: 3.1521

Entry



B1L103S, U1-ch79, adc1

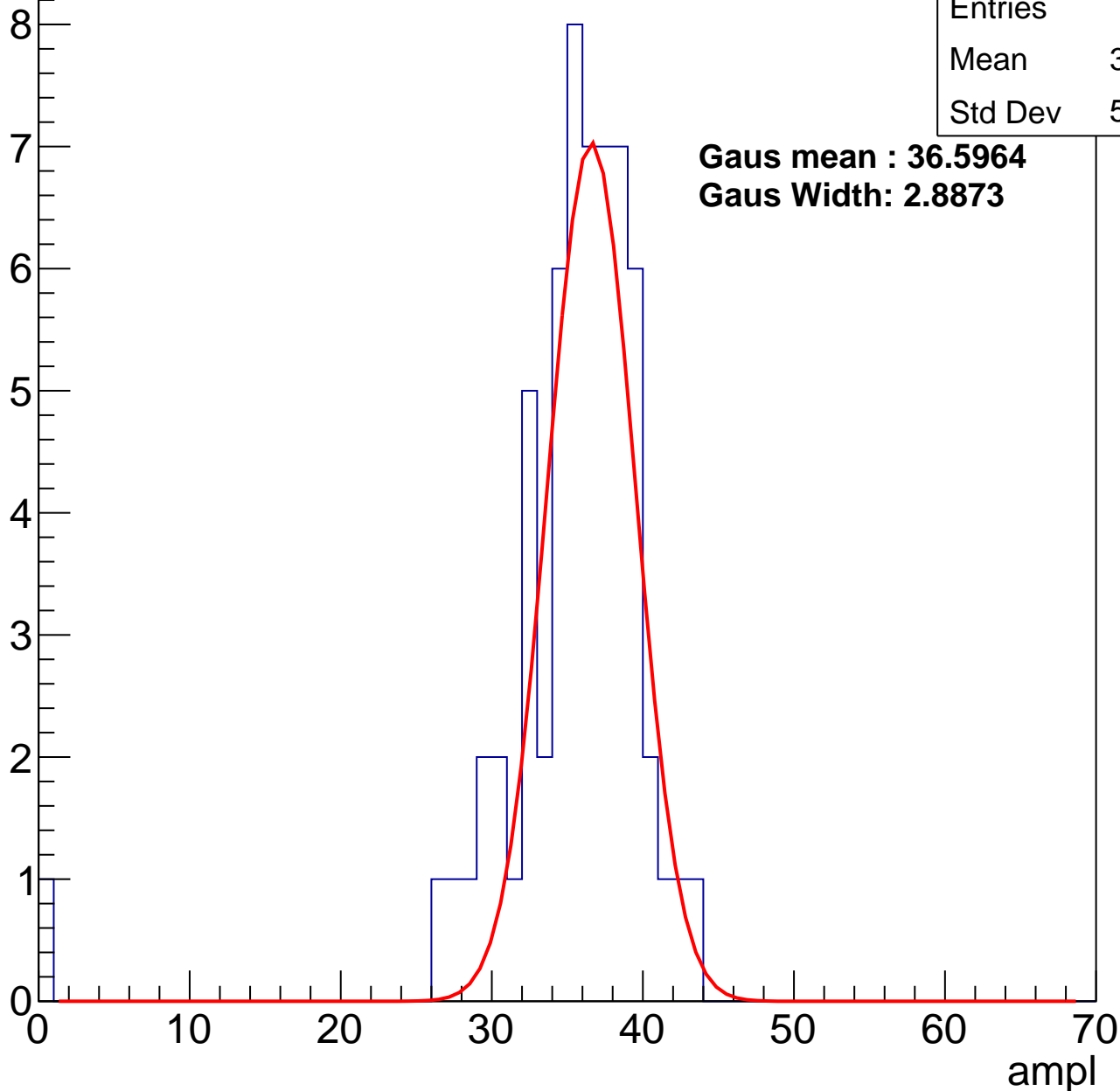
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.79
Std Dev	5.689

Gaus mean : 36.5964

Gaus Width: 2.8873



B1L103S, U1-ch79, adc2

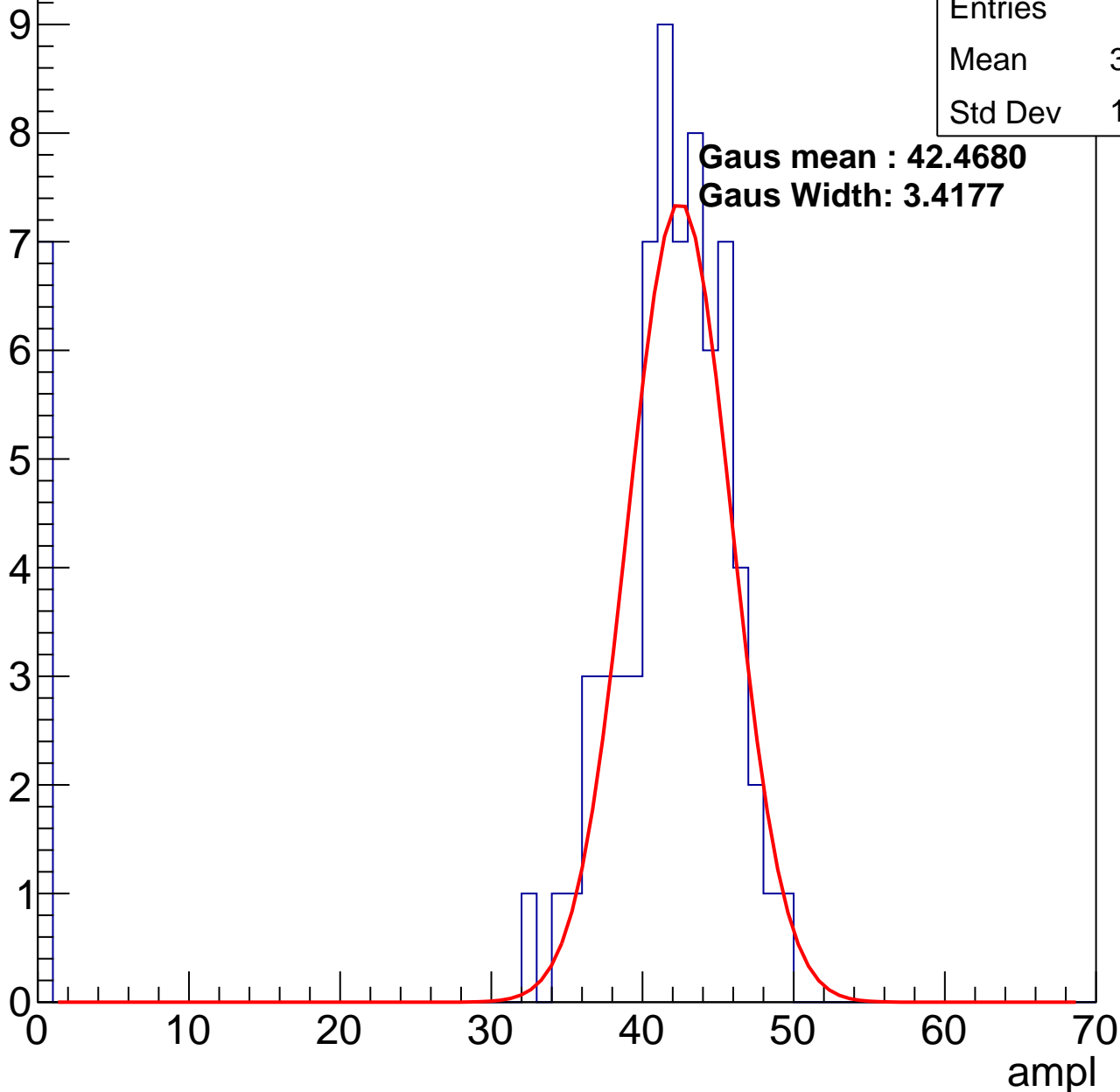
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	37.73
Std Dev	12.63

Gaus mean : 42.4680

Gaus Width: 3.4177

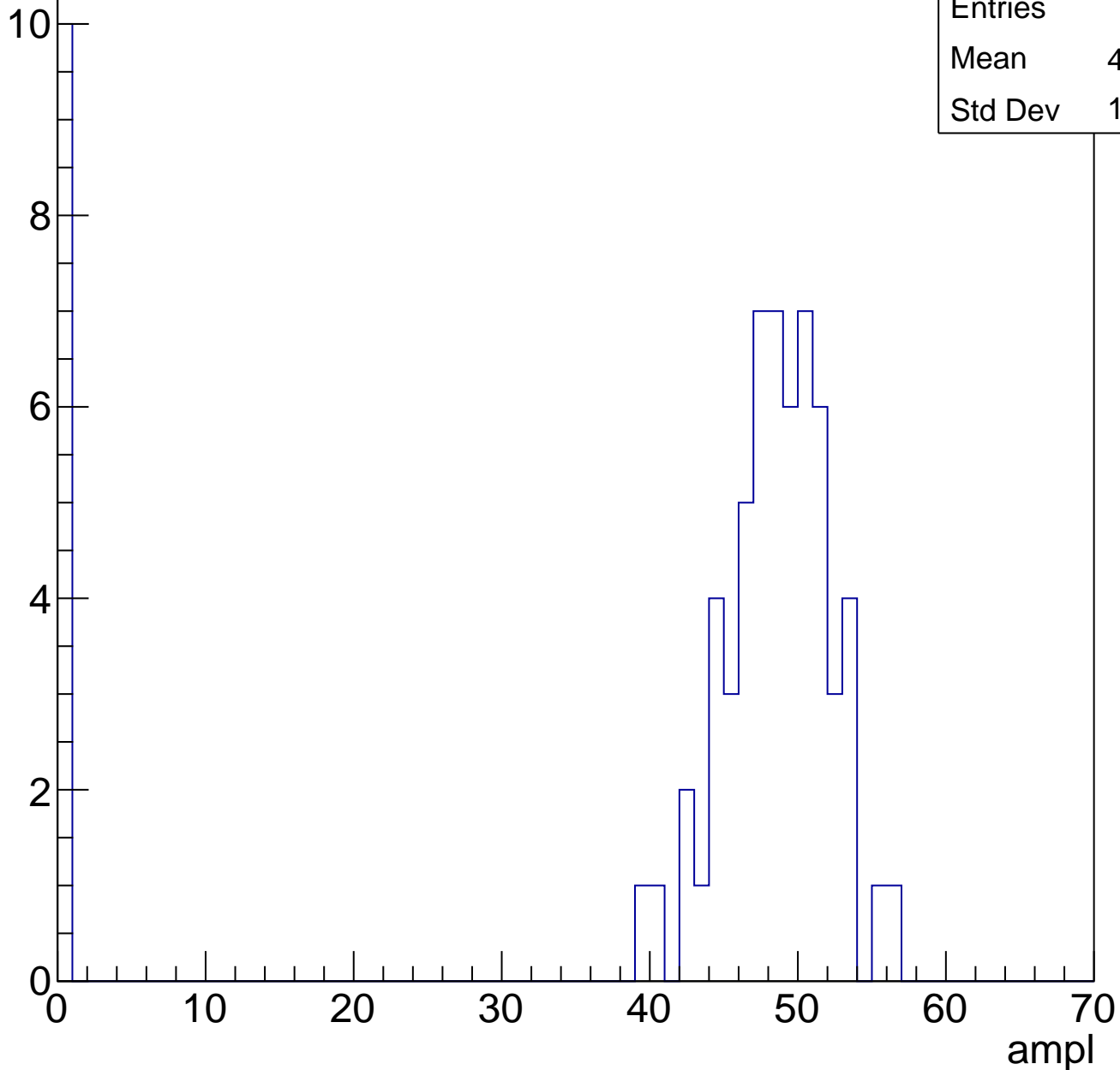


B1L103S, U1-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	41.17
Std Dev	17.25

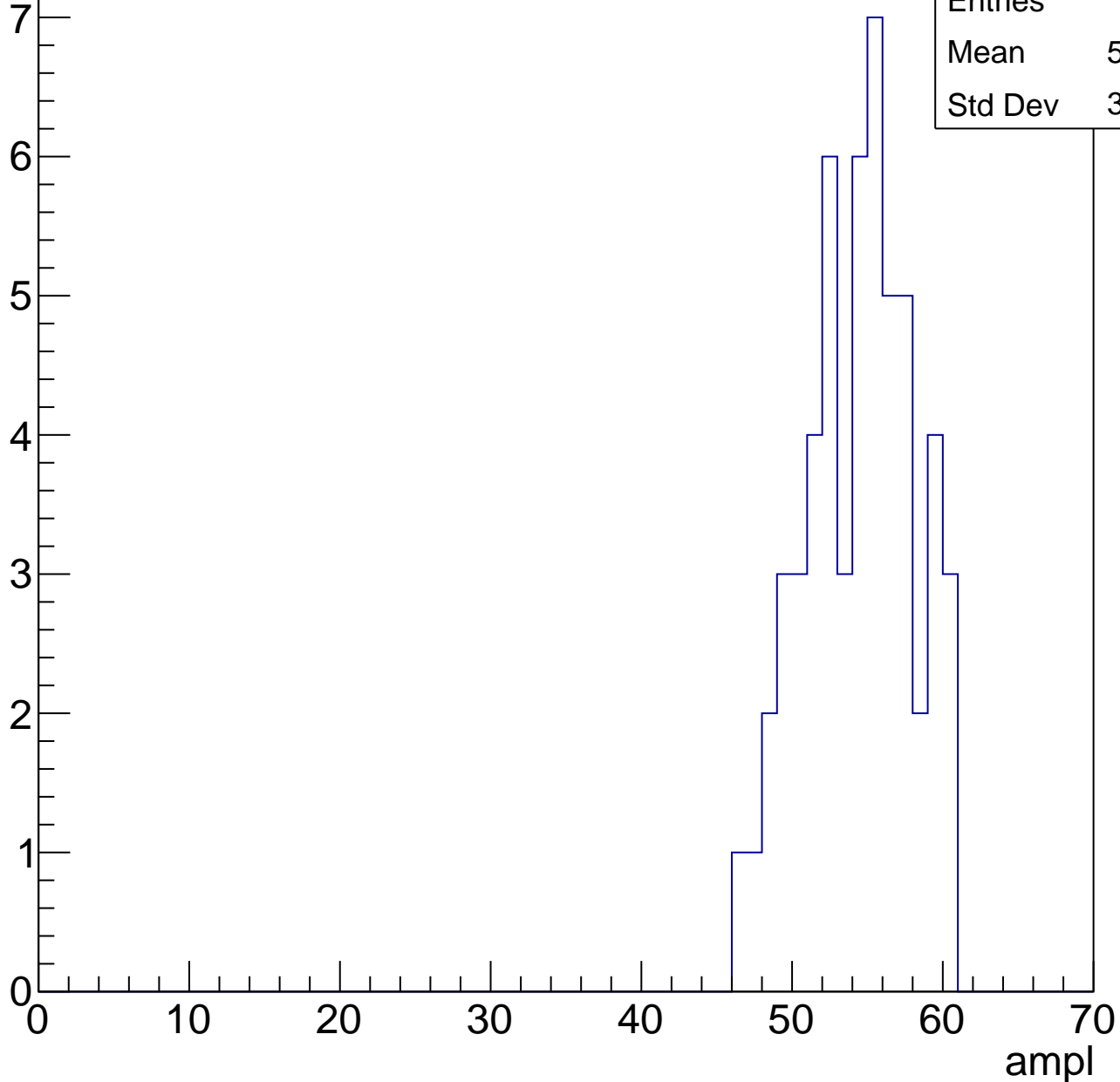
Entry



B1L103S, U1-ch79, adc4

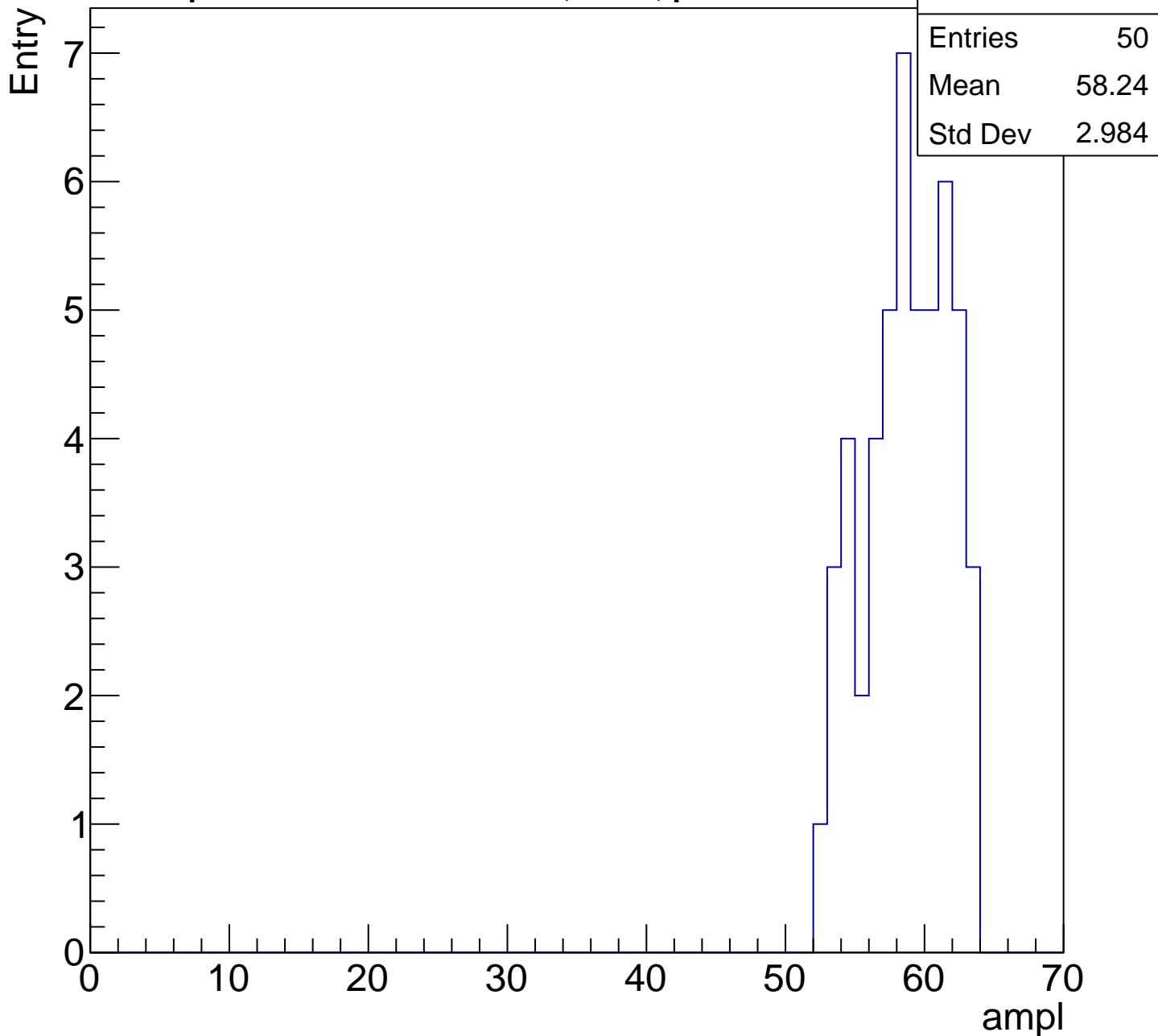
calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

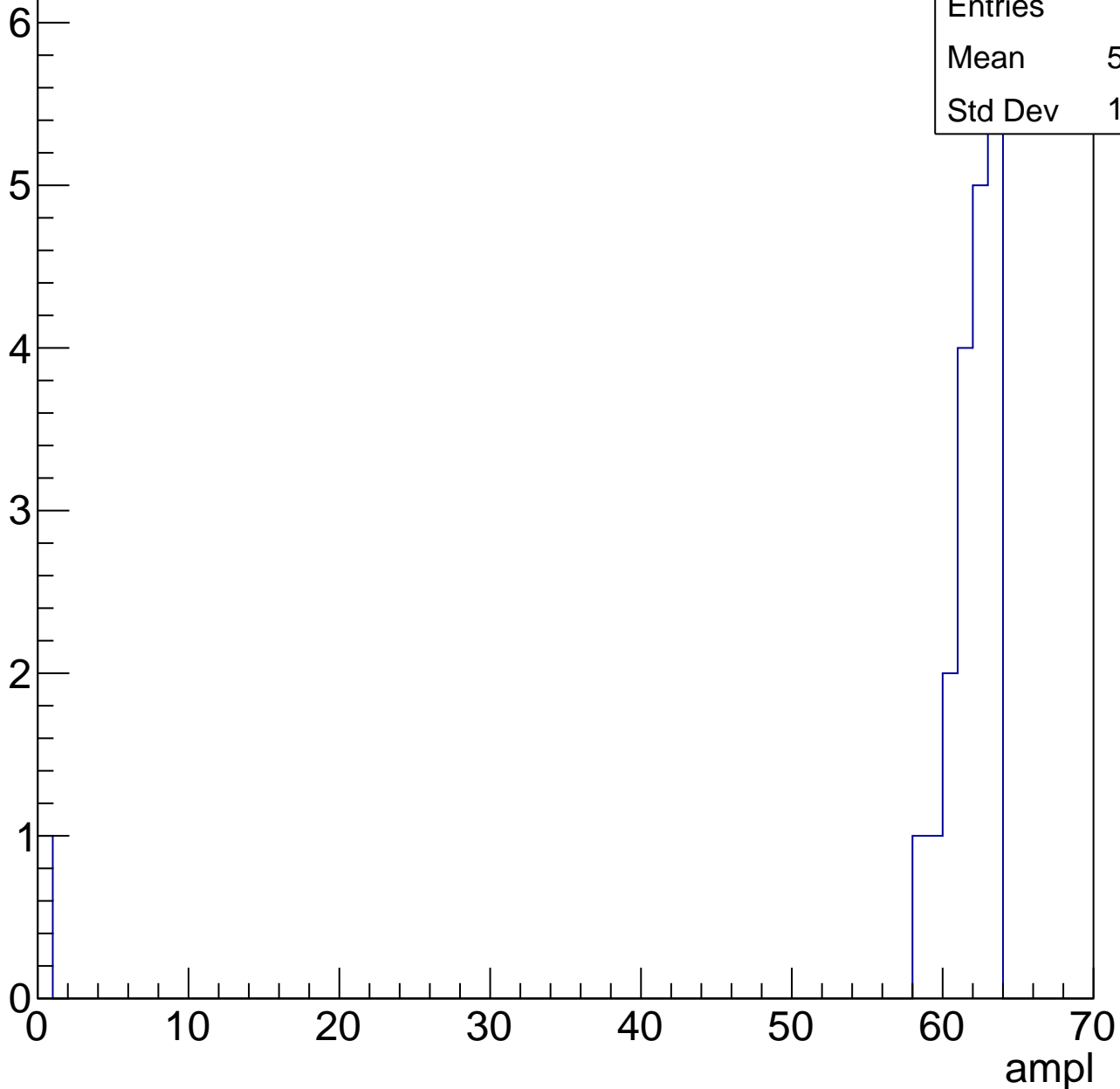


B1L103S, U1-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.45
Std Dev	13.48



B1L103S, U1-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U1-ch80, adc0

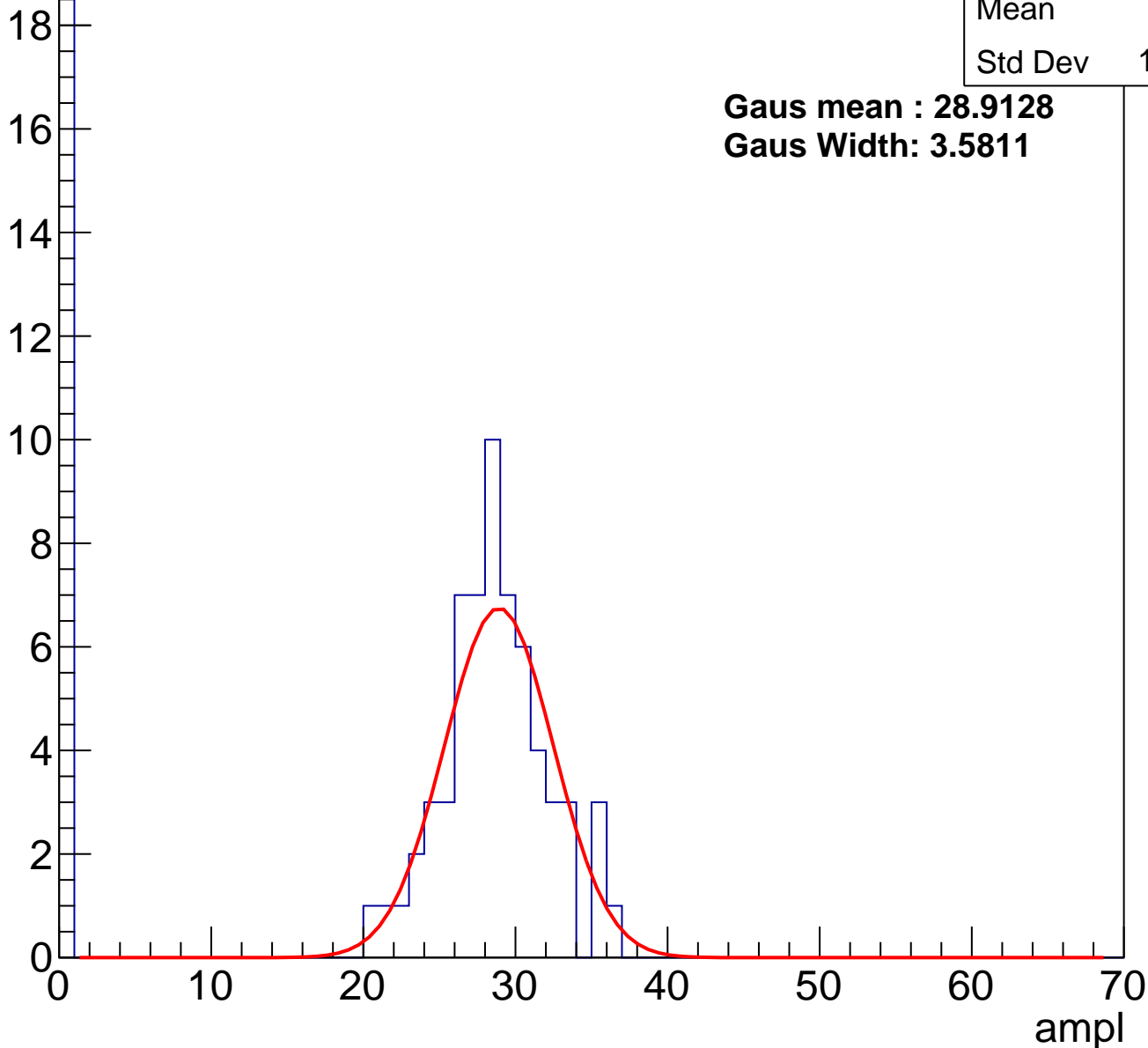
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.6
Std Dev	12.32

Gaus mean : 28.9128

Gaus Width: 3.5811

Entry



B1L103S, U1-ch80, adc1

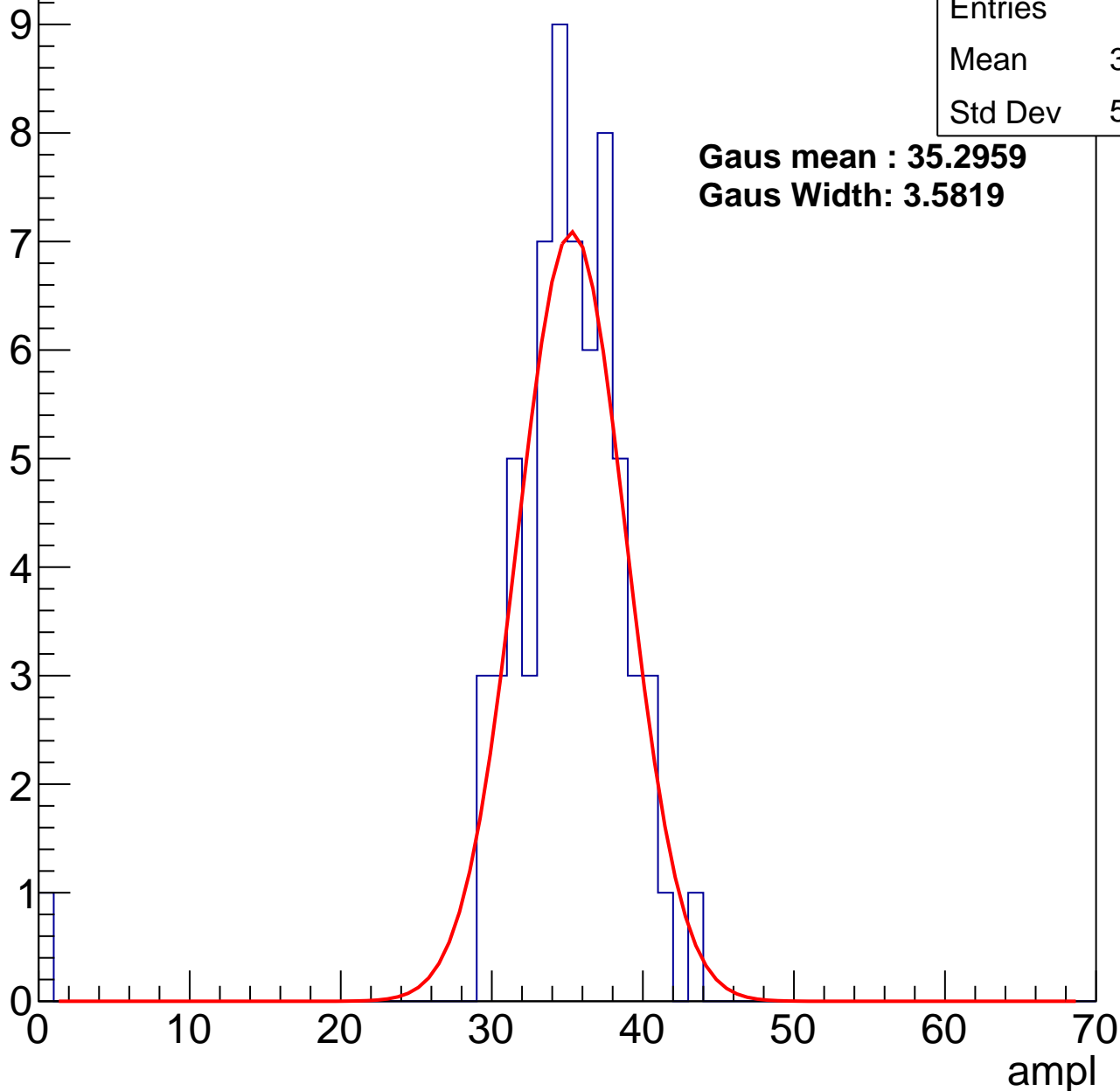
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.35
Std Dev	5.307

Gaus mean : 35.2959

Gaus Width: 3.5819



B1L103S, U1-ch80, adc2

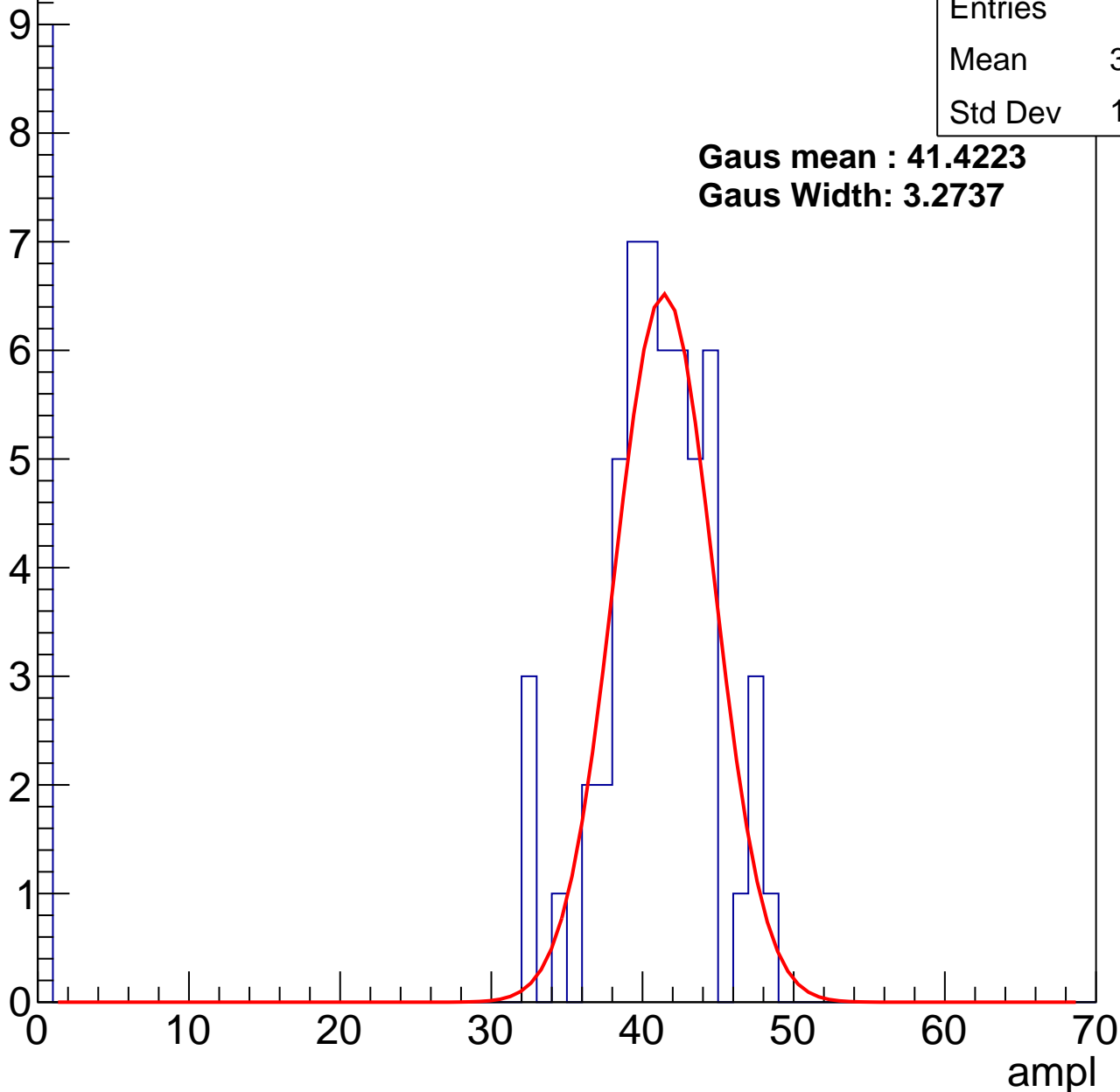
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.86
Std Dev	14.49

Gaus mean : 41.4223

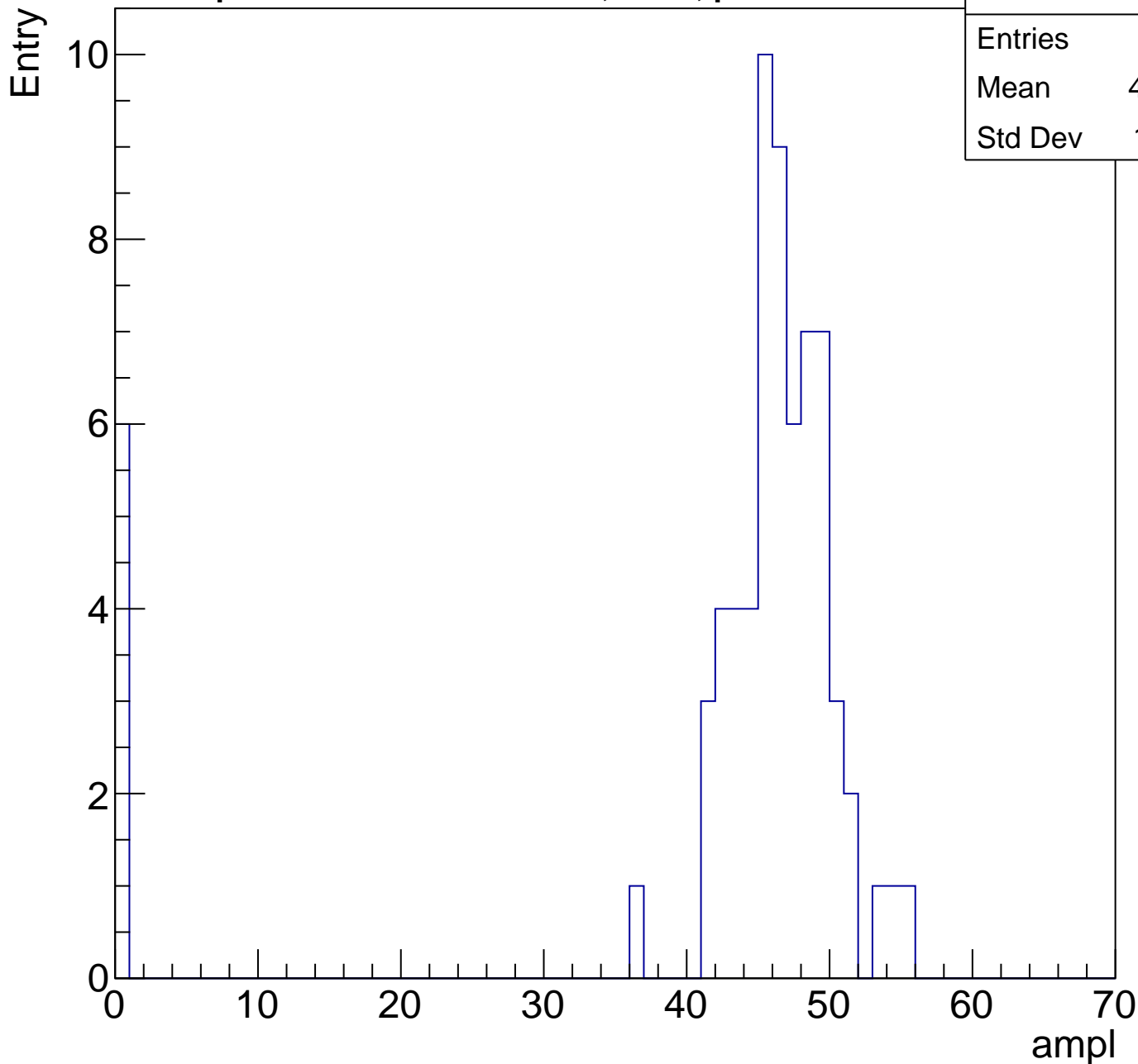
Gaus Width: 3.2737



B1L103S, U1-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	42.23
Std Dev	13.41

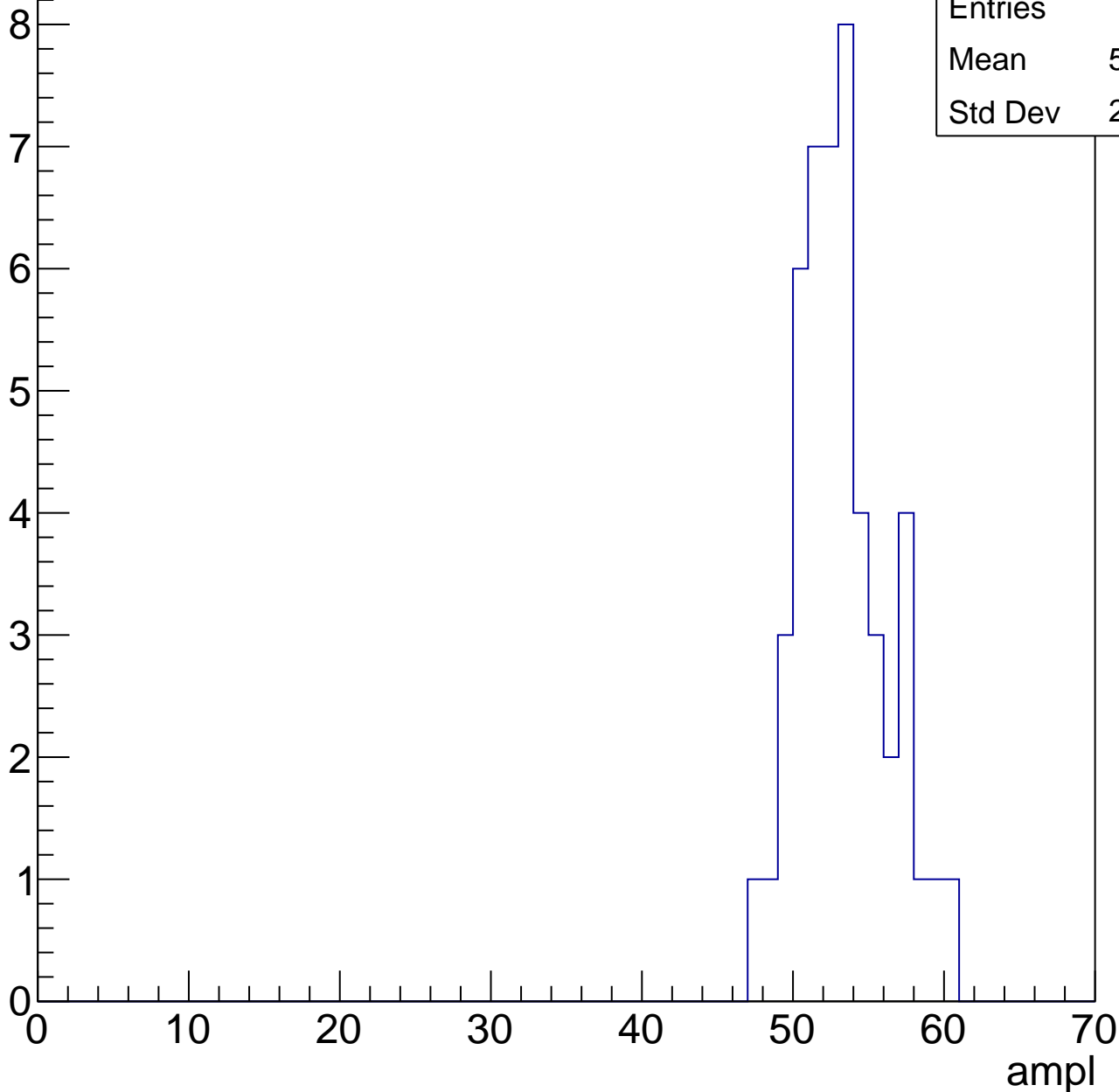


B1L103S, U1-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.76
Std Dev	2.868

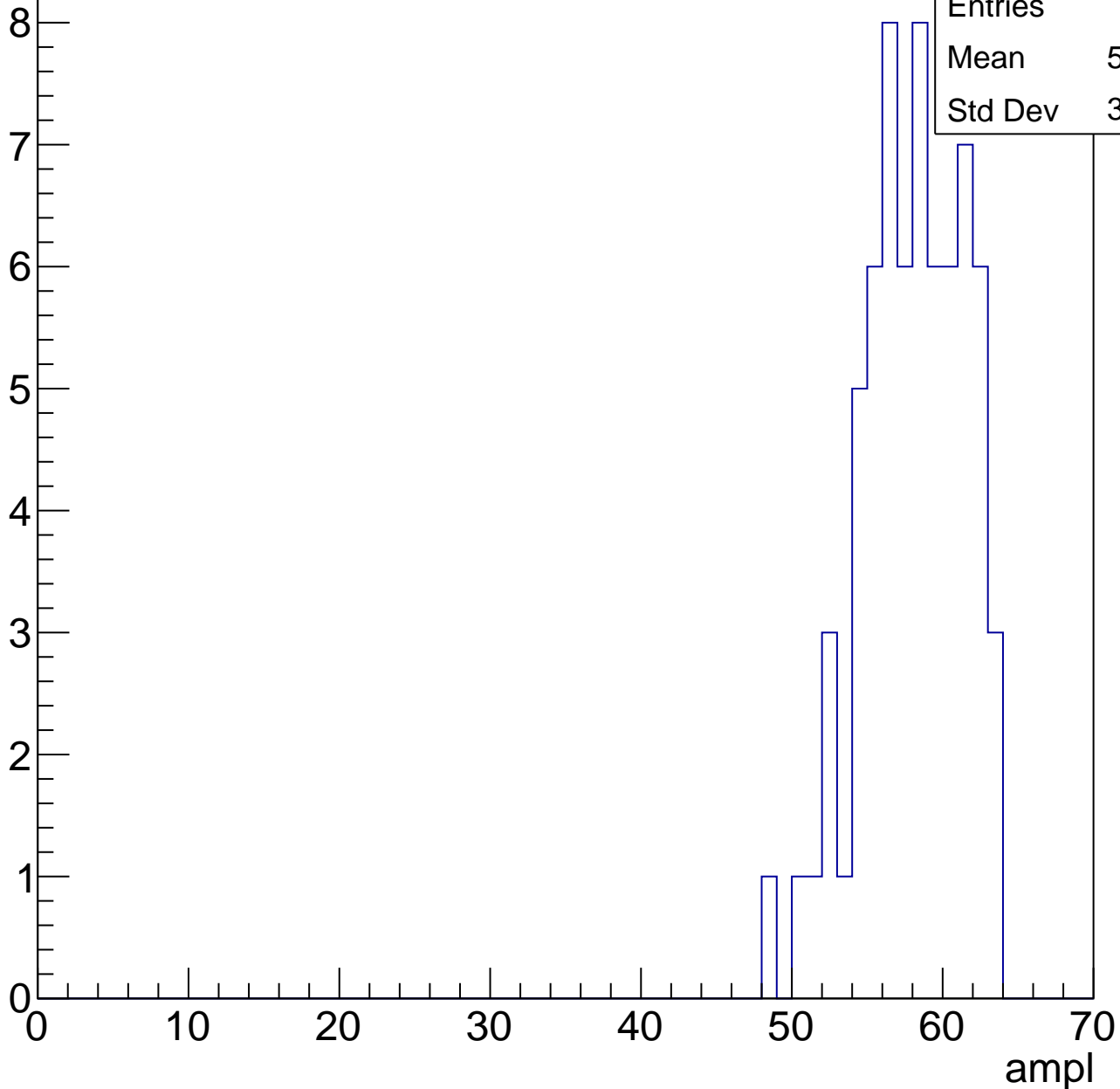


B1L103S, U1-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	57.56
Std Dev	3.362

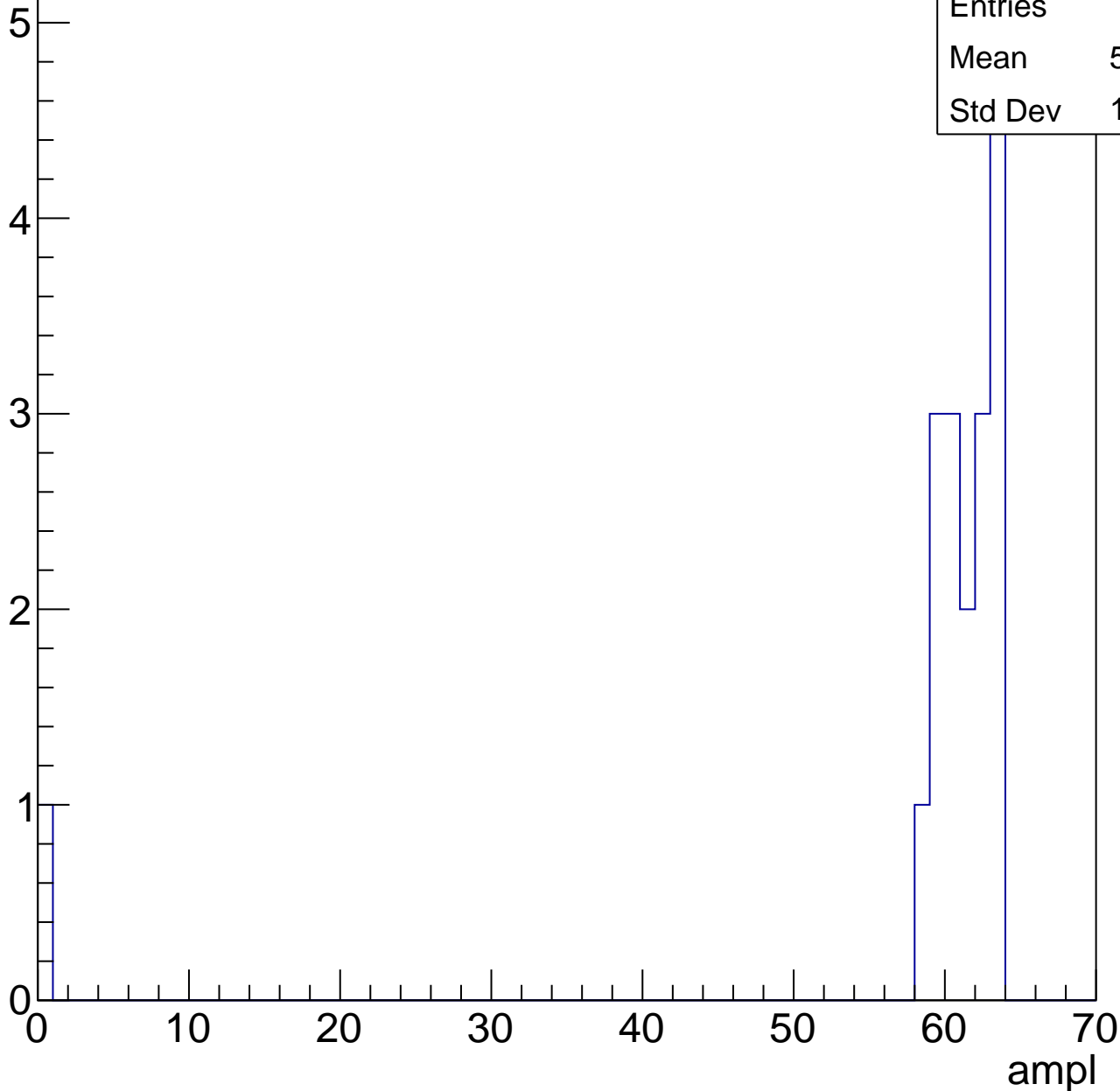


B1L103S, U1-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.67
Std Dev	14.08

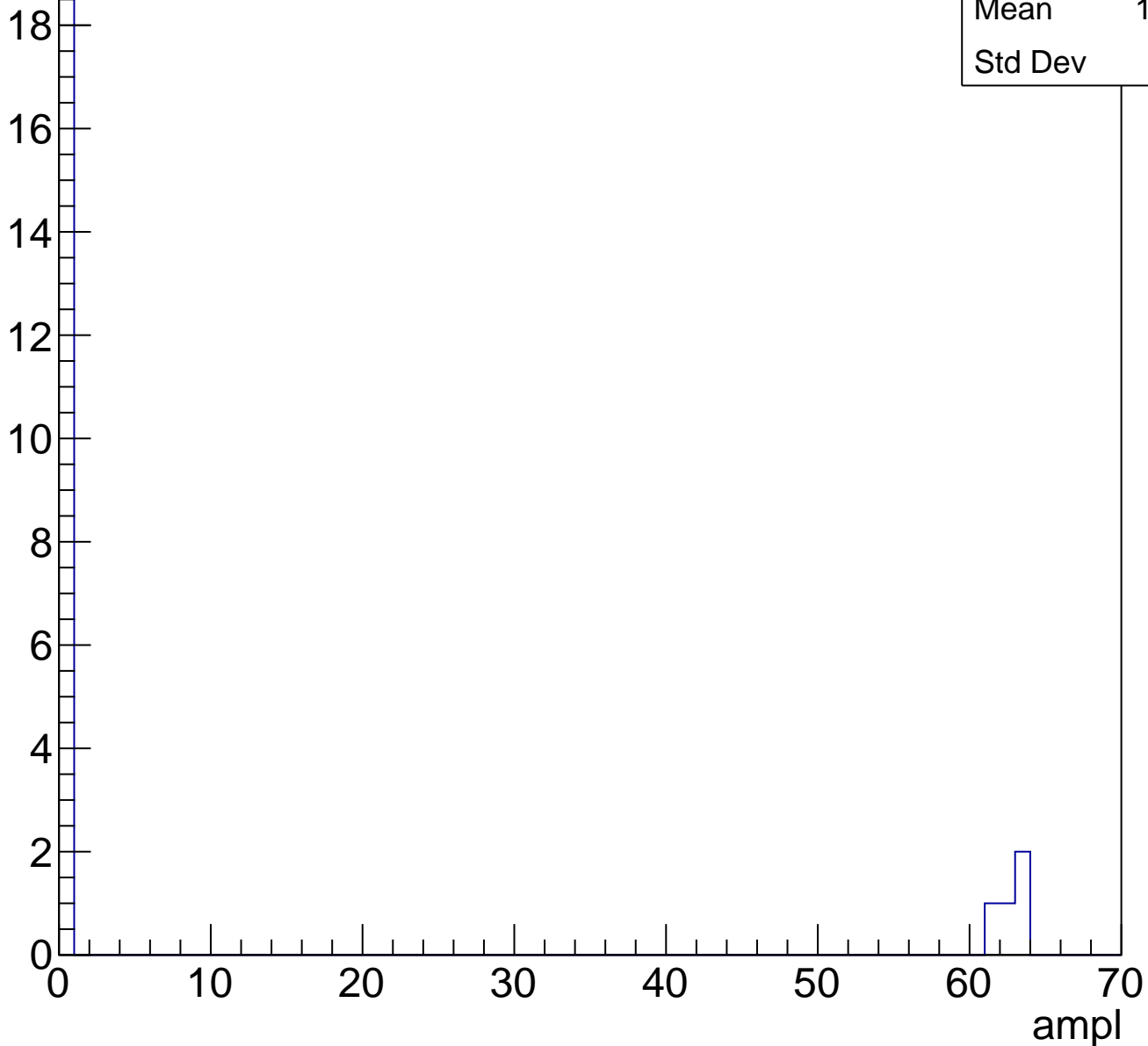


B1L103S, U1-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	10.83
Std Dev	23.6

Entry



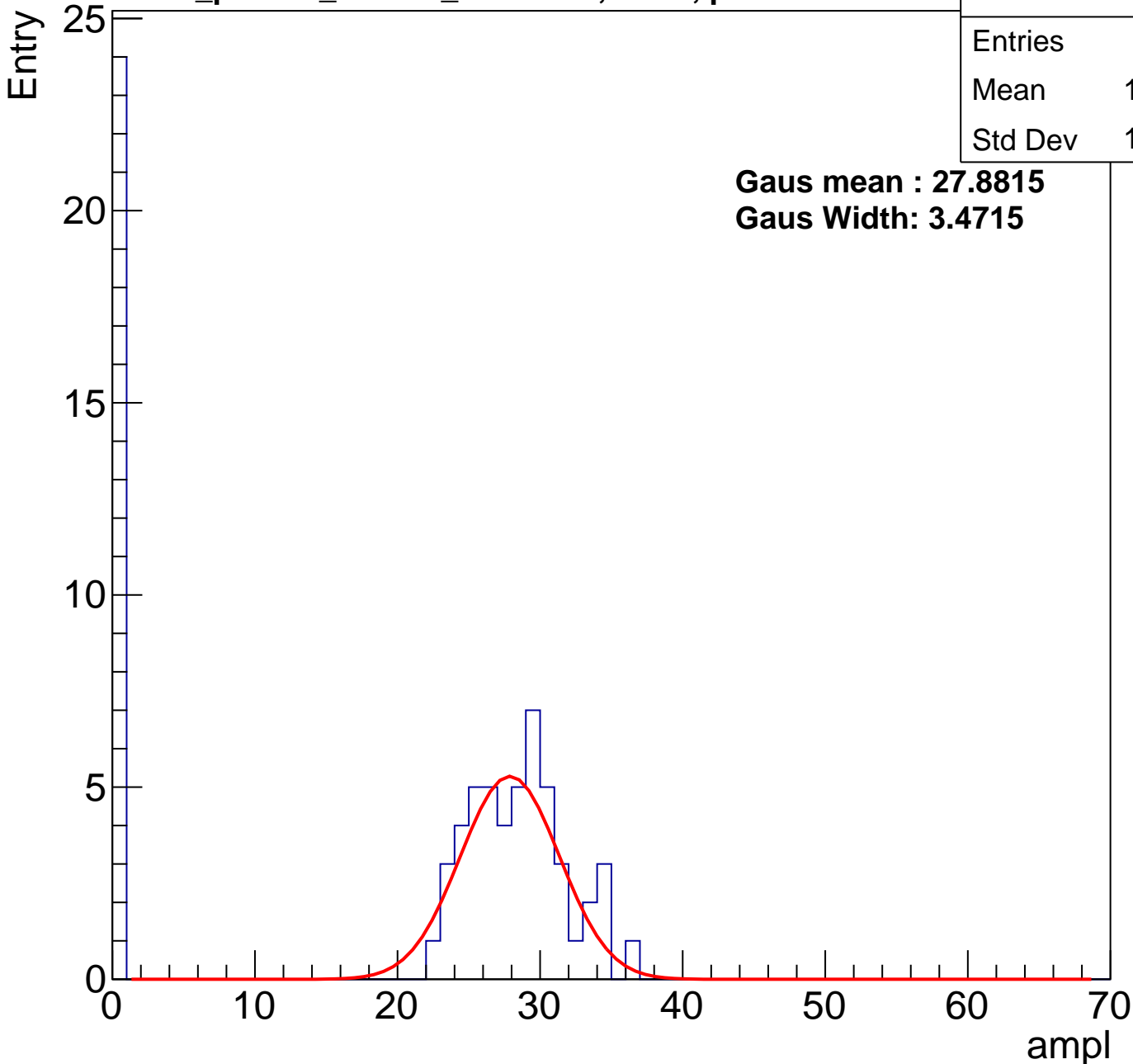
B1L103S, U1-ch81, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	18.79
Std Dev	13.43

Gaus mean : 27.8815

Gaus Width: 3.4715



B1L103S, U1-ch81, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	29.31
Std Dev	12.76

Gaus mean : 34.7225

Gaus Width: 3.8127

Entry

12

10

8

6

4

2

0

0

10

20

30

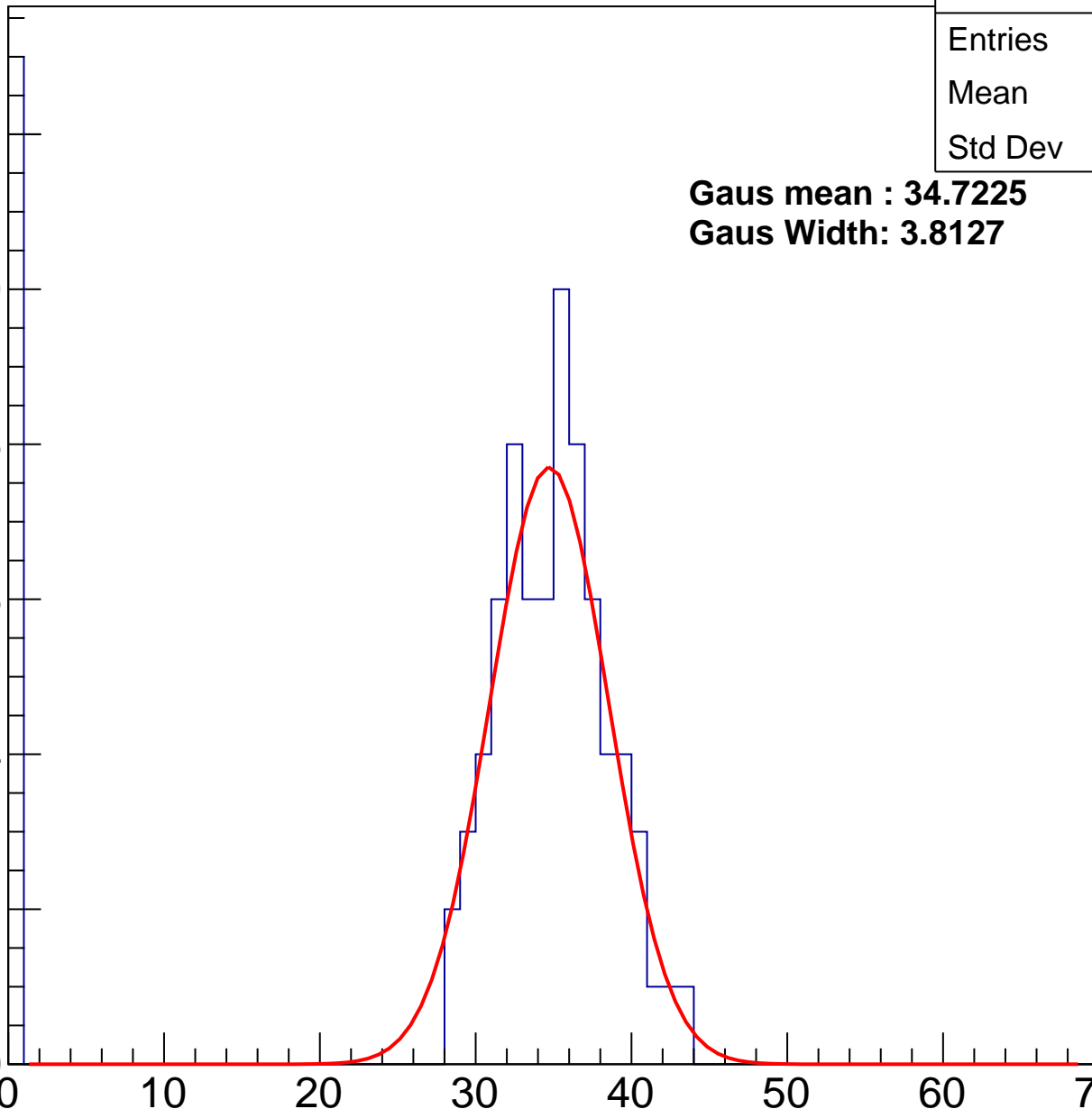
40

50

60

70

ampl



B1L103S, U1-ch81, adc2

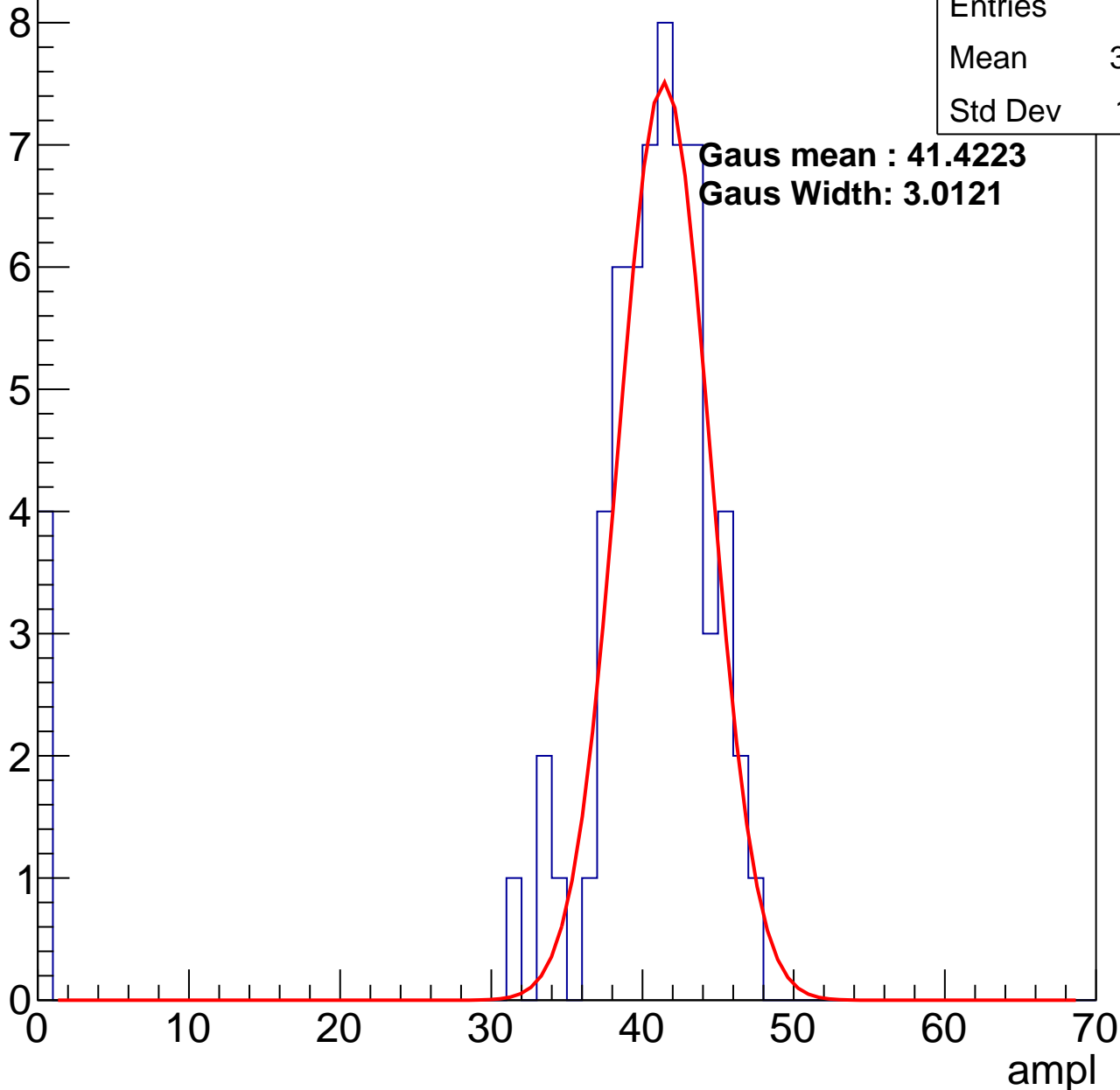
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.98
Std Dev	10.31

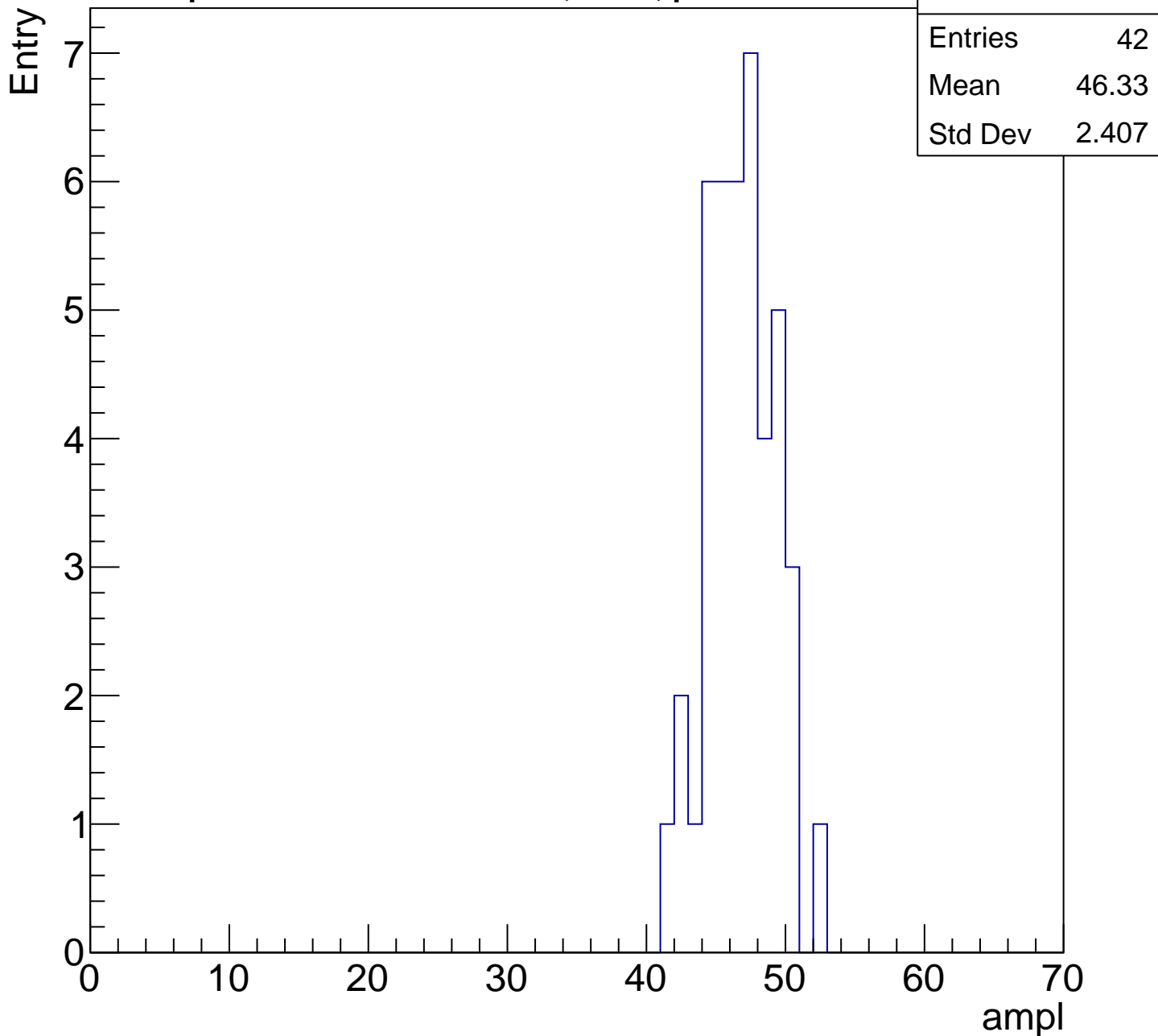
Gaus mean : 41.4223

Gaus Width: 3.0121



B1L103S, U1-ch81, adc3

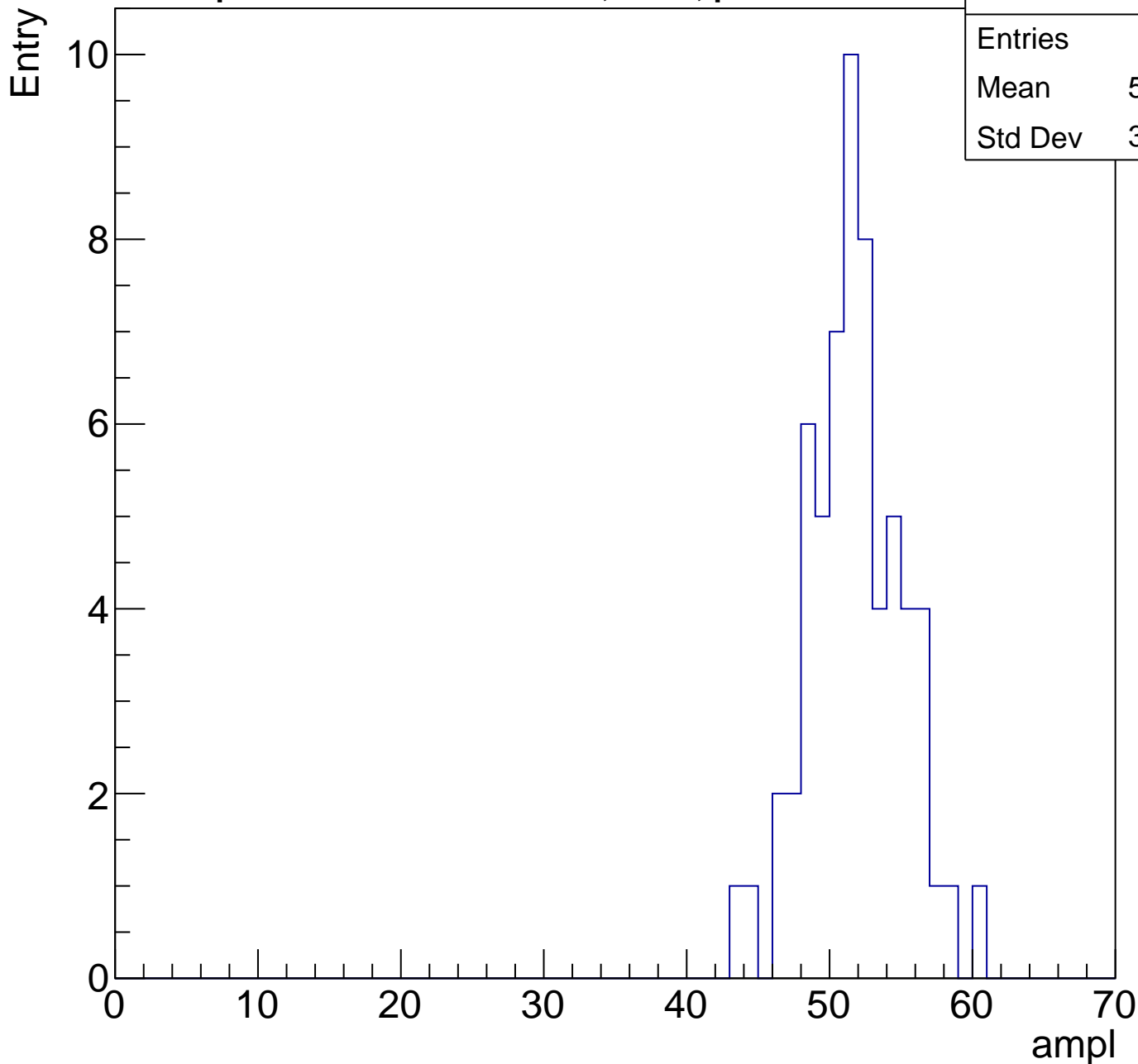
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U1-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	51.34
Std Dev	3.282

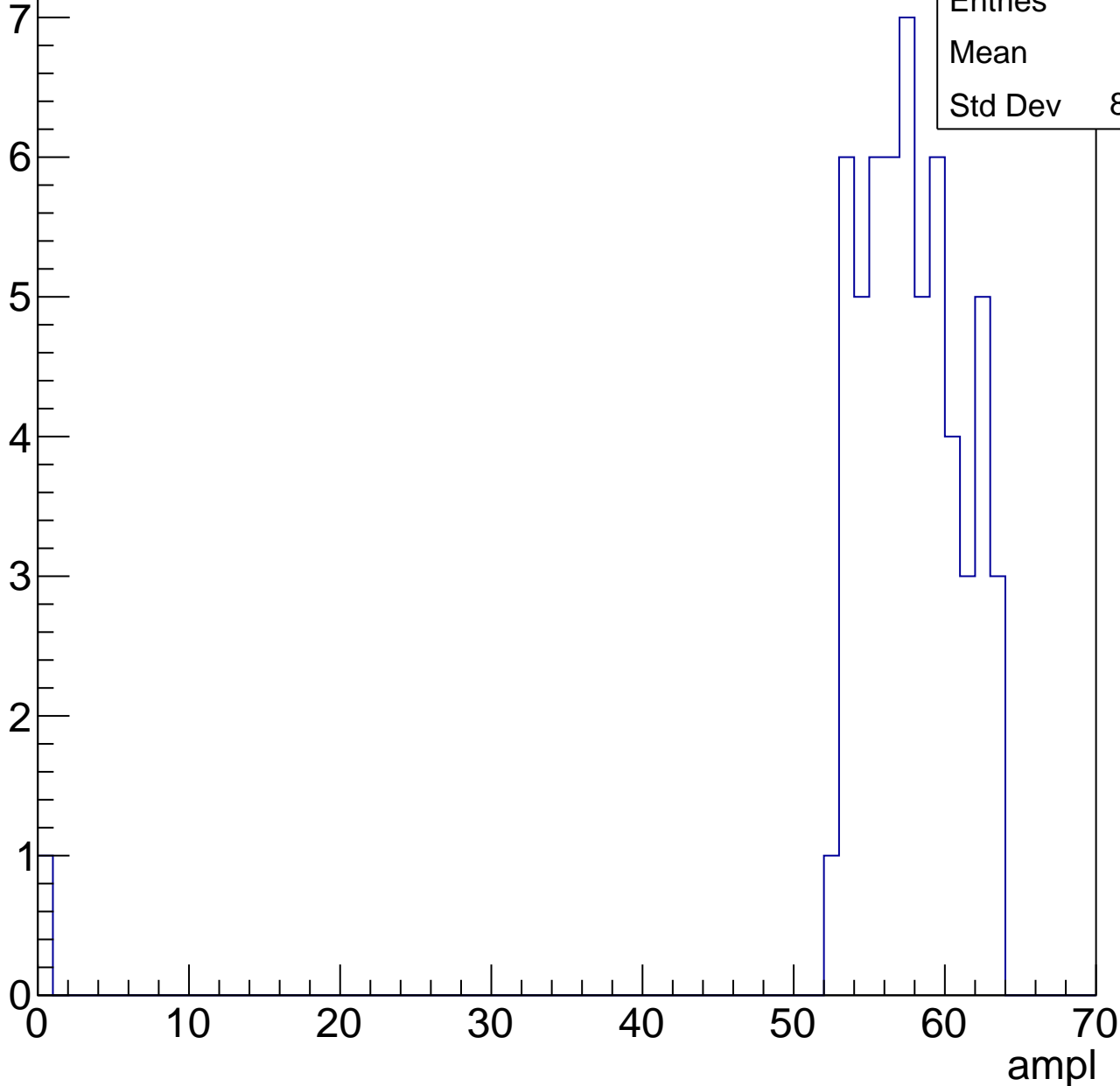


B1L103S, U1-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.4
Std Dev	8.062

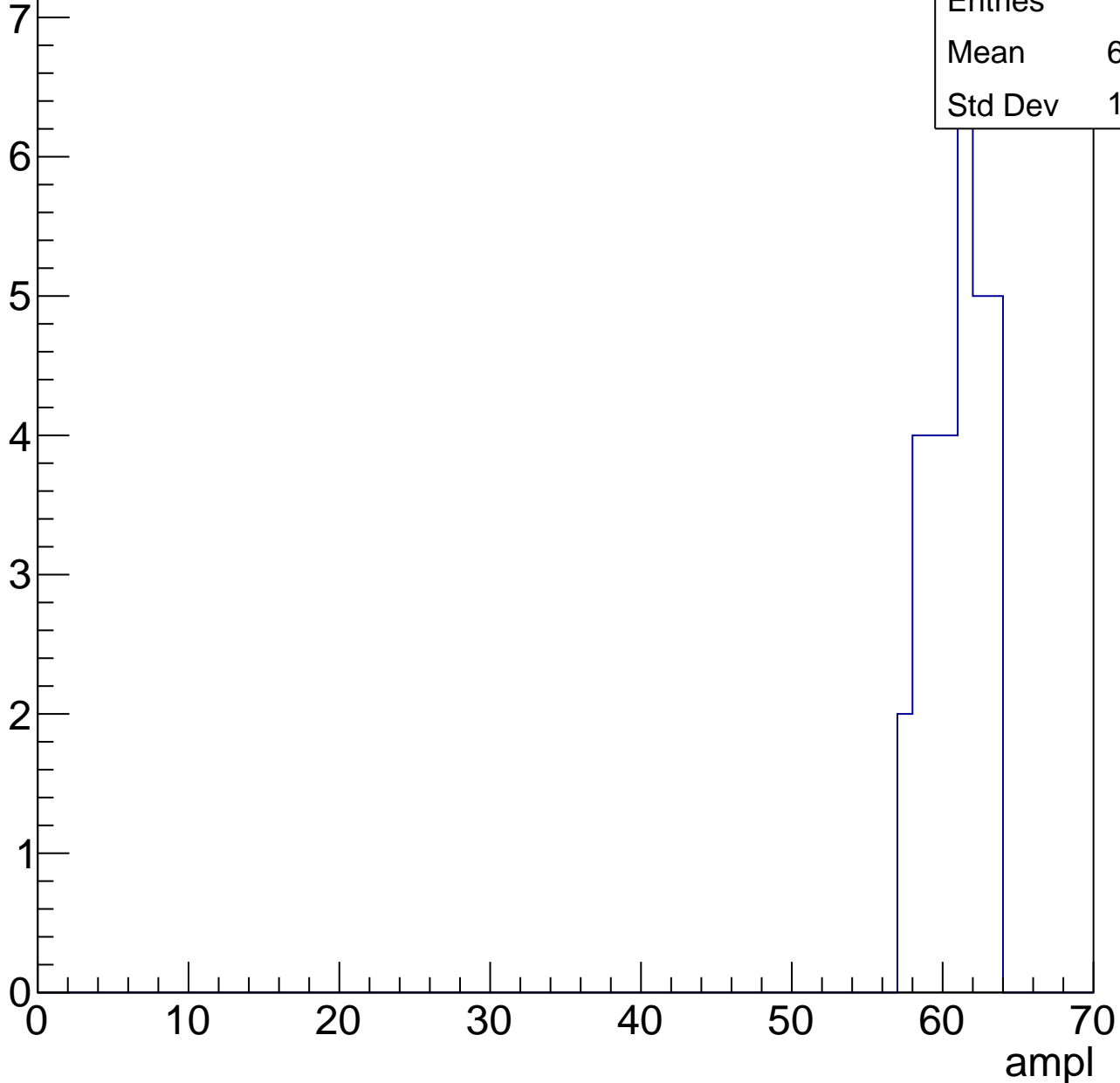


B1L103S, U1-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	60.45
Std Dev	1.829

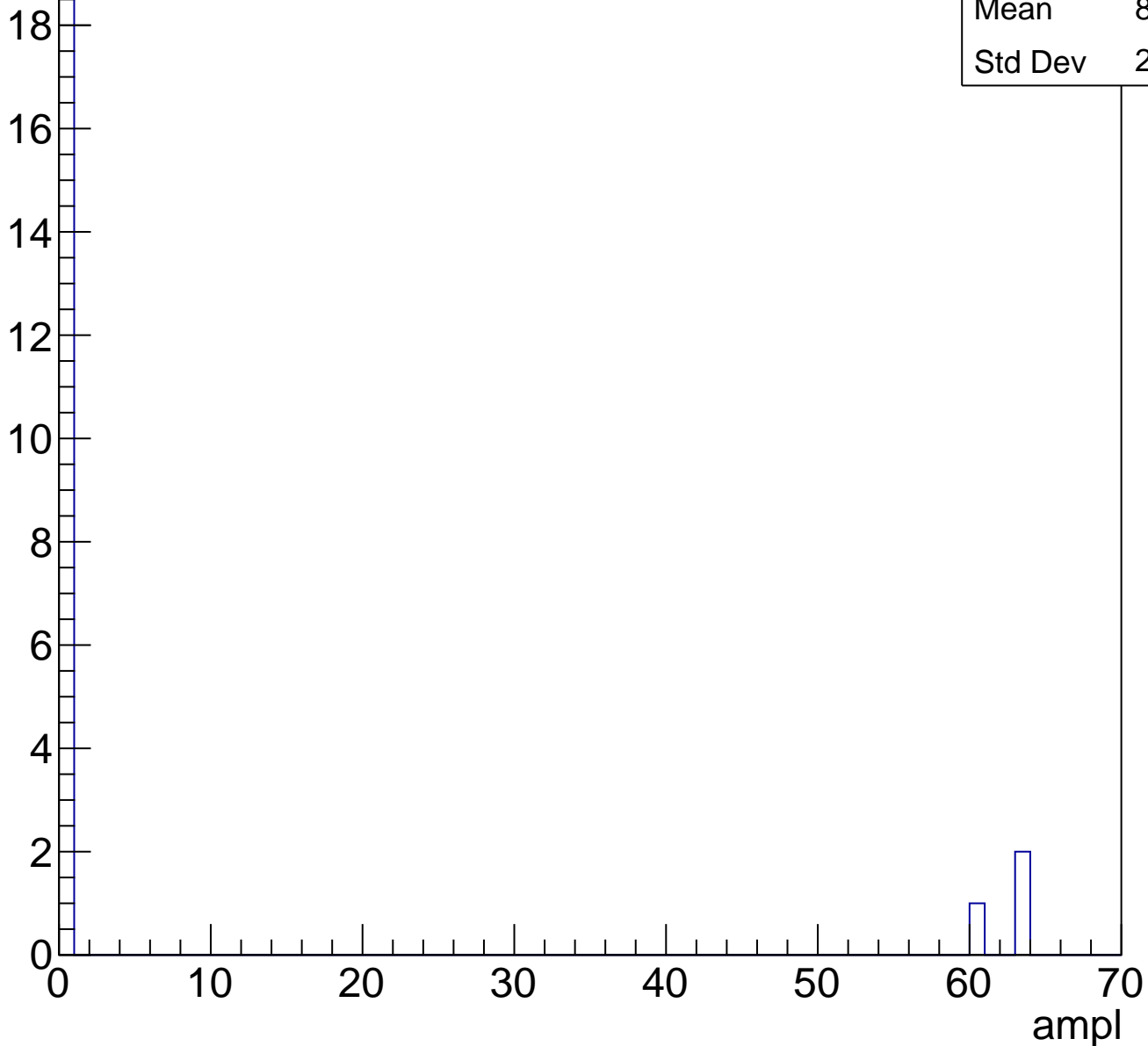


B1L103S, U1-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U1-ch82, adc0

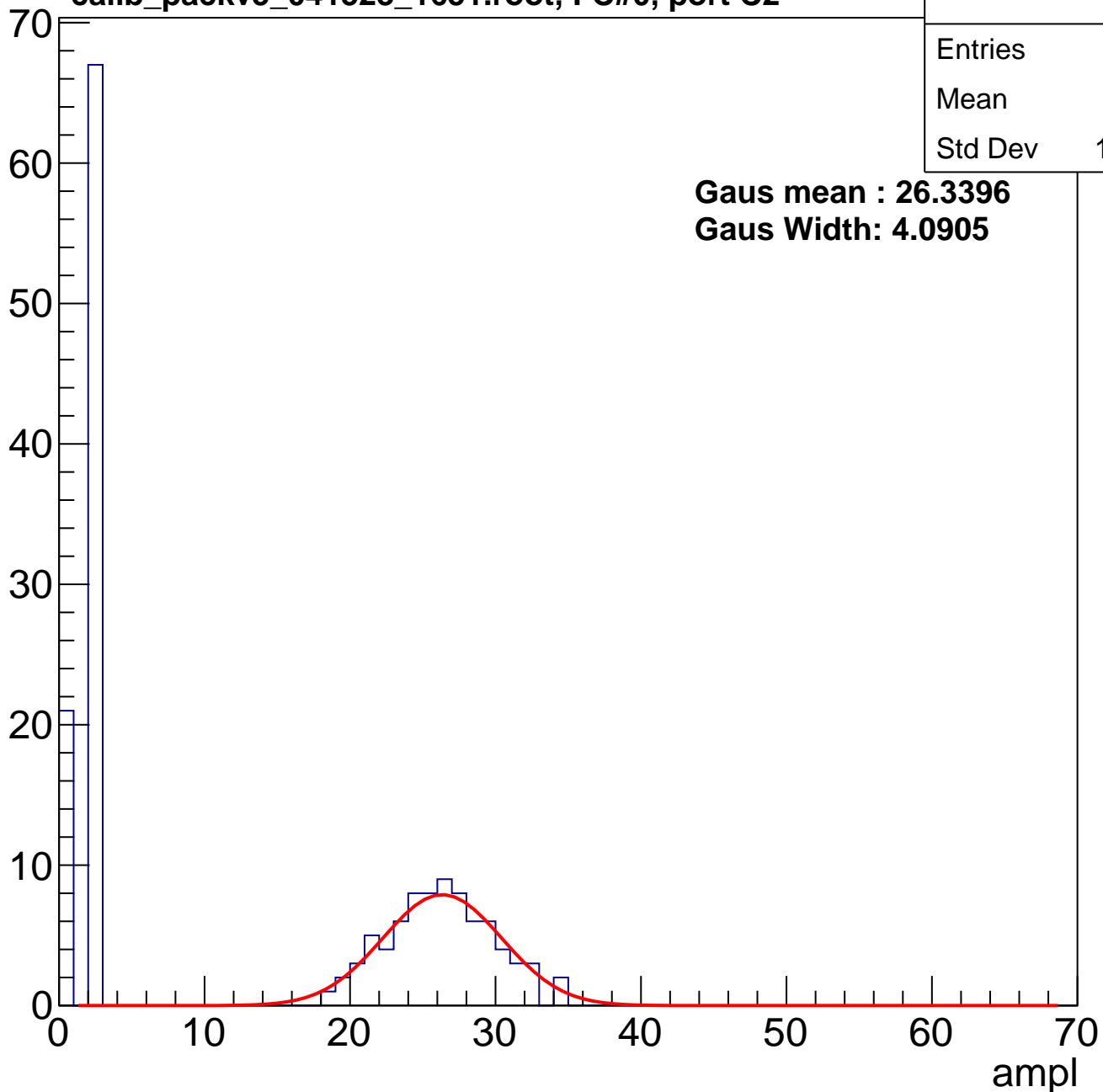
calib_packv5_041523_1651.root, FC#0, port C2

Entries	166
Mean	12.9
Std Dev	12.35

Gaus mean : 26.3396

Gaus Width: 4.0905

Entry



B1L103S, U1-ch82, adc1

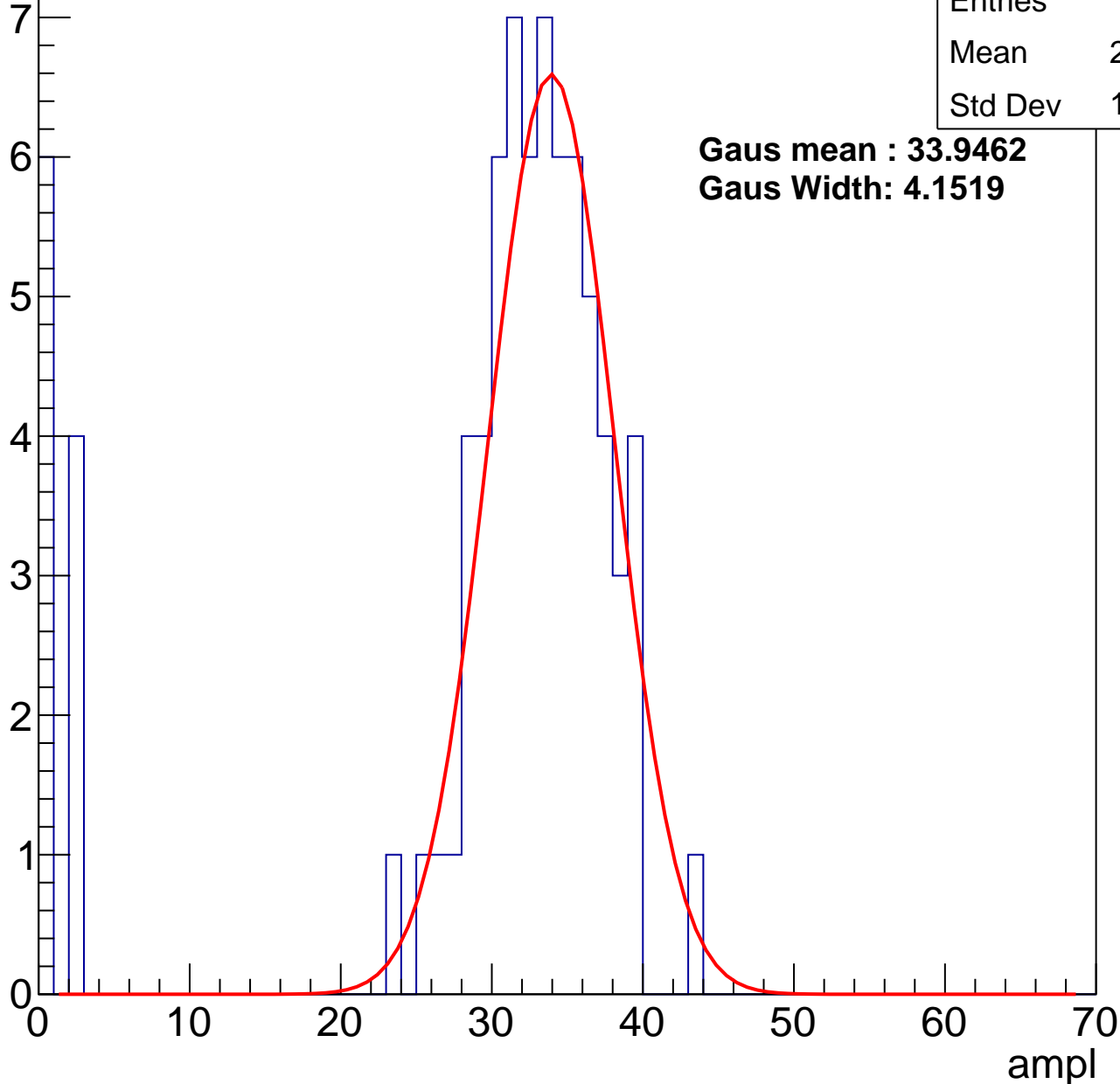
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	28.73
Std Dev	11.36

Gaus mean : 33.9462

Gaus Width: 4.1519



B1L103S, U1-ch82, adc2

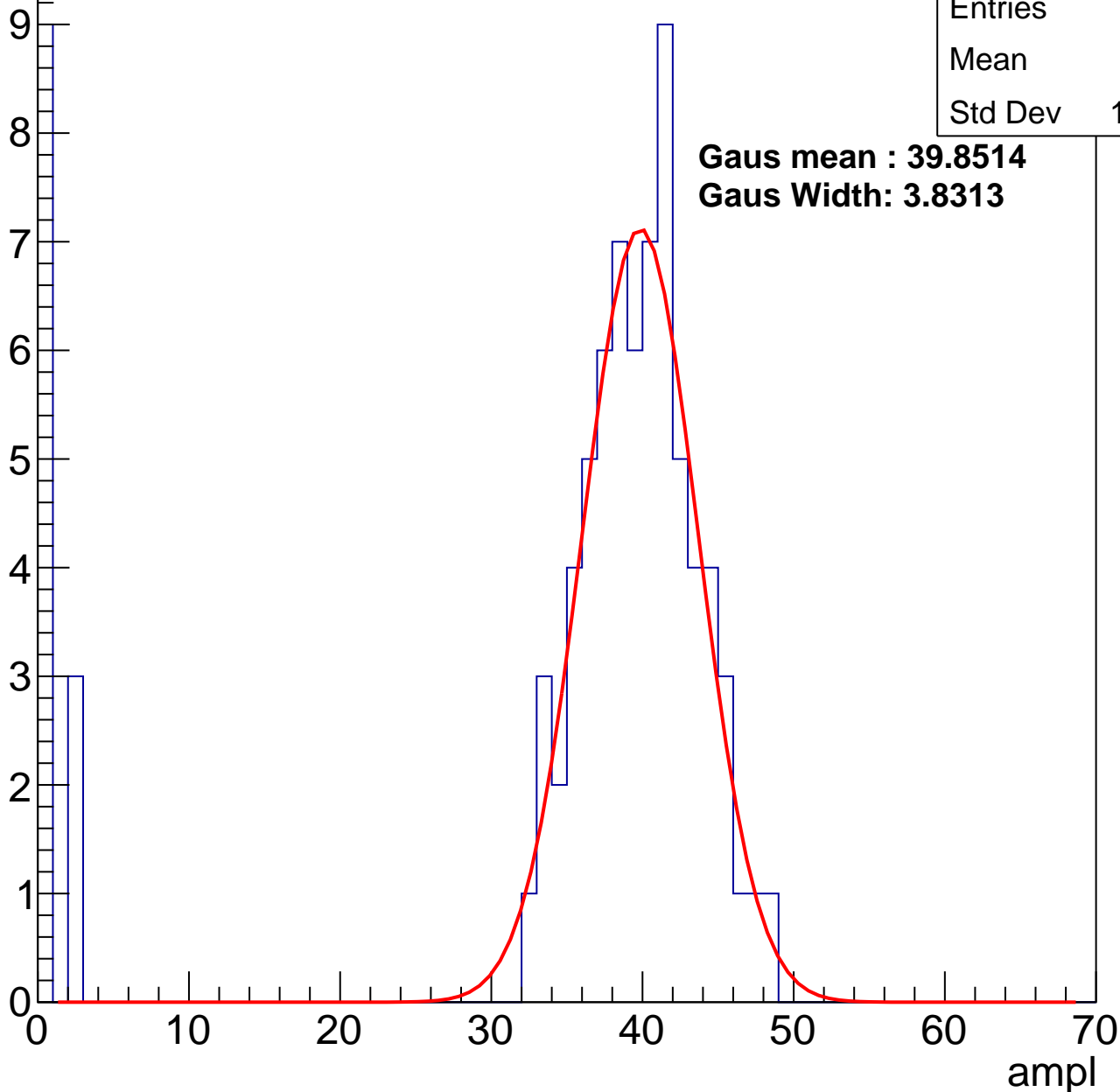
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	33.7
Std Dev	14.24

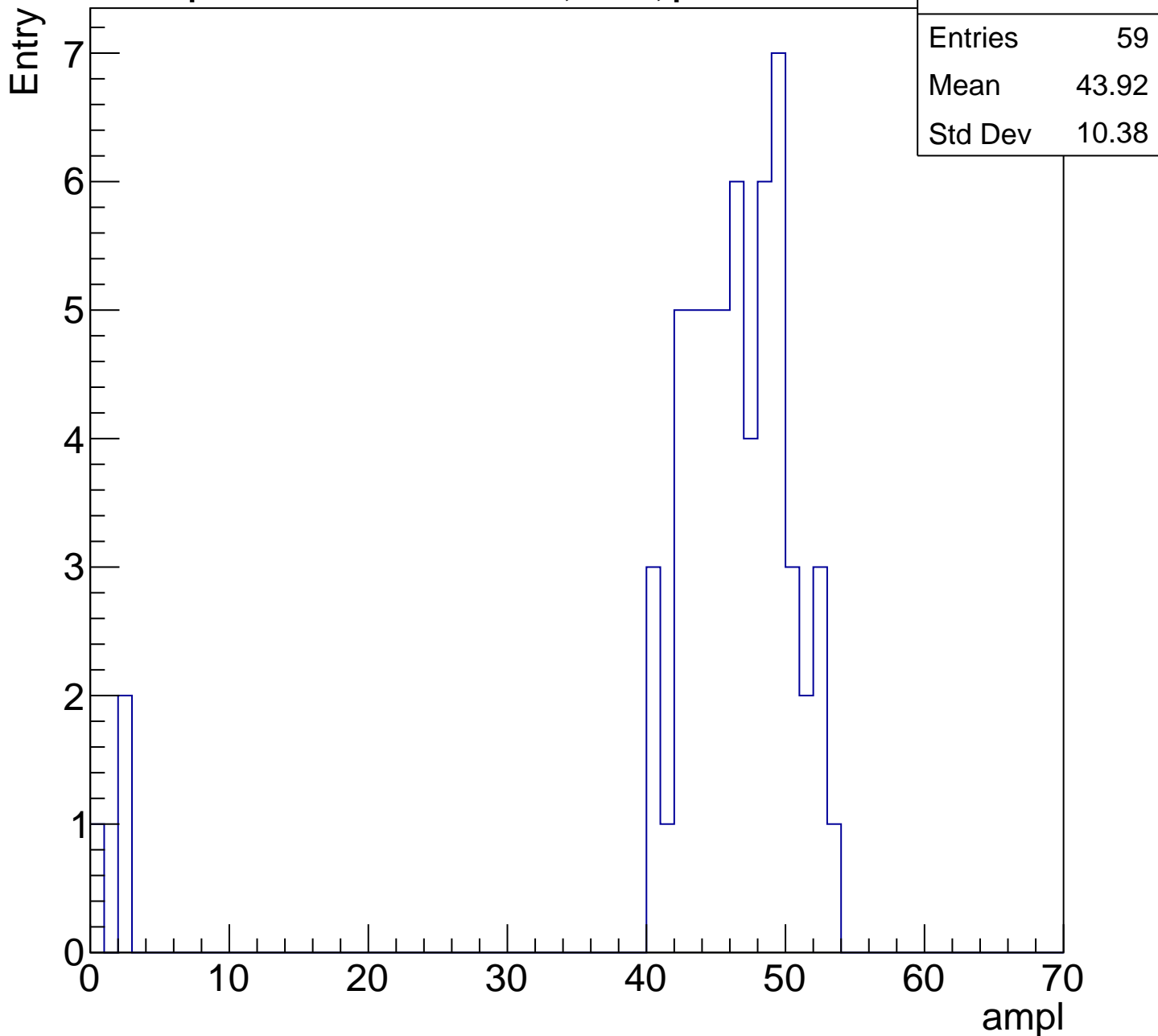
Gaus mean : 39.8514

Gaus Width: 3.8313



B1L103S, U1-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

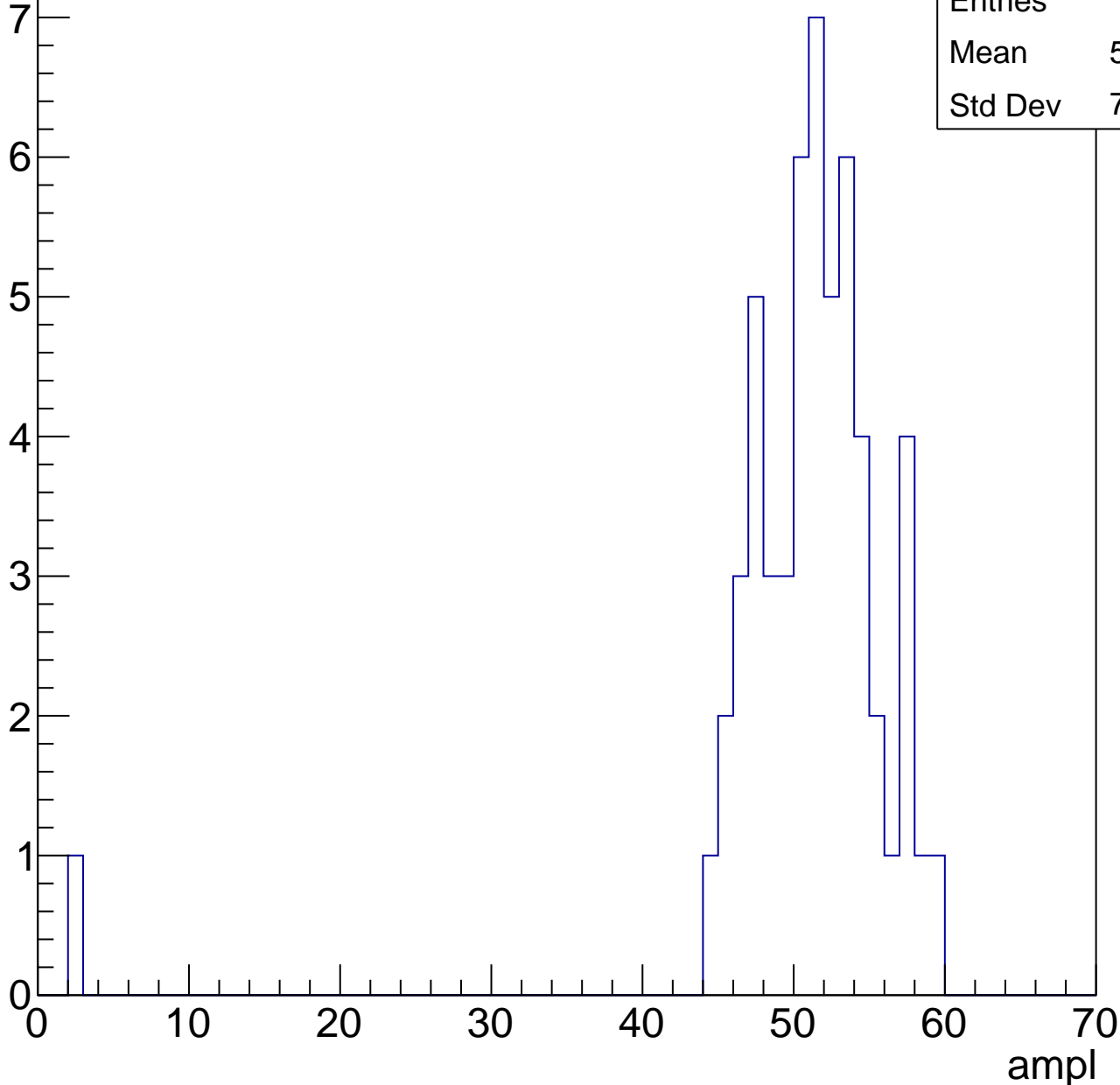


B1L103S, U1-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	50.22
Std Dev	7.468

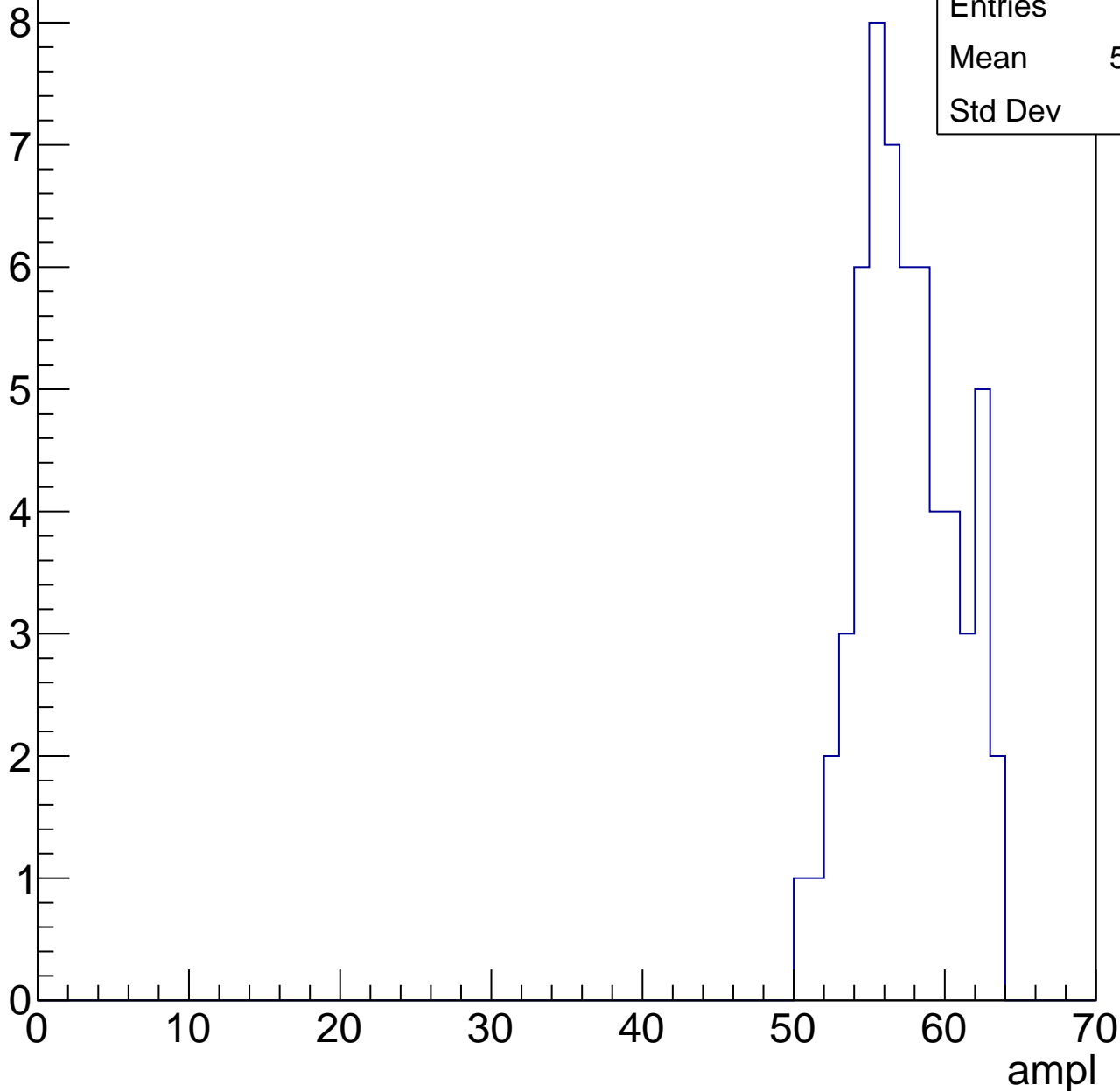


B1L103S, U1-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

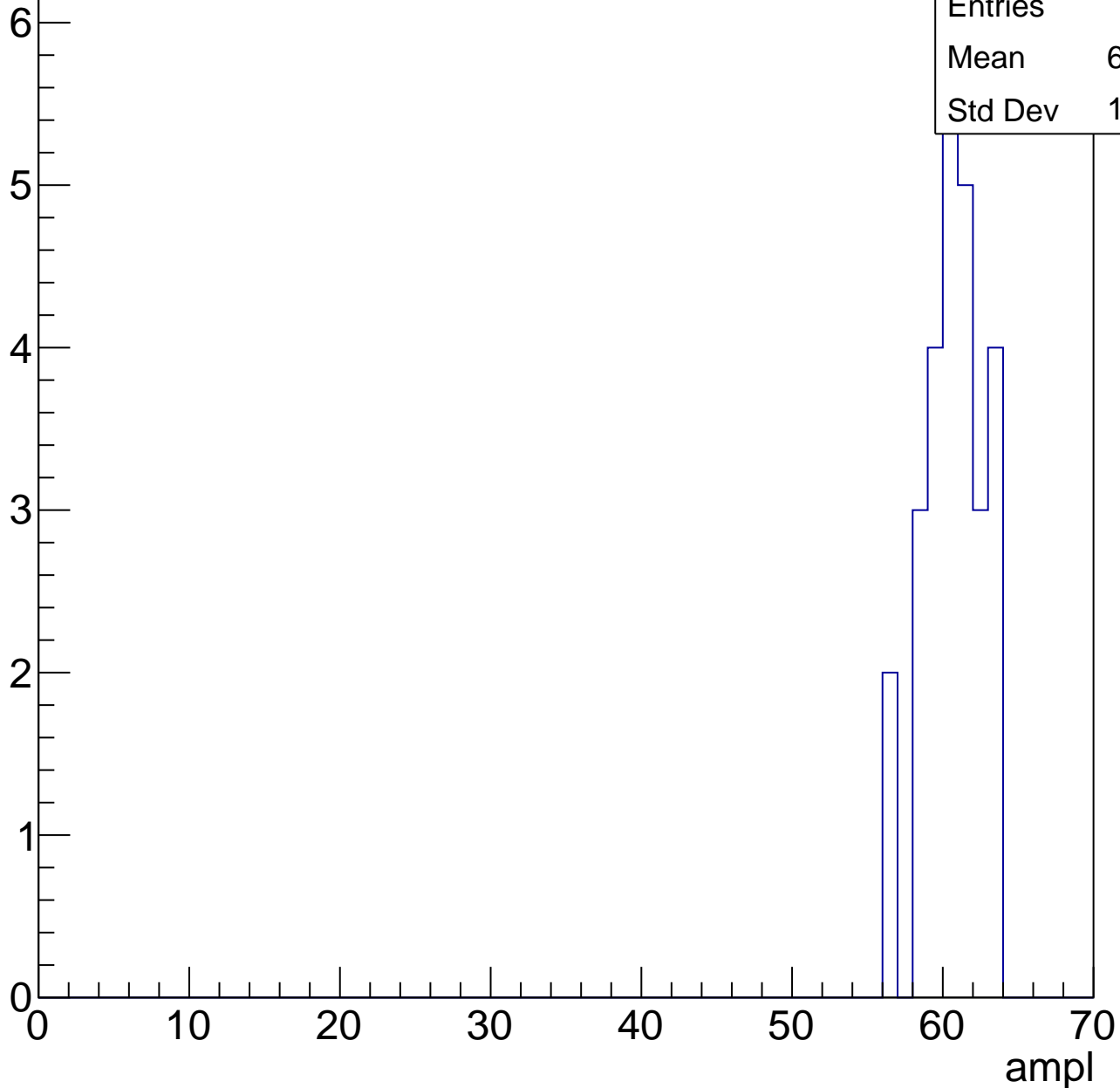
Entries	58
Mean	56.98
Std Dev	3.16



B1L103S, U1-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



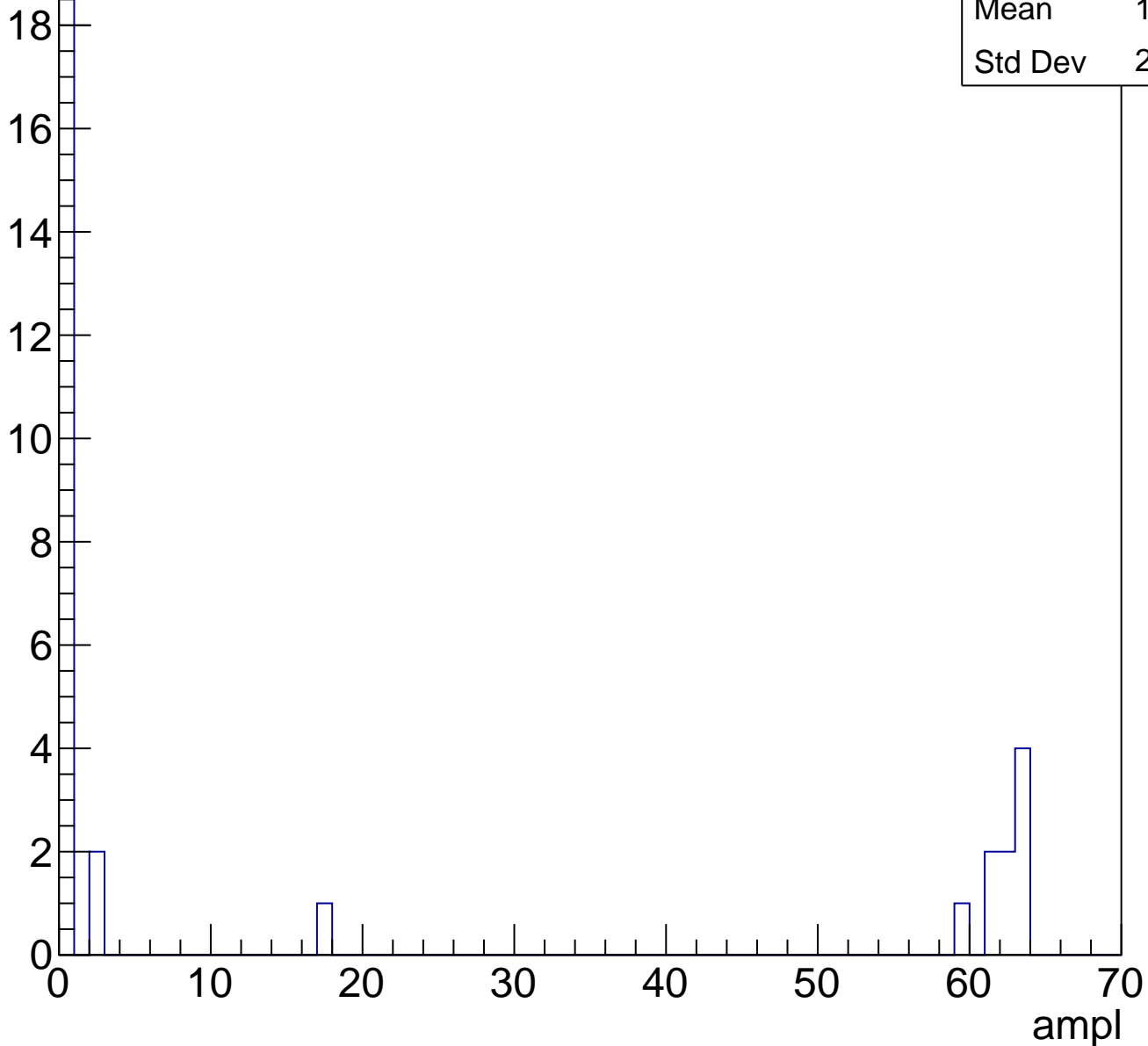
Entries	27
Mean	60.19
Std Dev	1.925

B1L103S, U1-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	31
Mean	18.65
Std Dev	27.83

Entry



B1L103S, U1-ch83, adc0

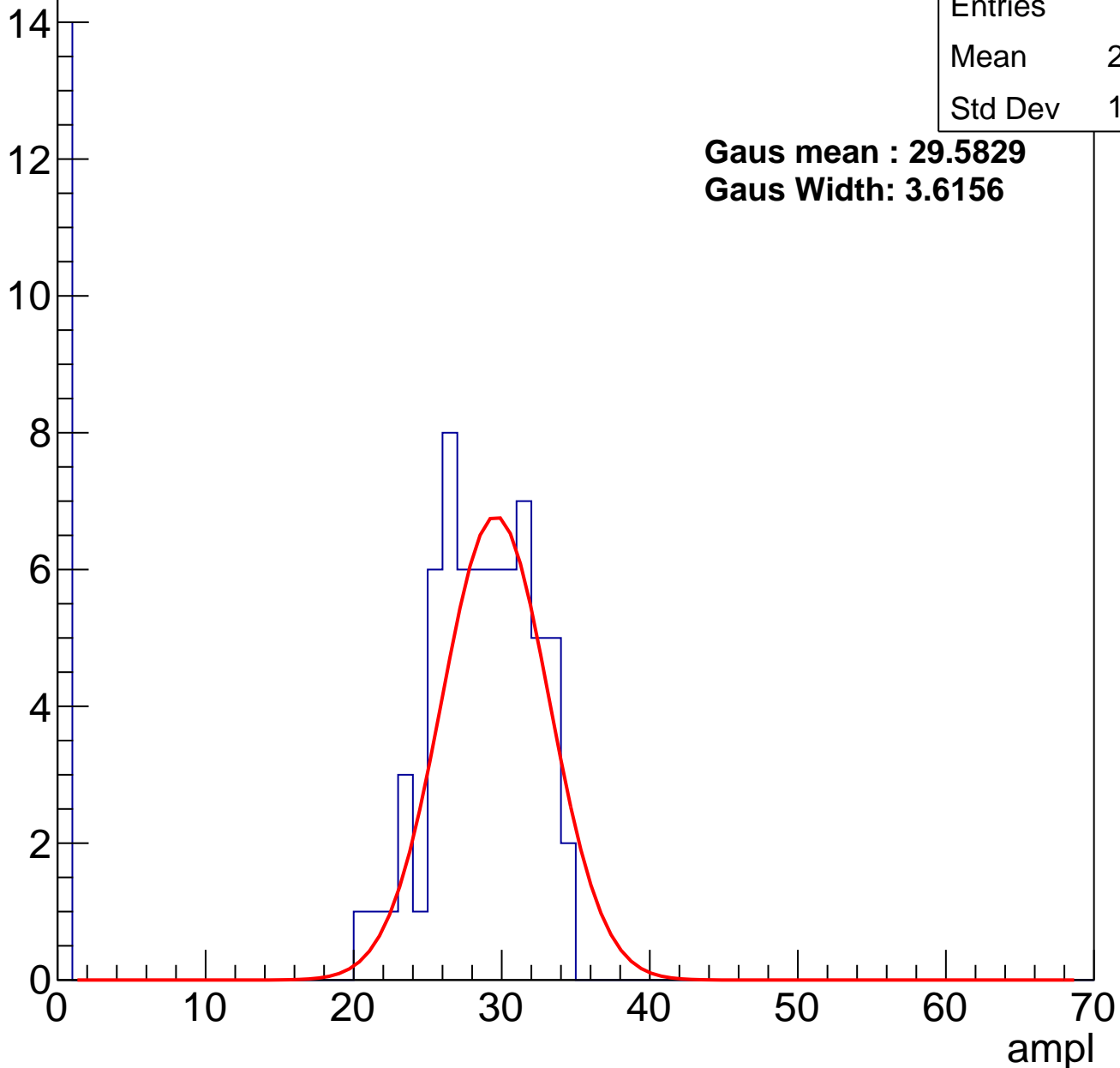
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	23.18
Std Dev	11.25

Gaus mean : 29.5829

Gaus Width: 3.6156

Entry



B1L103S, U1-ch83, adc1

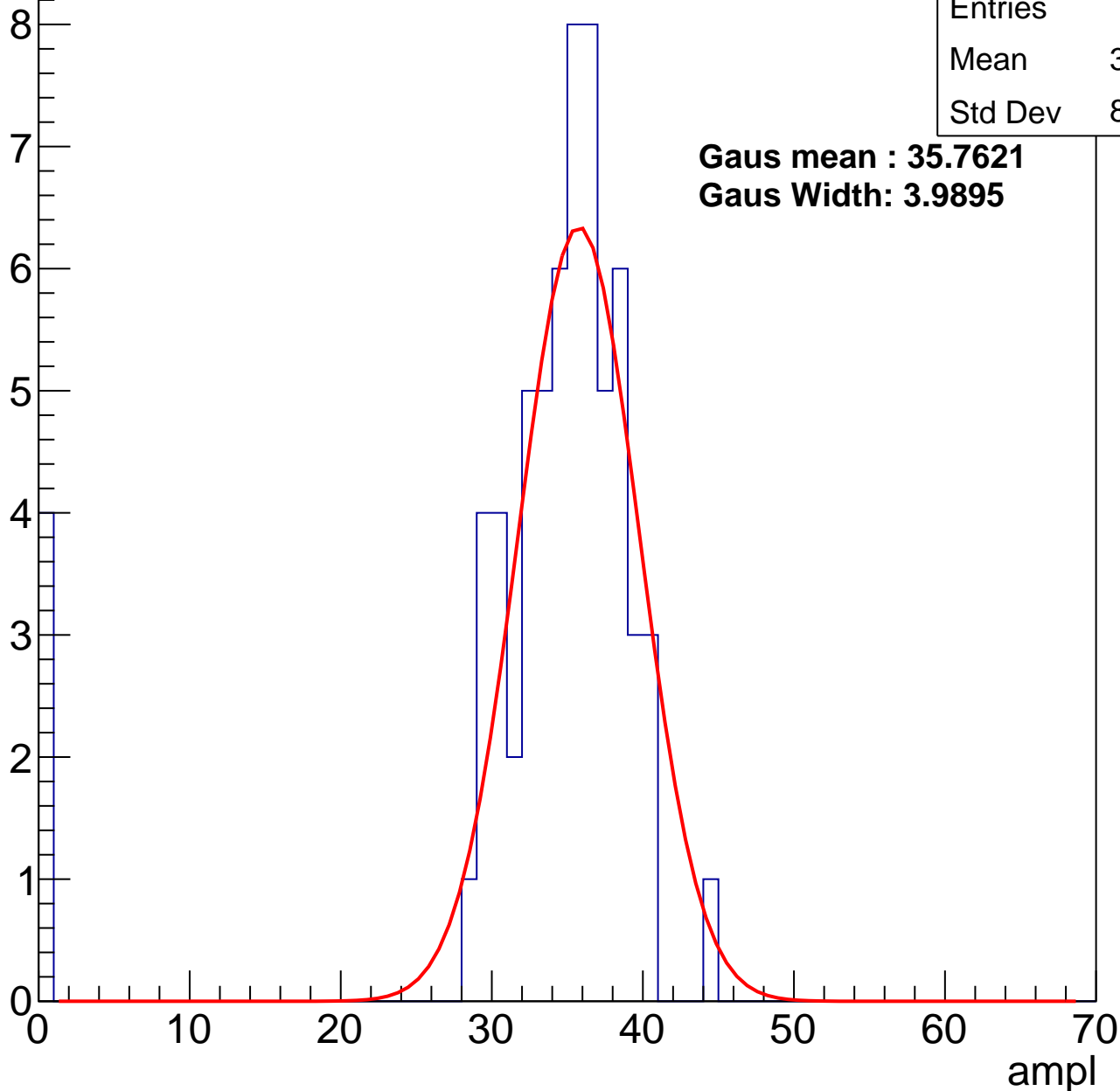
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	32.57
Std Dev	8.942

Gaus mean : 35.7621

Gaus Width: 3.9895



B1L103S, U1-ch83, adc2

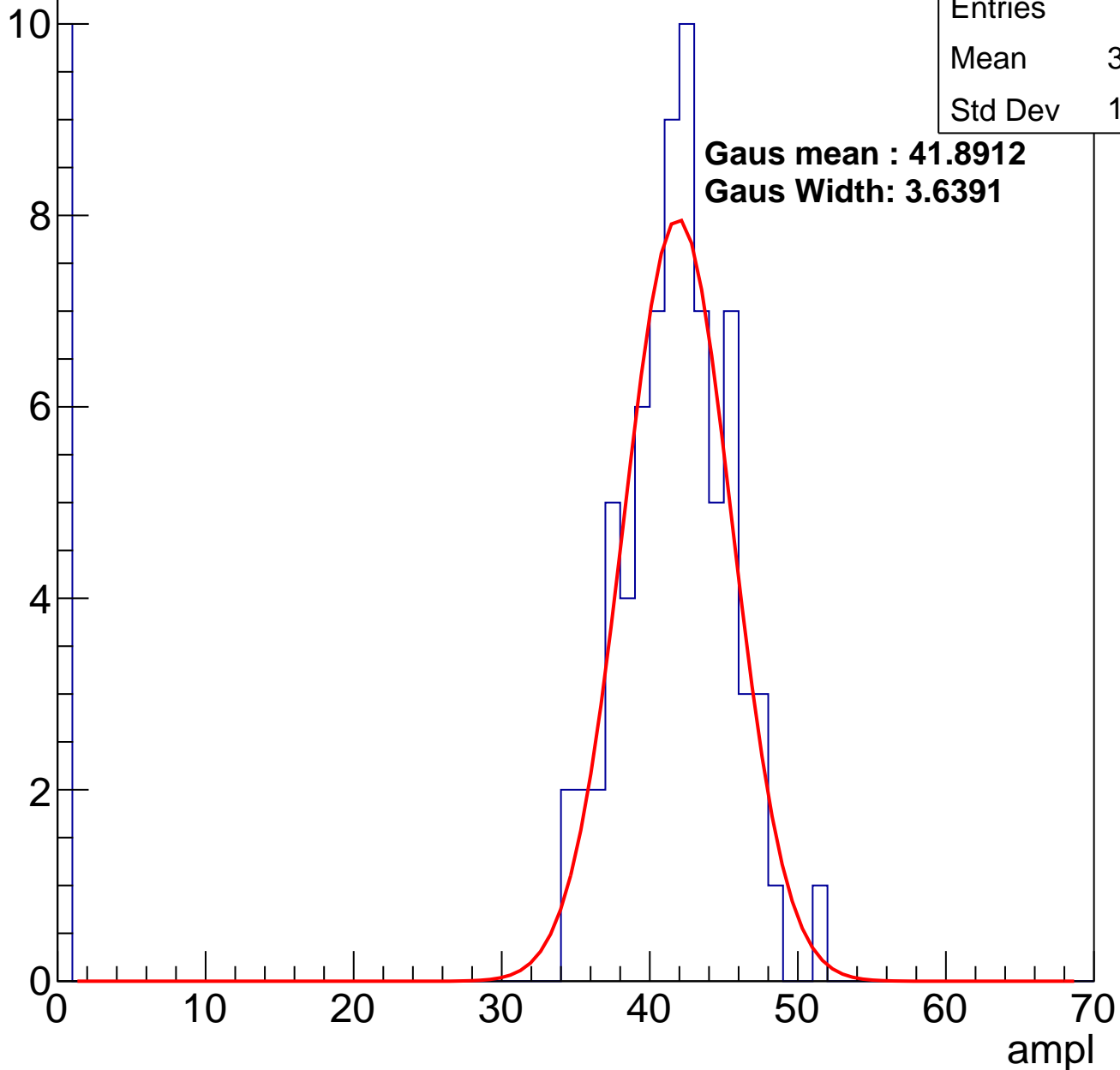
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	36.48
Std Dev	13.79

Gaus mean : 41.8912

Gaus Width: 3.6391

Entry

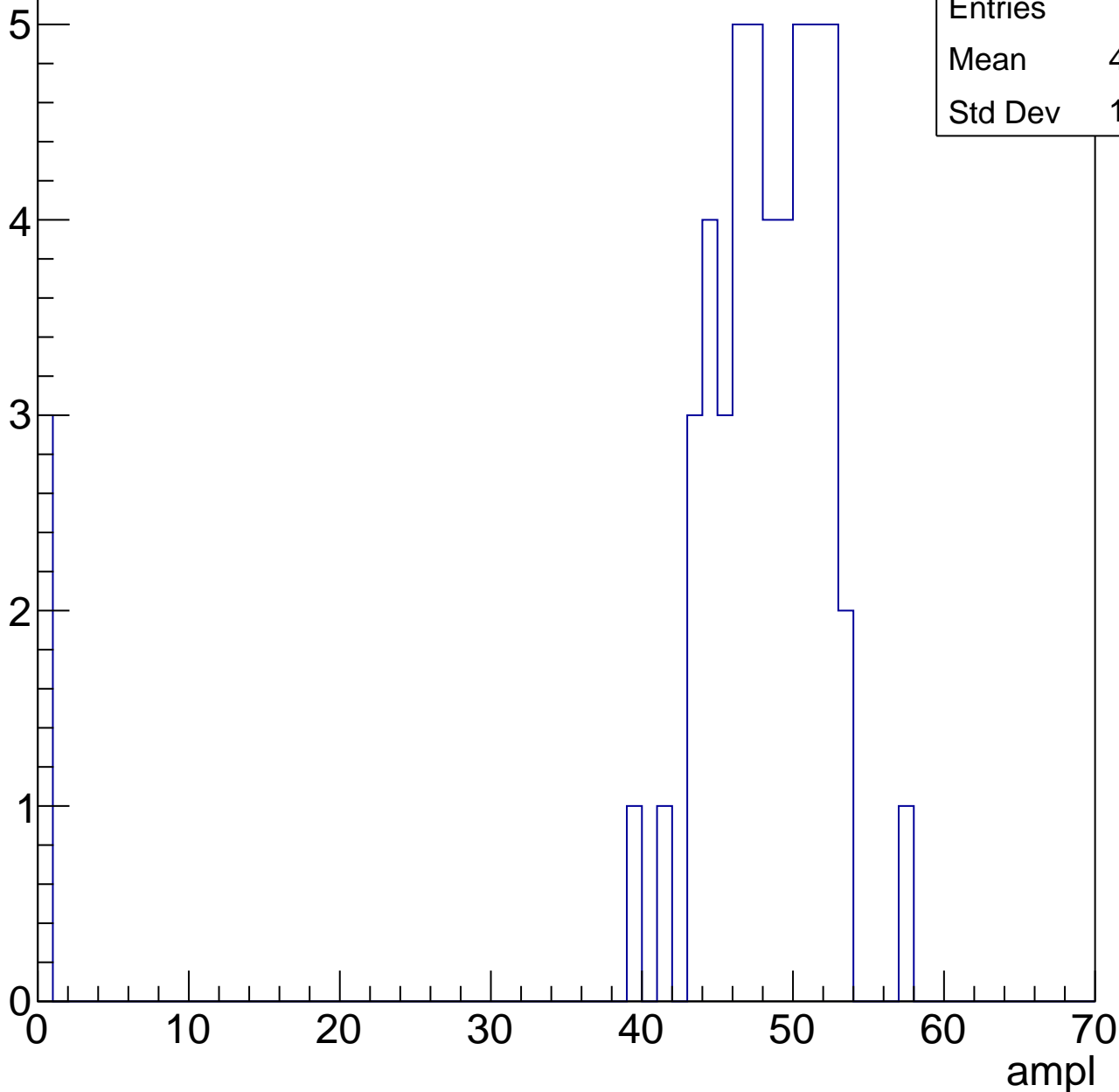


B1L103S, U1-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

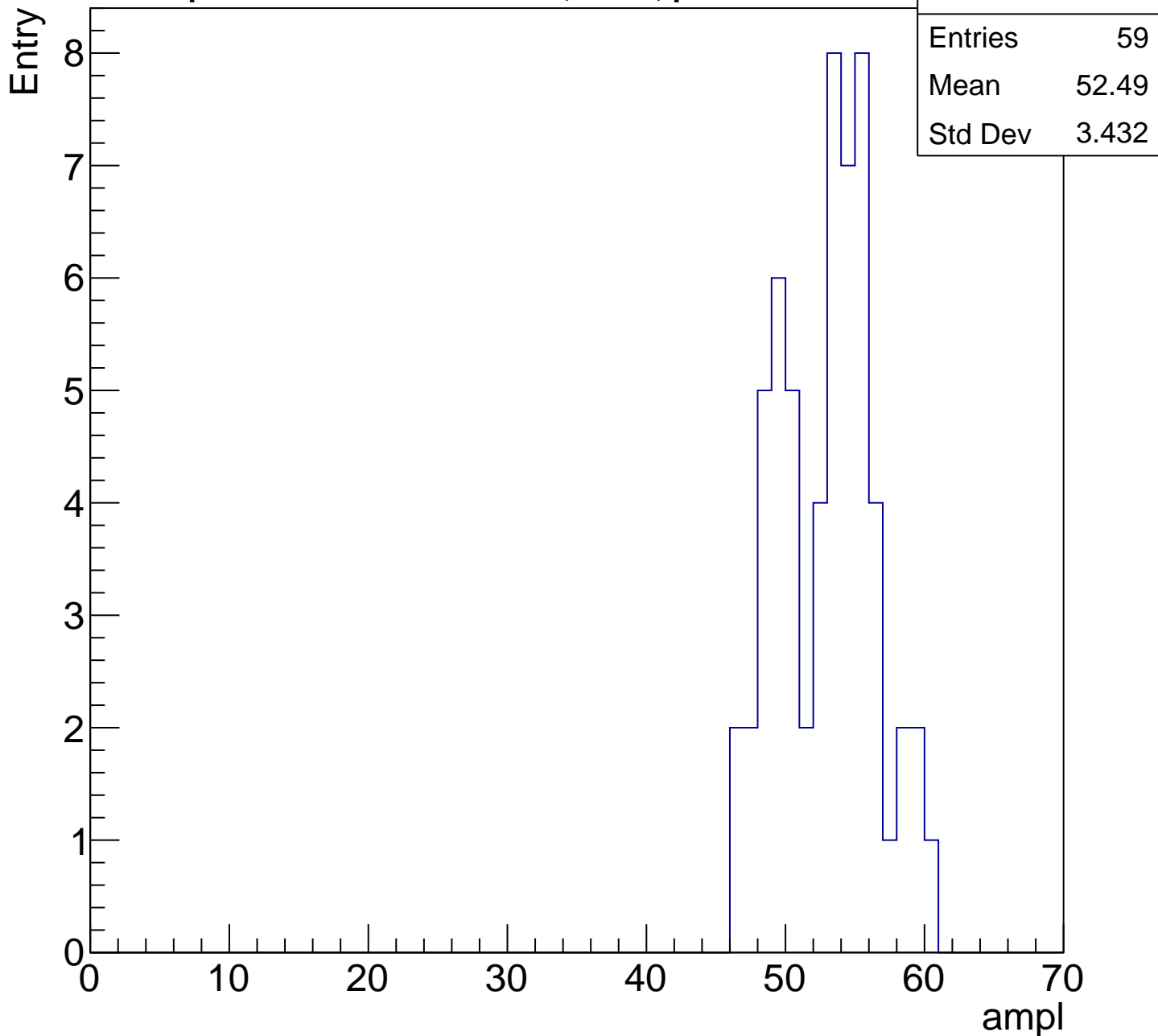
Entry

Entries	51
Mean	45.12
Std Dev	11.79



B1L103S, U1-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

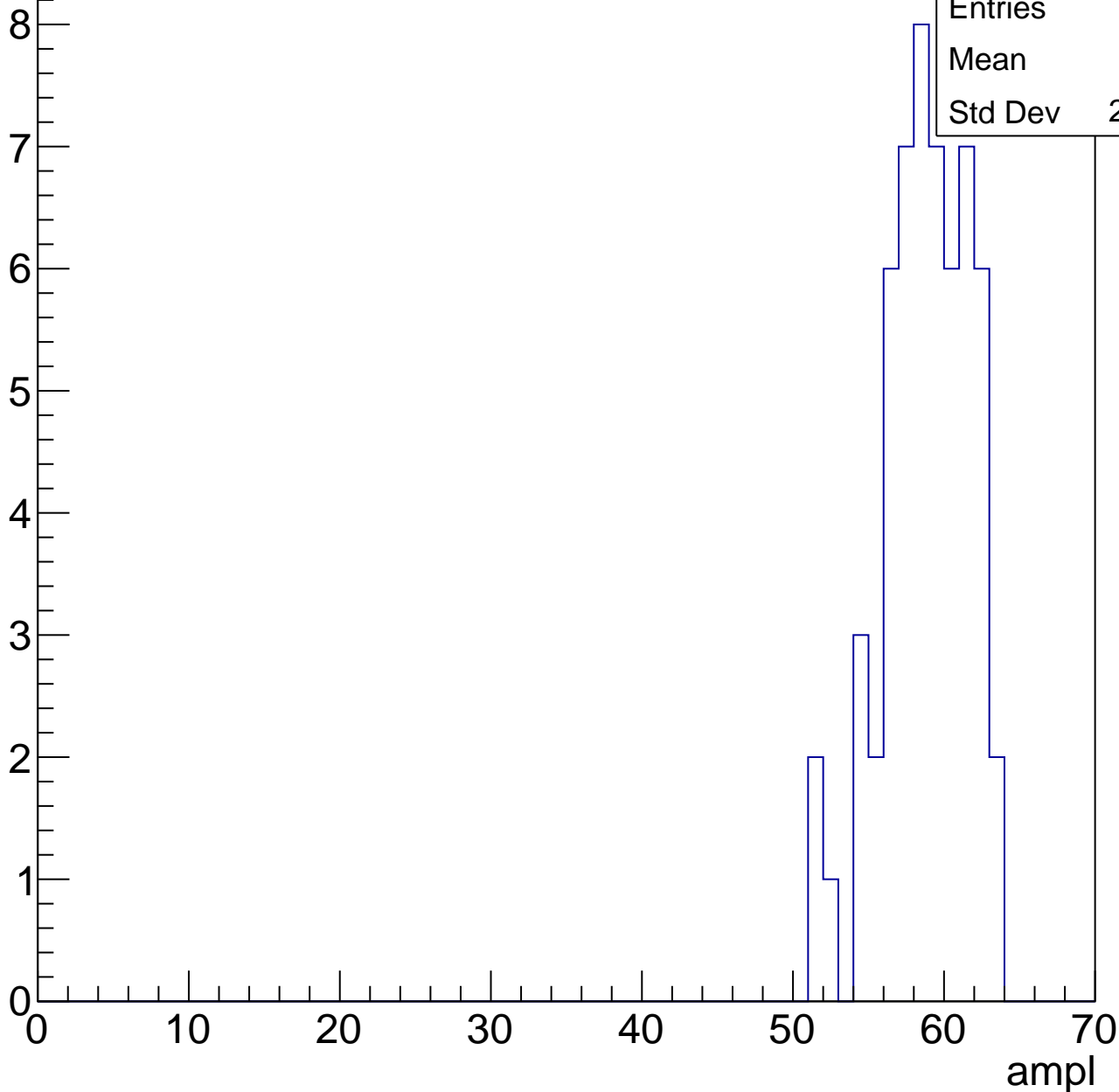


B1L103S, U1-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.3
Std Dev	2.859

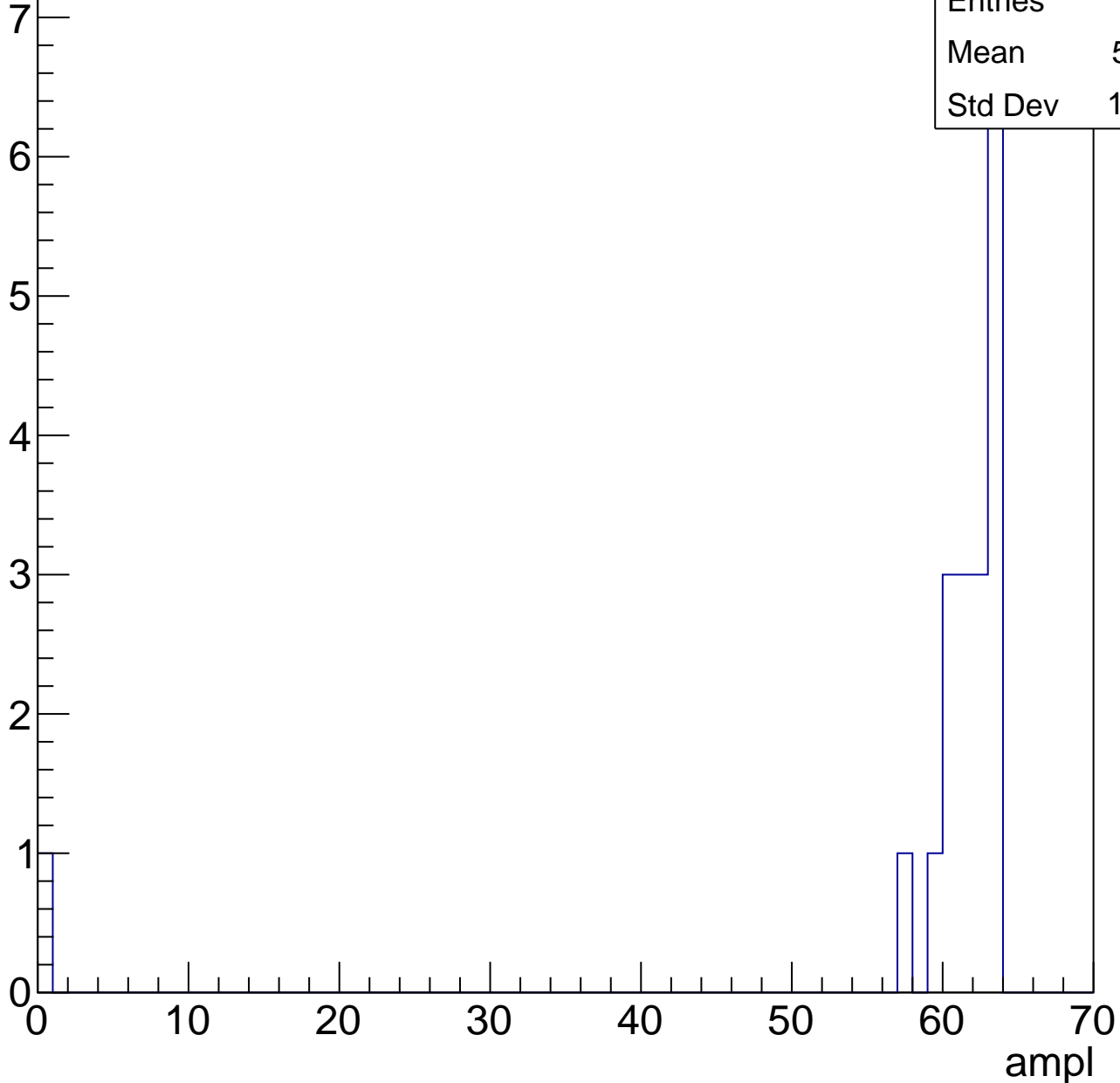


B1L103S, U1-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

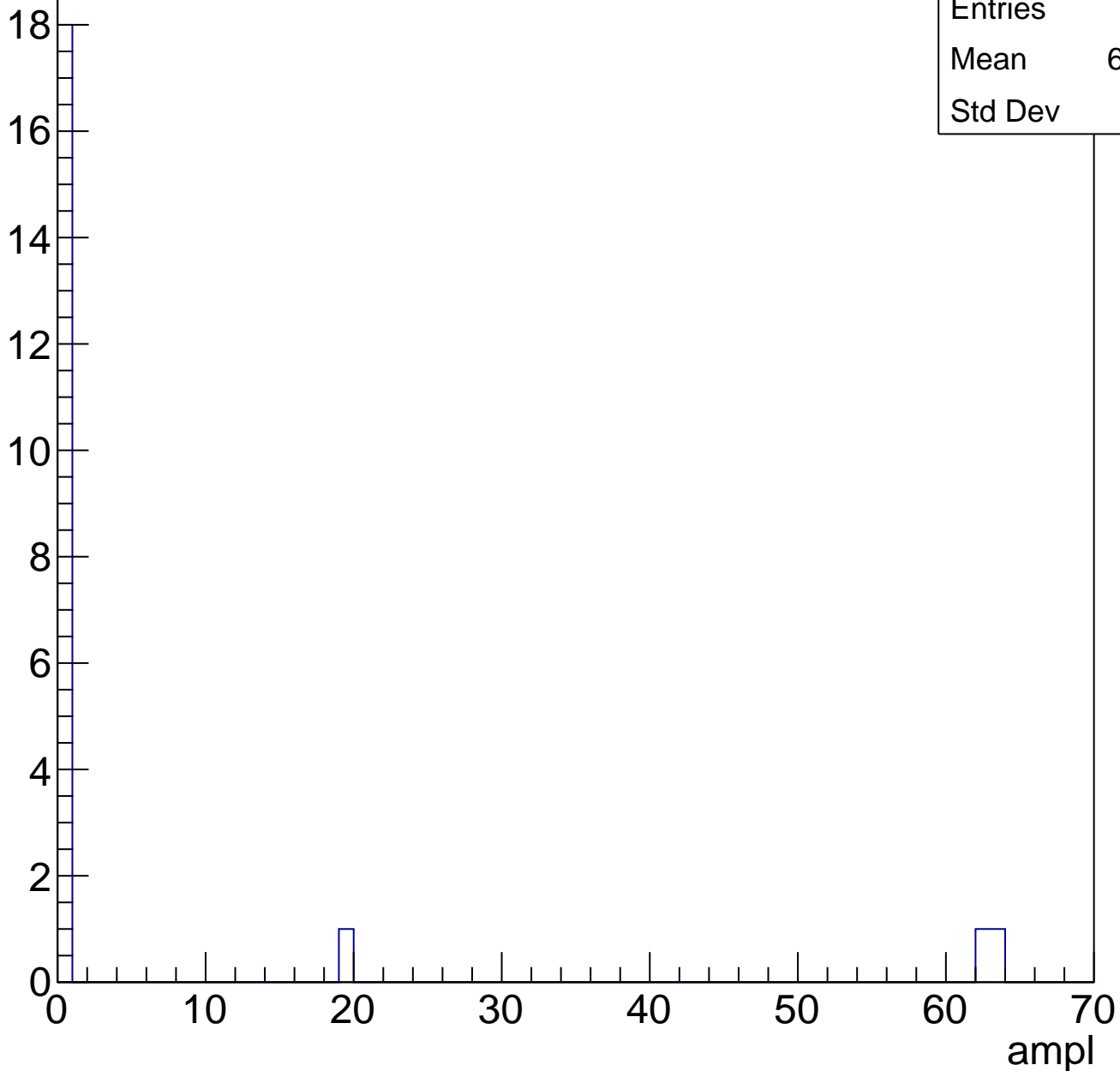
Entries	19
Mean	58.21
Std Dev	13.82



B1L103S, U1-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	6.857
Std Dev	18.5

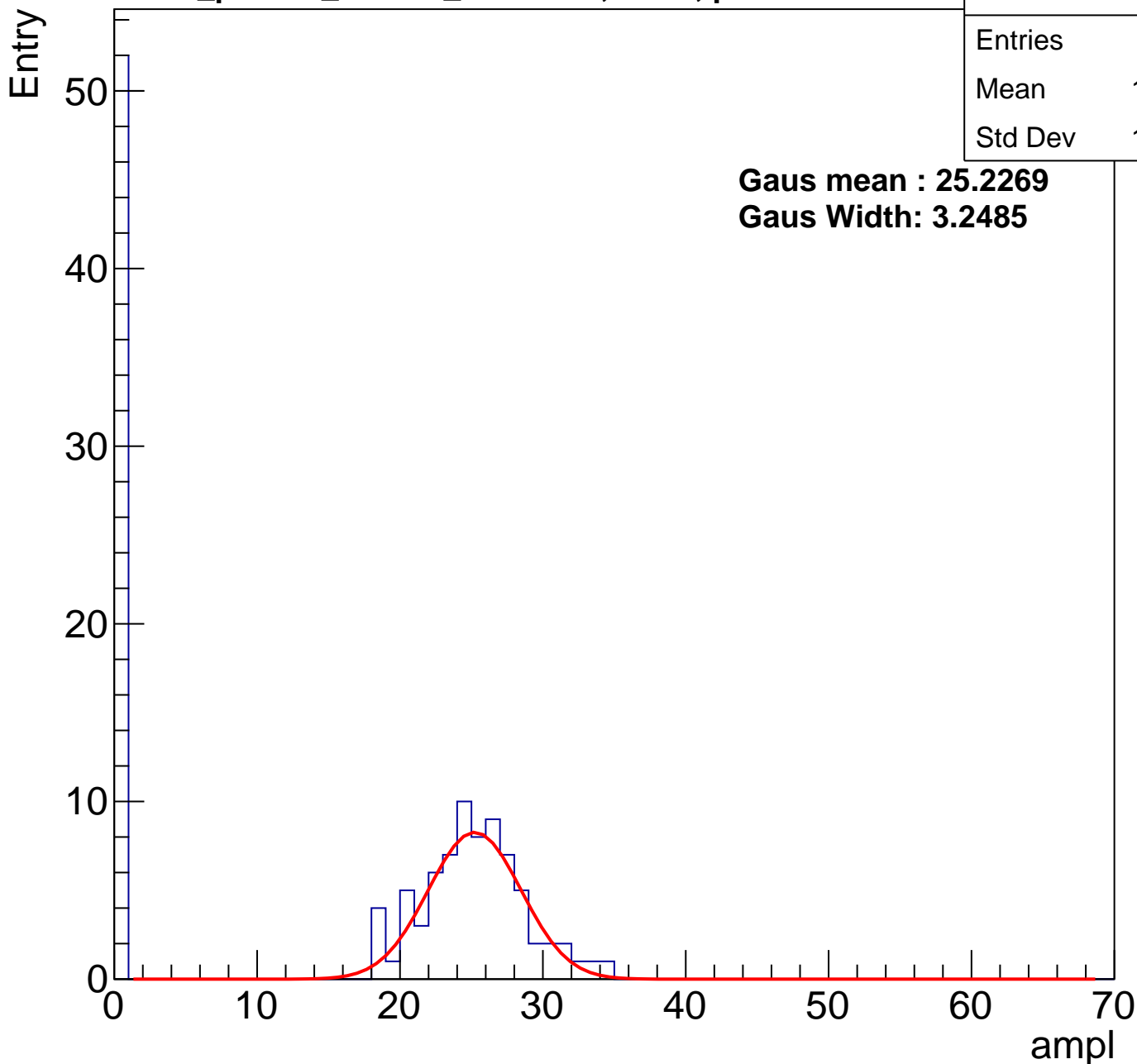
B1L103S, U1-ch84, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	126
Mean	14.52
Std Dev	12.47

Gaus mean : 25.2269

Gaus Width: 3.2485



B1L103S, U1-ch84, adc1

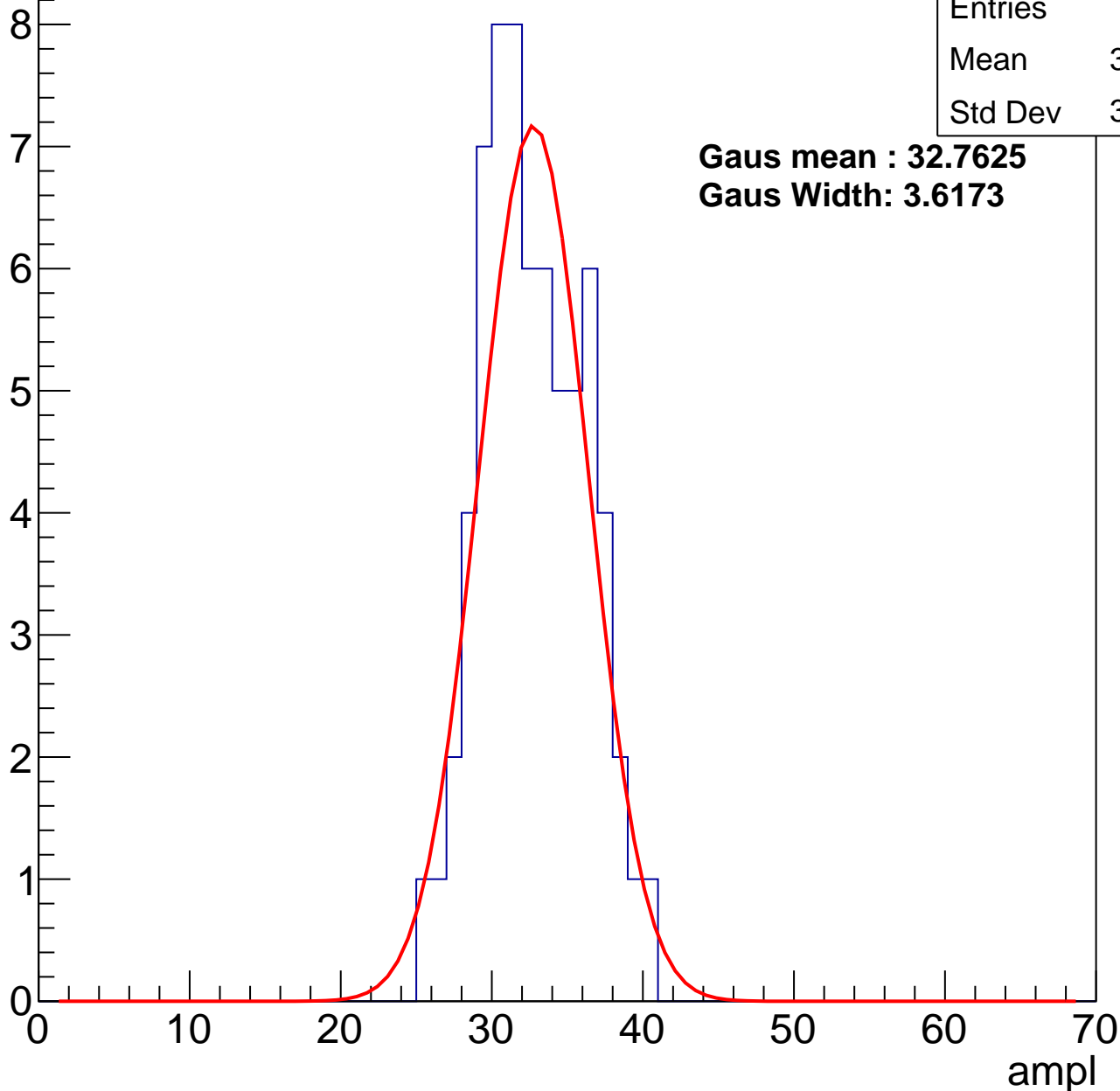
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.27
Std Dev	3.344

Gaus mean : 32.7625

Gaus Width: 3.6173



B1L103S, U1-ch84, adc2

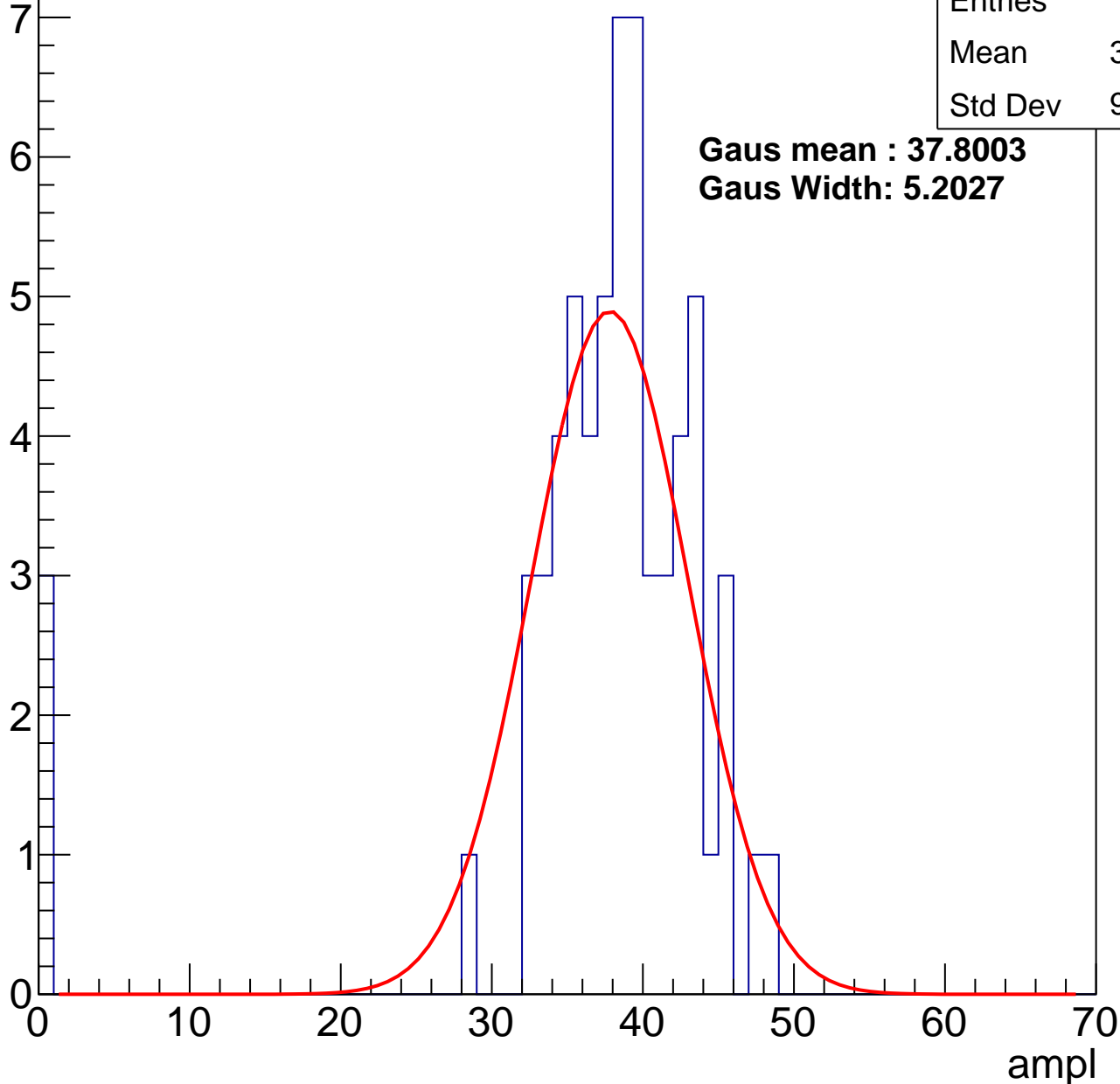
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.54
Std Dev	9.094

Gaus mean : 37.8003

Gaus Width: 5.2027



B1L103S, U1-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	38.01
Std Dev	15.71

Entry

10

8

6

4

2

0

0

10

20

30

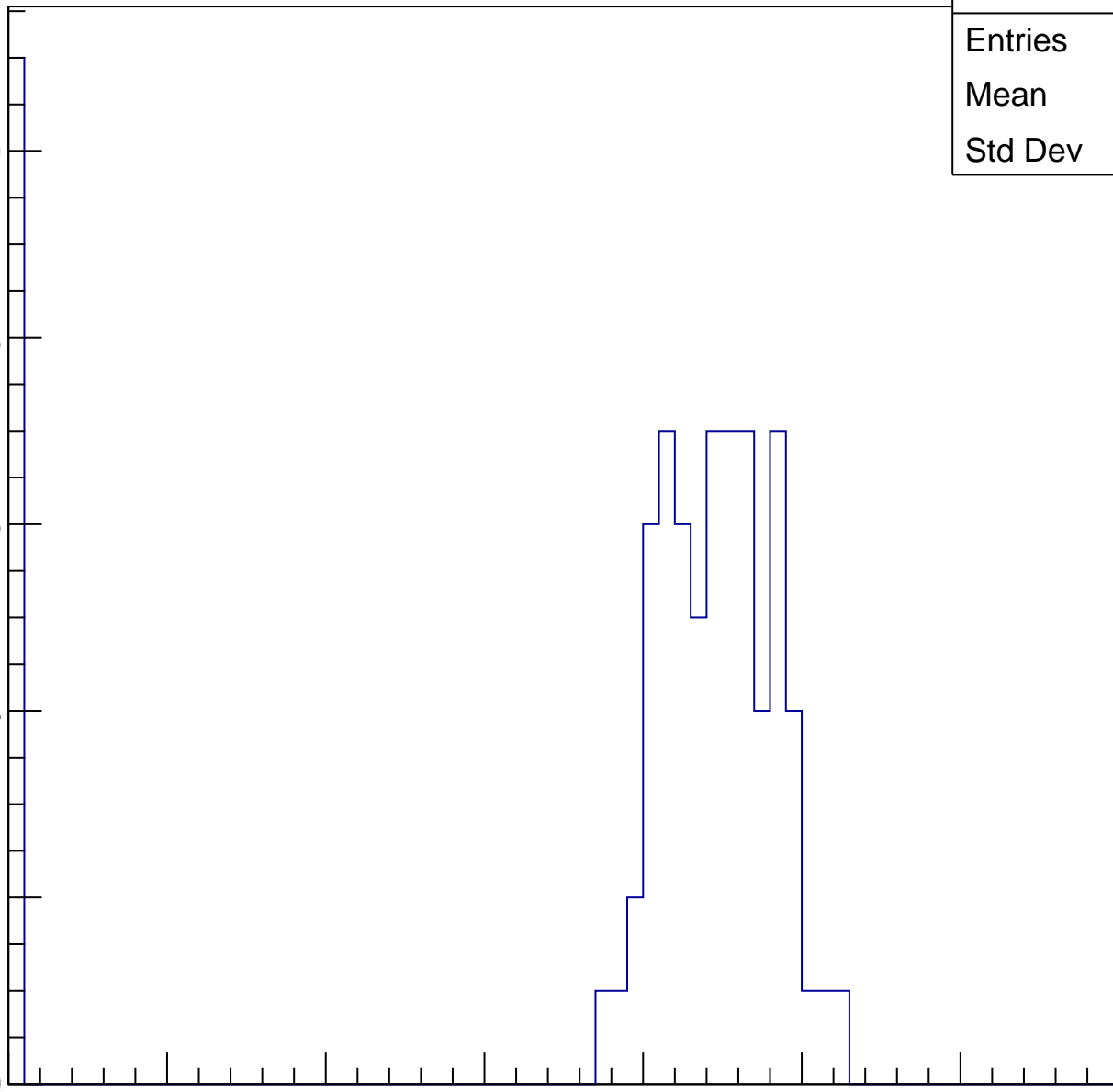
40

50

60

70

ampl

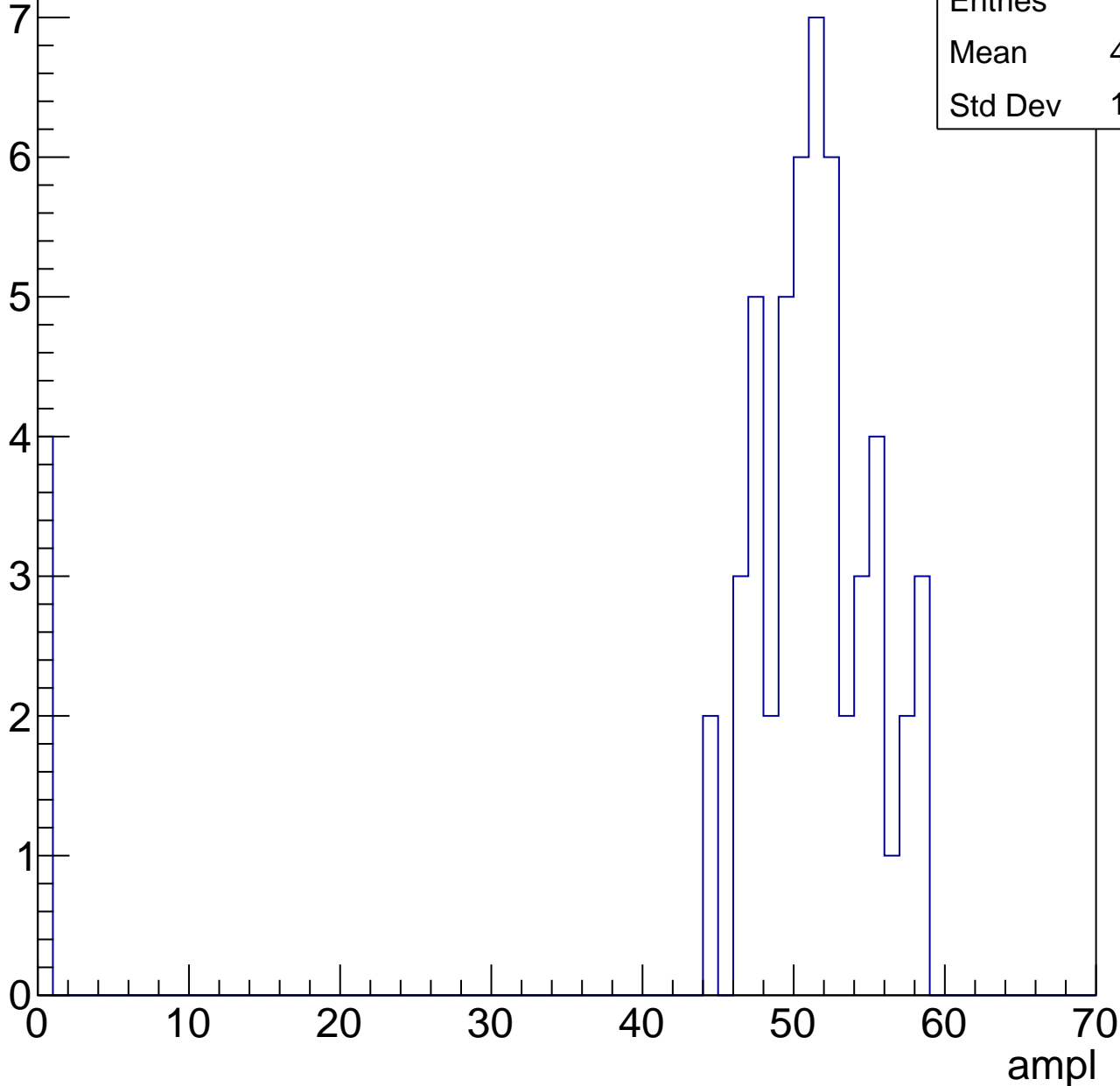


B1L103S, U1-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

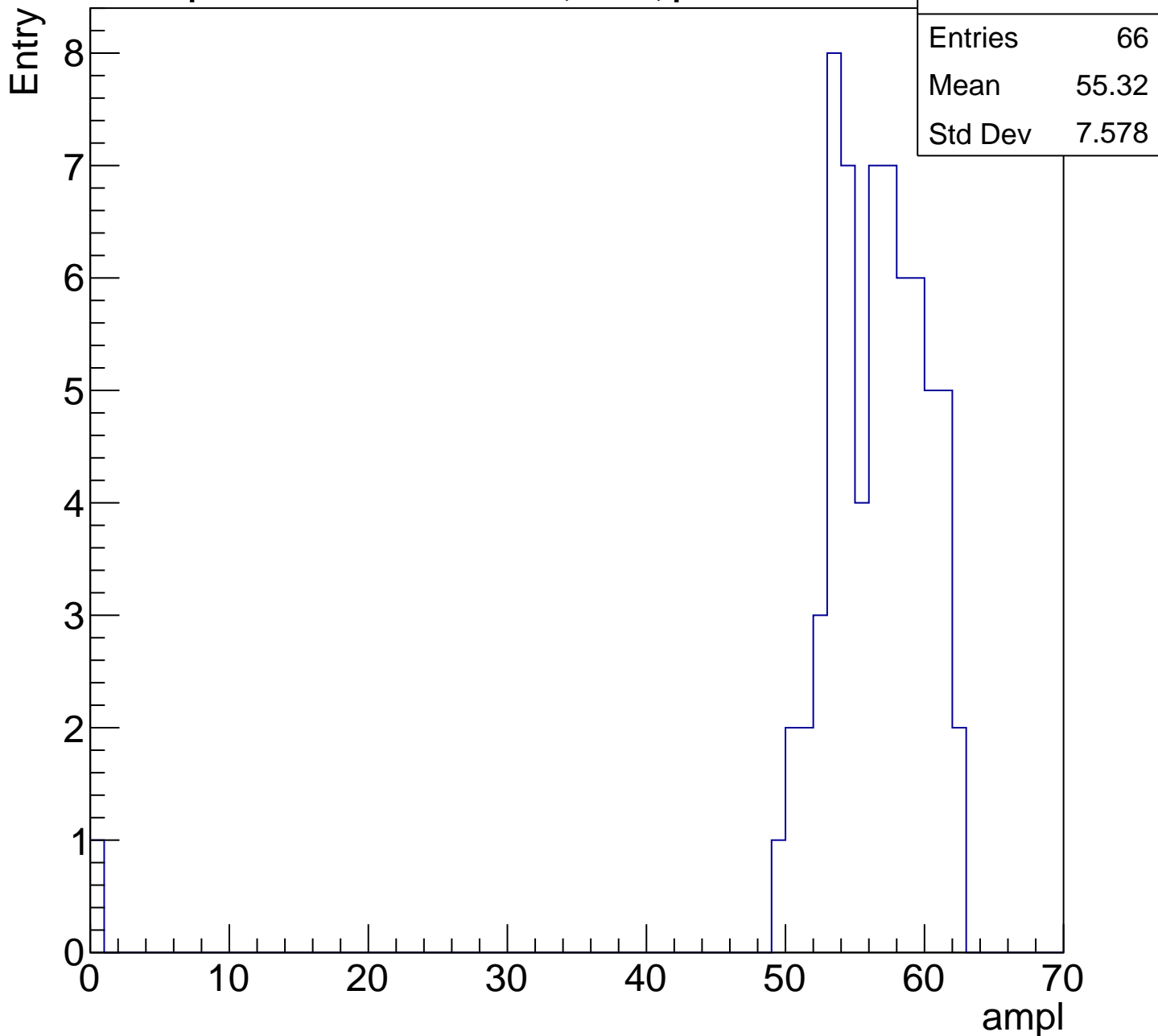
Entry

Entries	55
Mean	47.33
Std Dev	13.69



B1L103S, U1-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

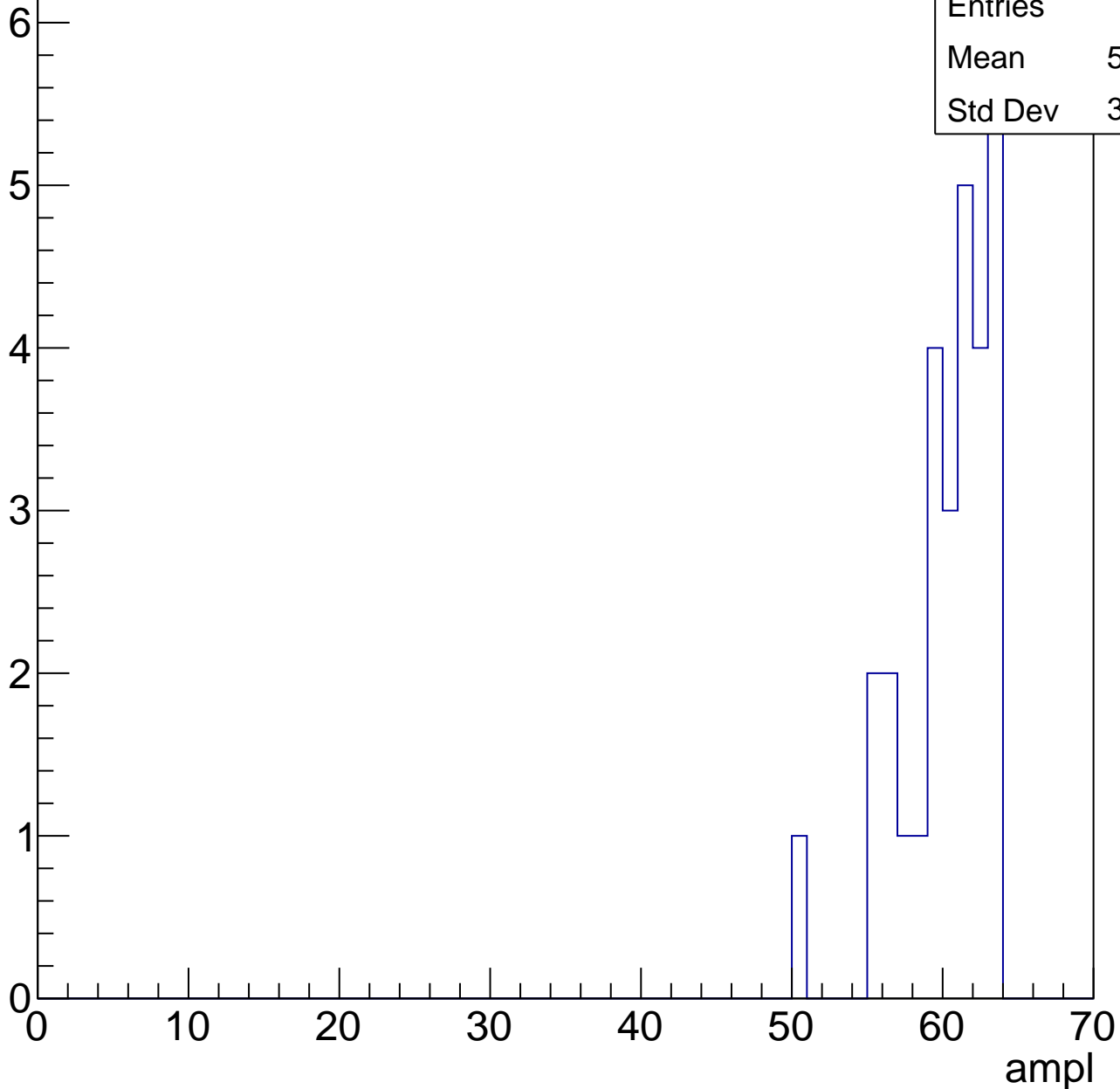


B1L103S, U1-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	59.79
Std Dev	3.067

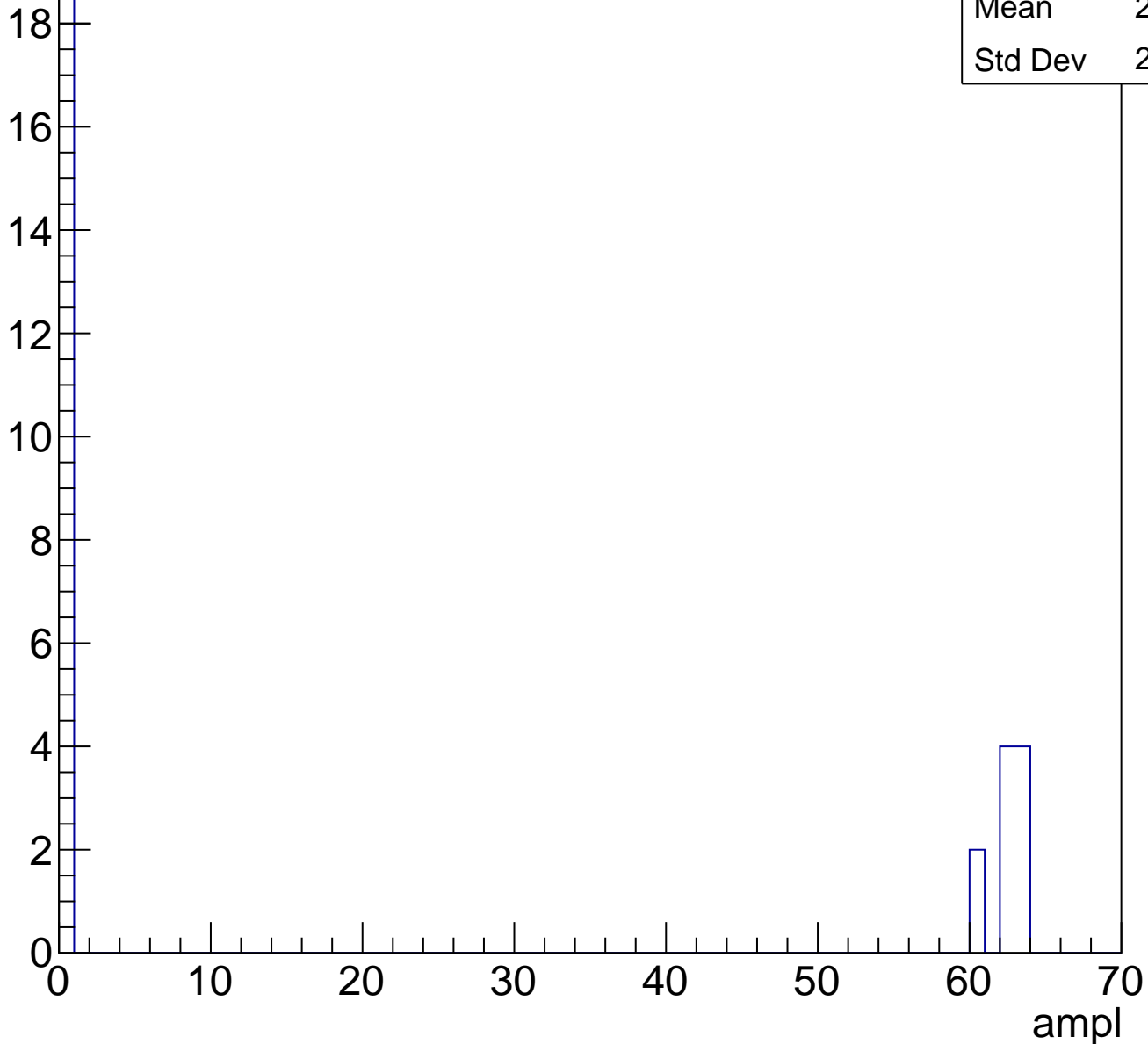


B1L103S, U1-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	29
Mean	21.38
Std Dev	29.48

Entry



B1L103S, U1-ch85, adc0

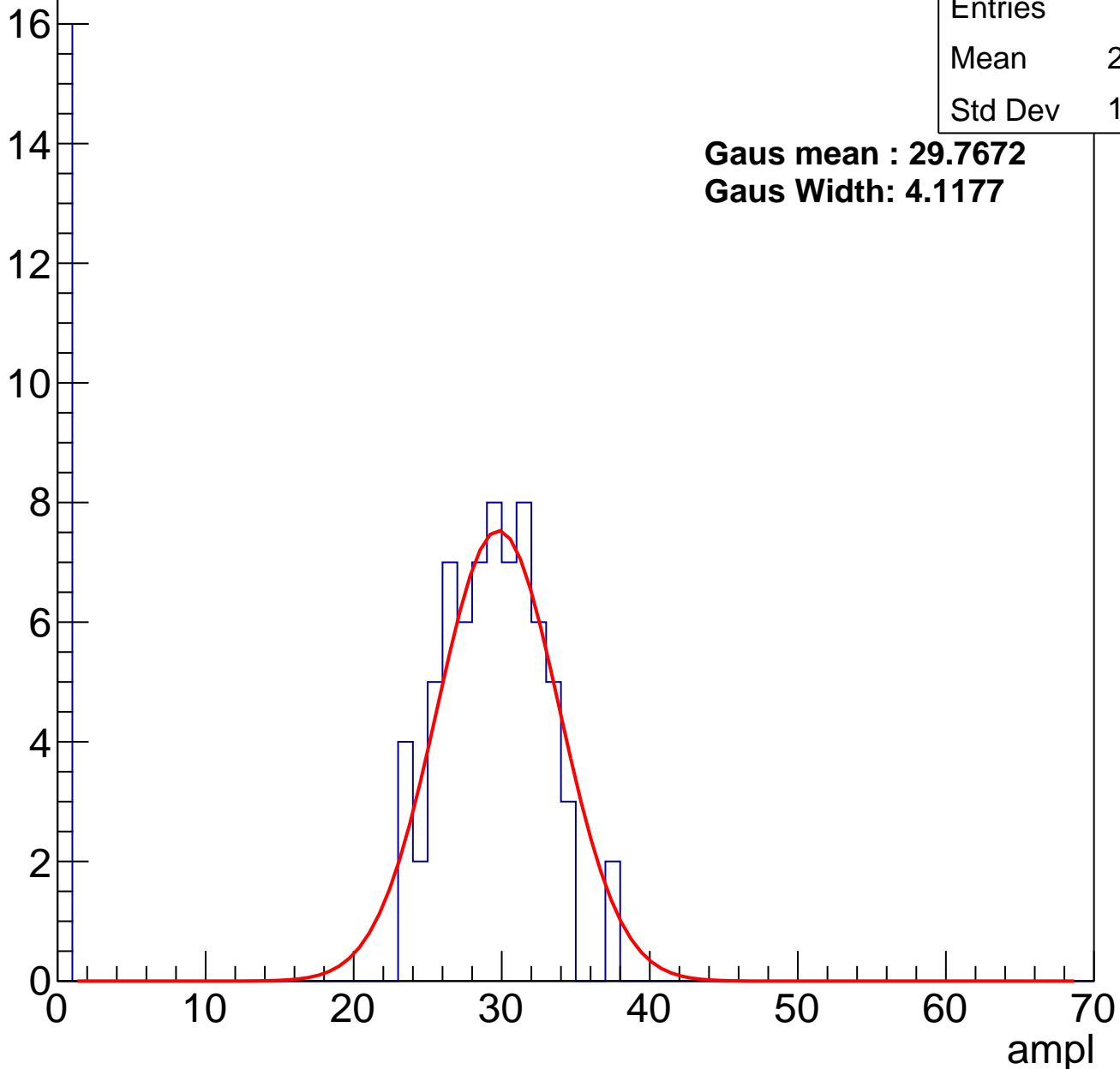
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	23.58
Std Dev	11.65

Gaus mean : 29.7672

Gaus Width: 4.1177

Entry



B1L103S, U1-ch85, adc1

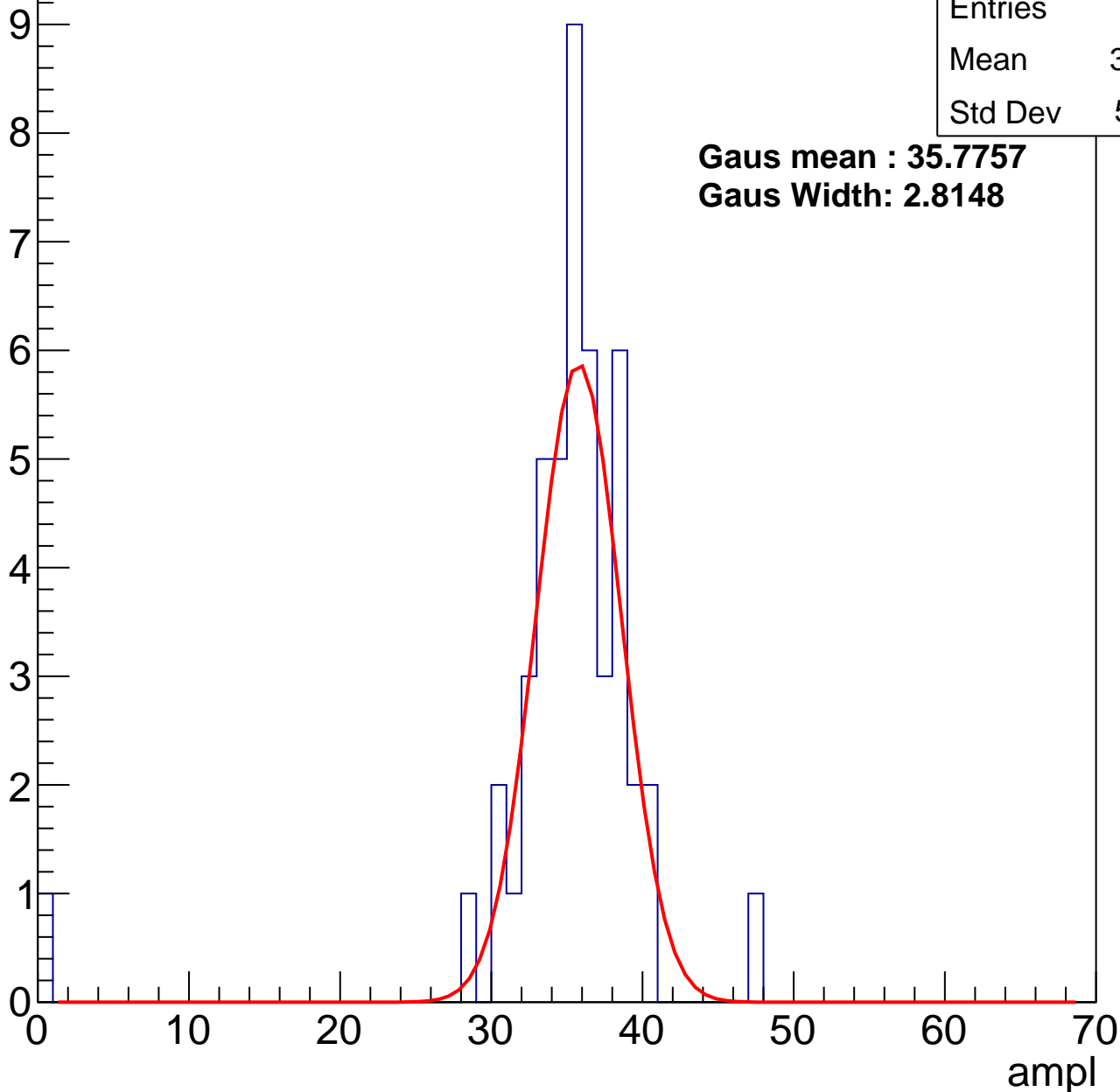
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	34.57
Std Dev	5.981

Gaus mean : 35.7757

Gaus Width: 2.8148



B1L103S, U1-ch85, adc2

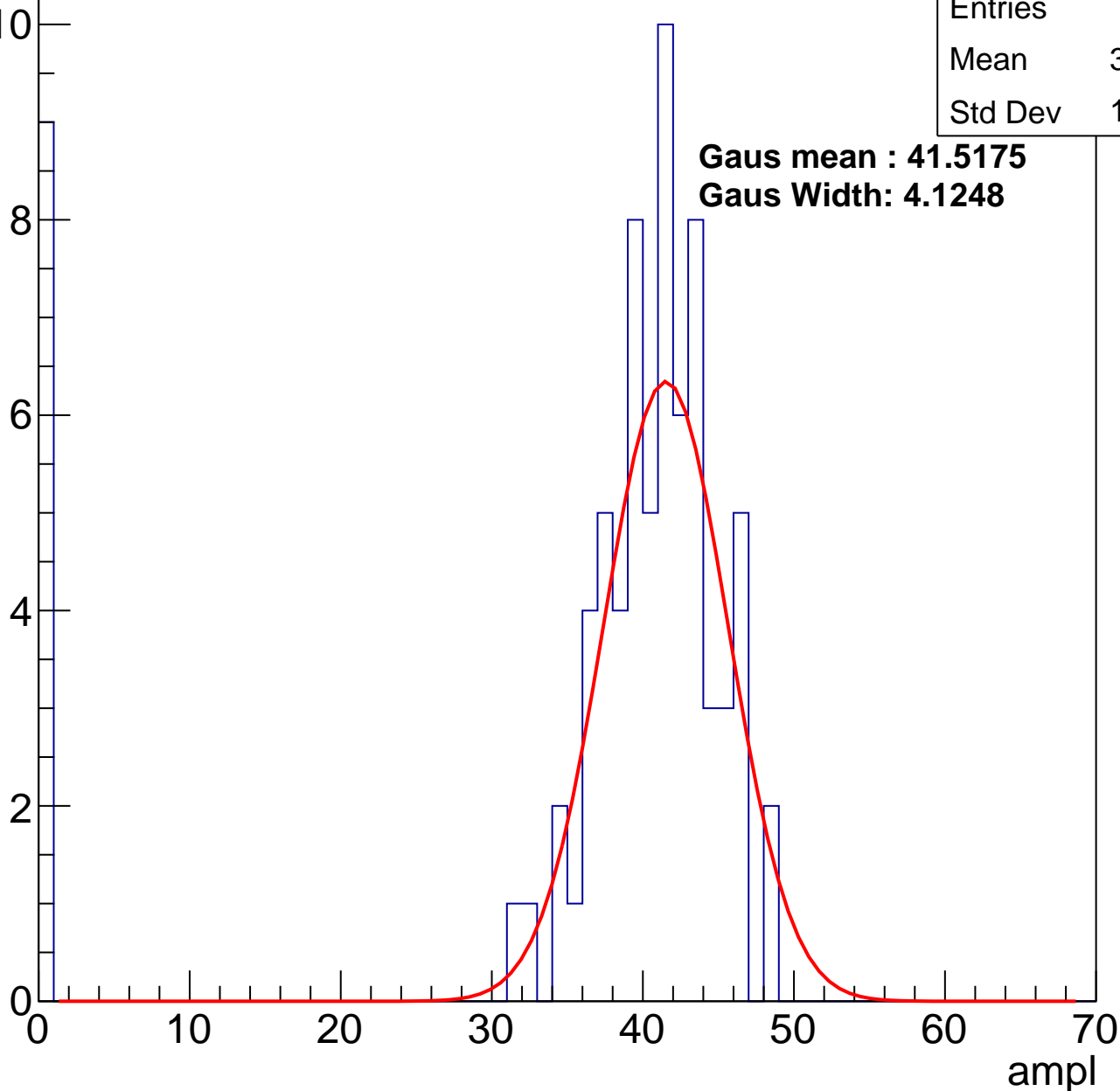
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	35.82
Std Dev	13.47

Gaus mean : 41.5175

Gaus Width: 4.1248

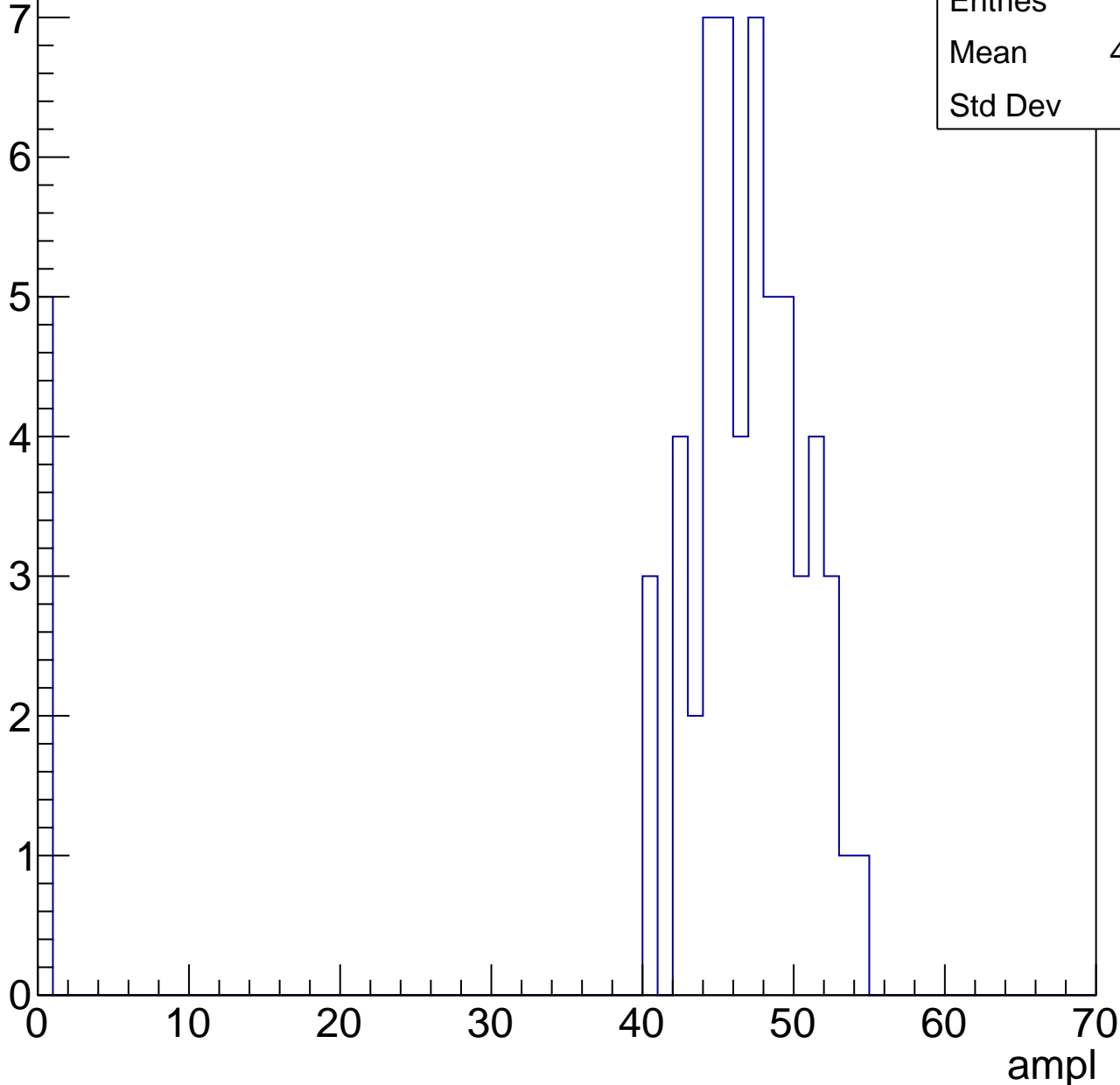


B1L103S, U1-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	42.82
Std Dev	13.2

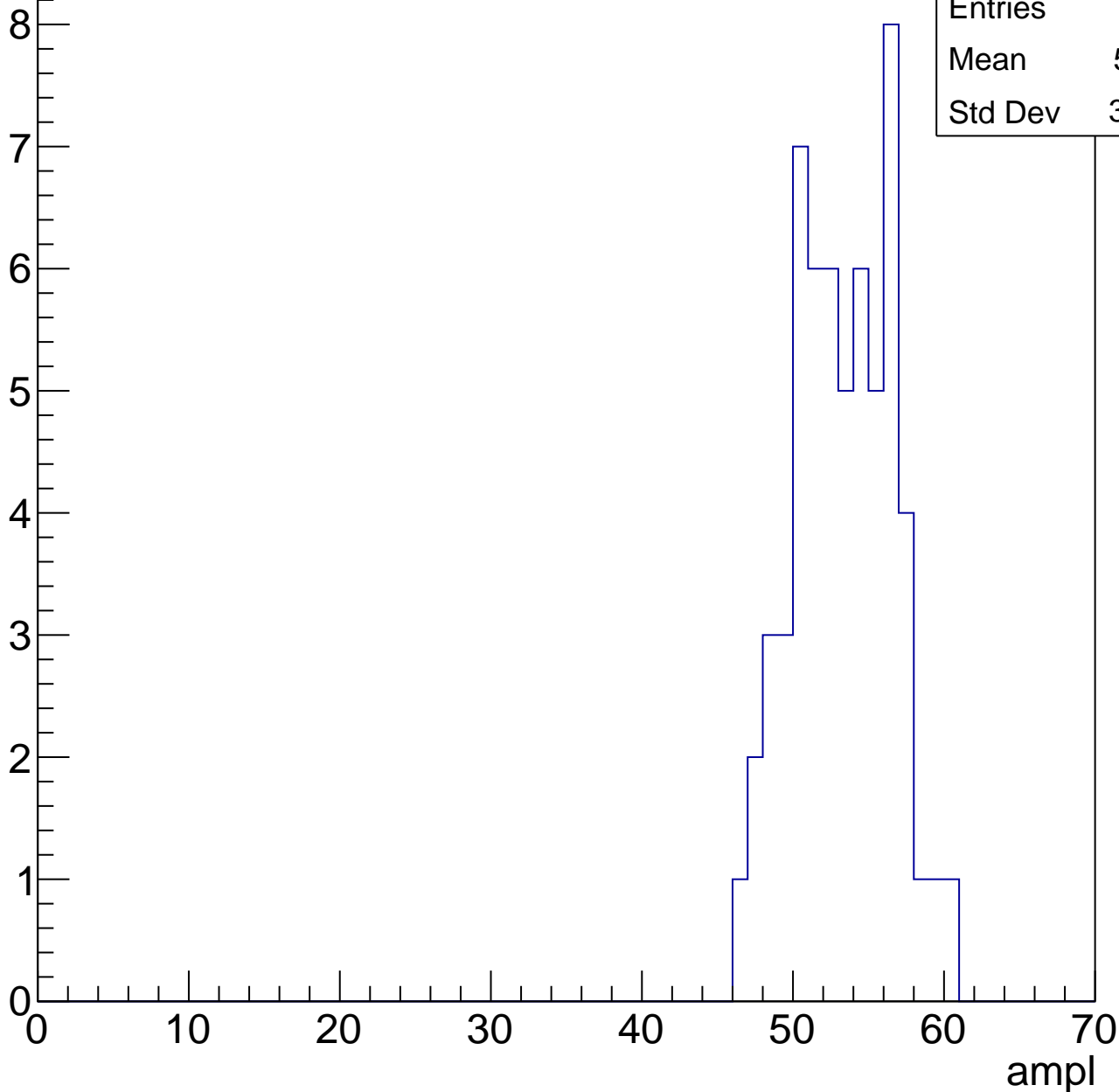


B1L103S, U1-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.81
Std Dev	3.207

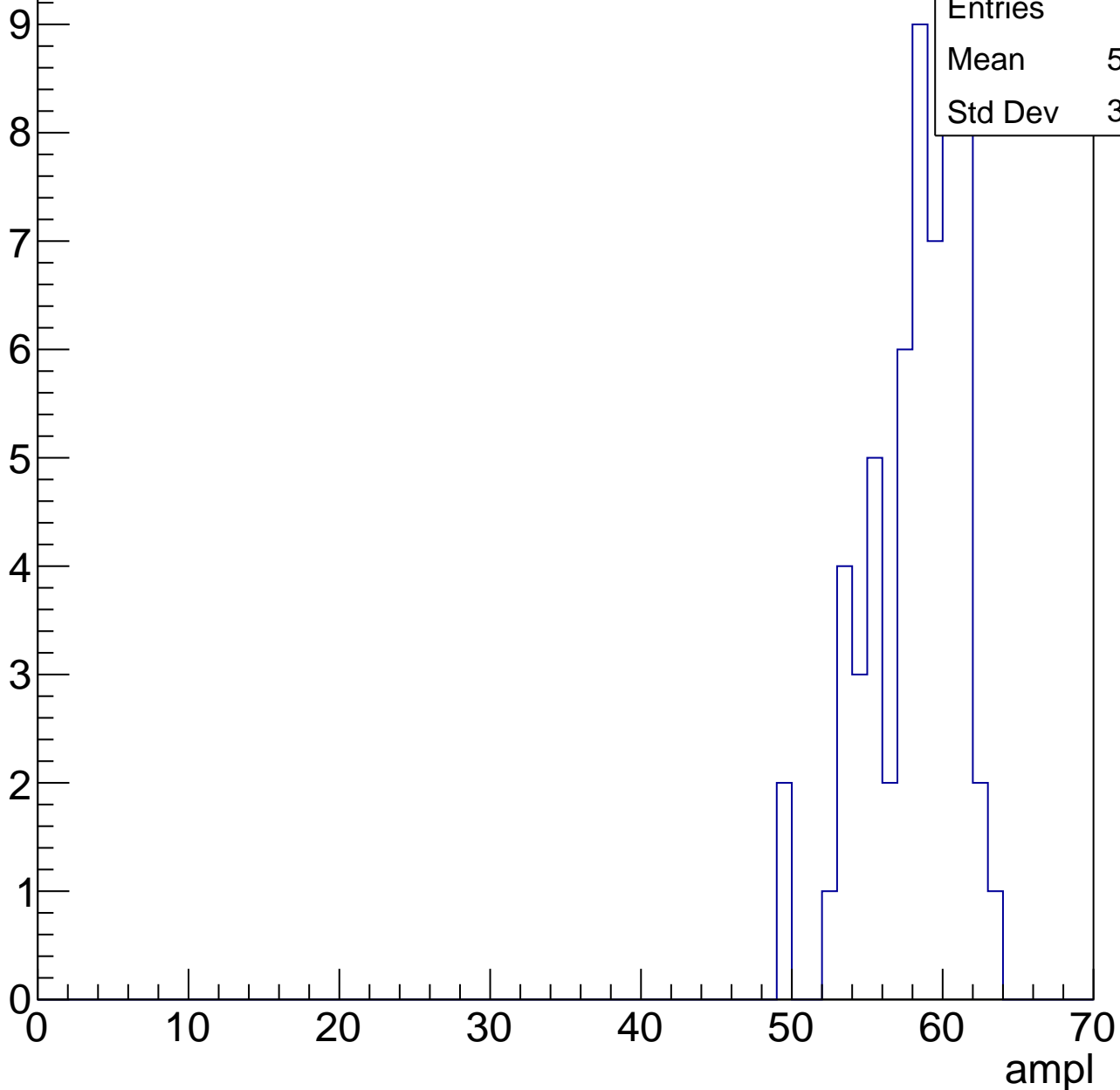


B1L103S, U1-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.64
Std Dev	3.122

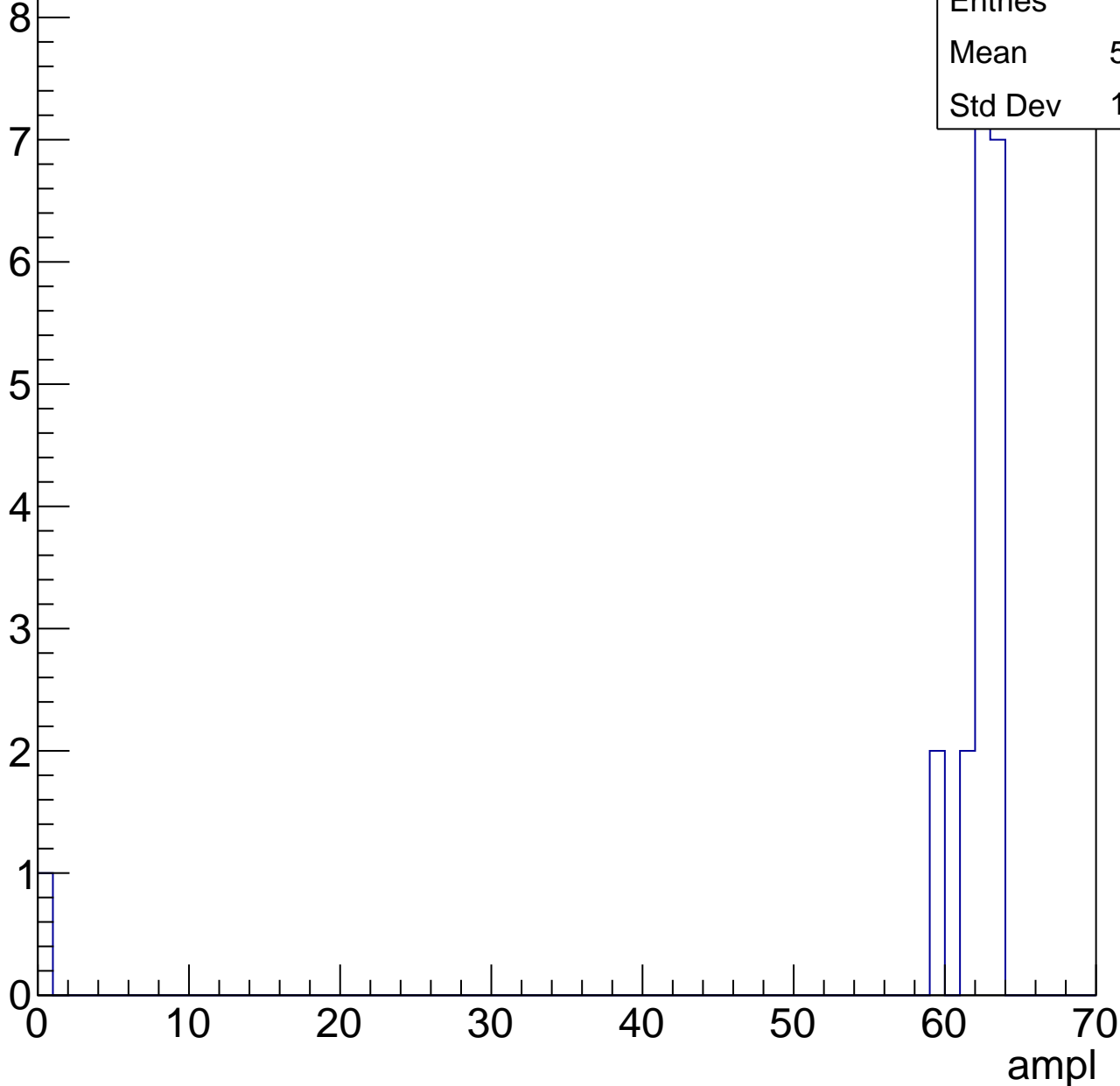


B1L103S, U1-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

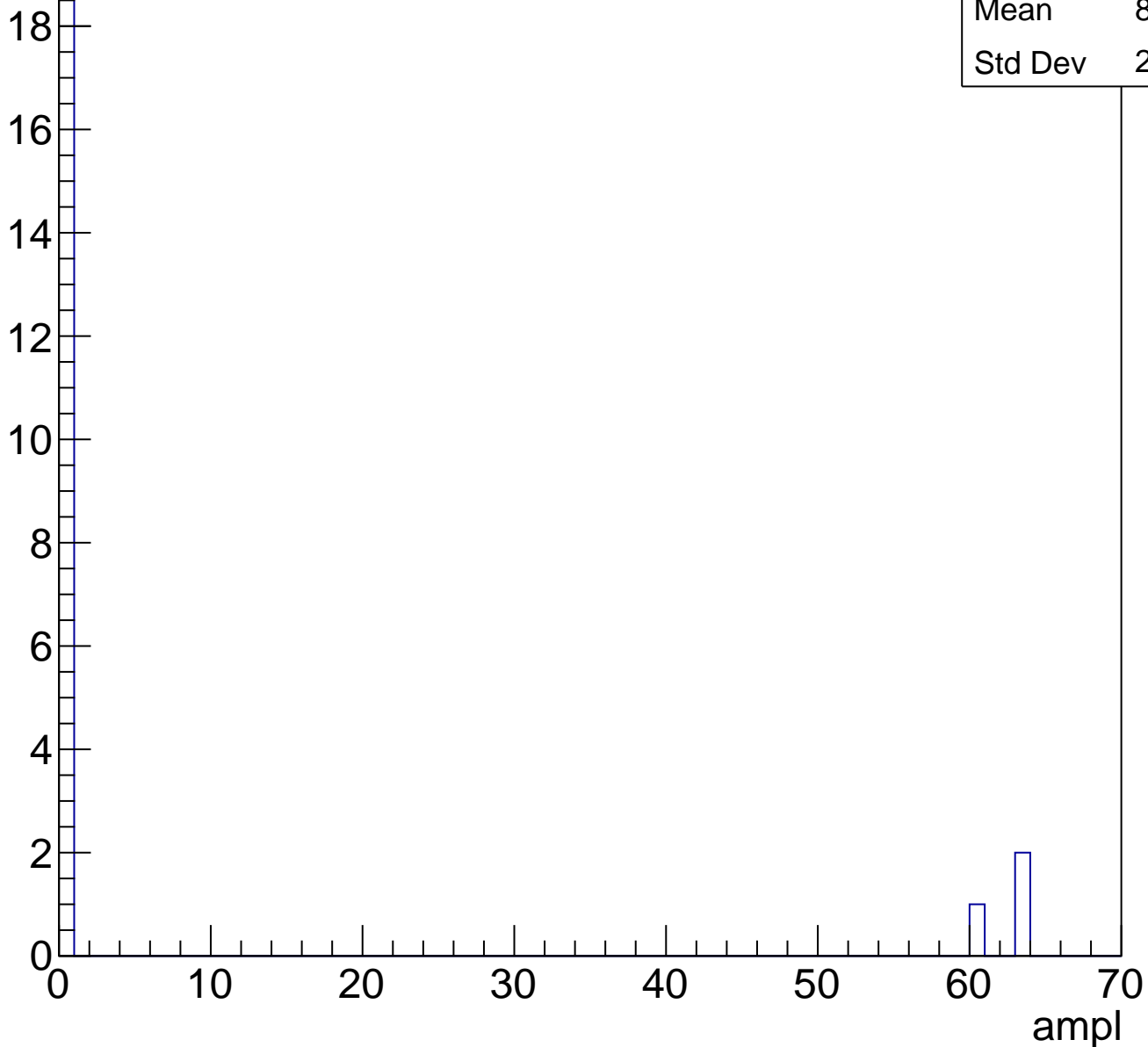
Entries	20
Mean	58.85
Std Dev	13.55



B1L103S, U1-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.455
Std Dev	21.28

B1L103S, U1-ch86, adc0

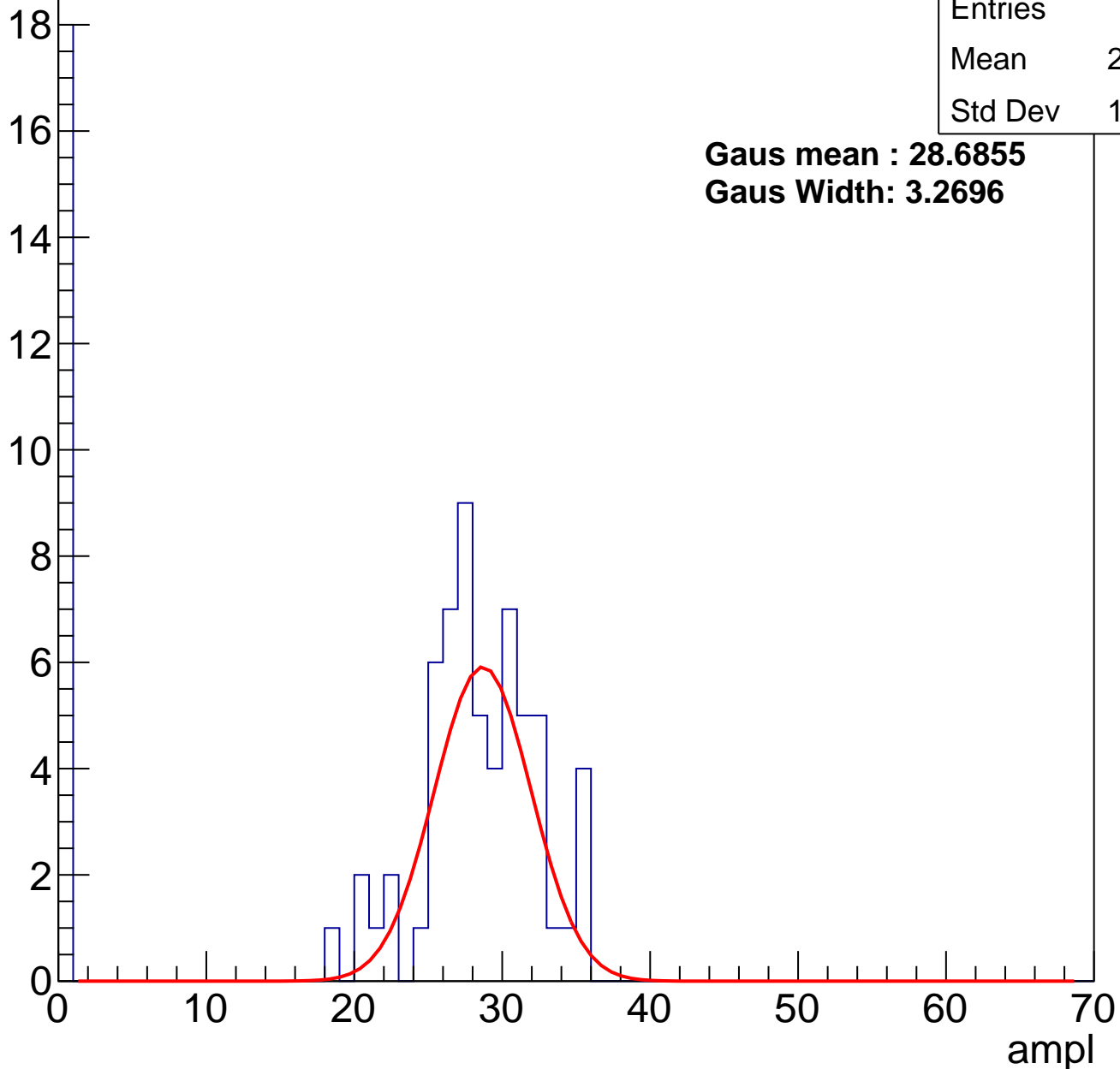
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	21.65
Std Dev	12.22

Gaus mean : 28.6855

Gaus Width: 3.2696

Entry



B1L103S, U1-ch86, adc1

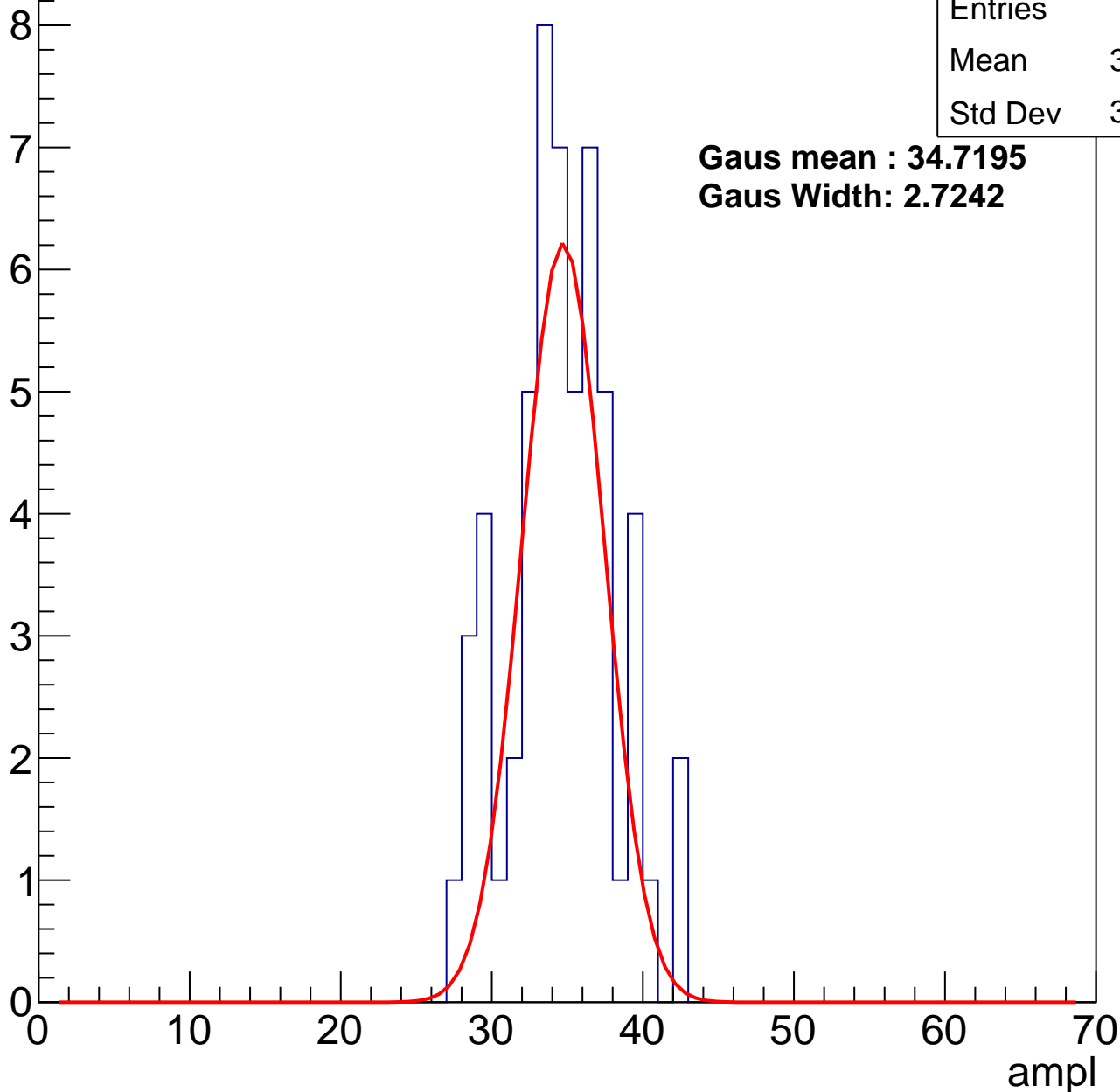
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	34.12
Std Dev	3.475

Gaus mean : 34.7195

Gaus Width: 2.7242



B1L103S, U1-ch86, adc2

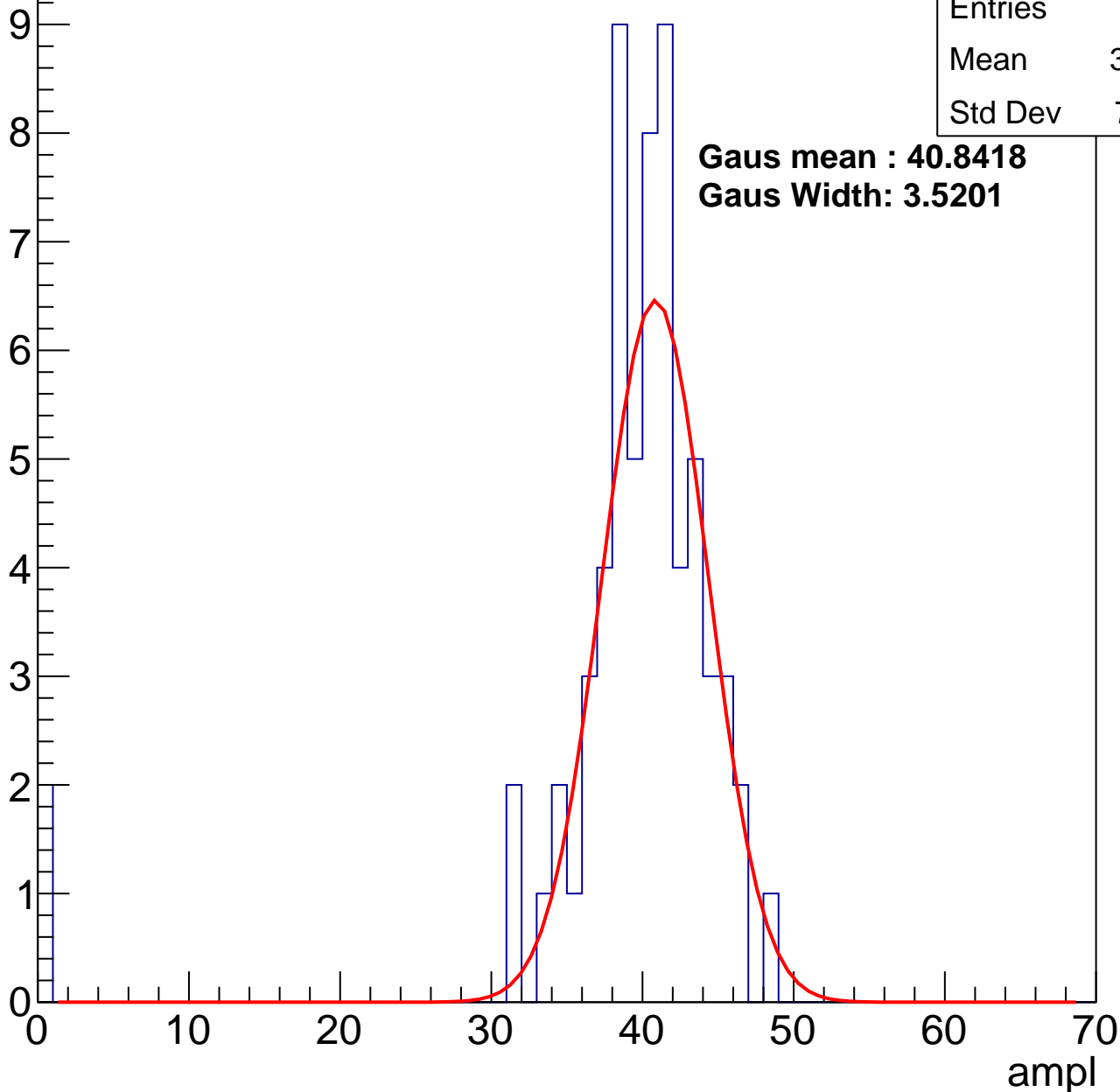
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.59
Std Dev	7.741

Gaus mean : 40.8418

Gaus Width: 3.5201



B1L103S, U1-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	37.53
Std Dev	18.58

Entry

12

10

8

6

4

2

0

0

10

20

30

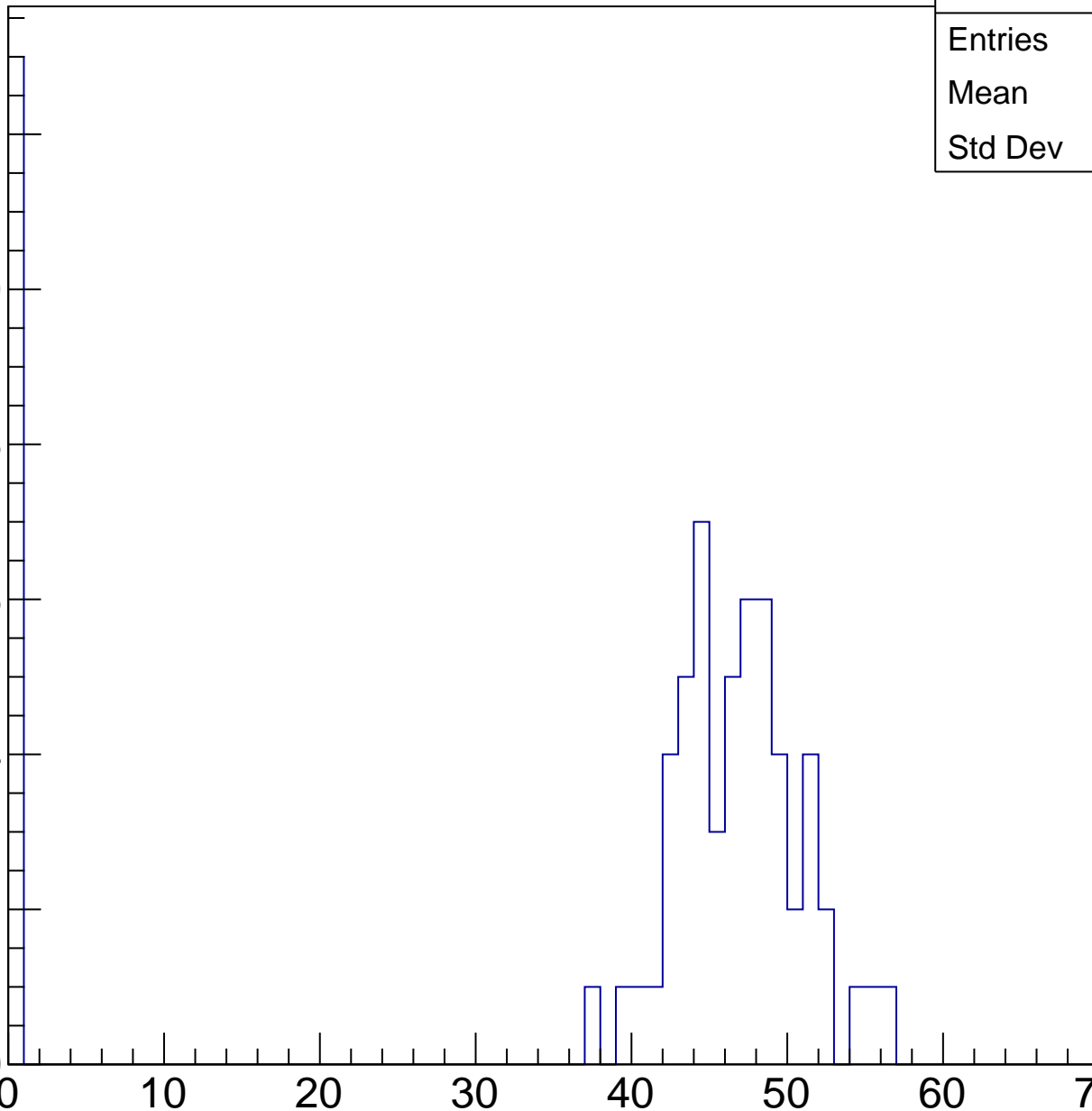
40

50

60

70

ampl

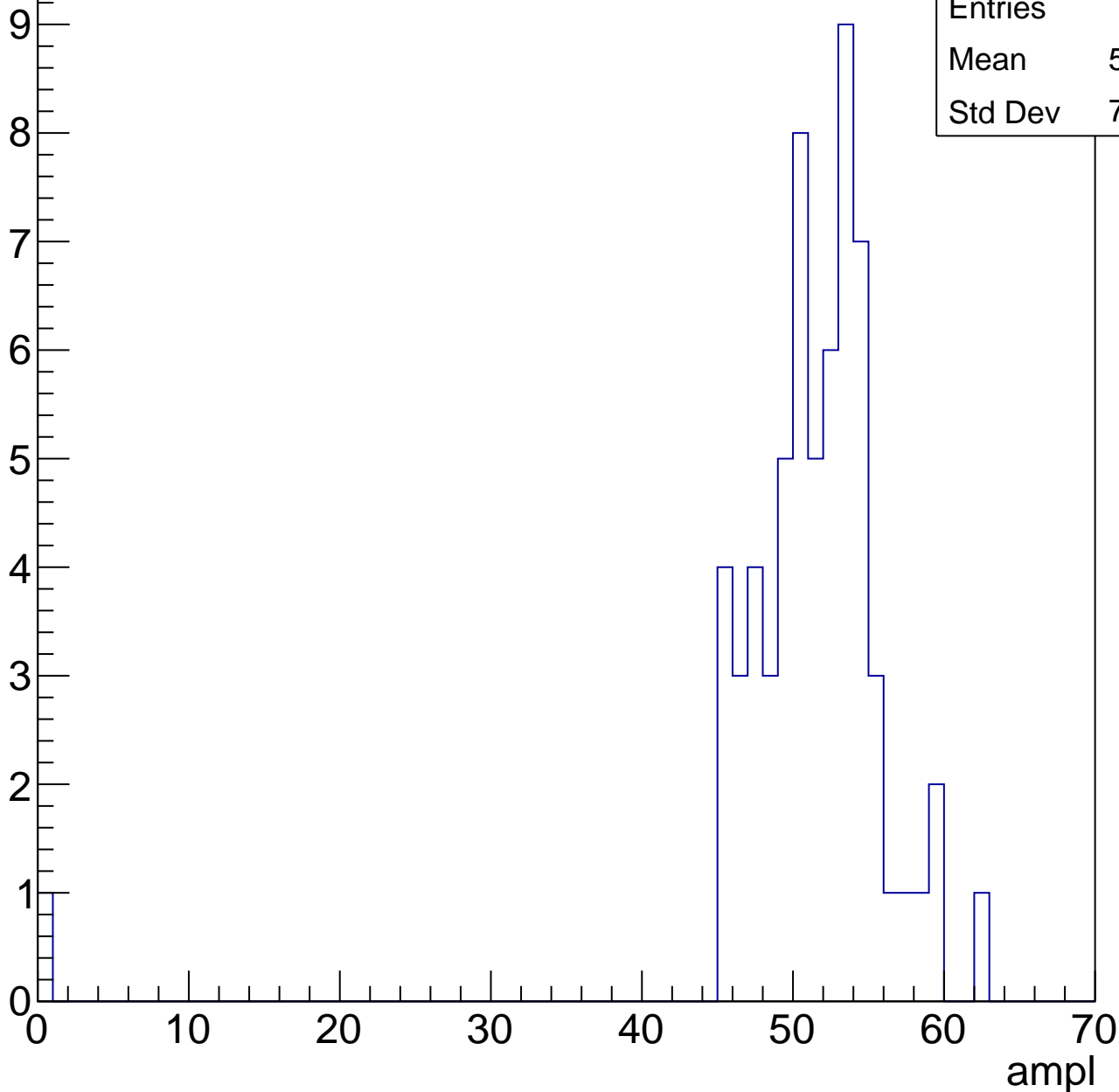


B1L103S, U1-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

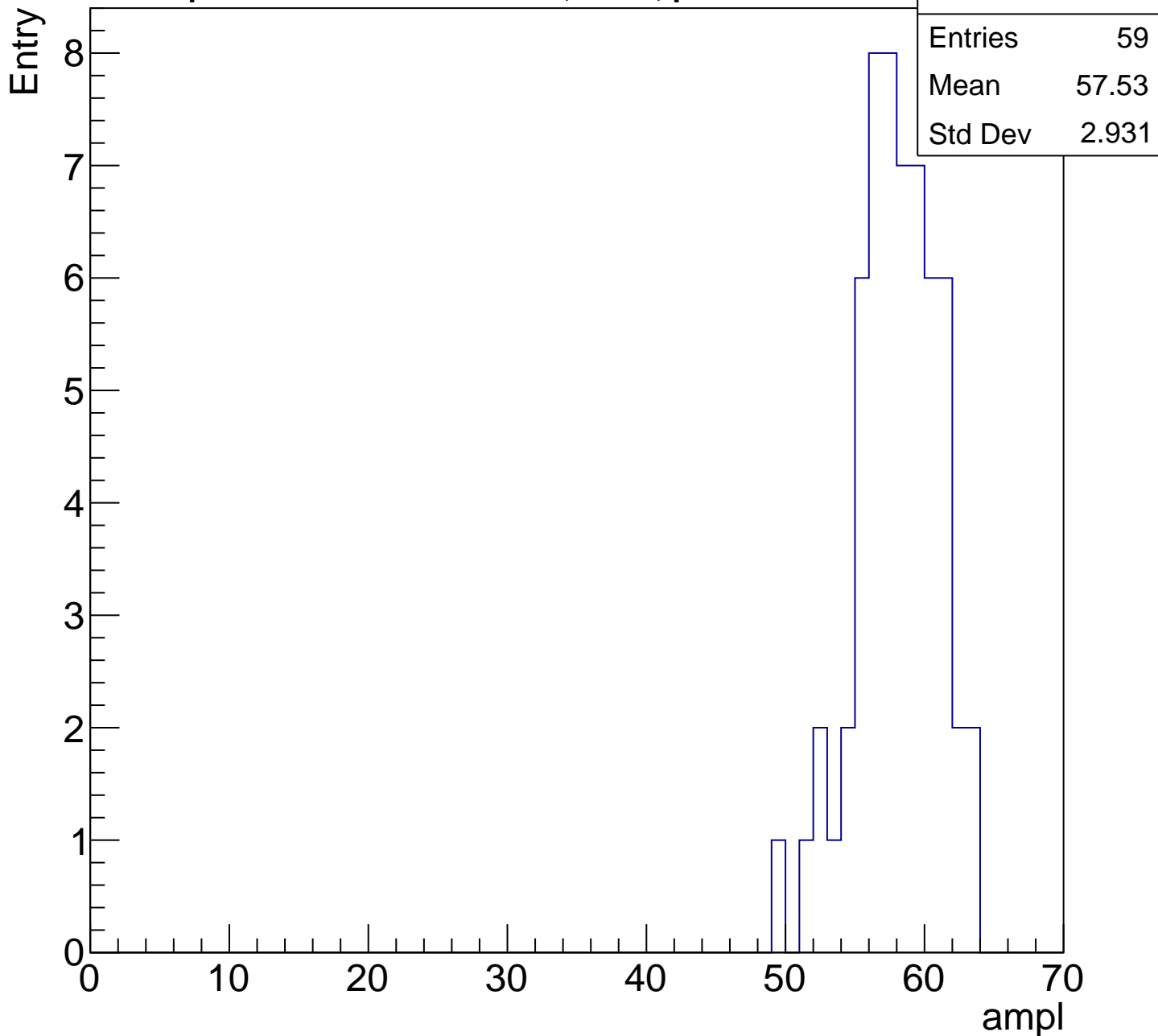
Entry

Entries	64
Mean	50.52
Std Dev	7.327



B1L103S, U1-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

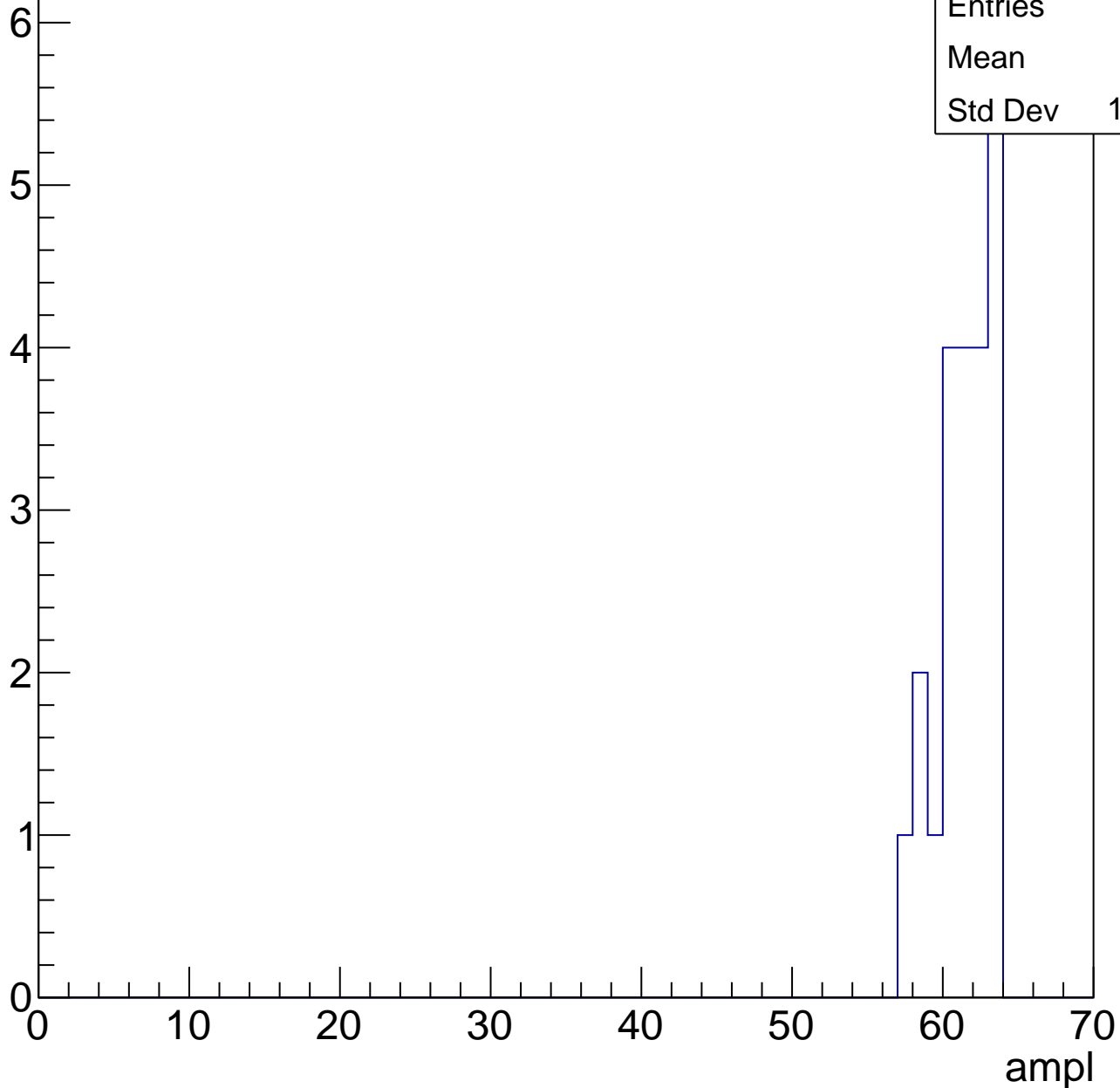


B1L103S, U1-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

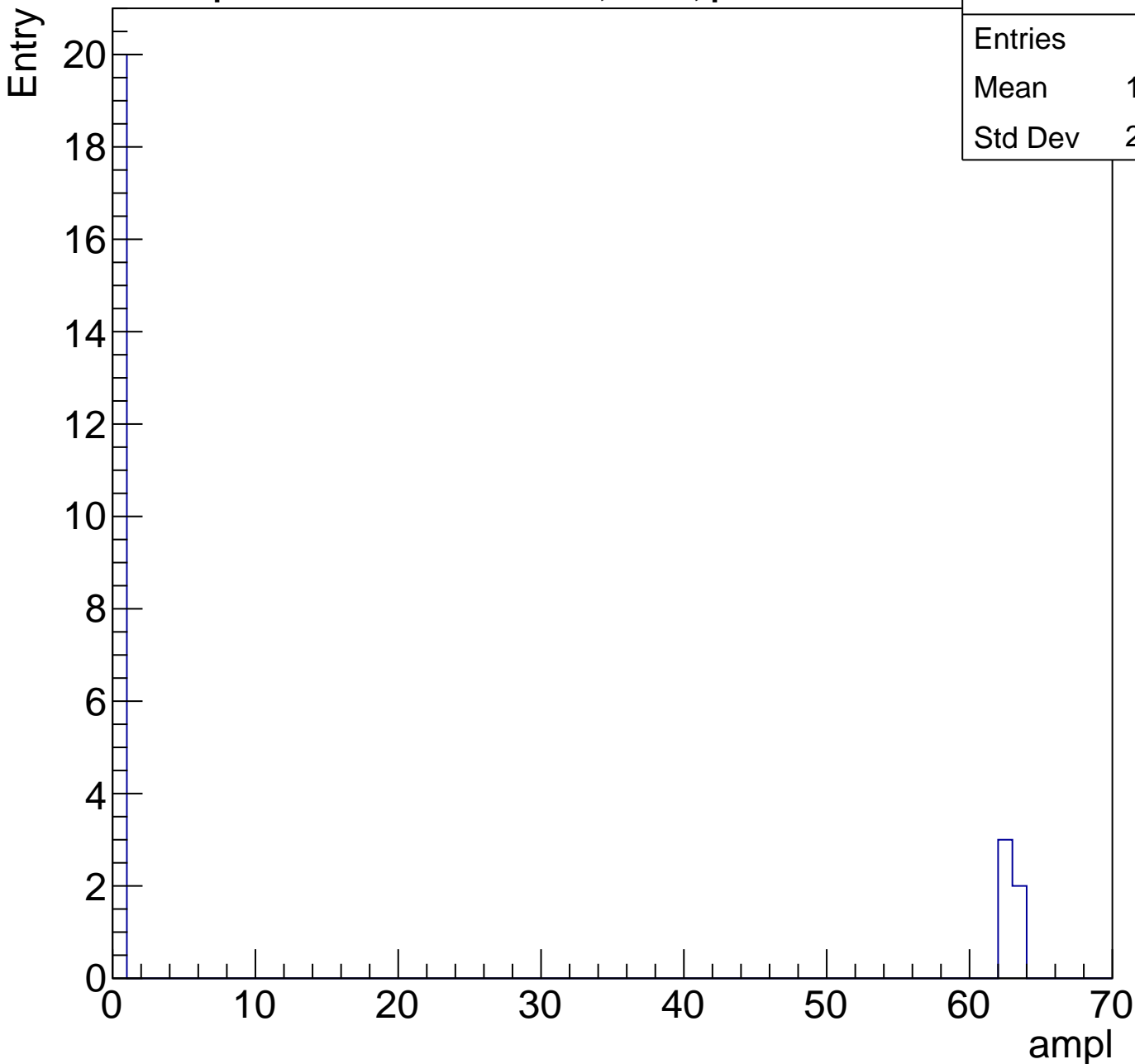
Entries	22
Mean	61
Std Dev	1.784



B1L103S, U1-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	25
Mean	12.48
Std Dev	24.96



B1L103S, U1-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	20.95
Std Dev	12.85

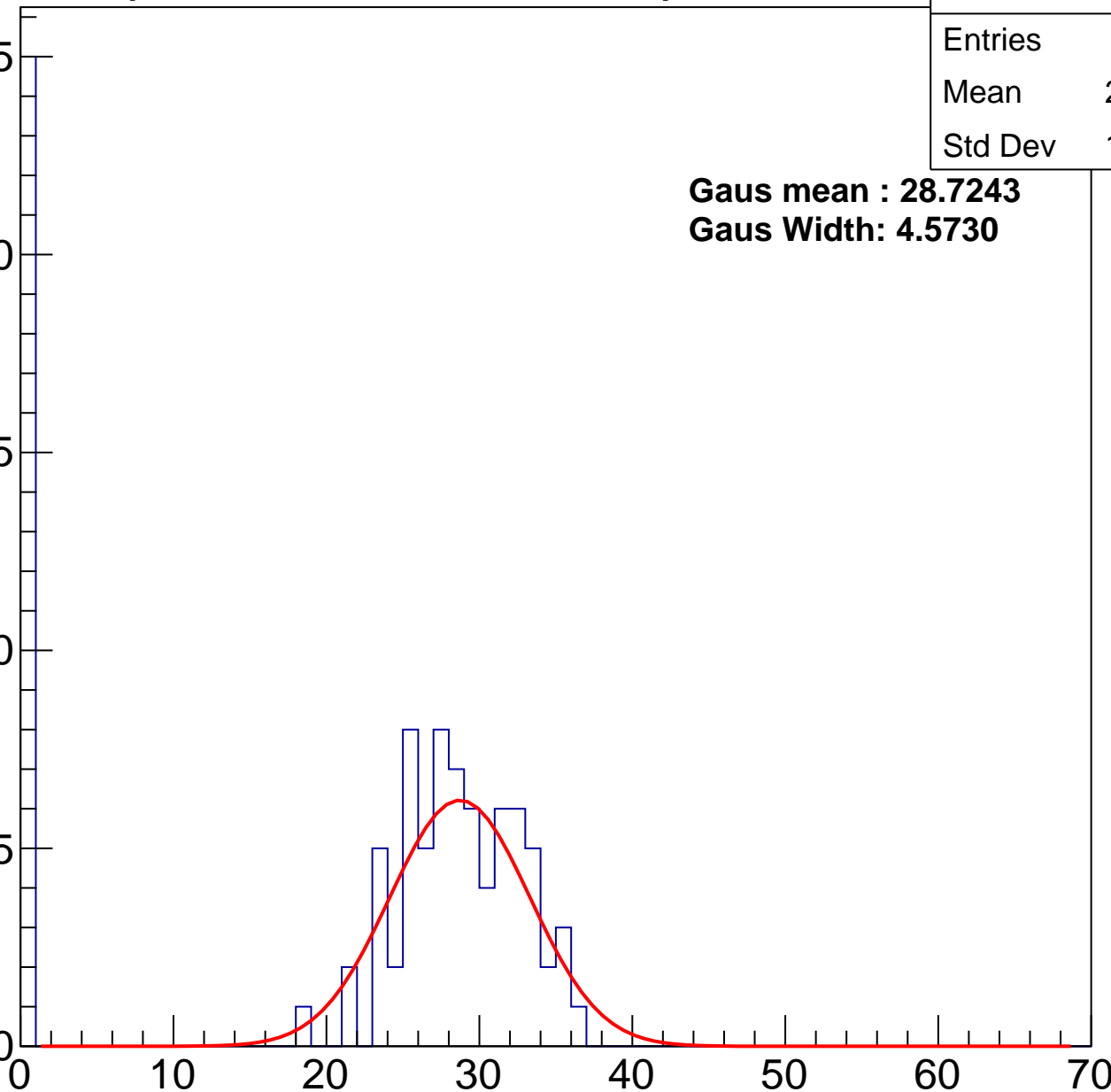
Gaus mean : 28.7243

Gaus Width: 4.5730

Entry

25
20
15
10
5
0

ampl



B1L103S, U1-ch87, adc1

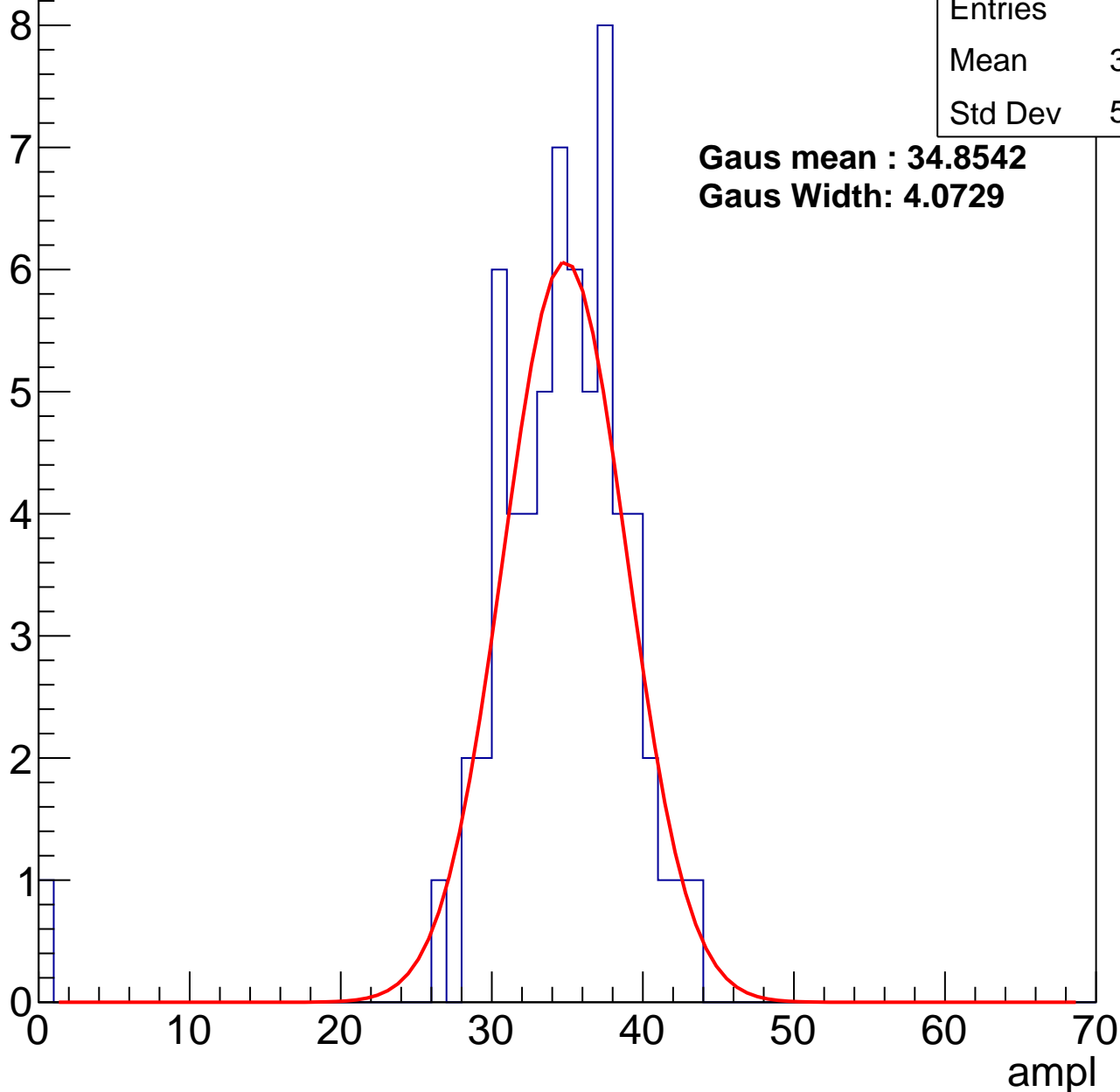
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.98
Std Dev	5.622

Gaus mean : 34.8542

Gaus Width: 4.0729



B1L103S, U1-ch87, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	35.77
Std Dev	14.62

Gaus mean : 41.8646

Gaus Width: 3.9705

10

8

6

4

2

0

0

10

20

30

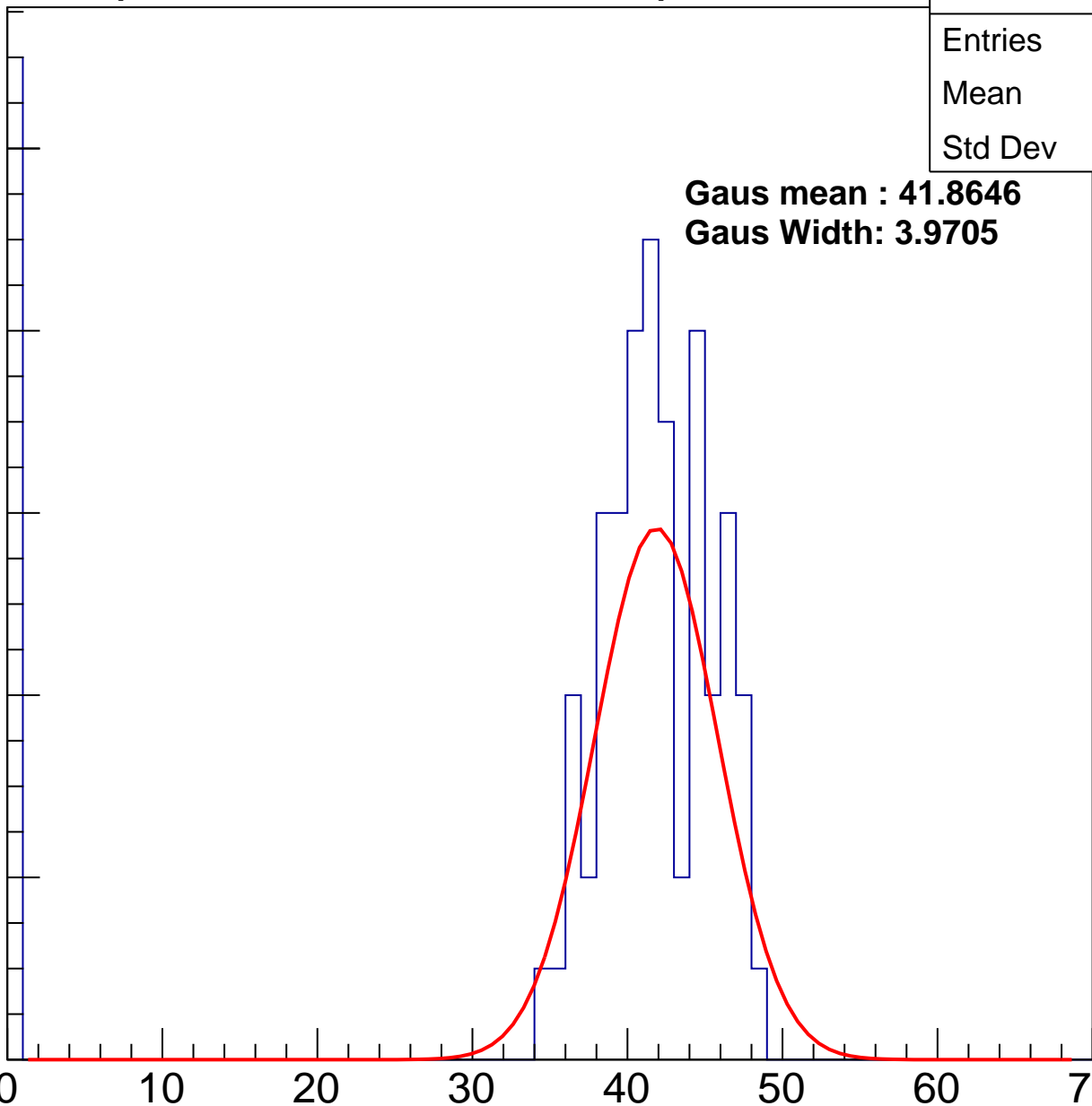
40

50

60

70

ampl

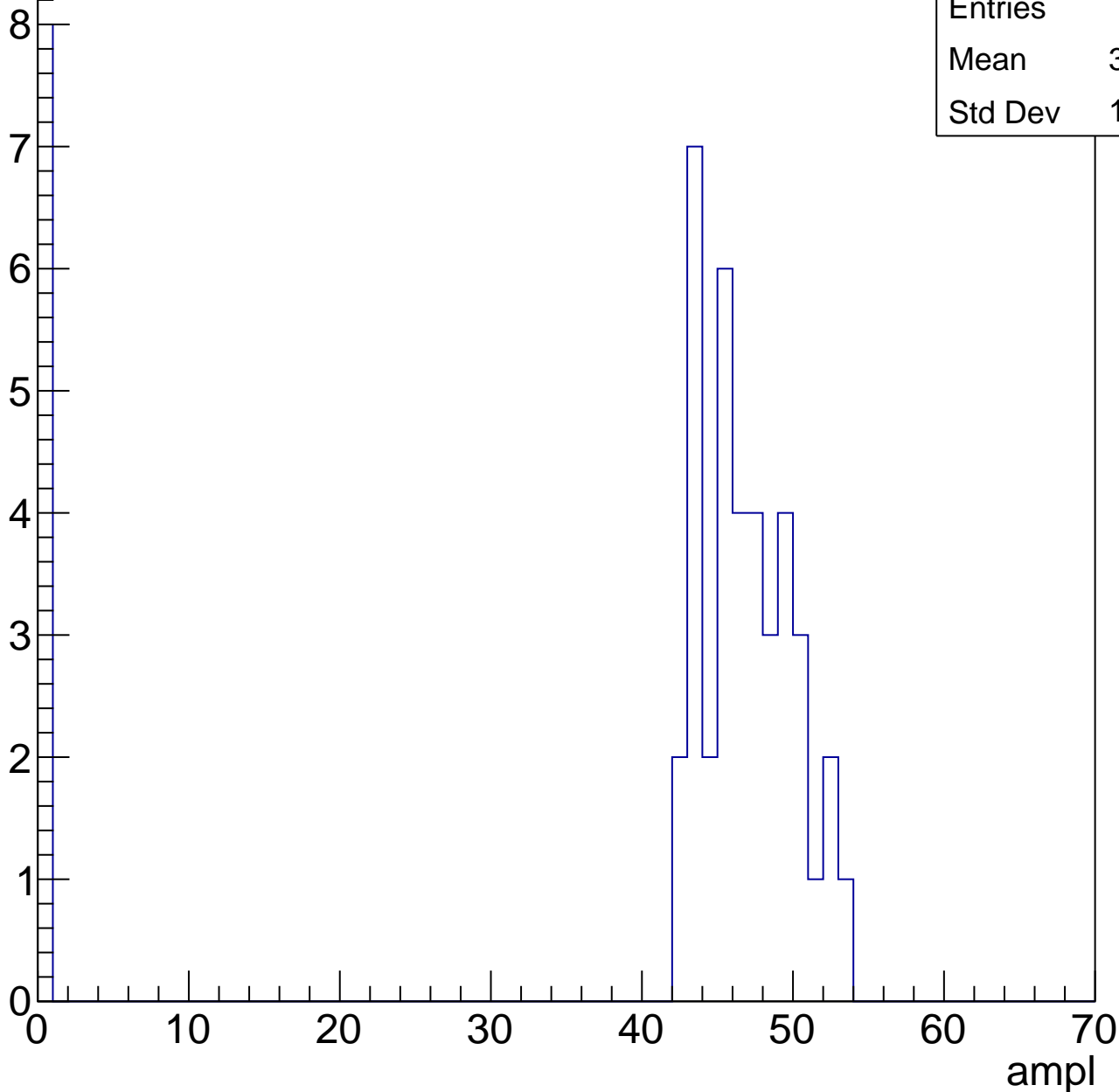


B1L103S, U1-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	38.57
Std Dev	17.68

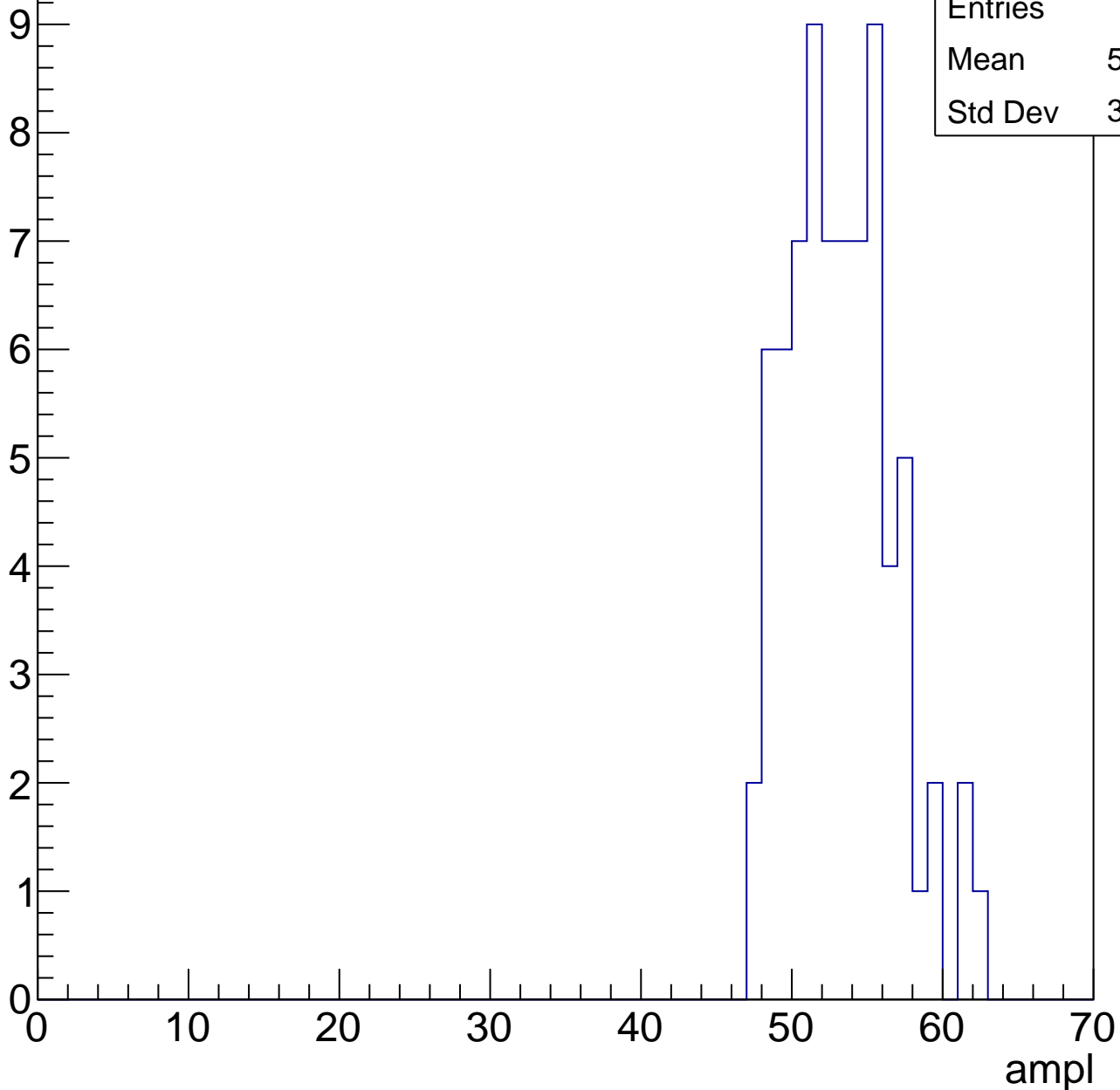


B1L103S, U1-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	52.83
Std Dev	3.439

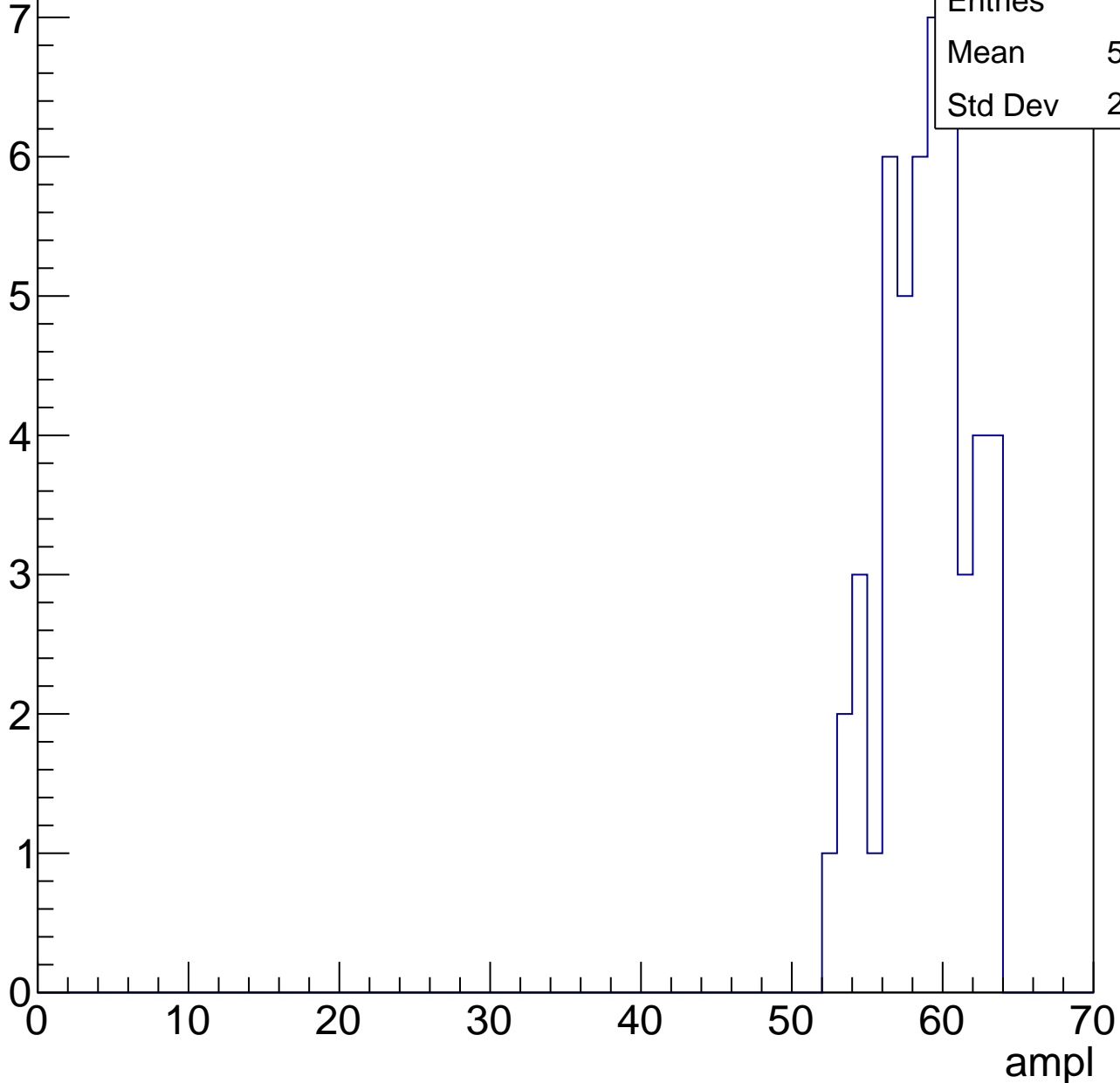


B1L103S, U1-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.37
Std Dev	2.826

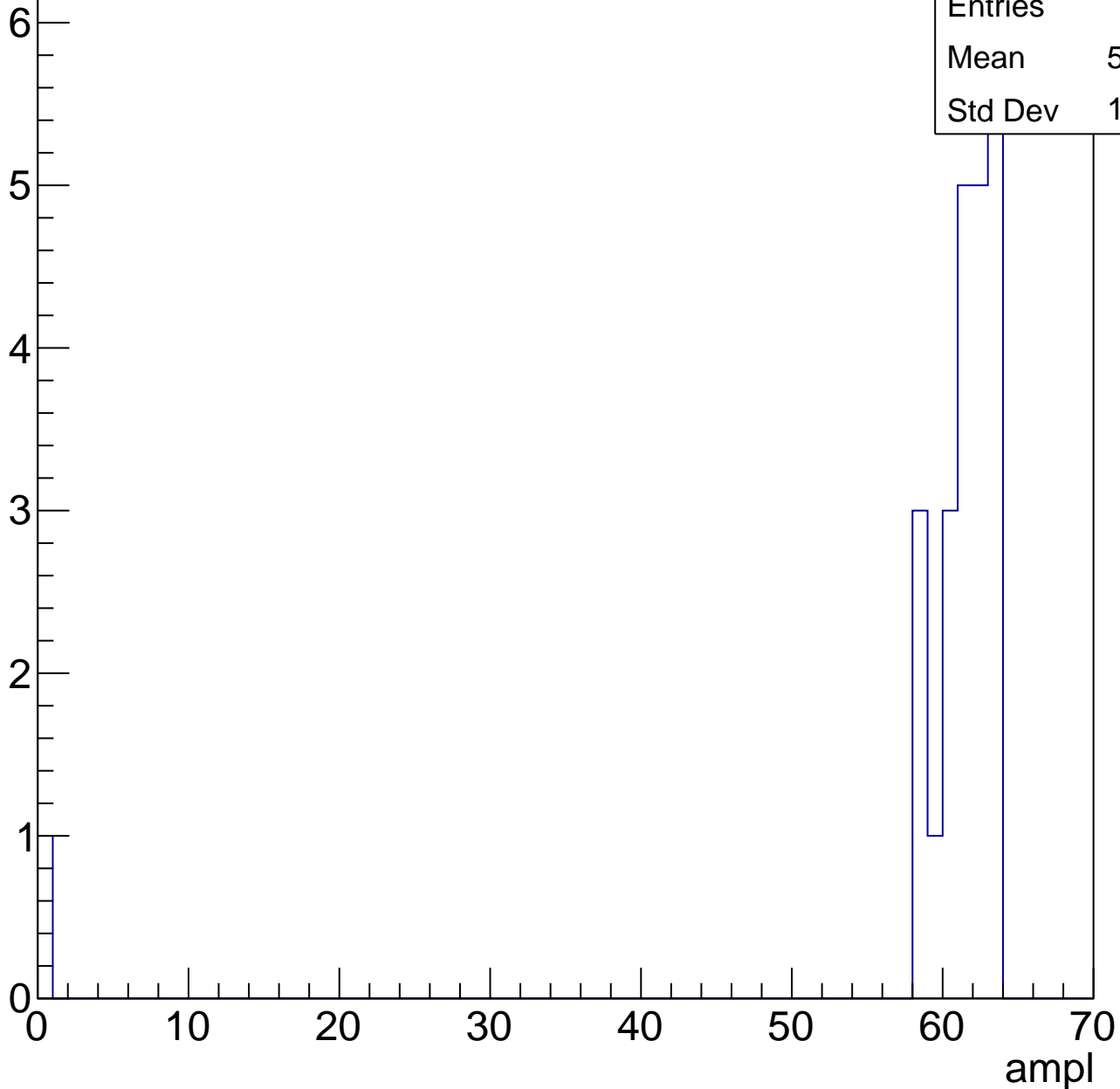


B1L103S, U1-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.58
Std Dev	12.32



B1L103S, U1-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

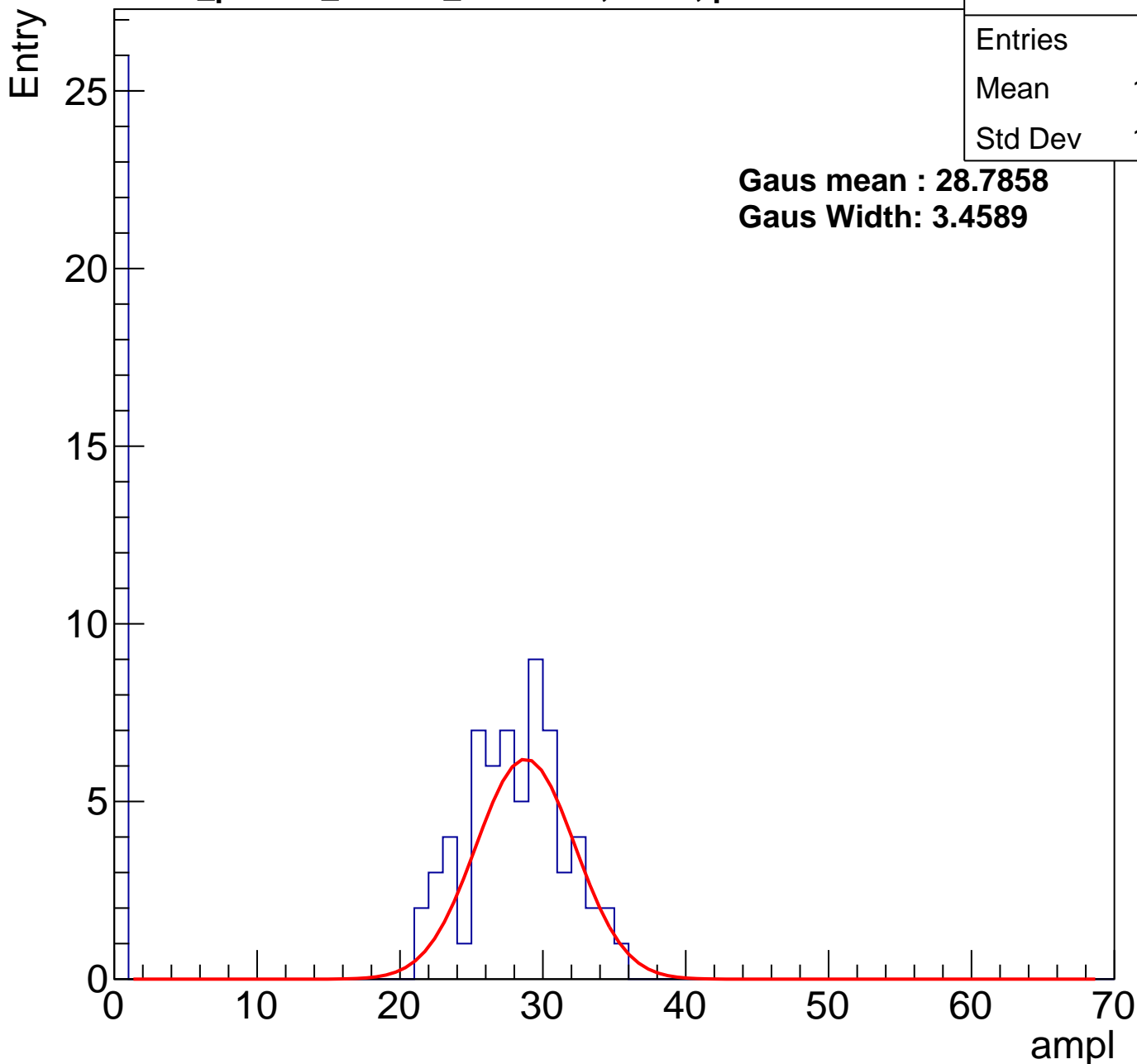
B1L103S, U1-ch88, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	19.61
Std Dev	12.91

Gaus mean : 28.7858

Gaus Width: 3.4589



B1L103S, U1-ch88, adc1

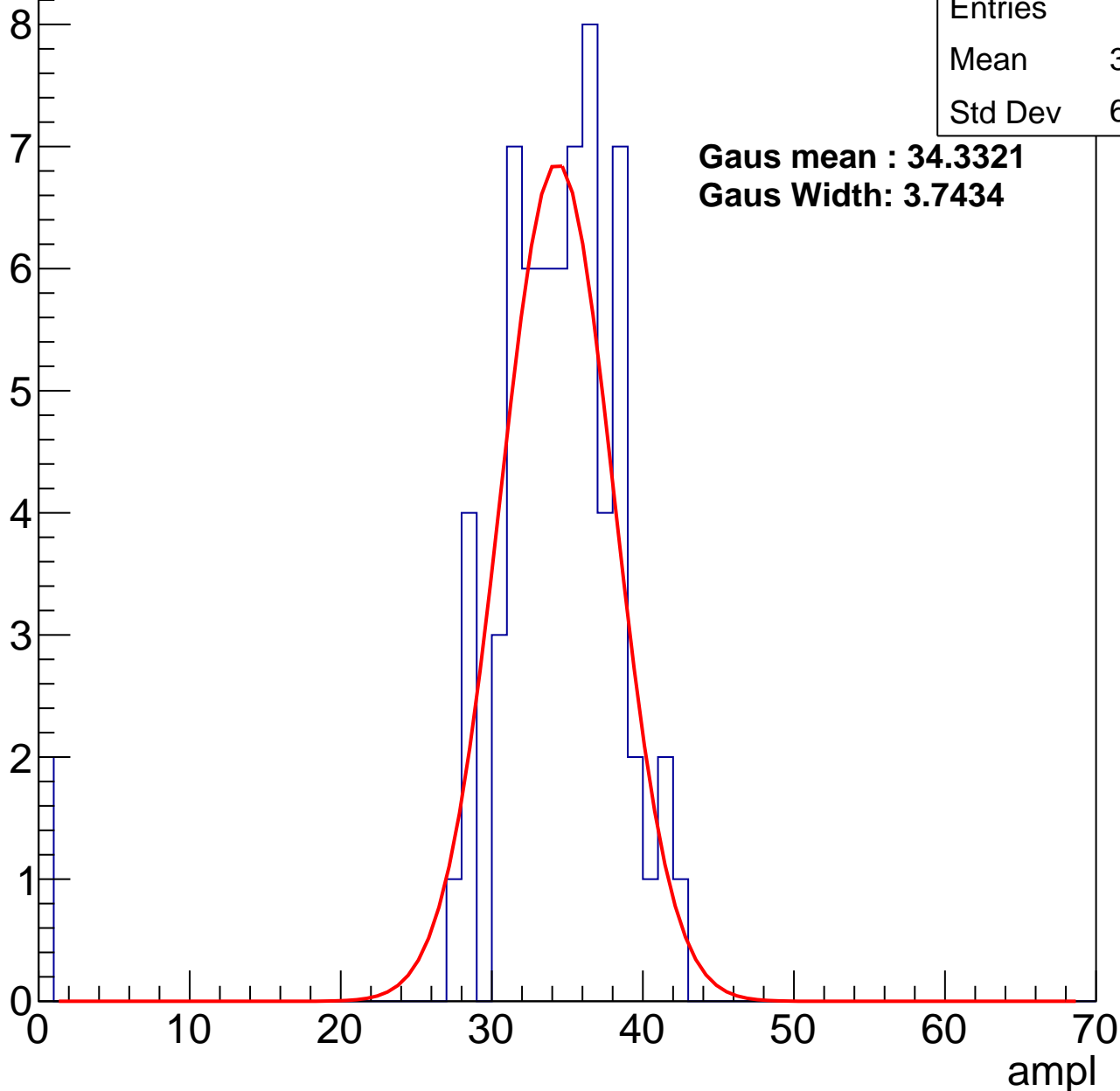
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.27
Std Dev	6.742

Gaus mean : 34.3321

Gaus Width: 3.7434



B1L103S, U1-ch88, adc2

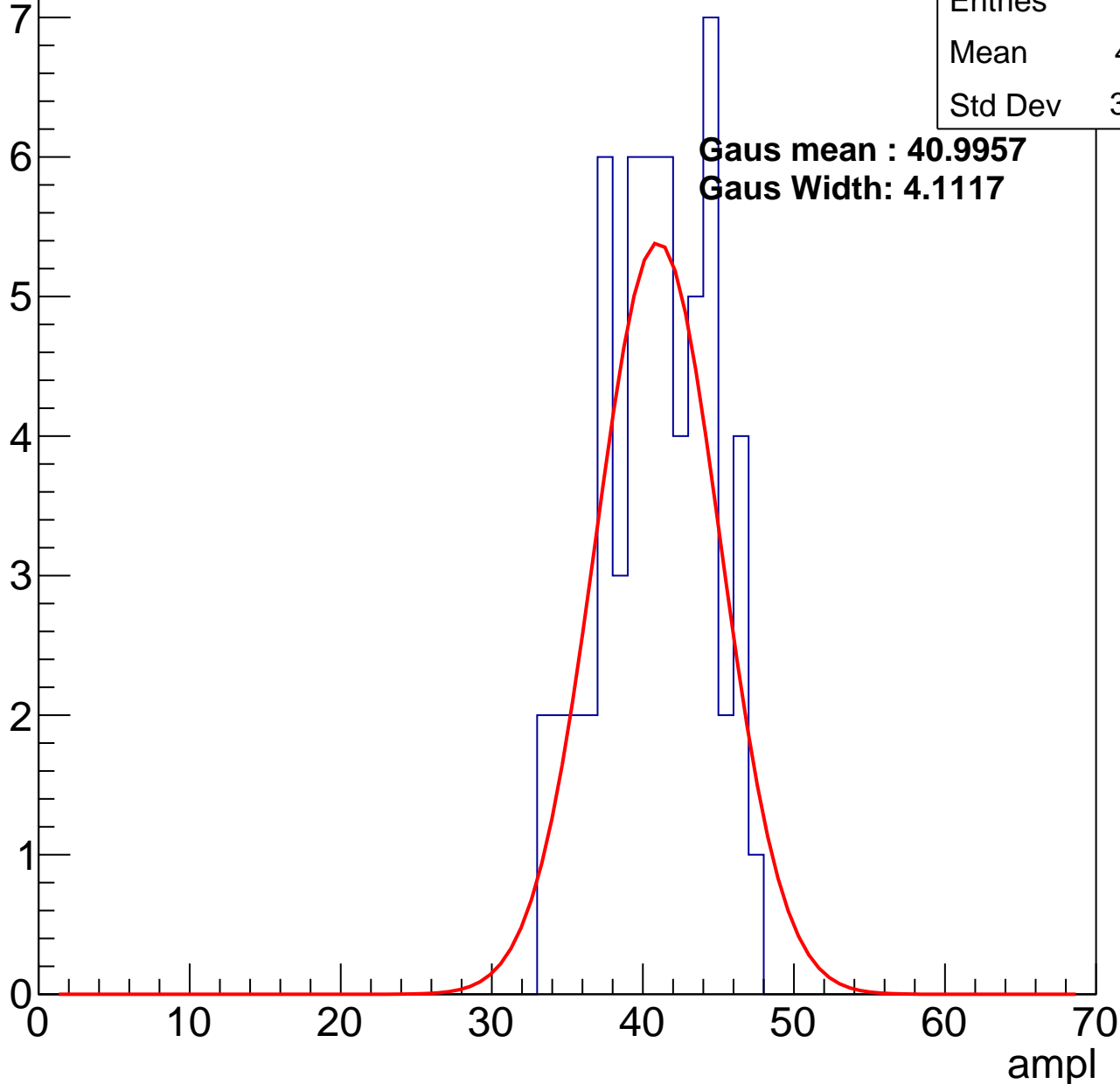
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.41
Std Dev	3.562

Gaus mean : 40.9957

Gaus Width: 4.1117

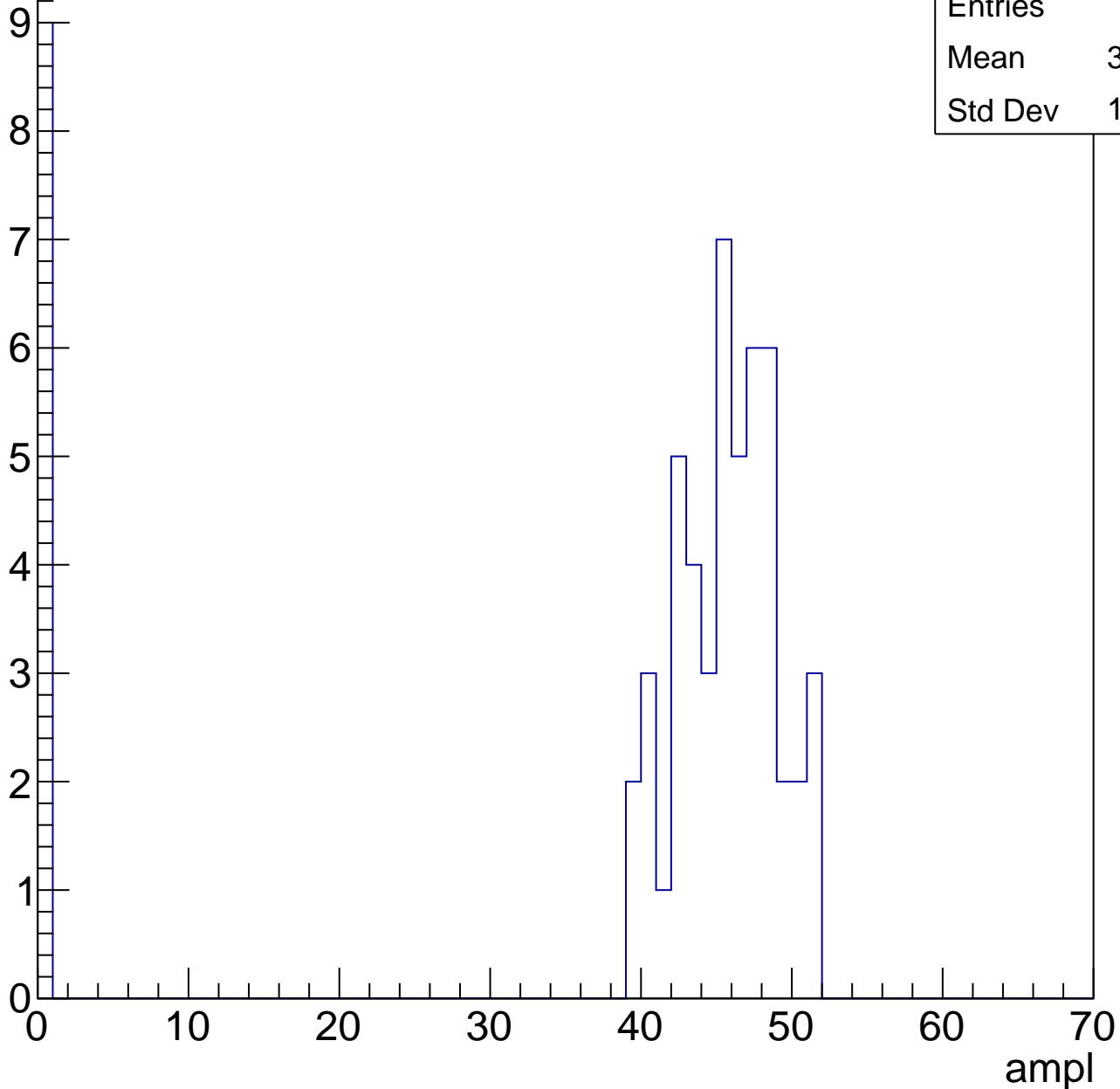


B1L103S, U1-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.26
Std Dev	16.66

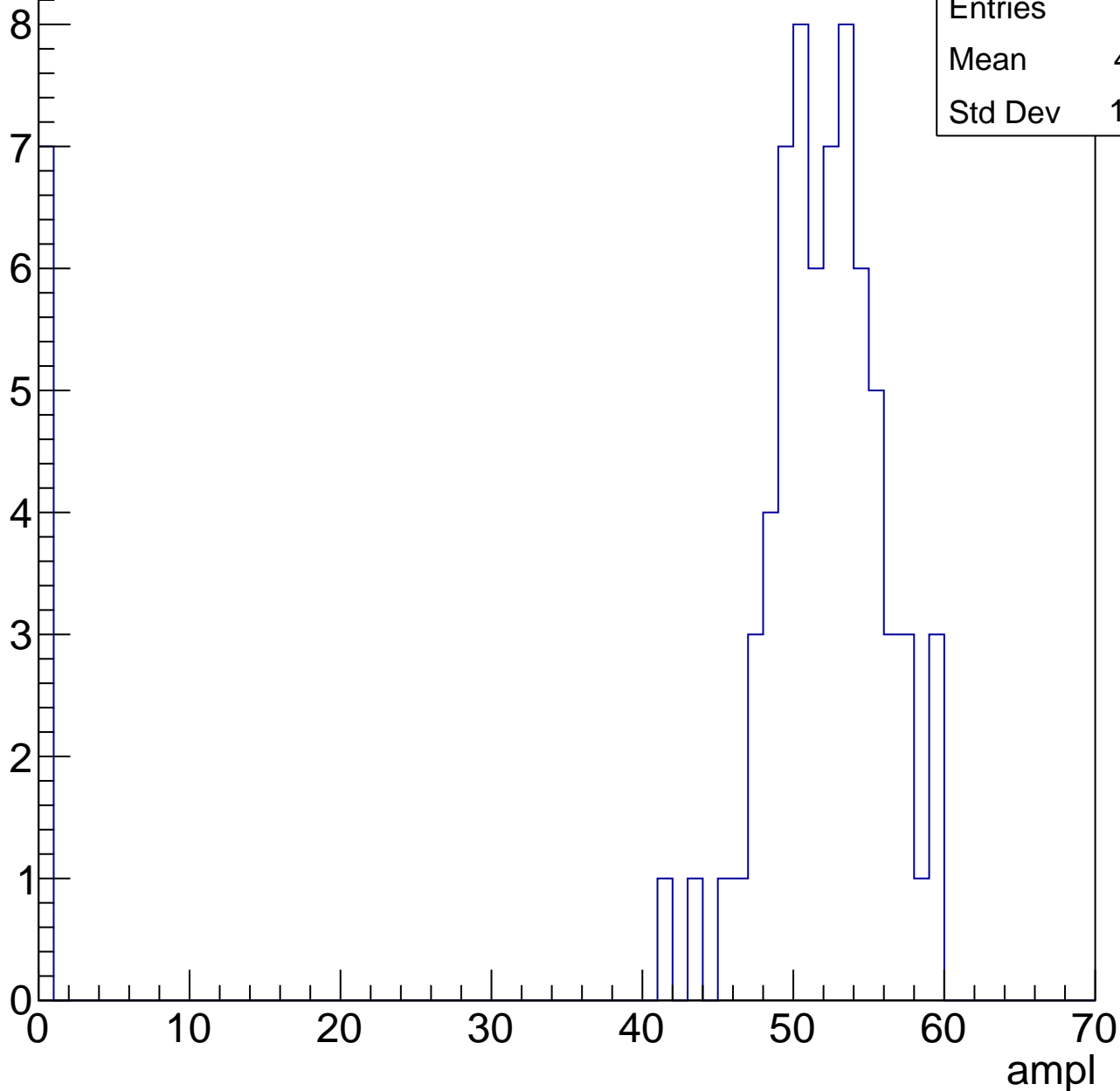


B1L103S, U1-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	46.91
Std Dev	15.45



B1L103S, U1-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

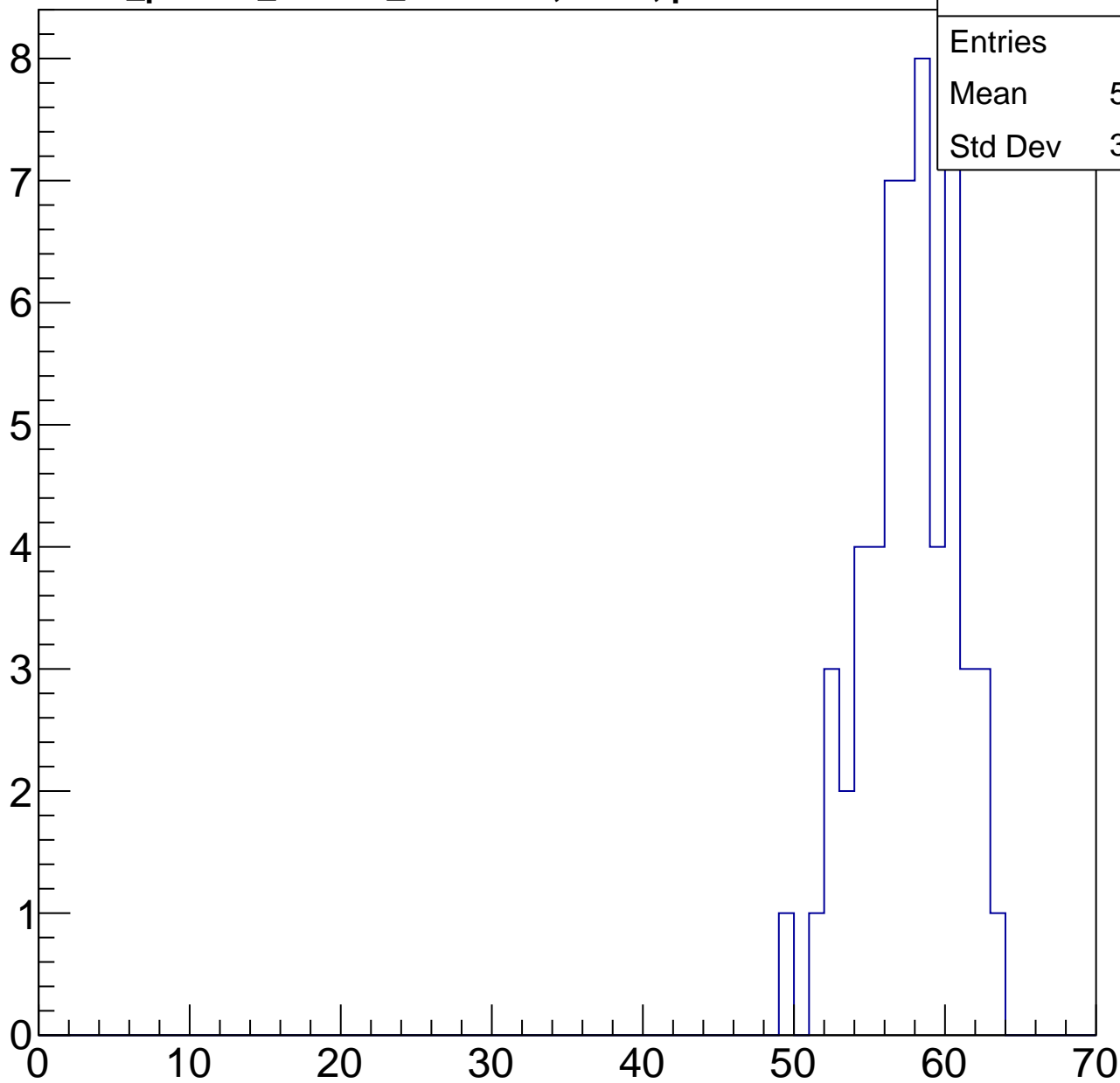
Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	57.16
Std Dev	3.046

ampl

0 10 20 30 40 50 60 70

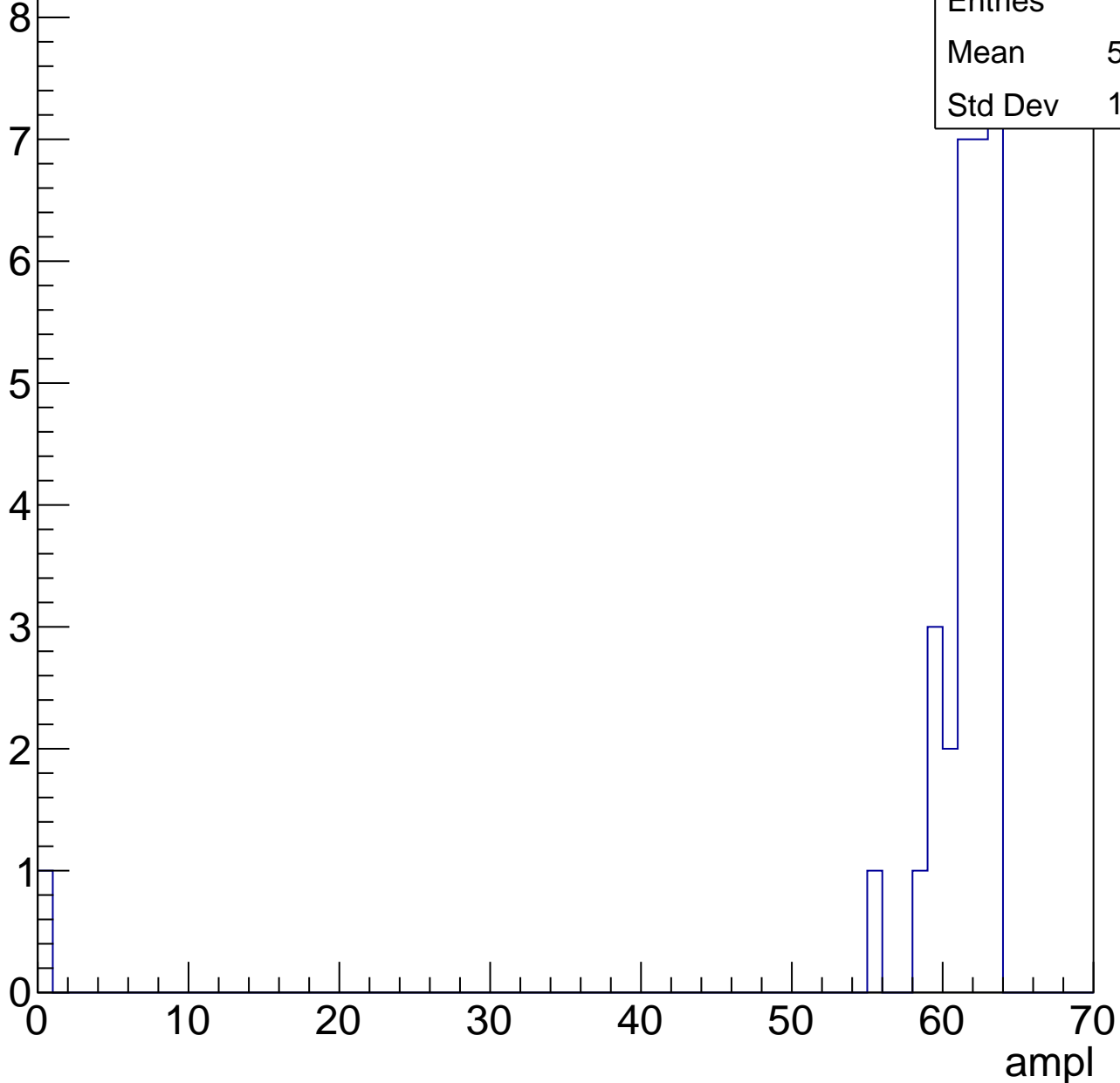


B1L103S, U1-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	59.17
Std Dev	11.13



B1L103S, U1-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



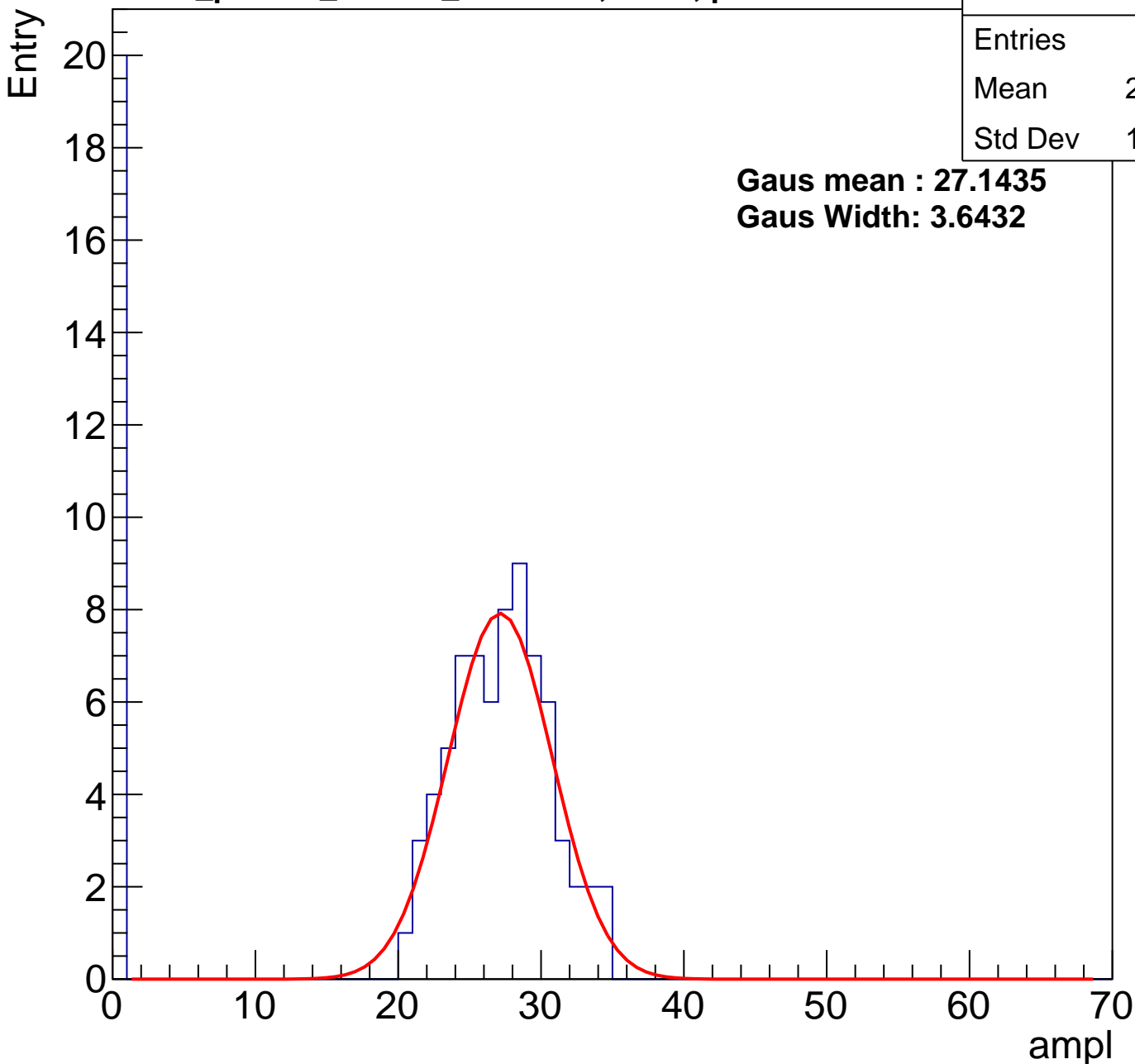
B1L103S, U1-ch89, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	20.95
Std Dev	11.42

Gaus mean : 27.1435

Gaus Width: 3.6432



B1L103S, U1-ch89, adc1

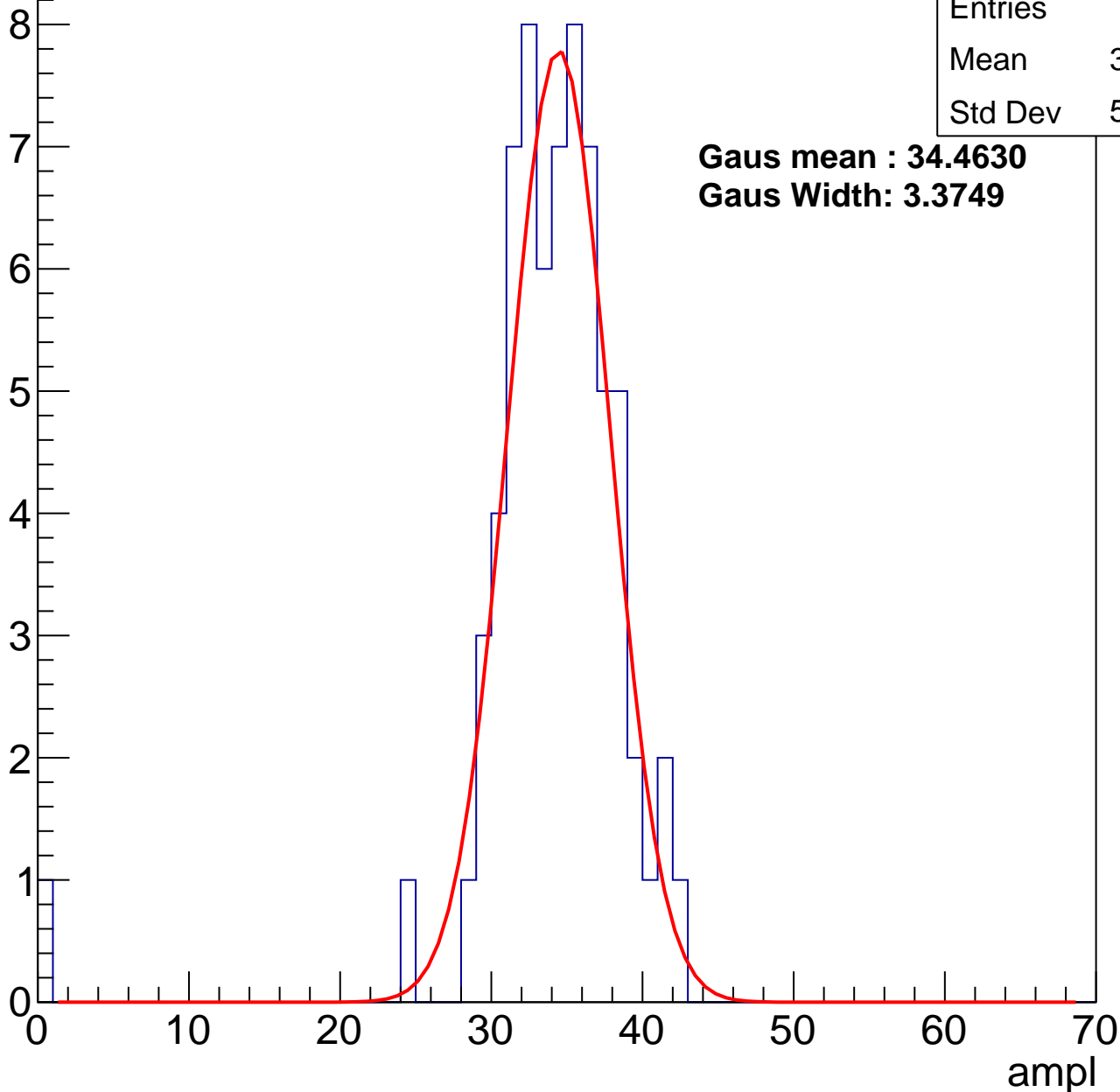
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.58
Std Dev	5.287

Gaus mean : 34.4630

Gaus Width: 3.3749



B1L103S, U1-ch89, adc2

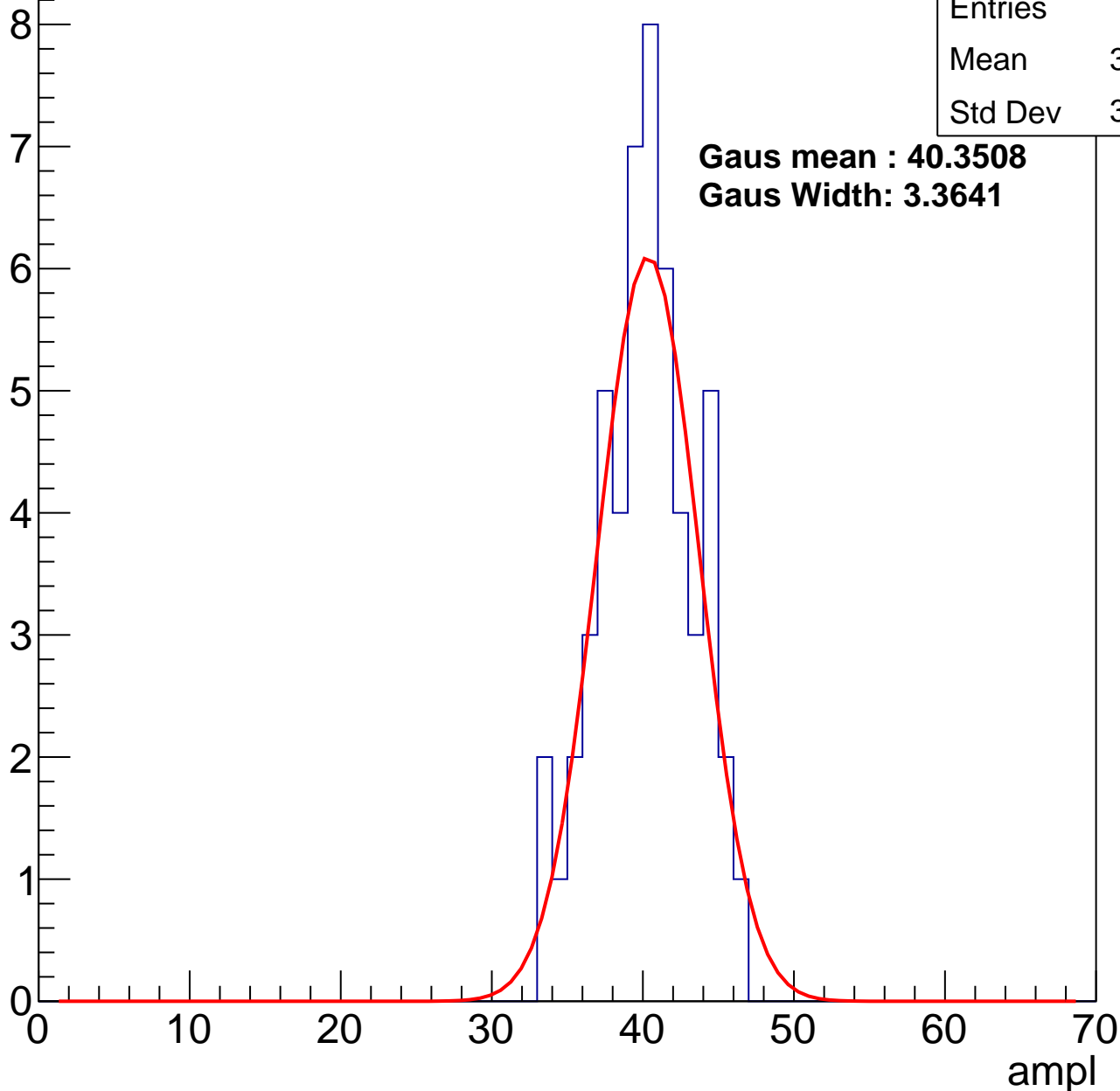
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	39.75
Std Dev	3.108

Gaus mean : 40.3508

Gaus Width: 3.3641

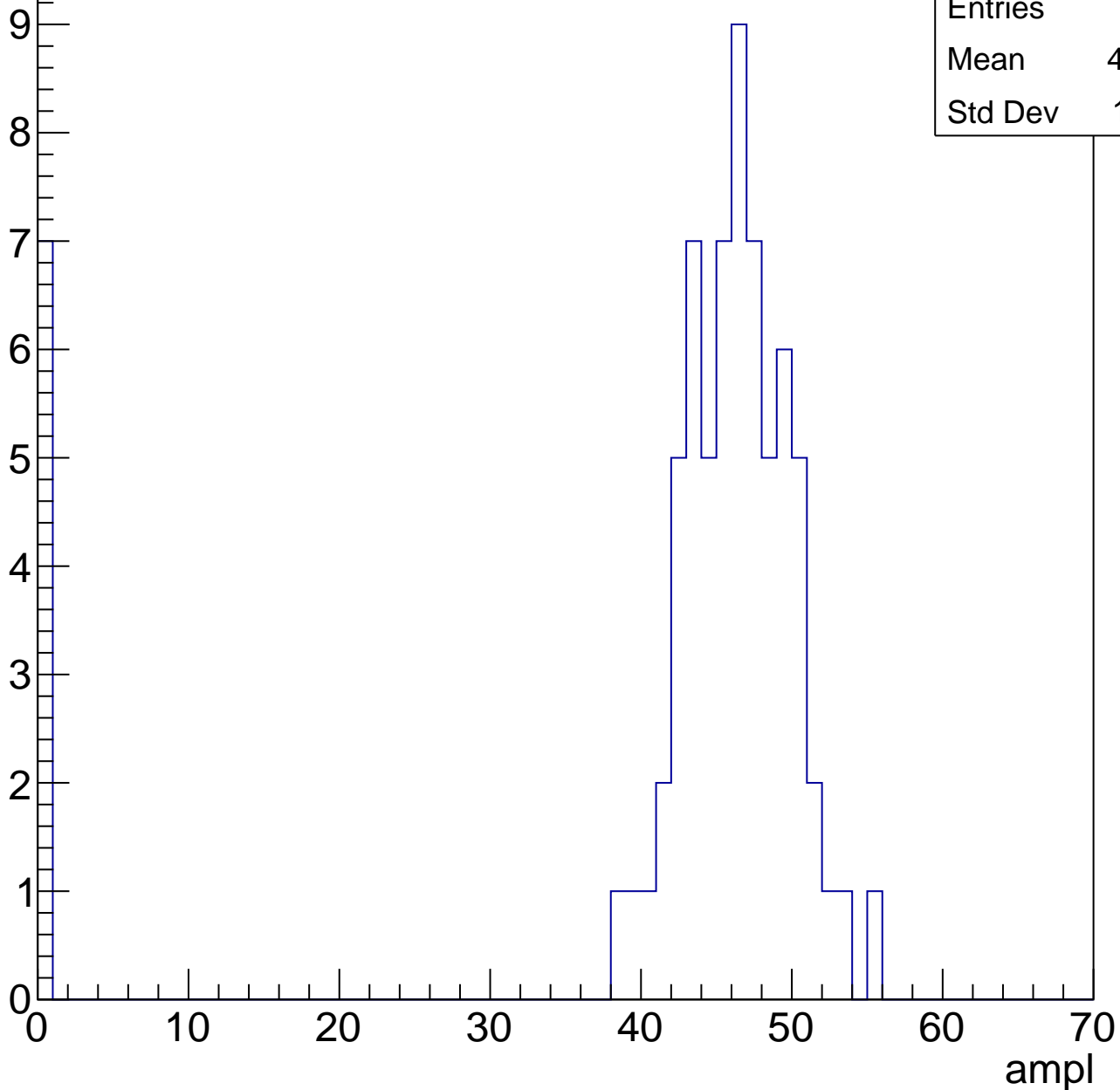


B1L103S, U1-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	41.56
Std Dev	13.91

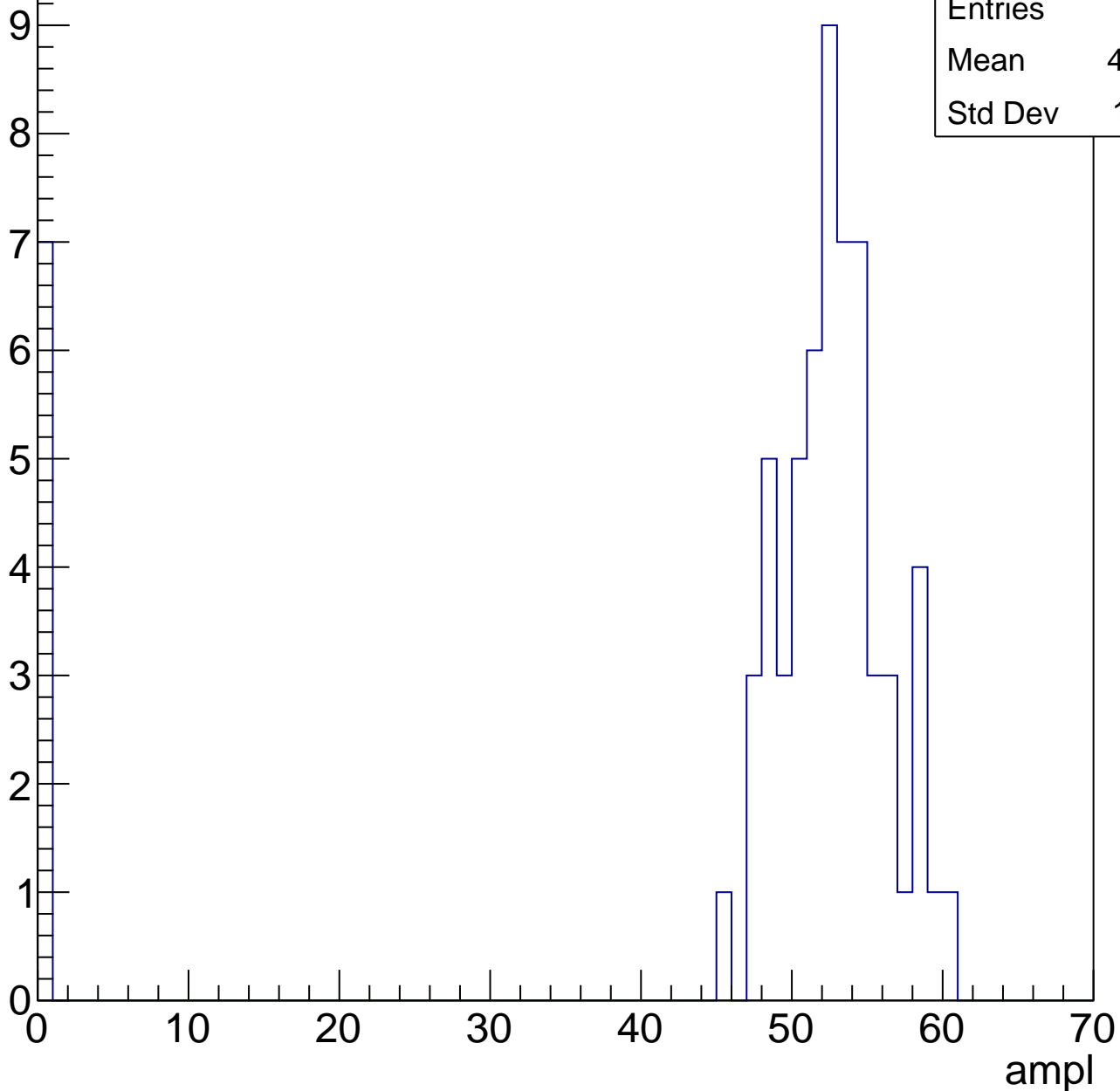


B1L103S, U1-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

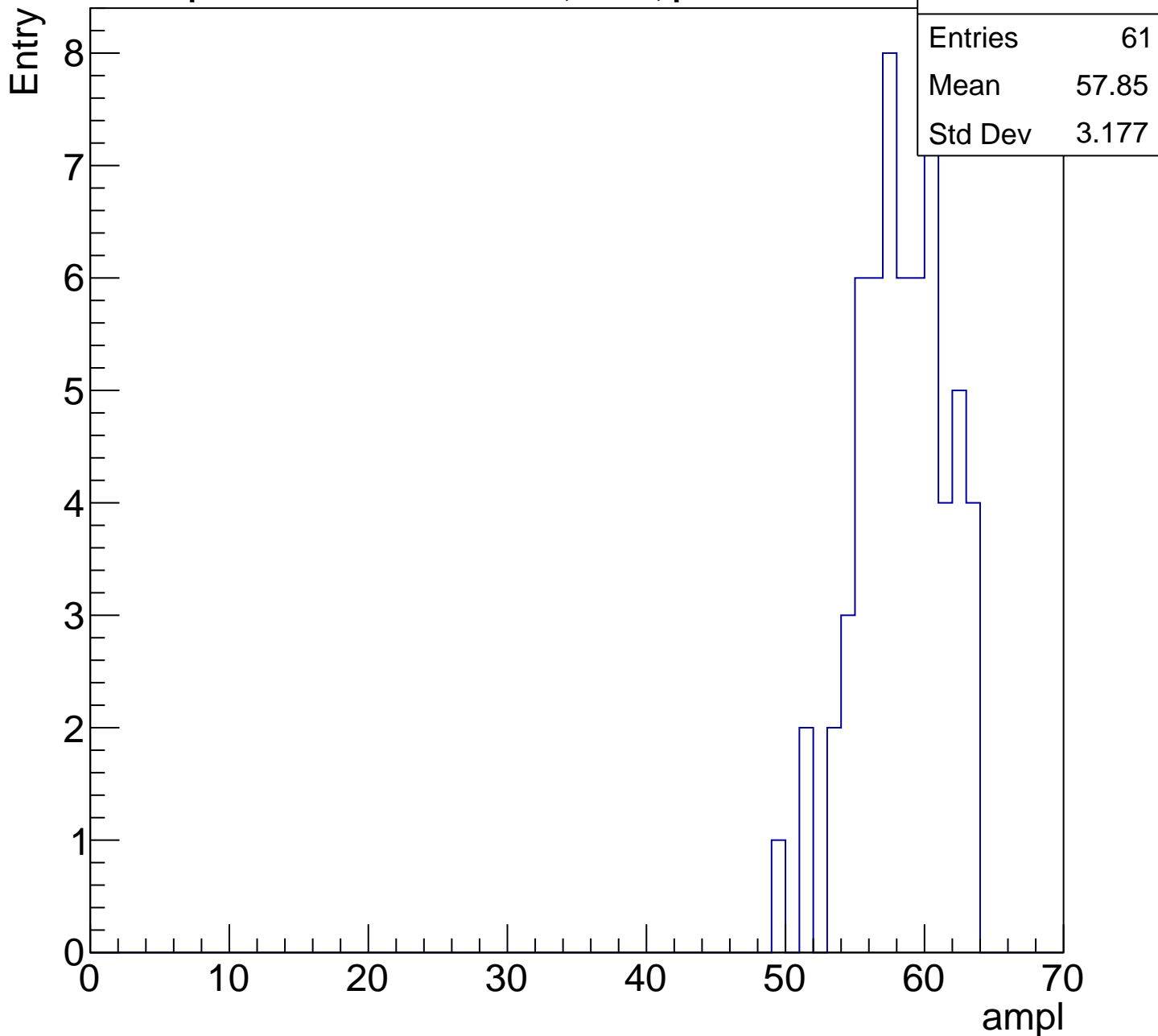
Entry

Entries	66
Mean	46.77
Std Dev	16.41



B1L103S, U1-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

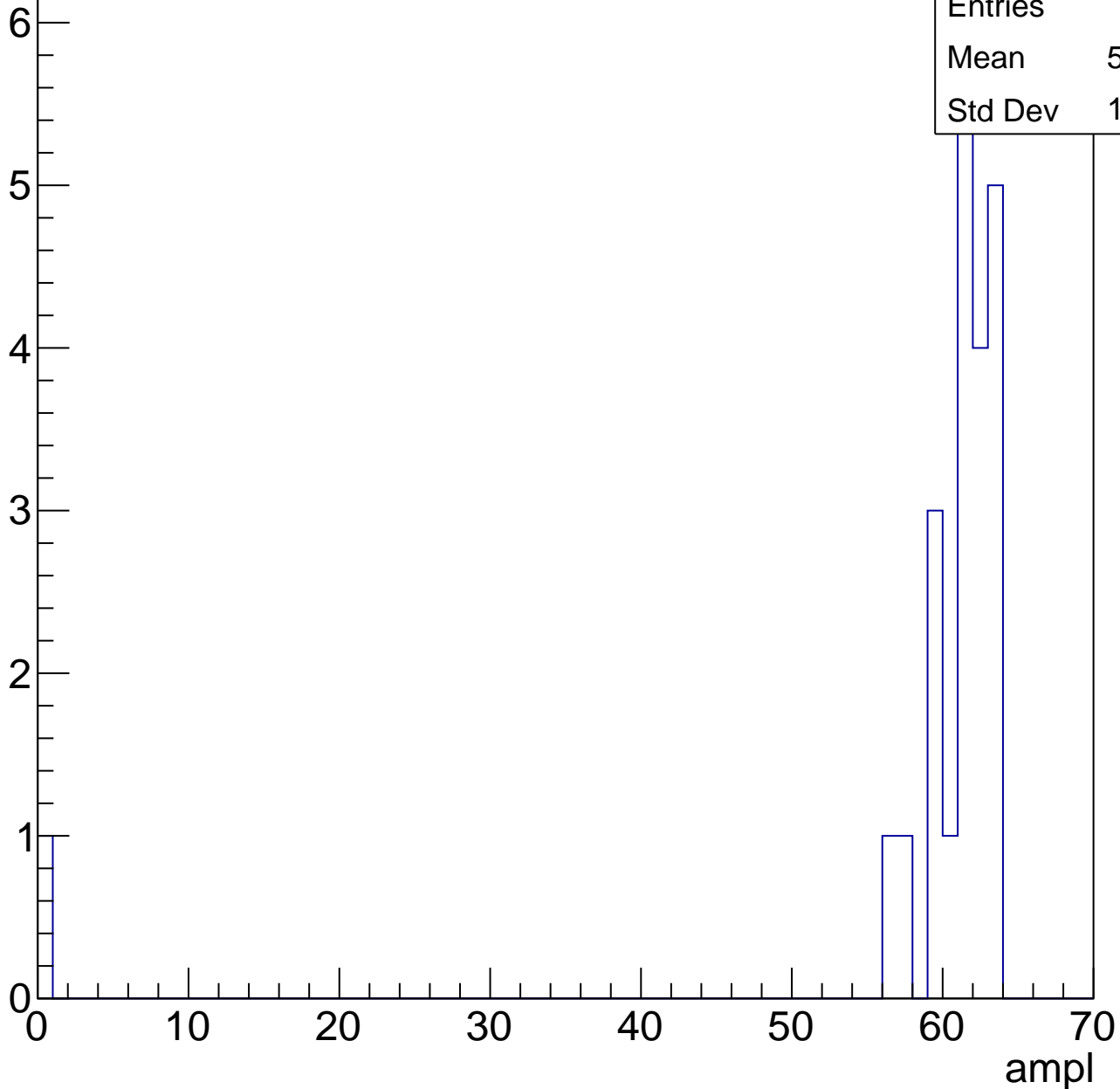


B1L103S, U1-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

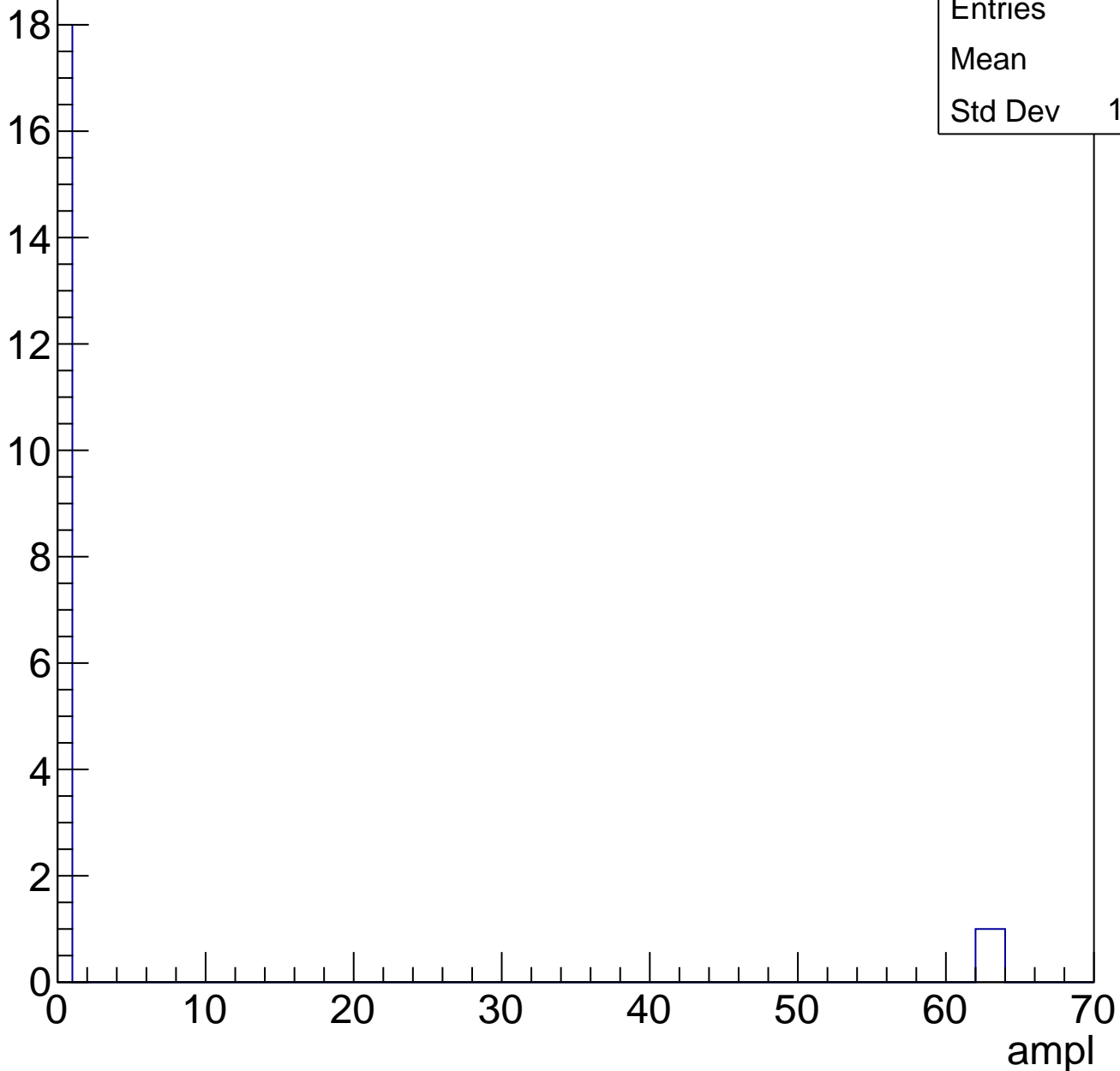
Entries	22
Mean	58.14
Std Dev	12.83



B1L103S, U1-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	6.25
Std Dev	18.75

B1L103S, U1-ch90, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	21.83
Std Dev	9.533

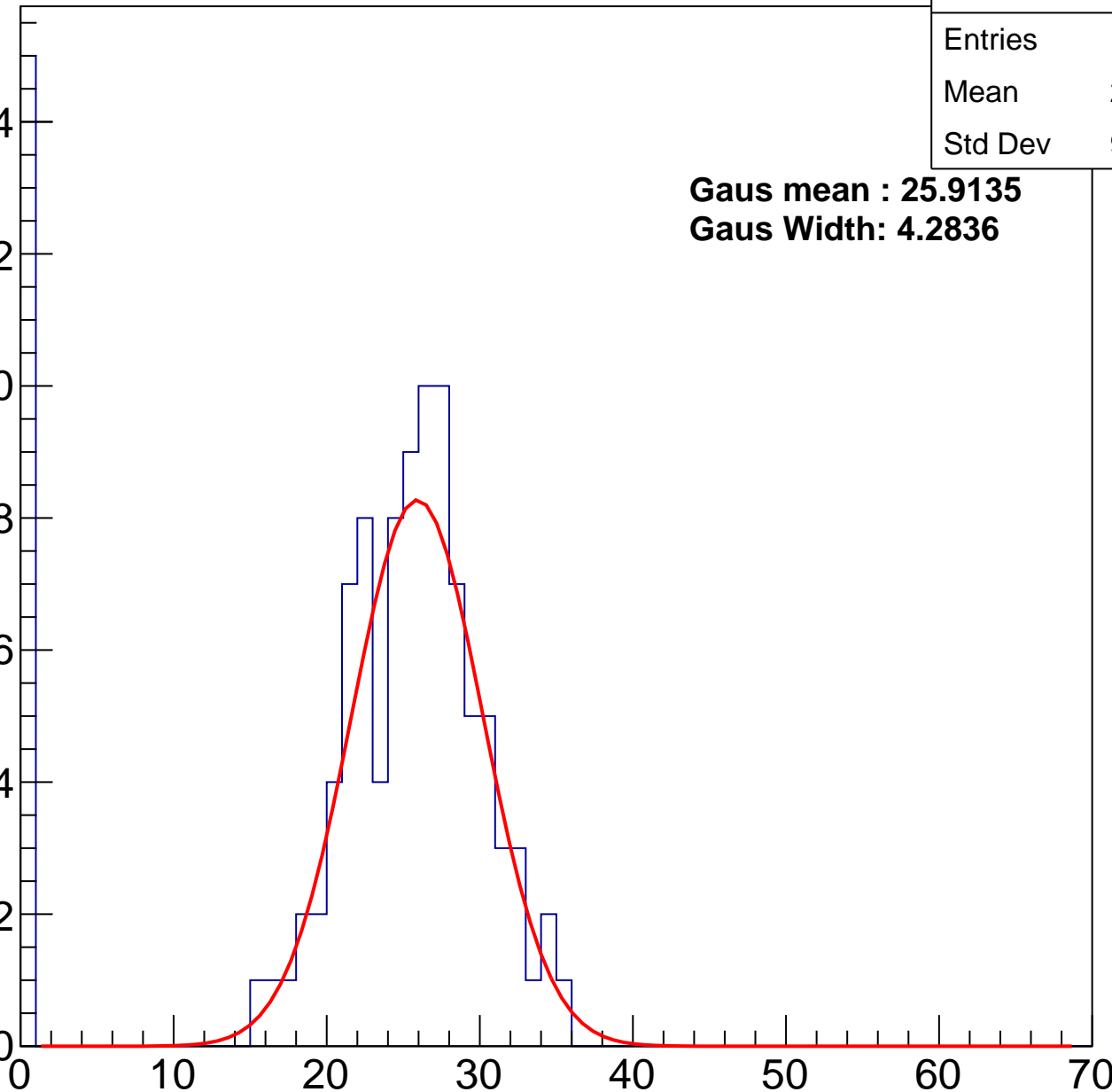
Gaus mean : 25.9135

Gaus Width: 4.2836

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U1-ch90, adc1

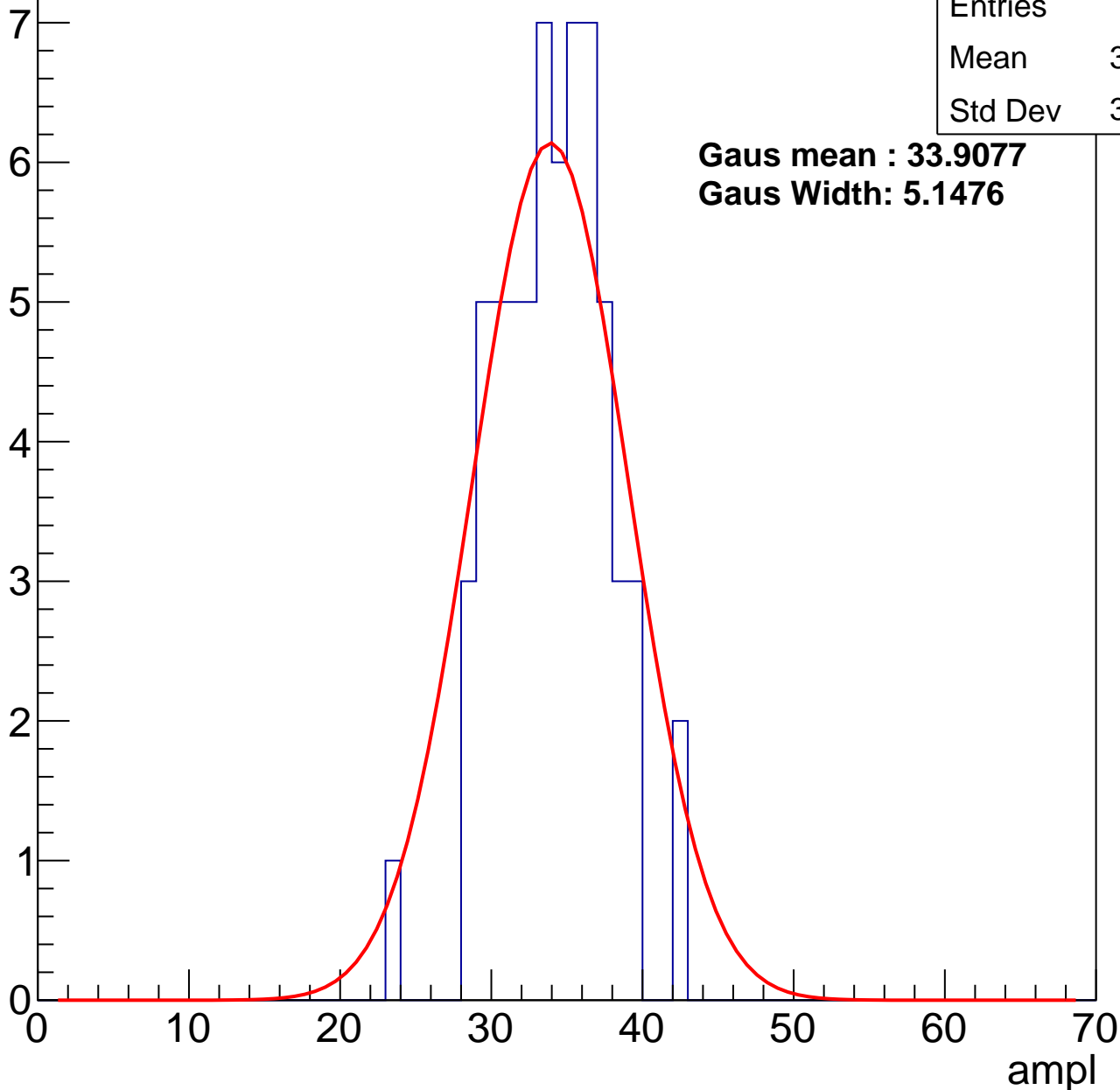
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.58
Std Dev	3.592

Gaus mean : 33.9077

Gaus Width: 5.1476



B1L103S, U1-ch90, adc2

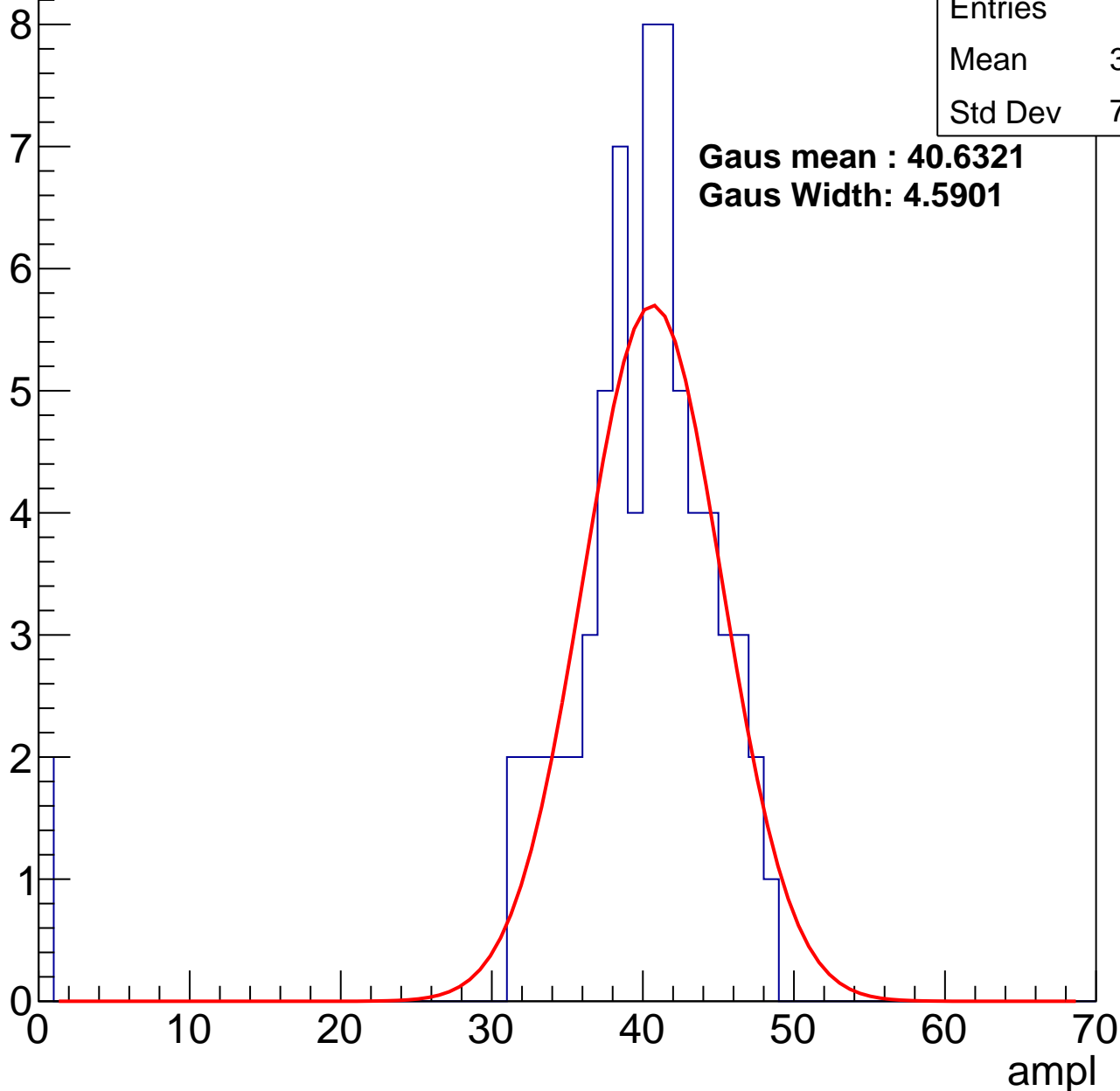
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	38.64
Std Dev	7.783

Gaus mean : 40.6321

Gaus Width: 4.5901

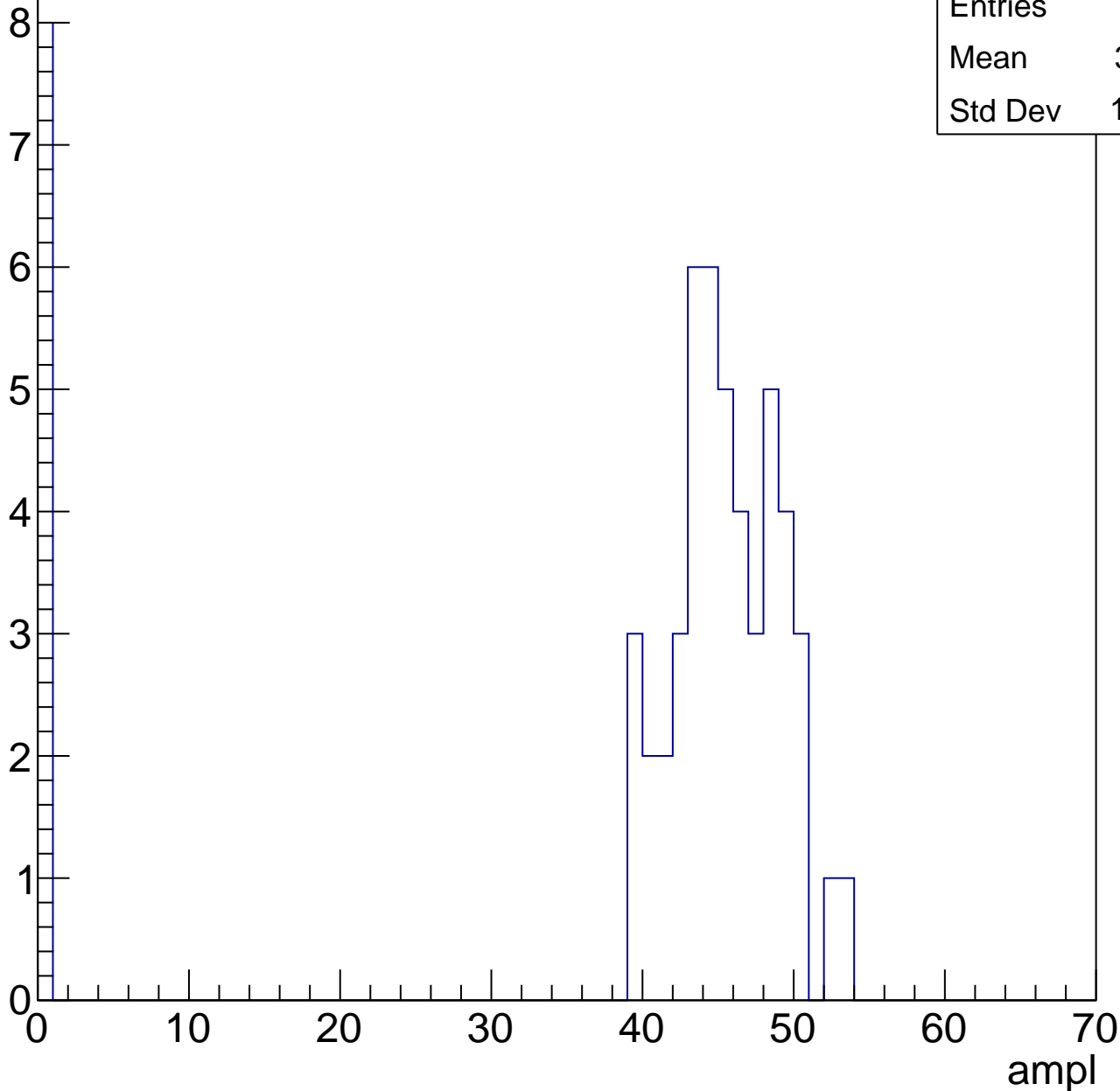


B1L103S, U1-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	38.71
Std Dev	16.12

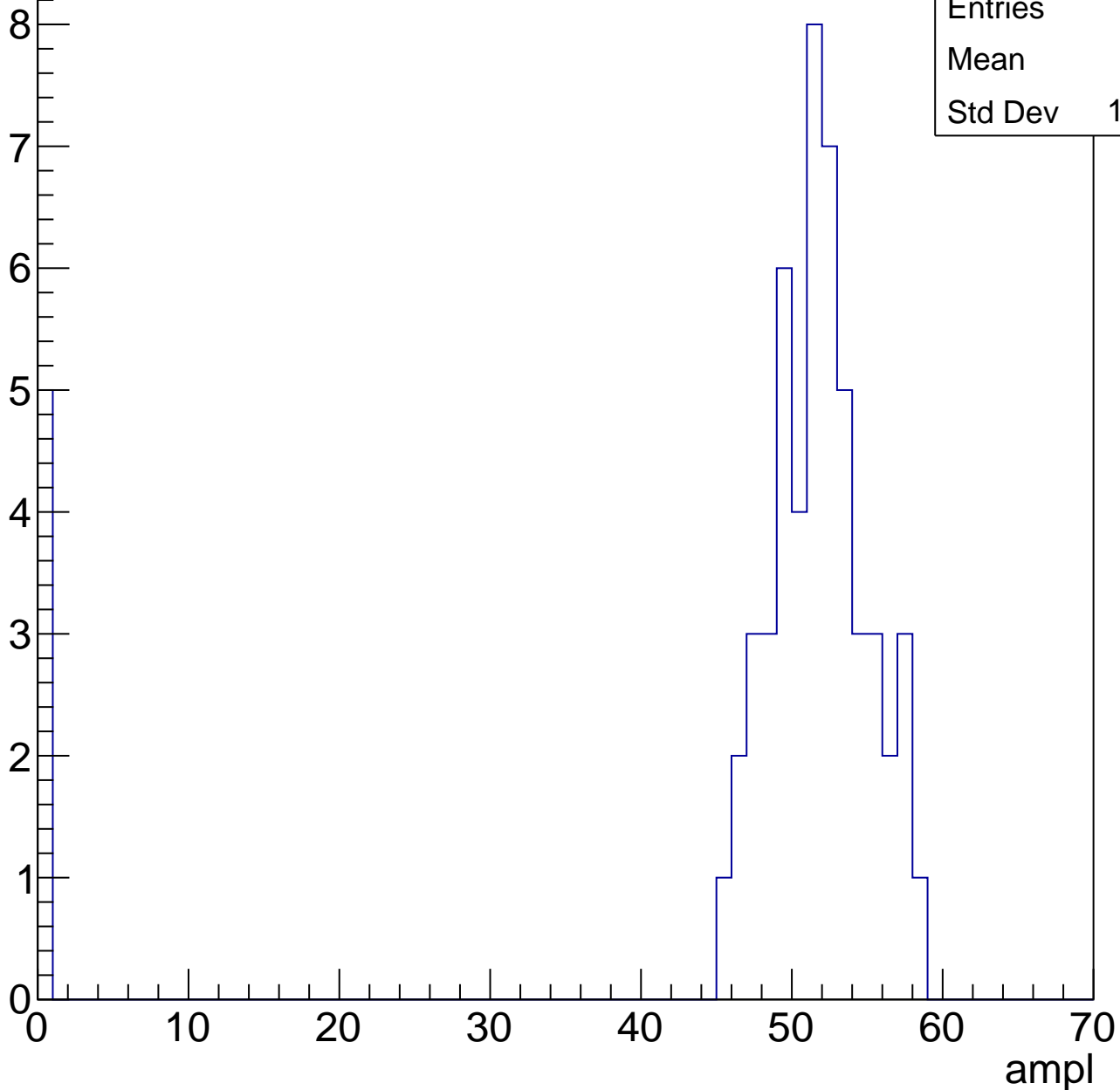


B1L103S, U1-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	46.8
Std Dev	14.95

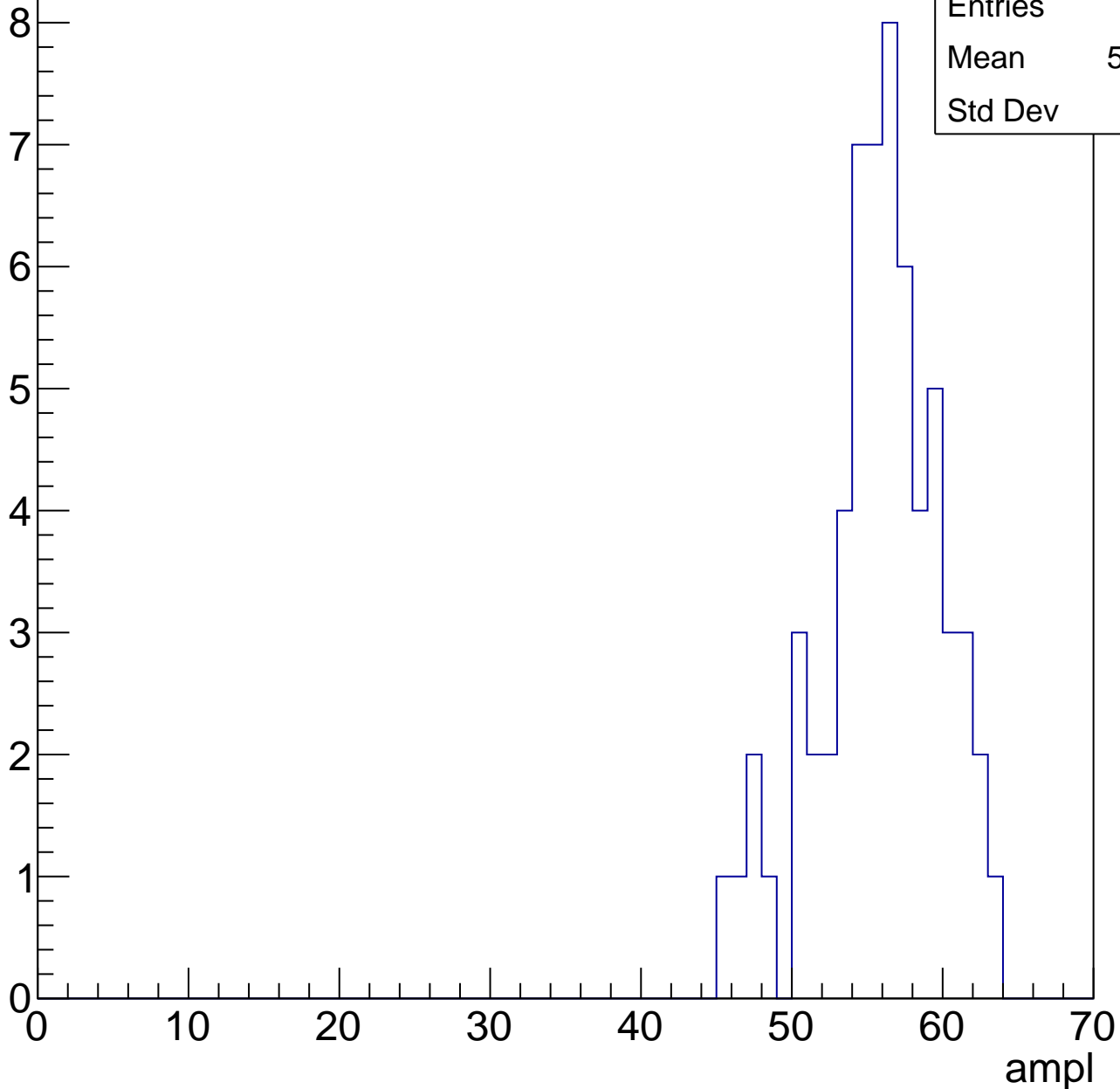


B1L103S, U1-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.34
Std Dev	4

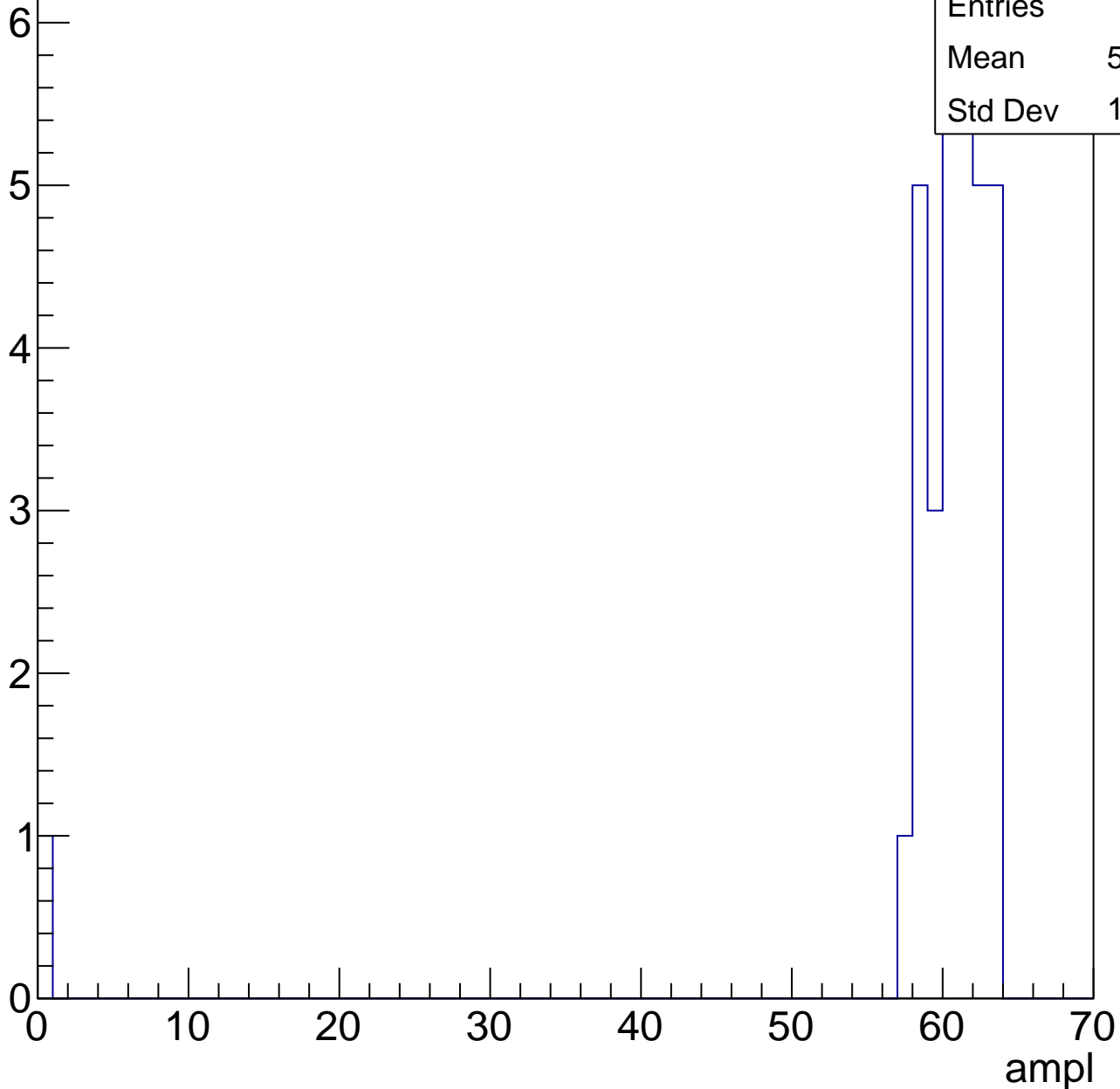


B1L103S, U1-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.59
Std Dev	10.66

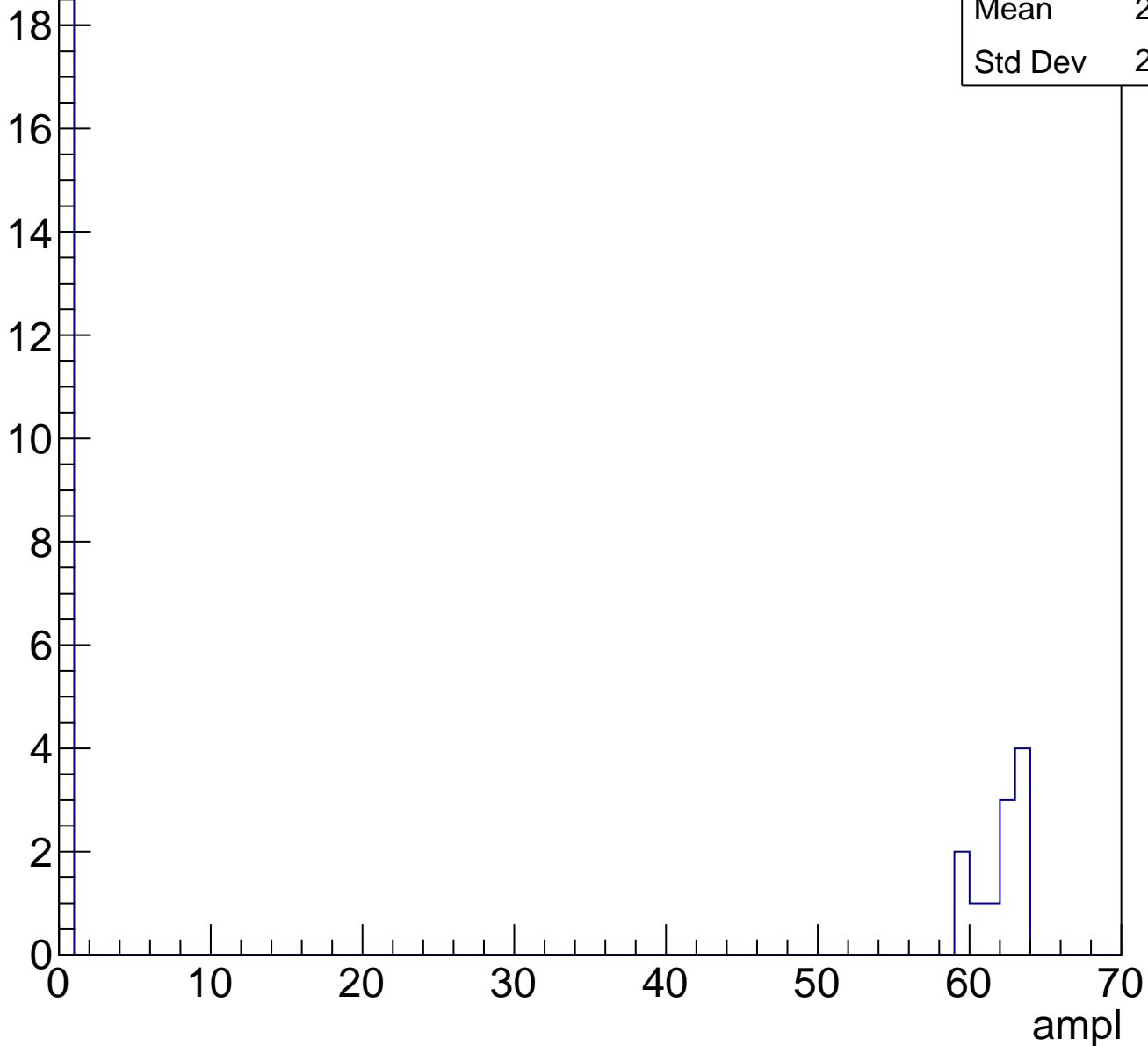


B1L103S, U1-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	30
Mean	22.57
Std Dev	29.67

Entry



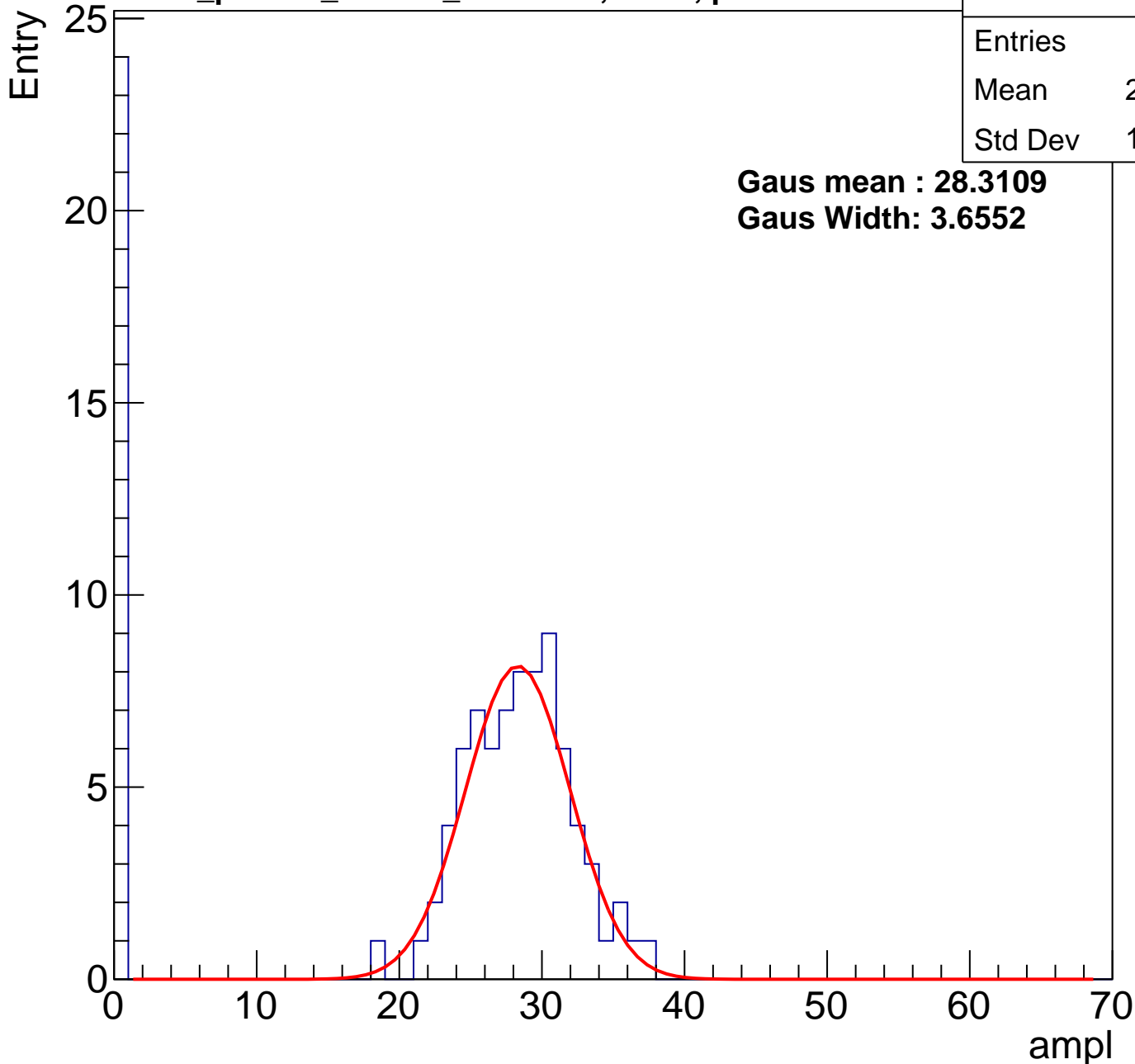
B1L103S, U1-ch91, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	21.34
Std Dev	12.33

Gaus mean : 28.3109

Gaus Width: 3.6552



B1L103S, U1-ch91, adc1

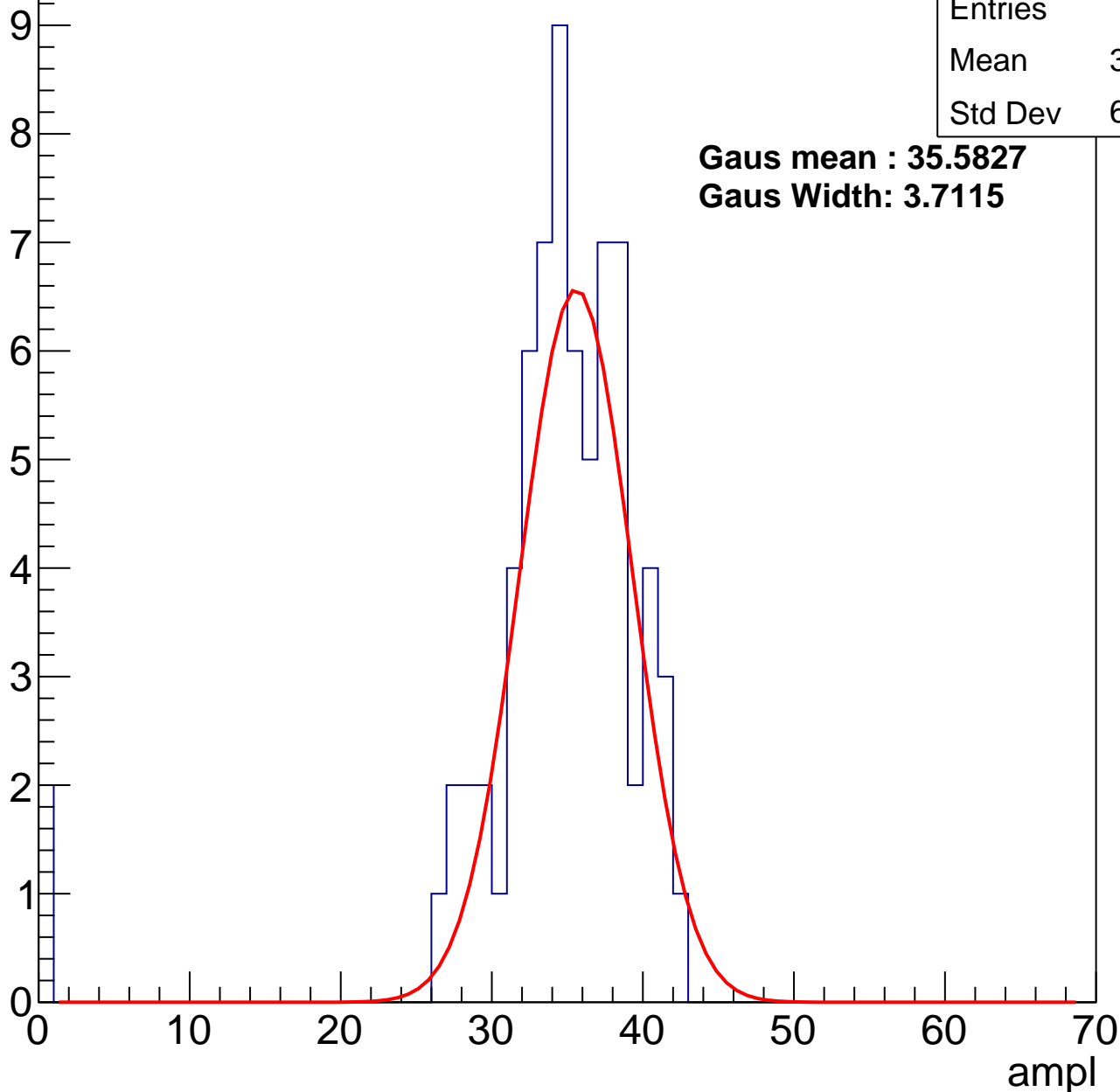
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.73
Std Dev	6.797

Gaus mean : 35.5827

Gaus Width: 3.7115



B1L103S, U1-ch91, adc2

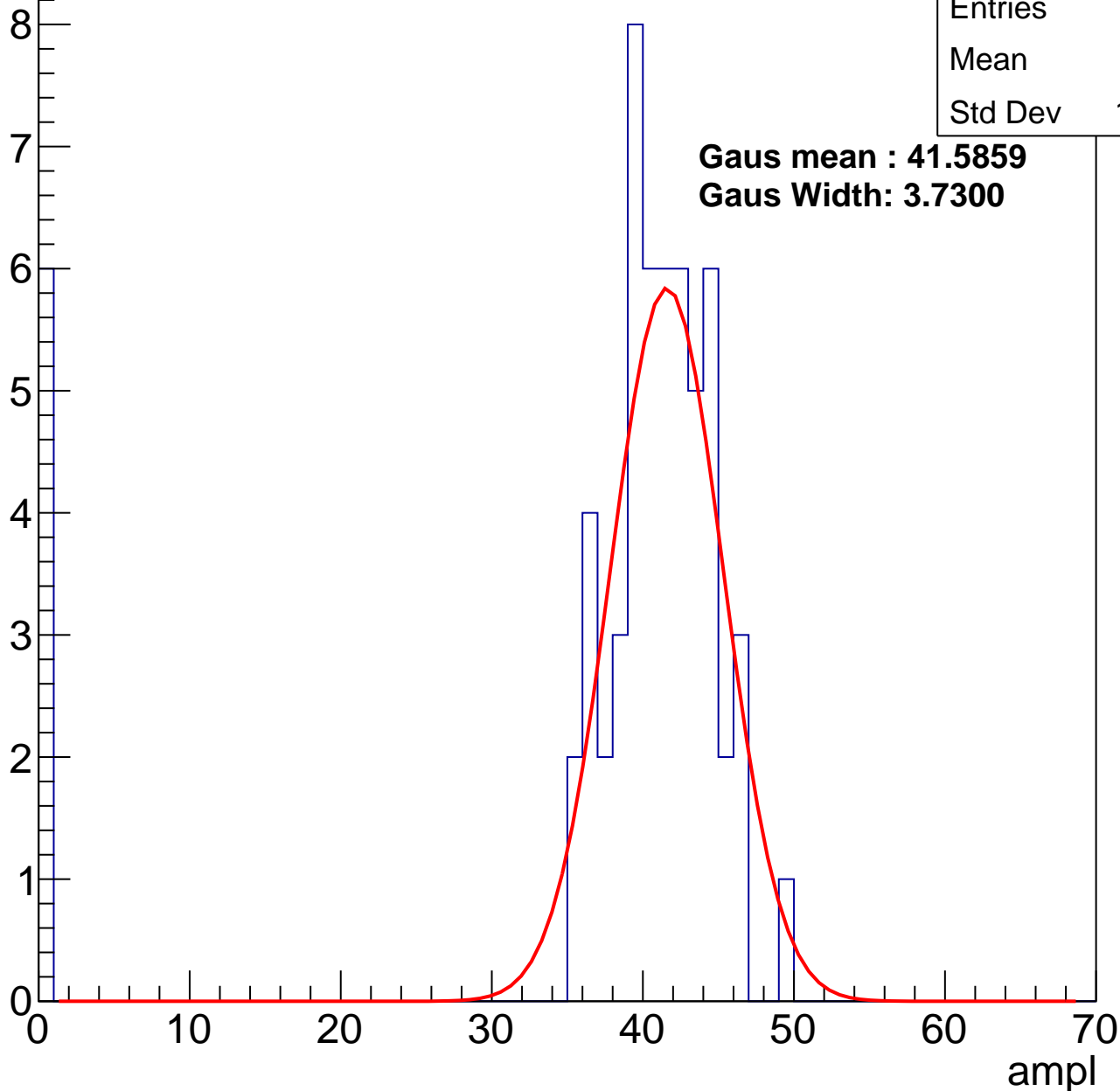
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	36.8
Std Dev	12.61

Gaus mean : 41.5859

Gaus Width: 3.7300

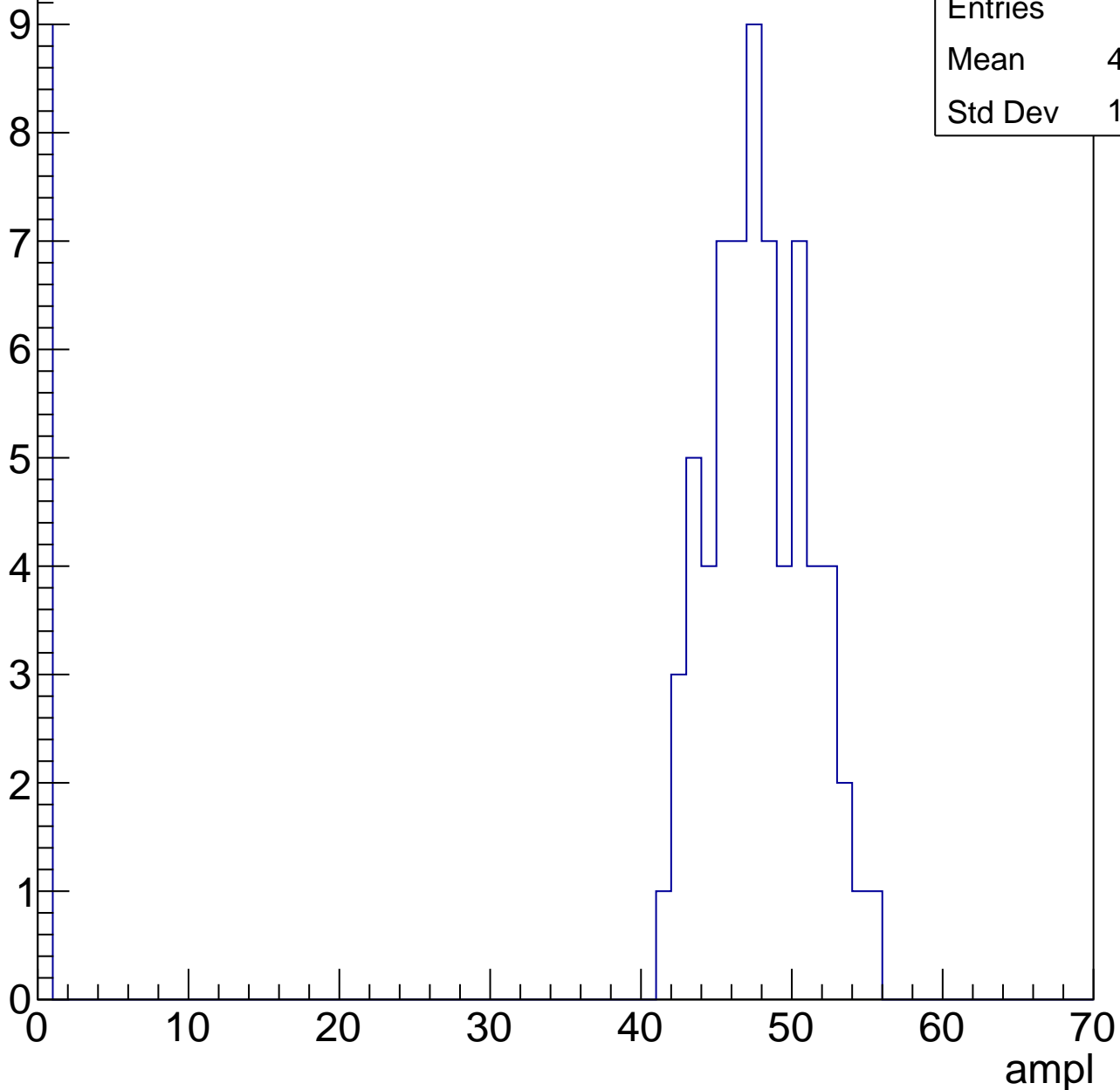


B1L103S, U1-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	41.69
Std Dev	15.69

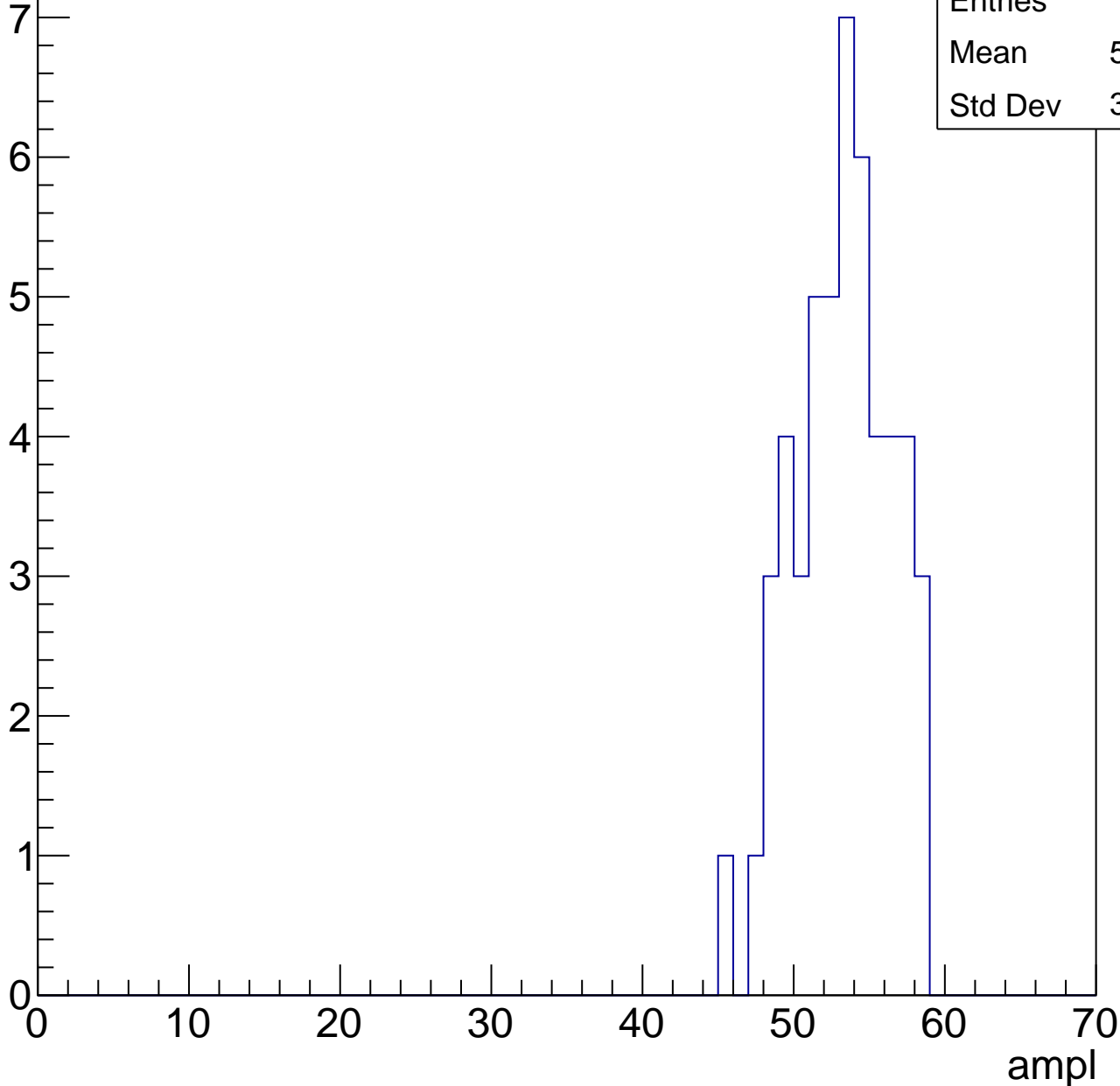


B1L103S, U1-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	52.76
Std Dev	3.115

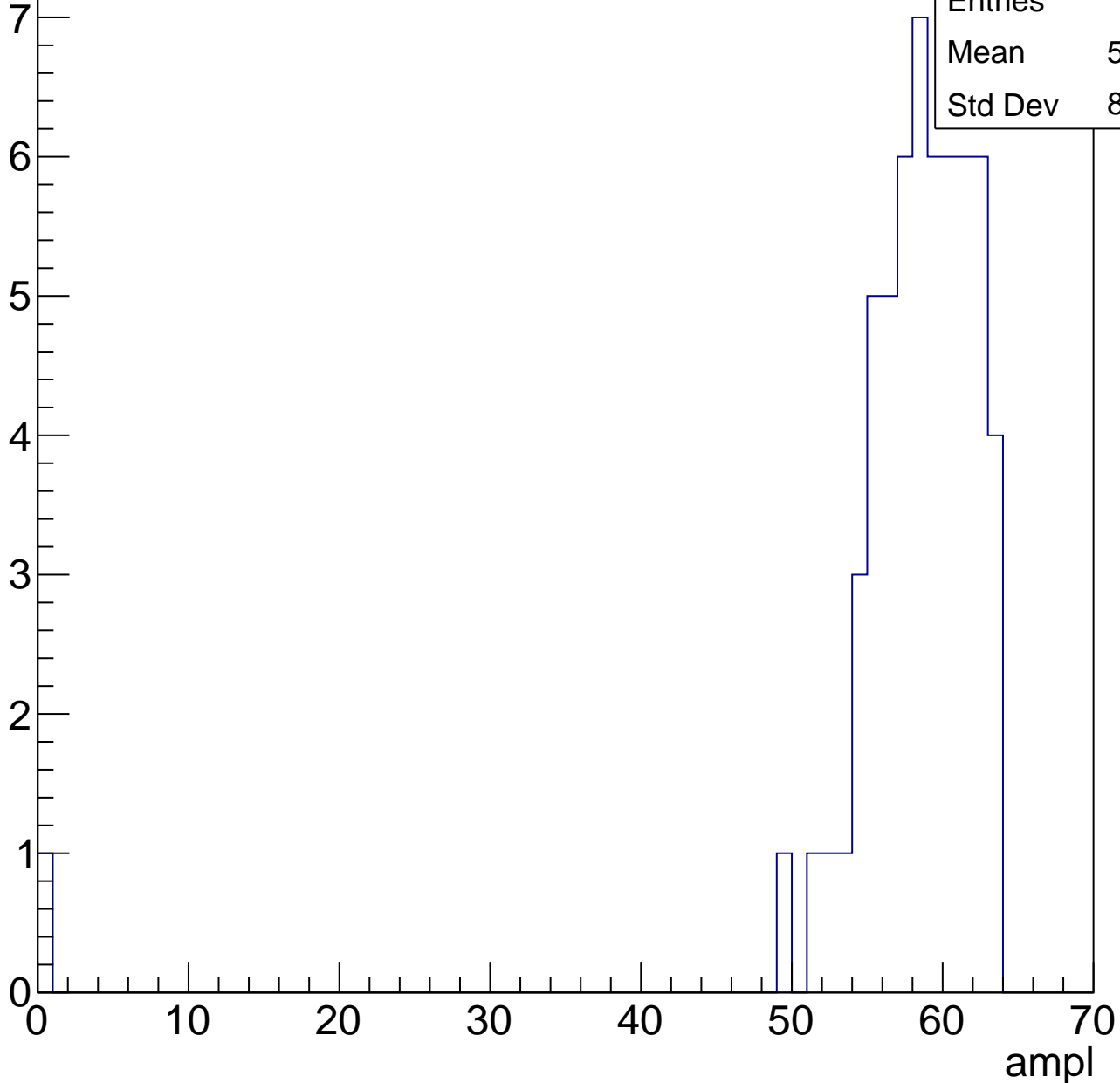


B1L103S, U1-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

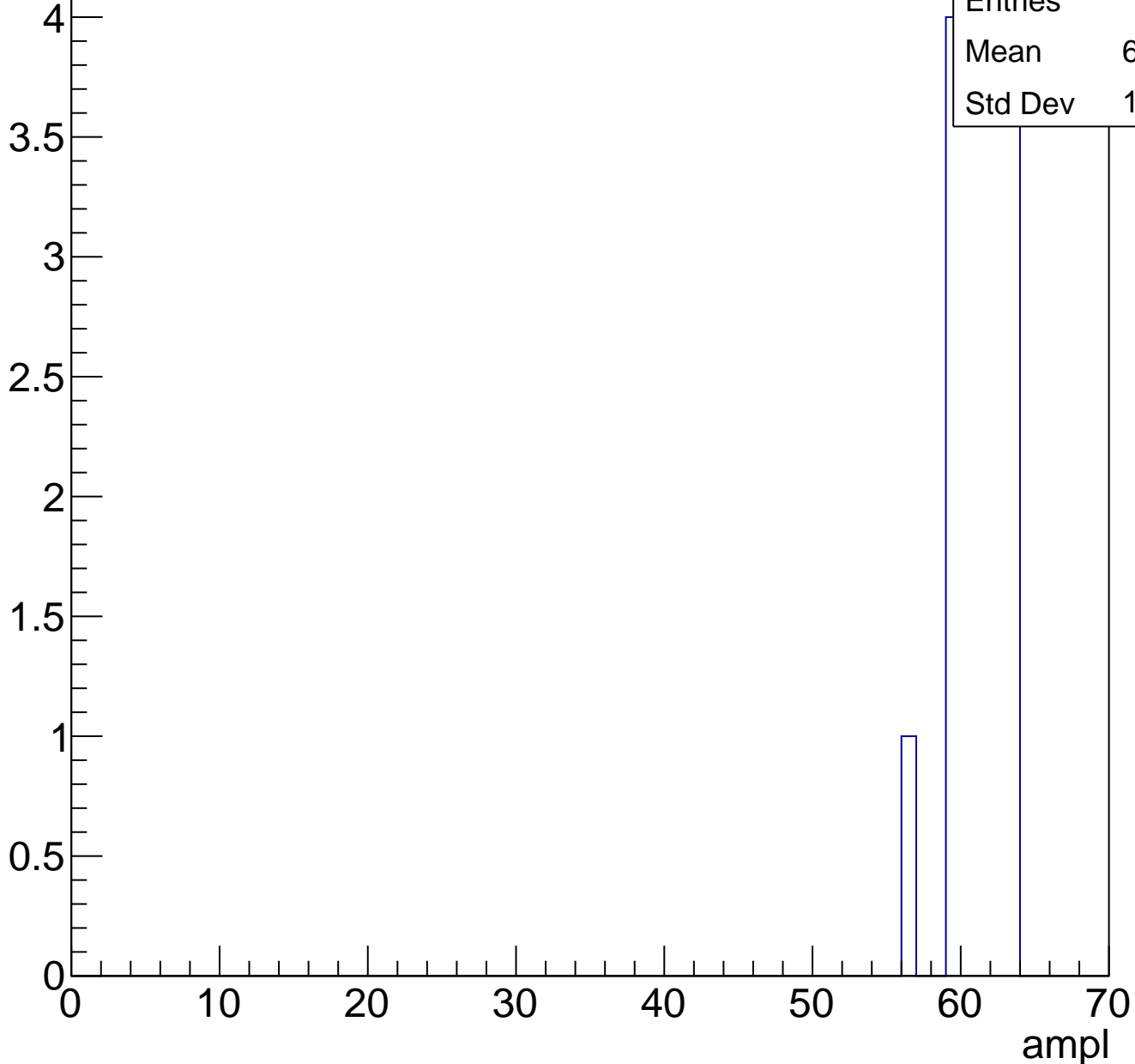
Entries	59
Mean	57.19
Std Dev	8.146



B1L103S, U1-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	122
Mean	19.4
Std Dev	11.15

Gaus mean : 25.7588

Gaus Width: 3.8867

Entry

25

20

15

10

5

0

0

10

20

30

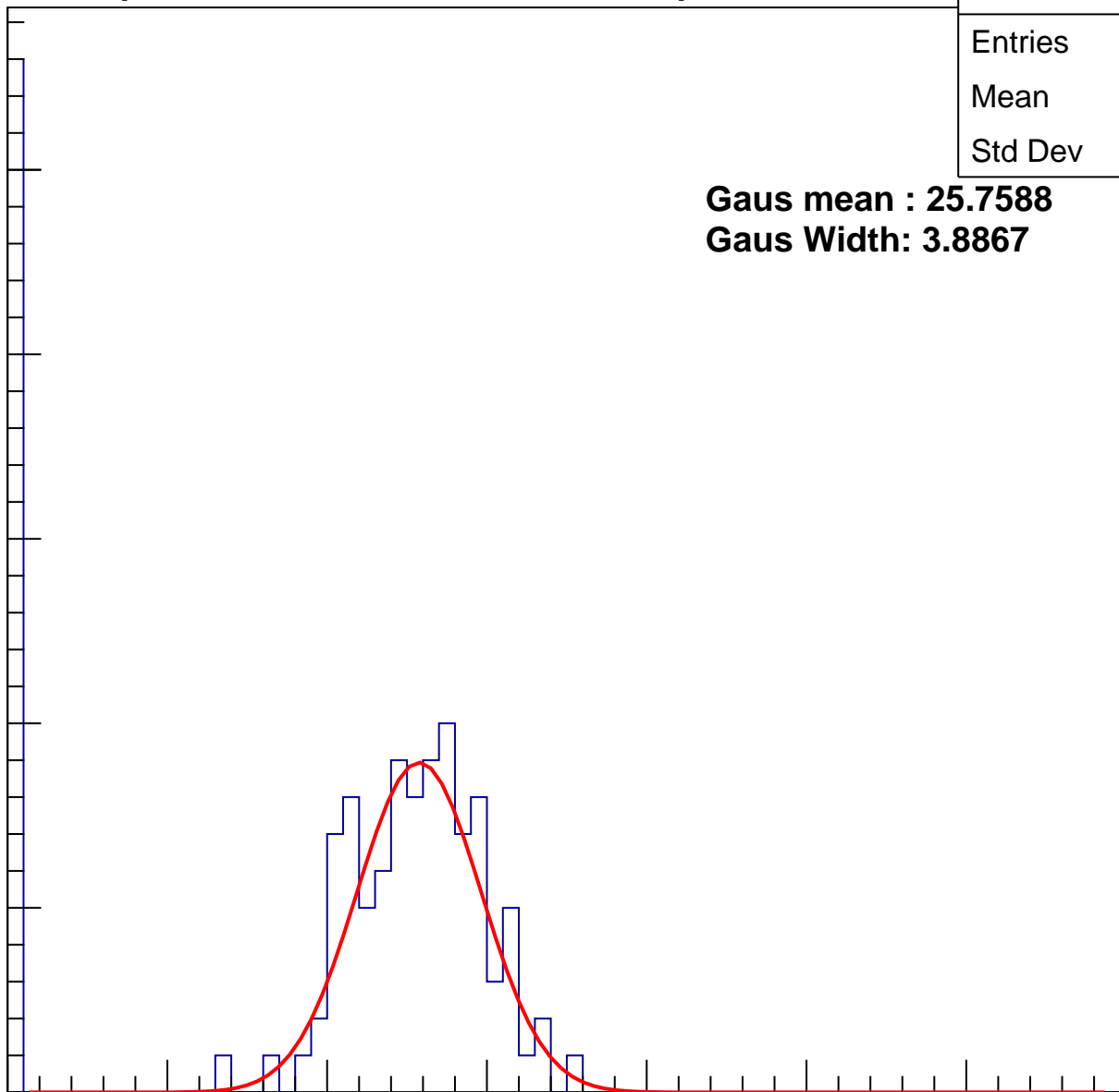
40

50

60

70

ampl



B1L103S, U1-ch92, adc1

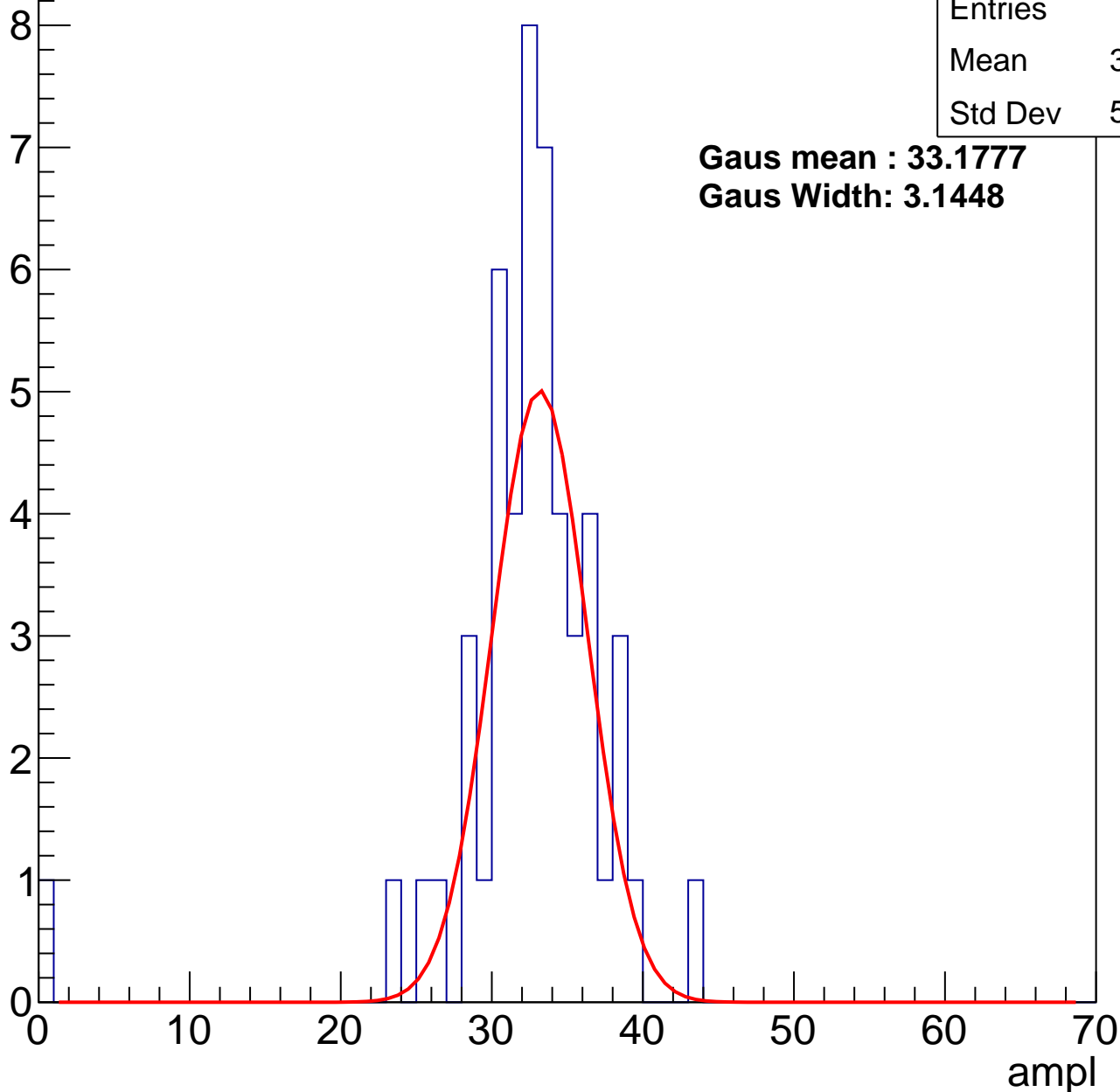
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	31.92
Std Dev	5.817

Gaus mean : 33.1777

Gaus Width: 3.1448



B1L103S, U1-ch92, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	37.31
Std Dev	6.991

Gaus mean : 38.9628

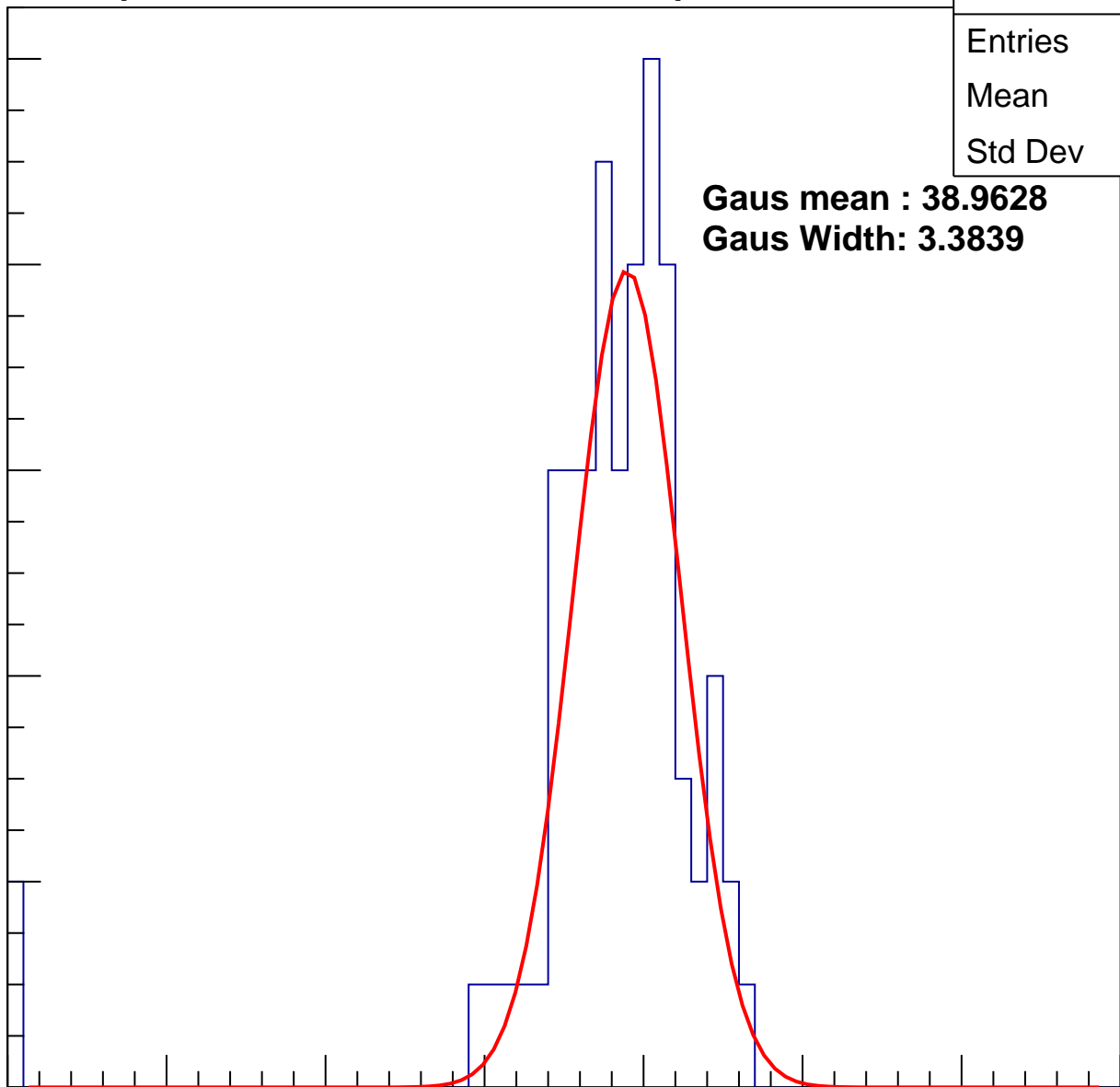
Gaus Width: 3.3839

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

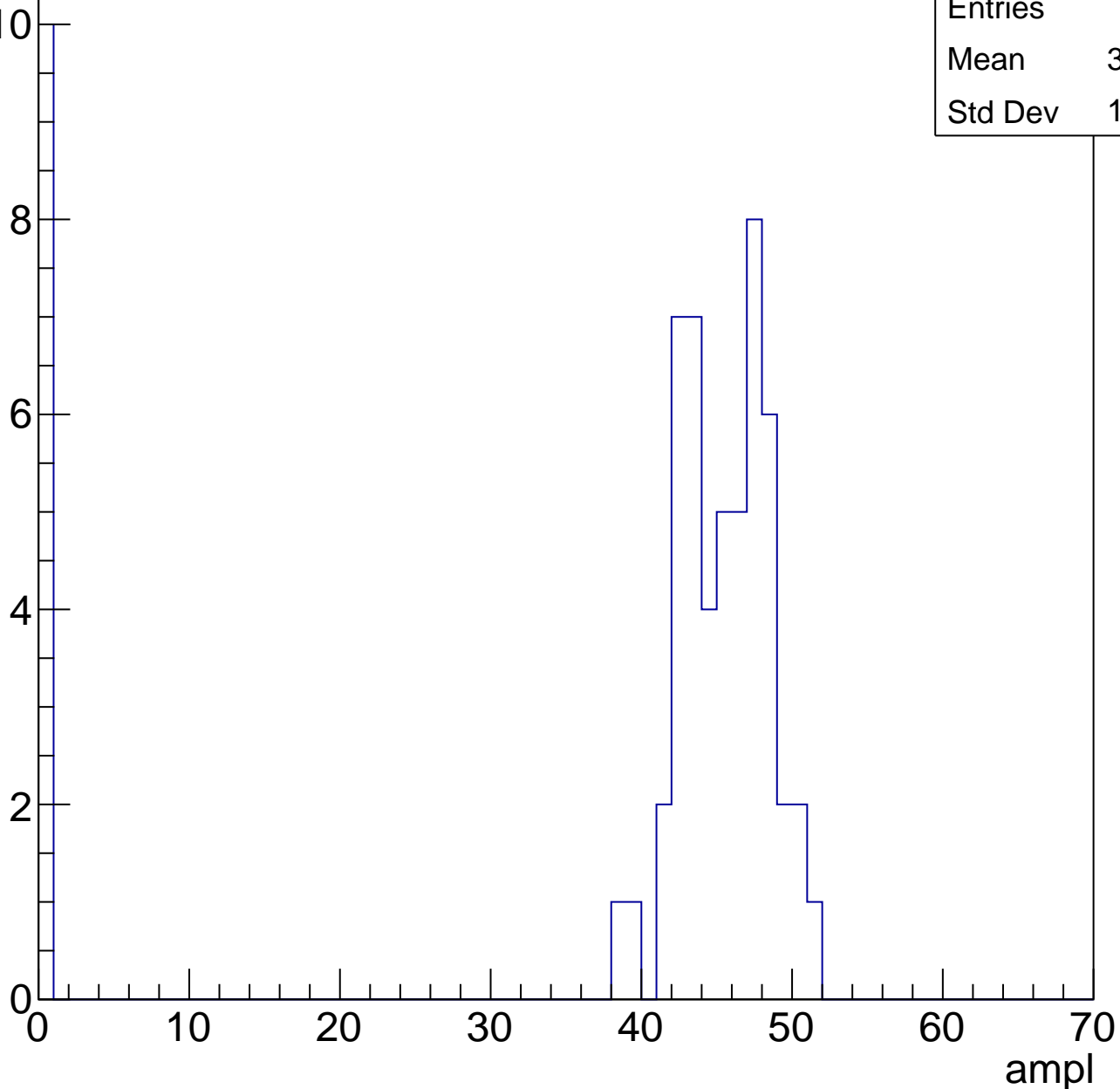


B1L103S, U1-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

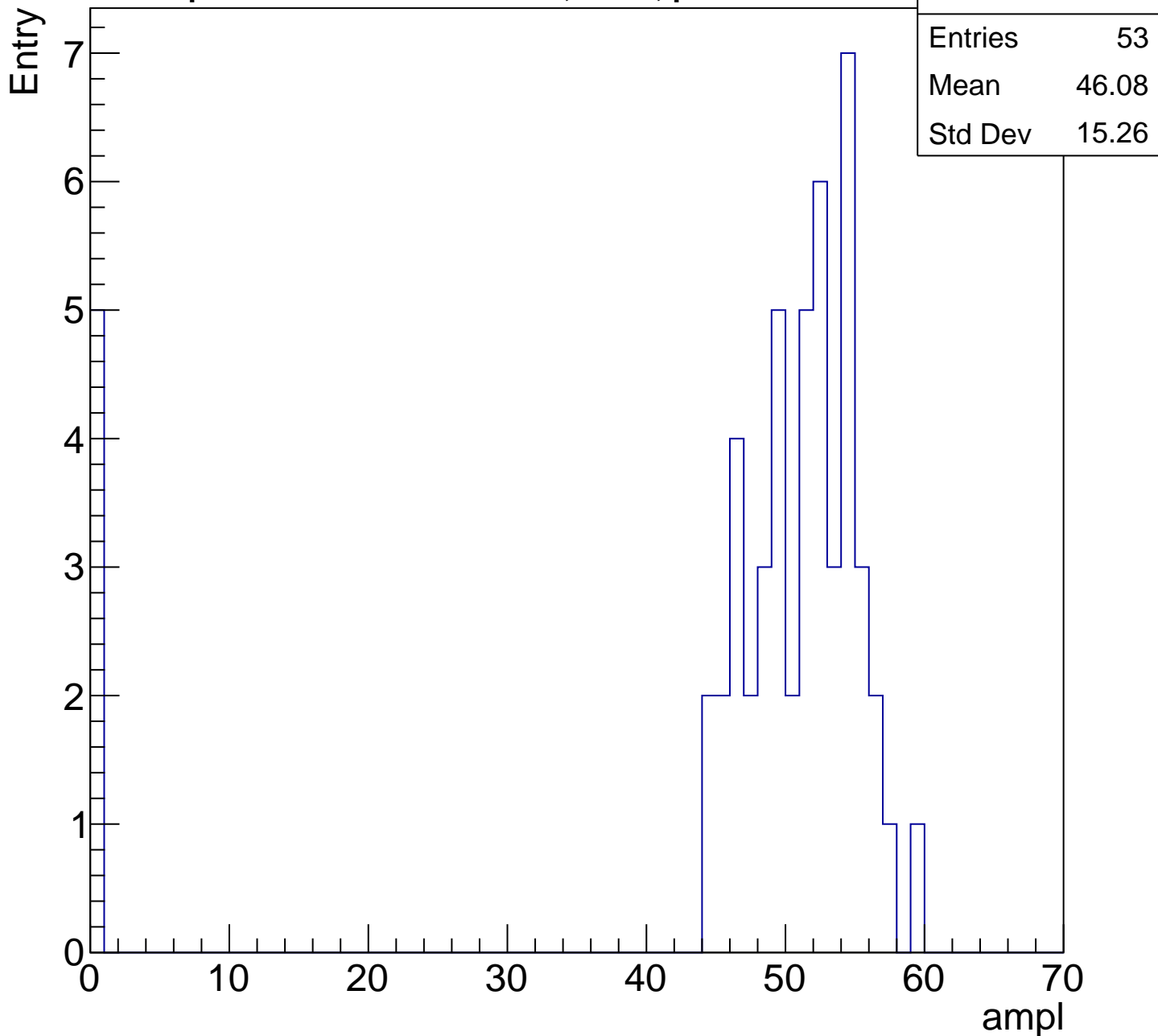
Entry

Entries	61
Mean	37.67
Std Dev	16.89



B1L103S, U1-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

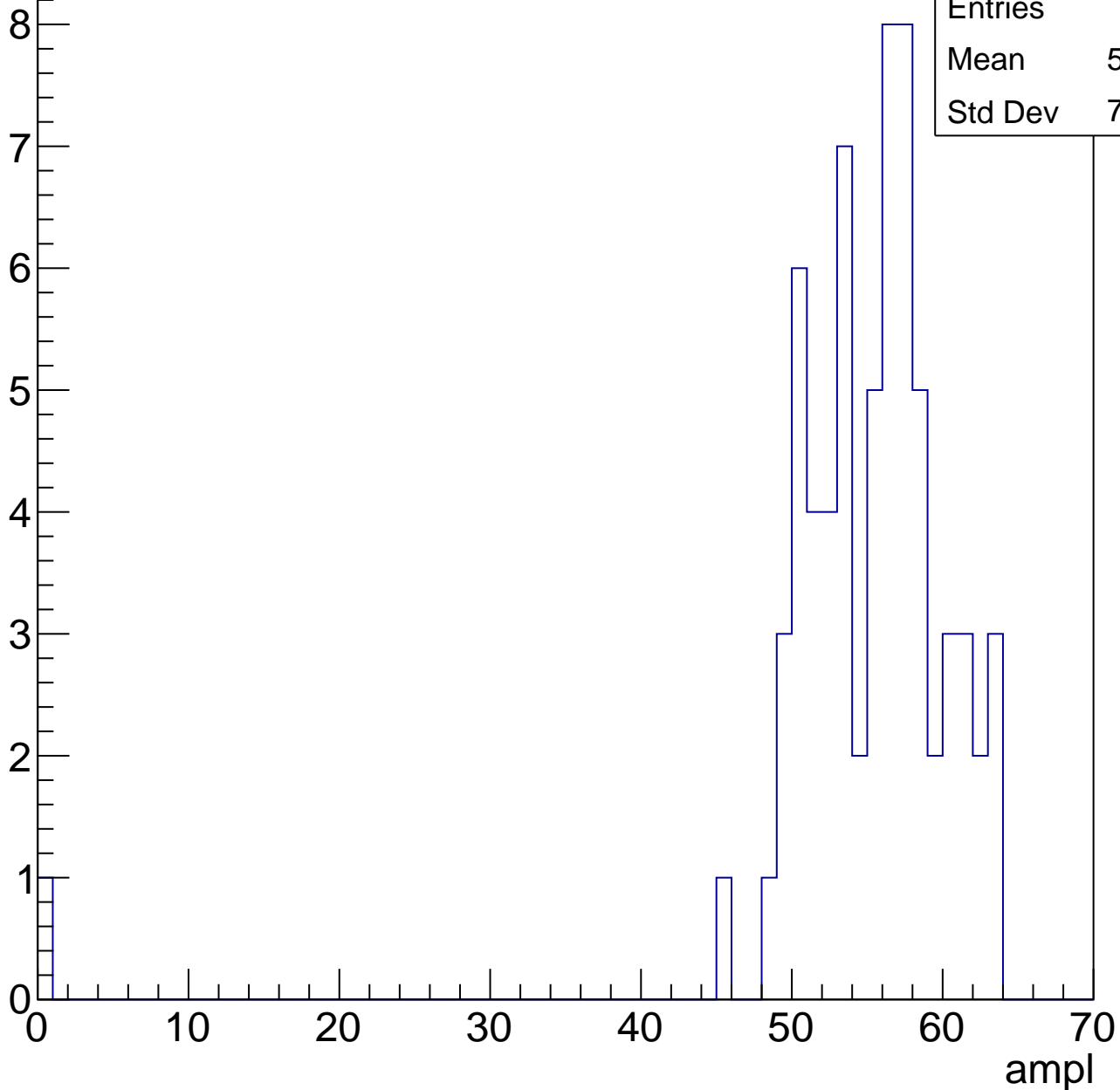


B1L103S, U1-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	54.32
Std Dev	7.785

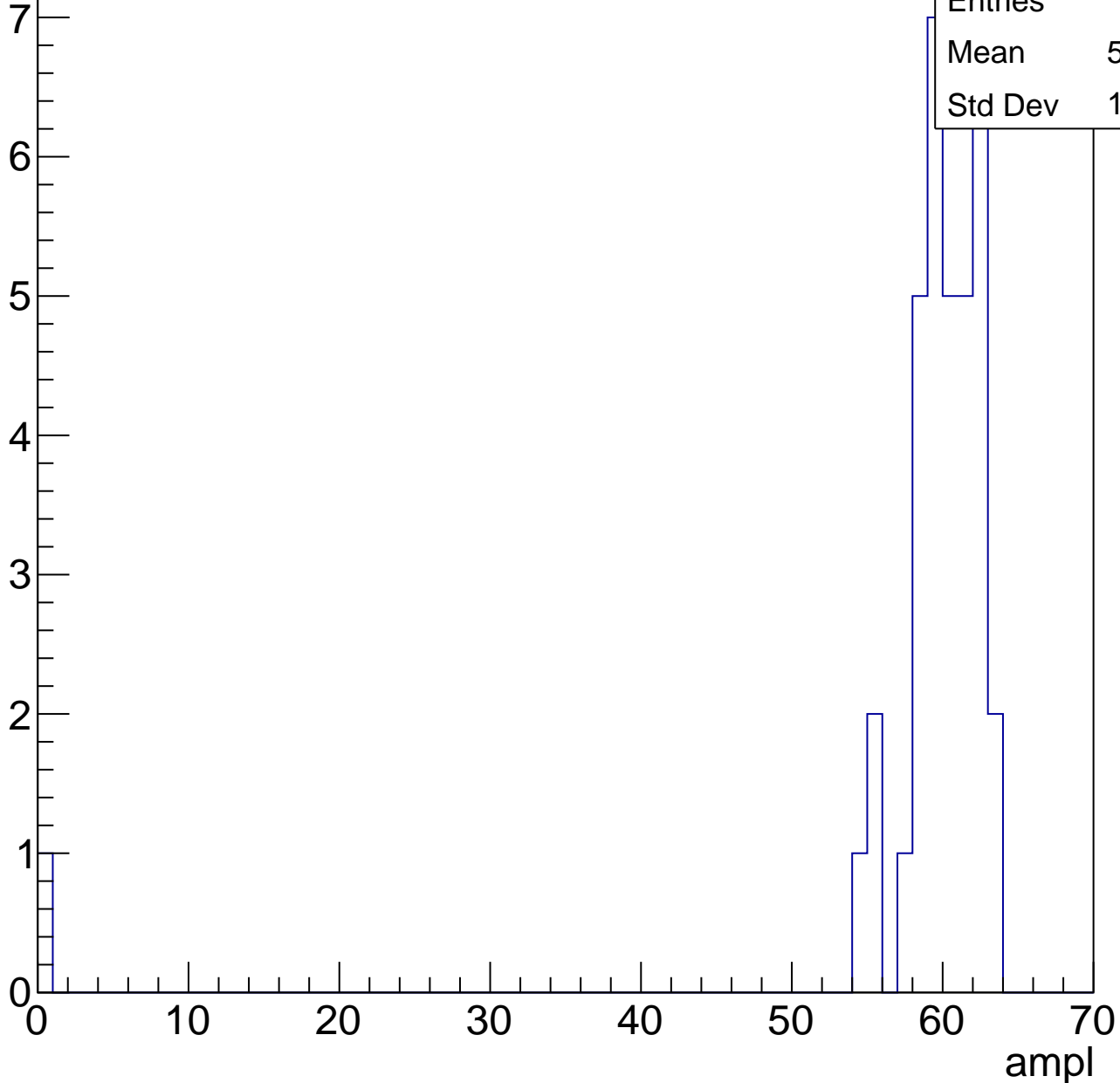


B1L103S, U1-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.03
Std Dev	10.05

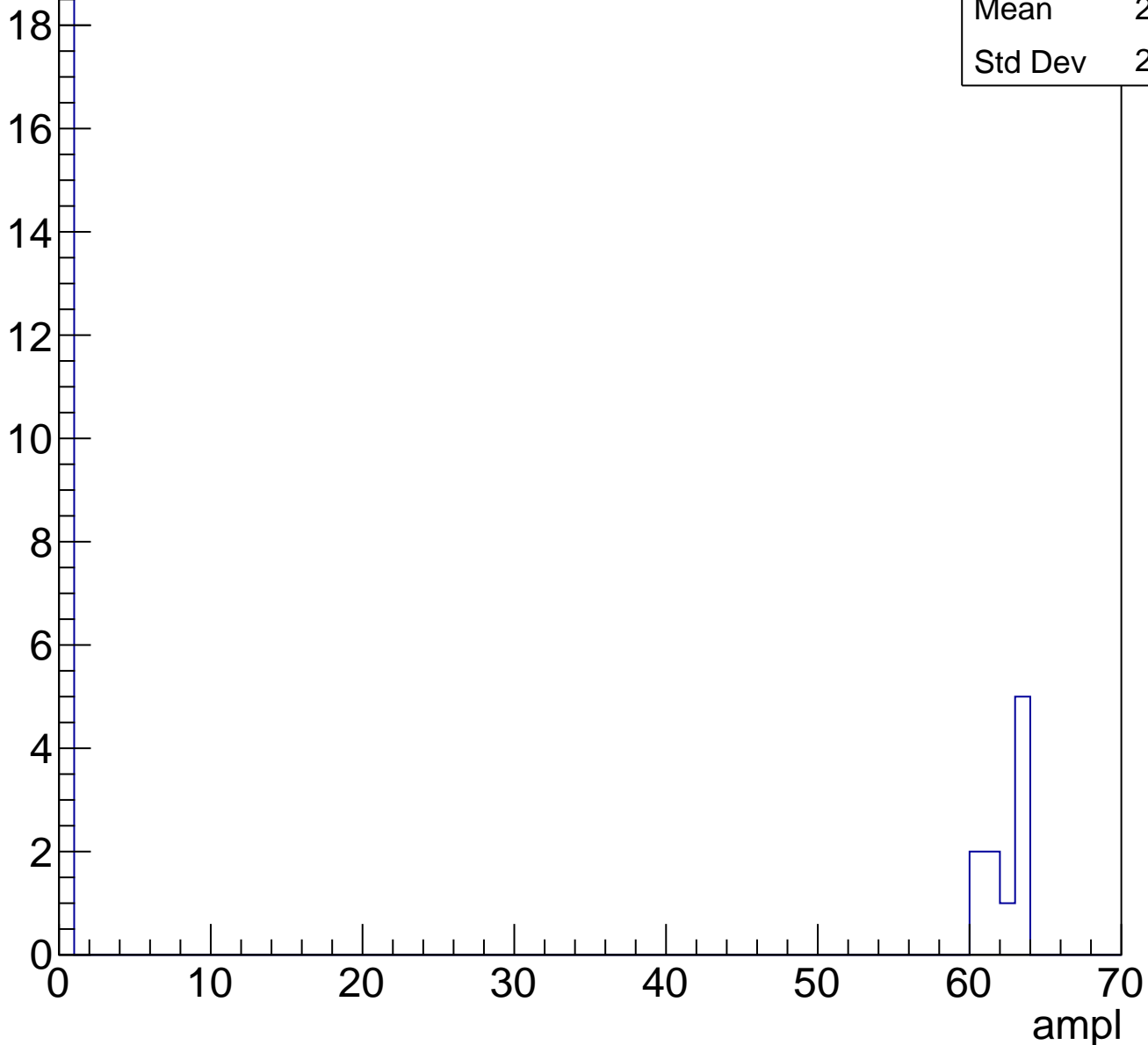


B1L103S, U1-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	29
Mean	21.34
Std Dev	29.43

Entry



B1L103S, U1-ch93, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	18.46
Std Dev	13.74

Gaus mean : 28.9968

Gaus Width: 3.6145

Entry

30

25

20

15

10

5

0

0

10

20

30

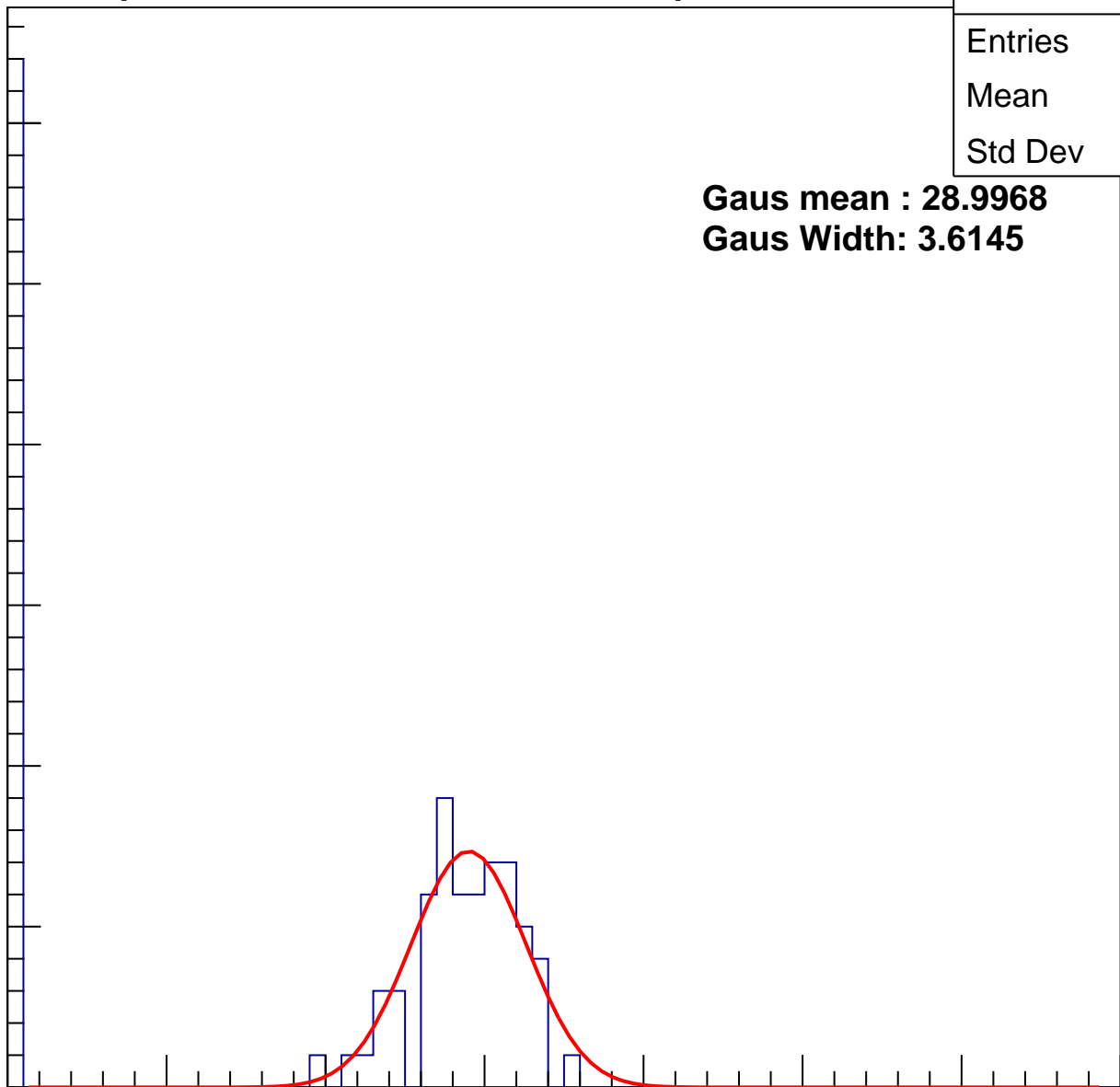
40

50

60

70

ampl



B1L103S, U1-ch93, adc1

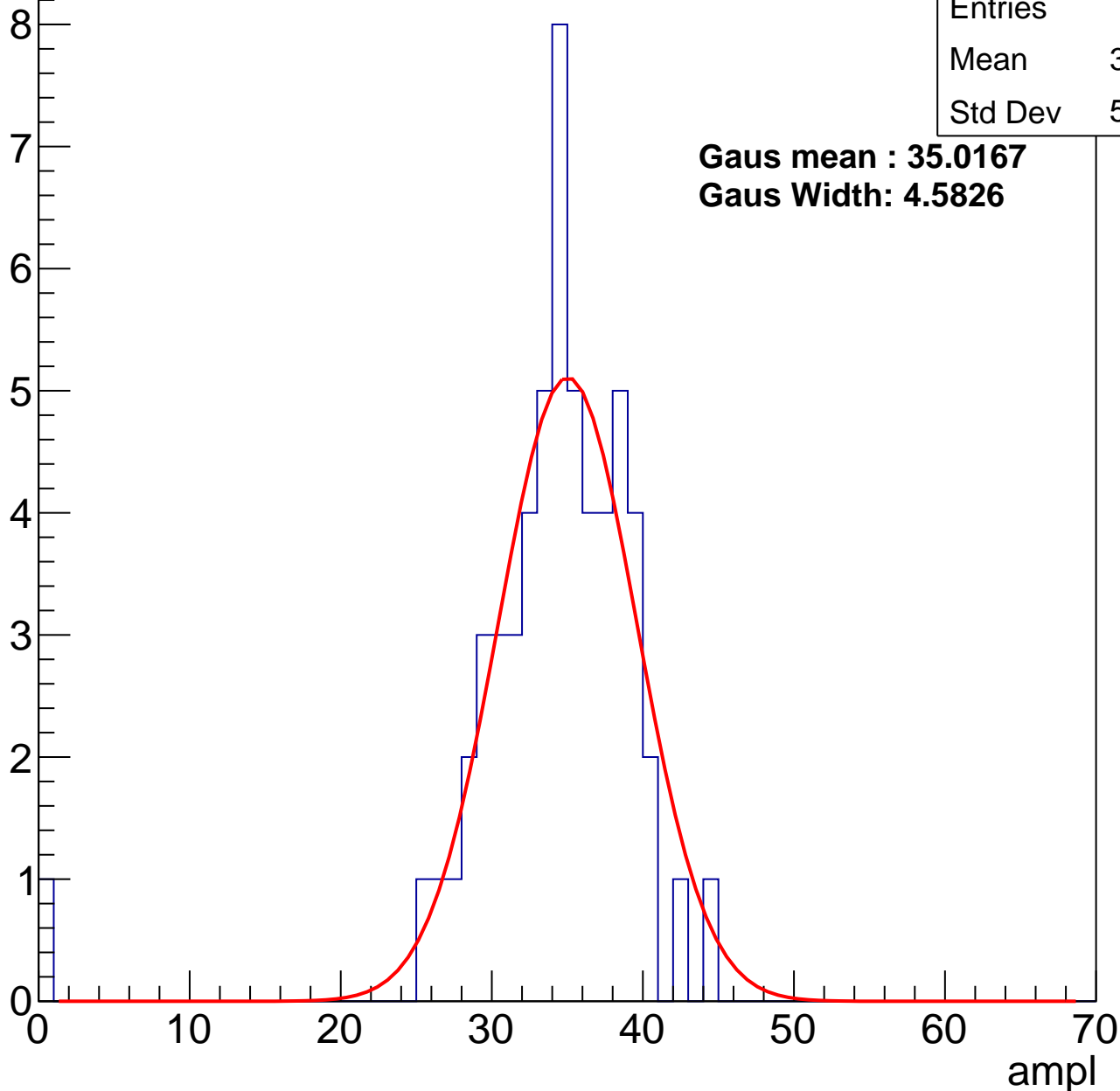
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.59
Std Dev	5.954

Gaus mean : 35.0167

Gaus Width: 4.5826



B1L103S, U1-ch93, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	35.92
Std Dev	13.67

Gaus mean : 41.2380

Gaus Width: 4.2901

Entry

10

8

6

4

2

0

0

10

20

30

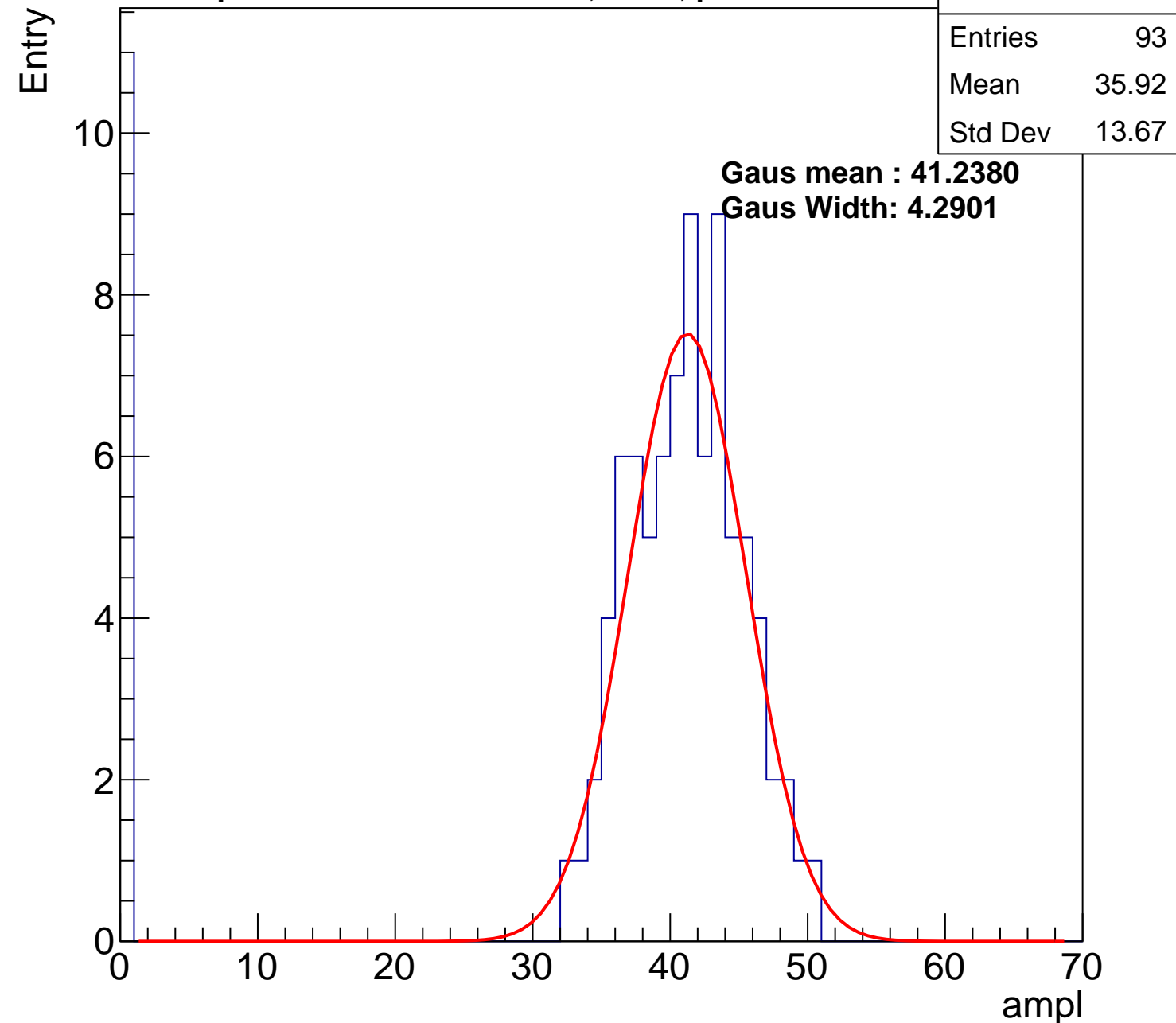
40

50

60

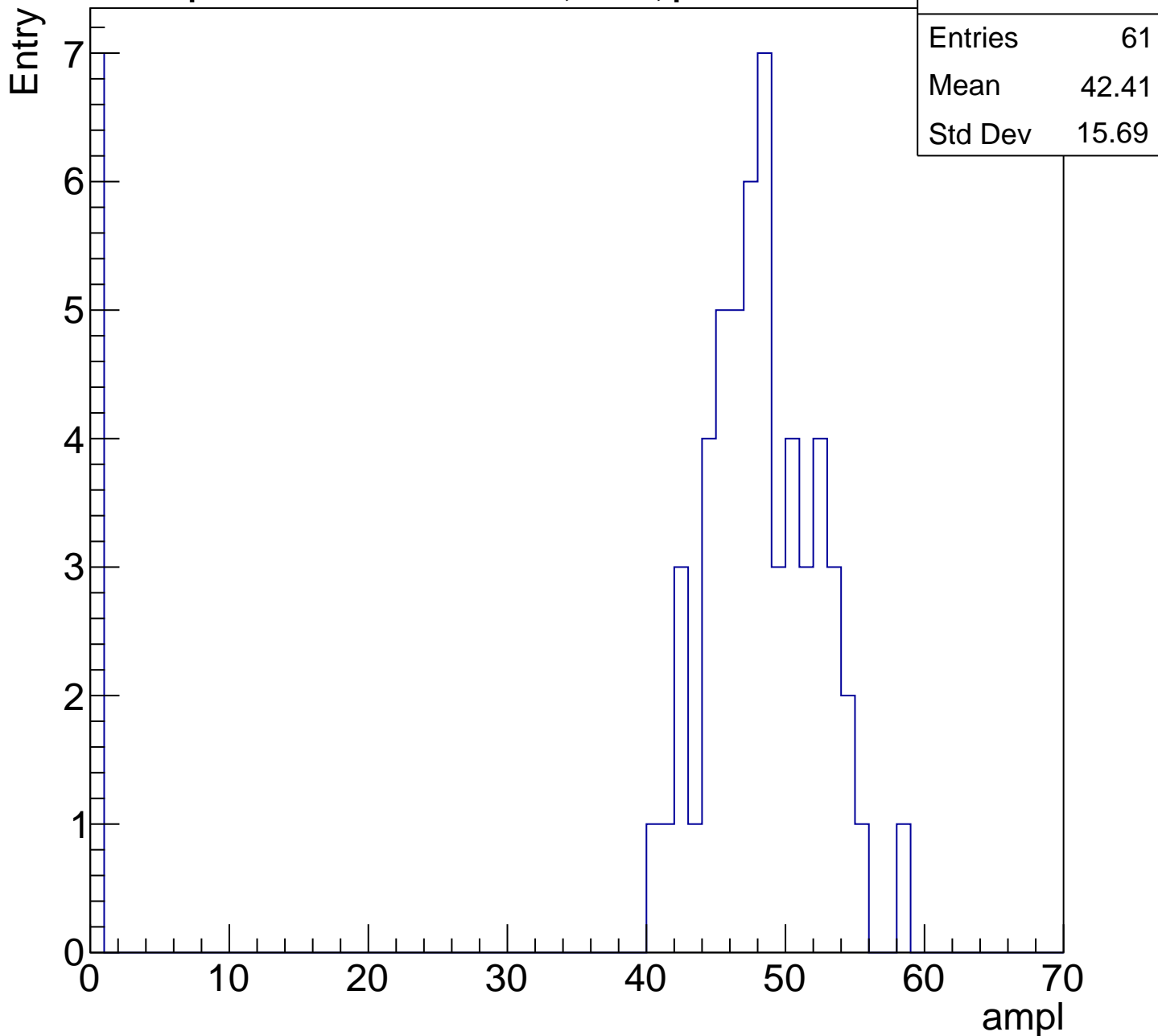
70

ampl



B1L103S, U1-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

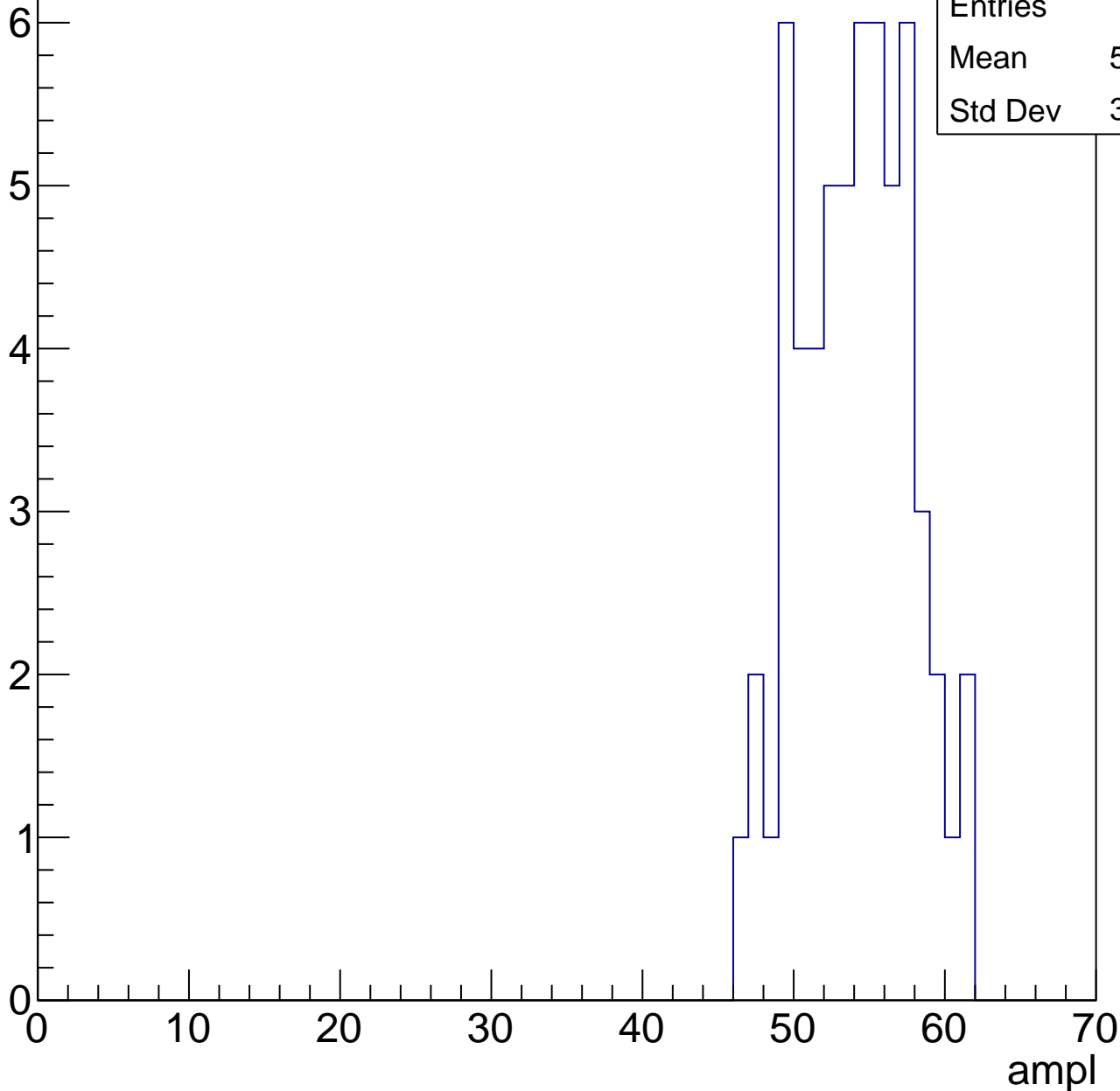


B1L103S, U1-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	53.58
Std Dev	3.623

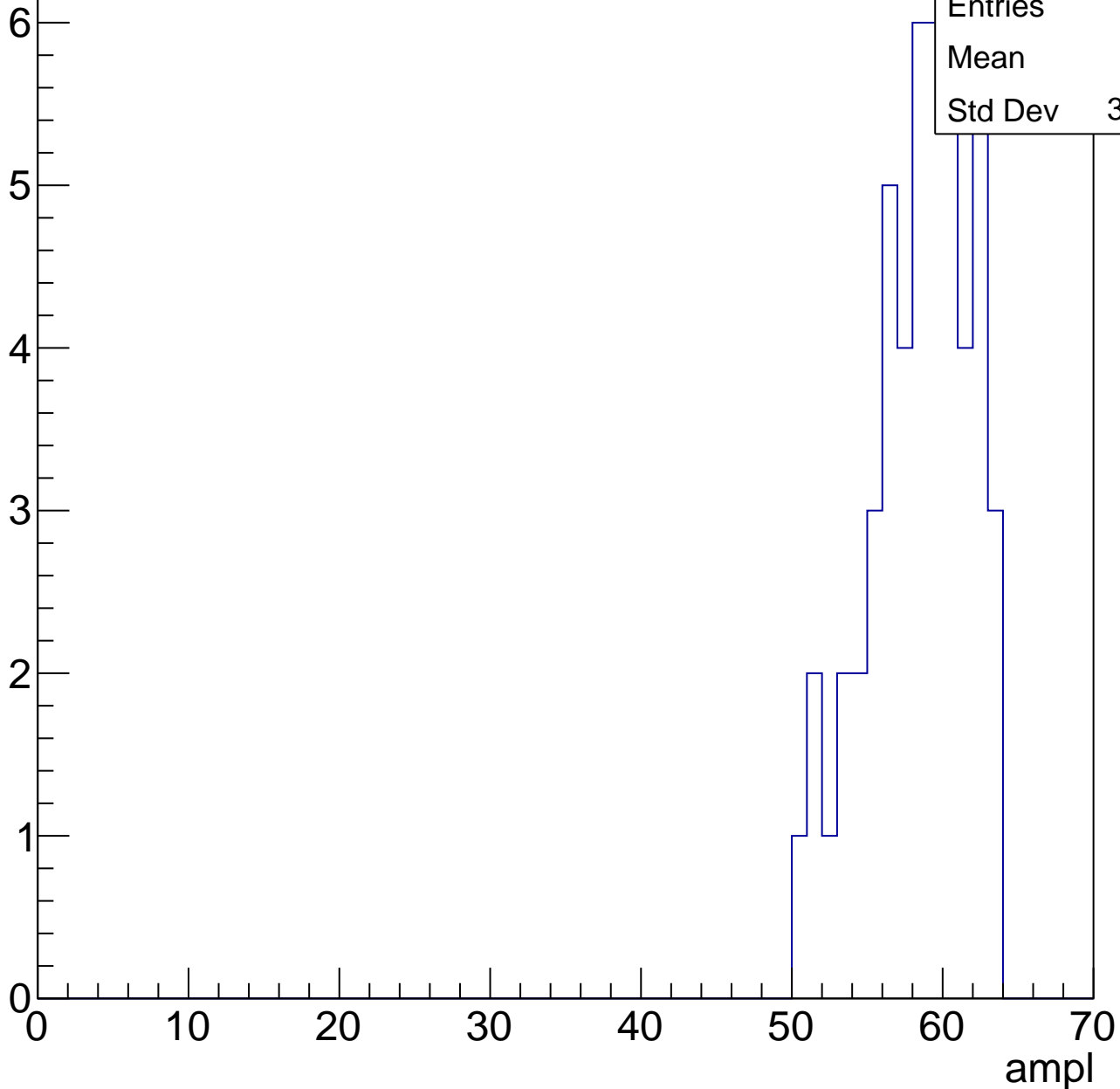


B1L103S, U1-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58
Std Dev	3.337

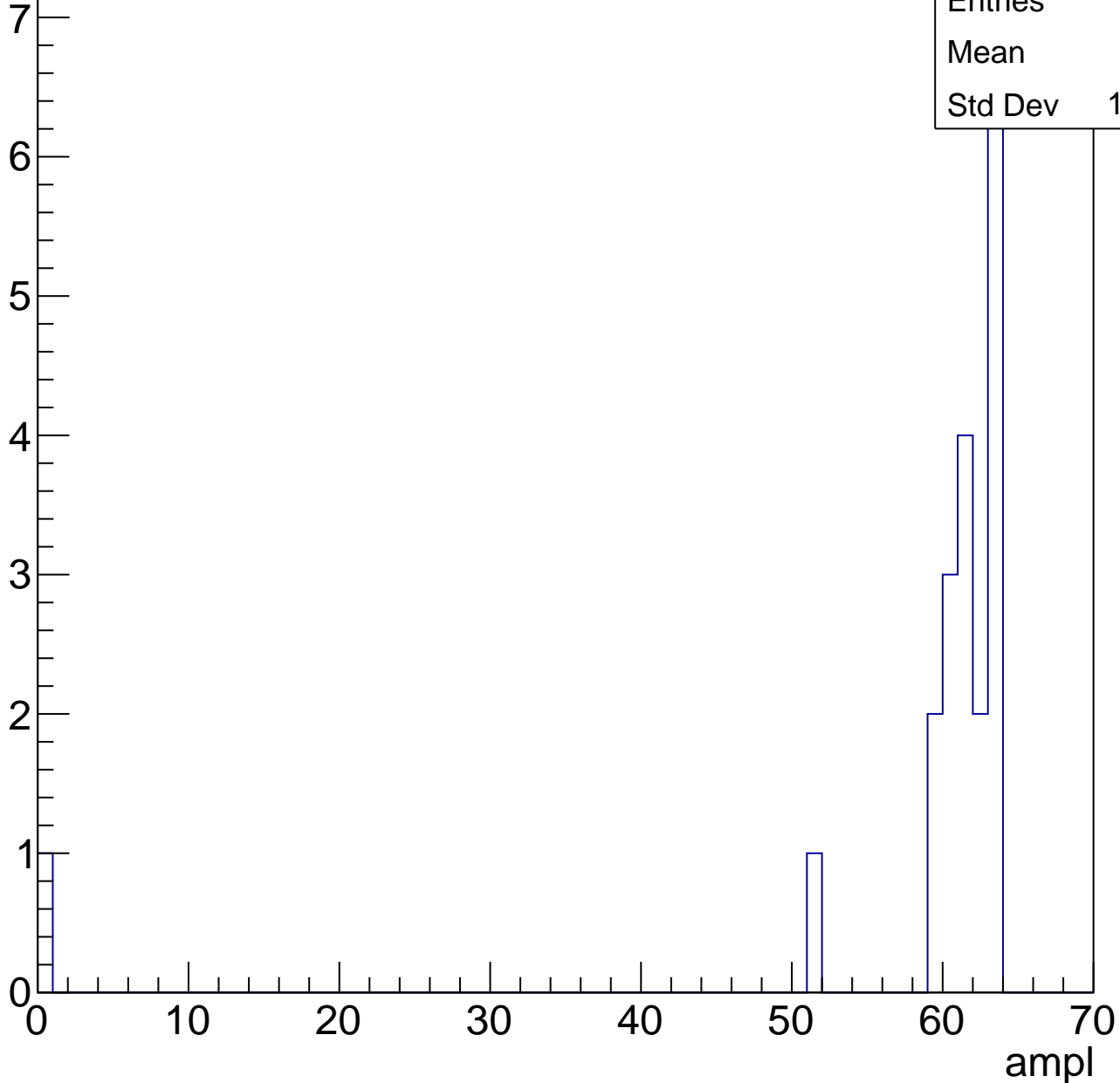


B1L103S, U1-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	57.9
Std Dev	13.55

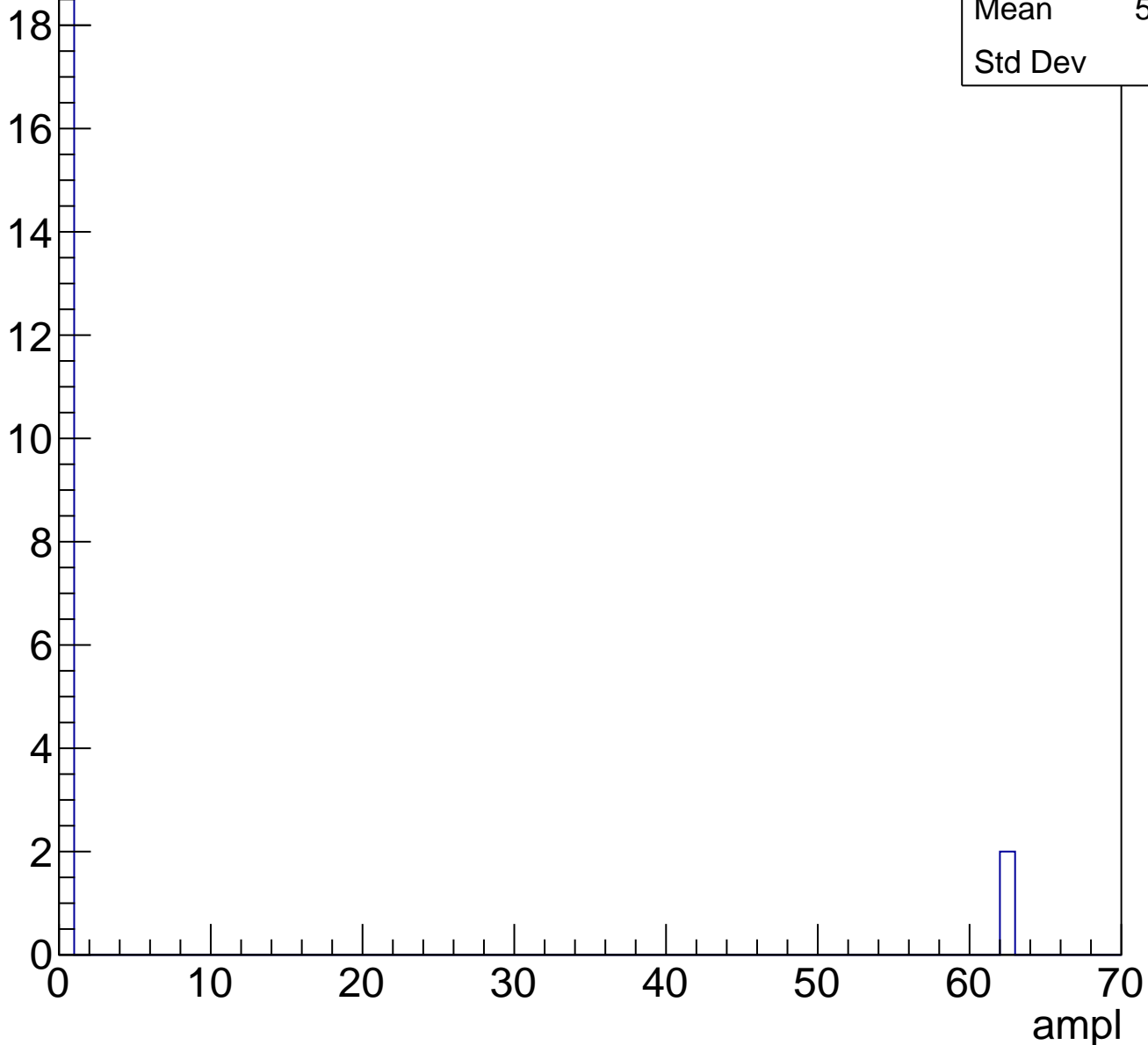


B1L103S, U1-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U1-ch94, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	21.96
Std Dev	11.22

Gaus mean : 27.9831

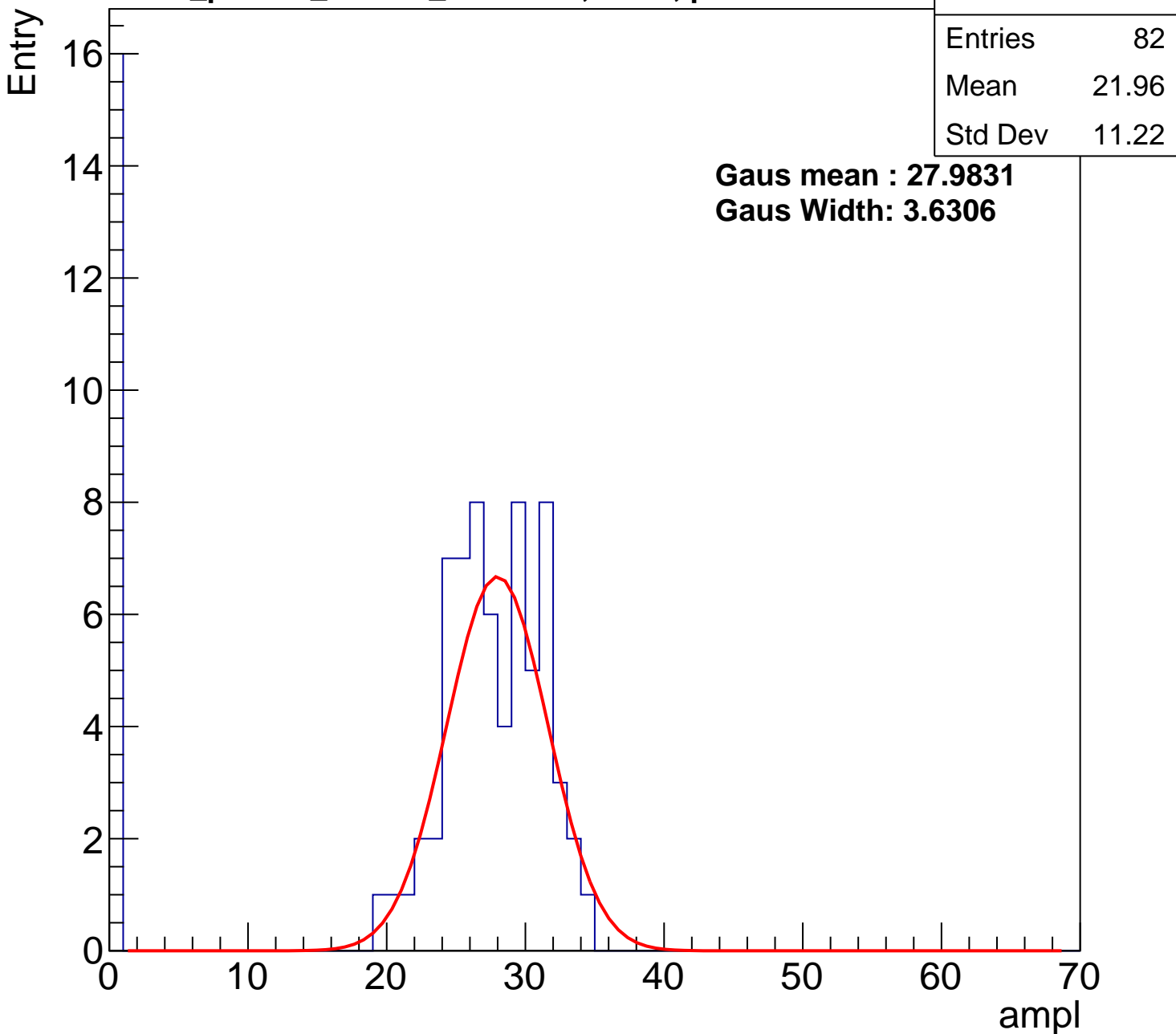
Gaus Width: 3.6306

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch94, adc1

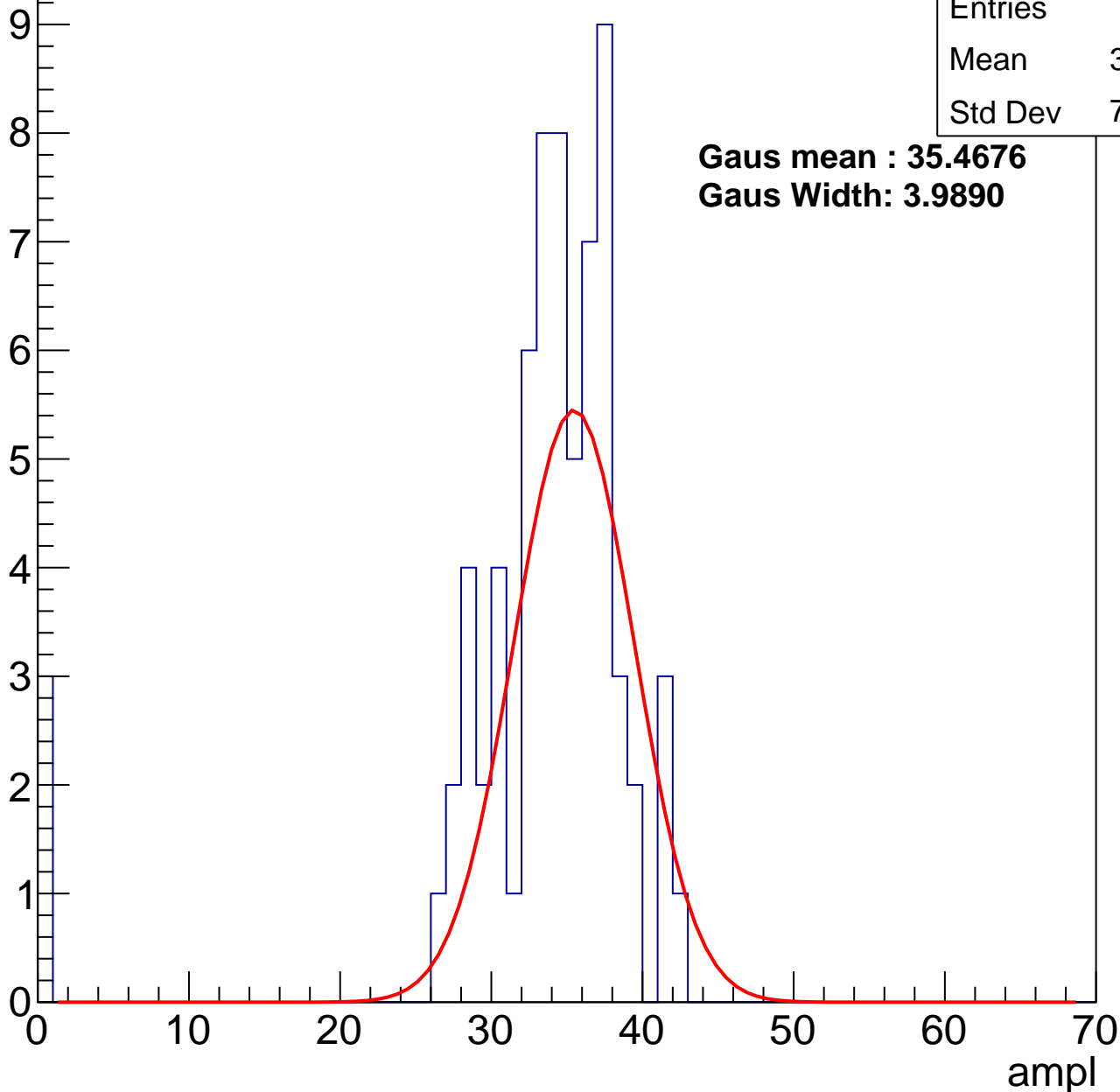
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	32.55
Std Dev	7.808

Gaus mean : 35.4676

Gaus Width: 3.9890



B1L103S, U1-ch94, adc2

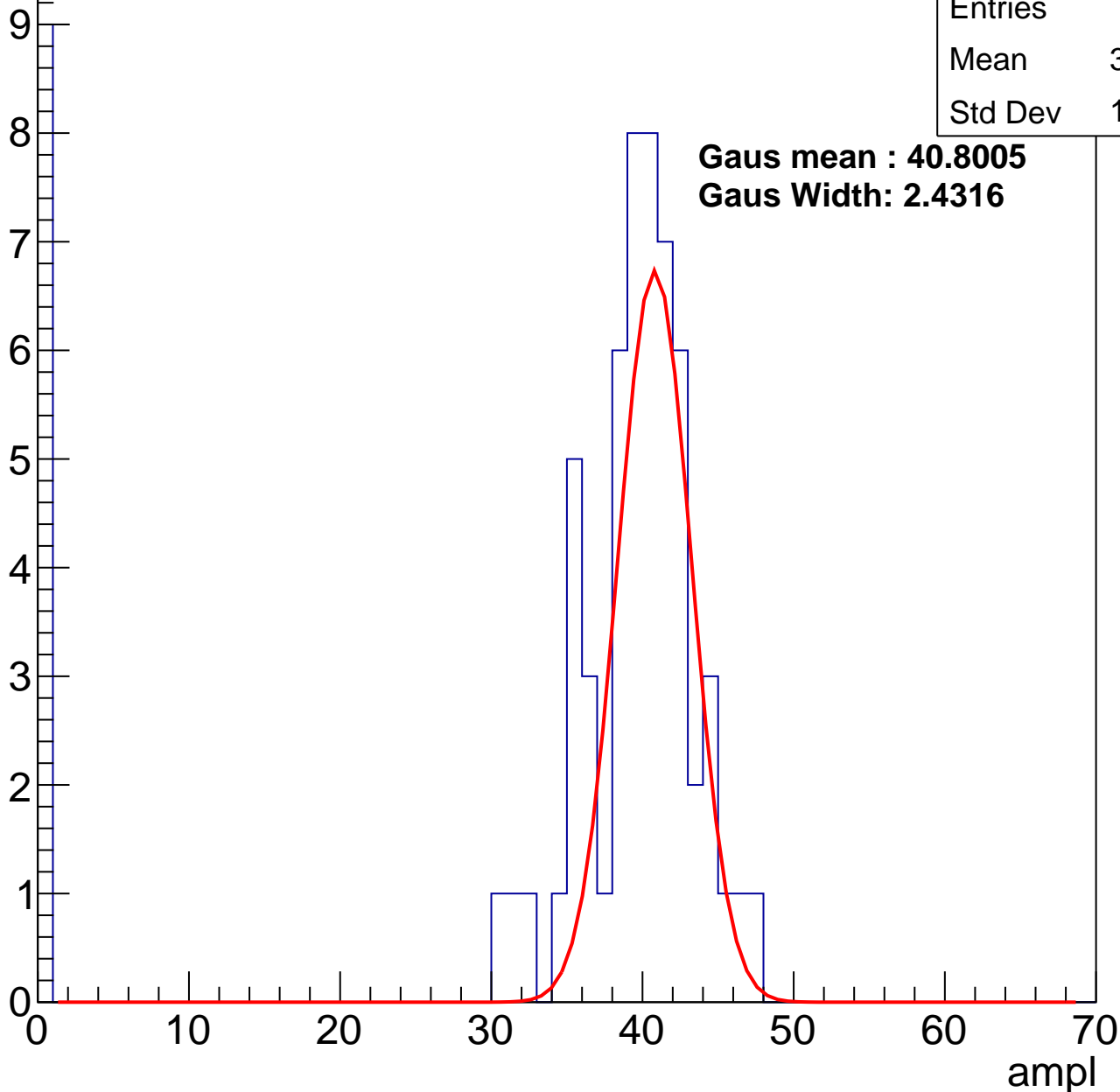
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	33.88
Std Dev	13.96

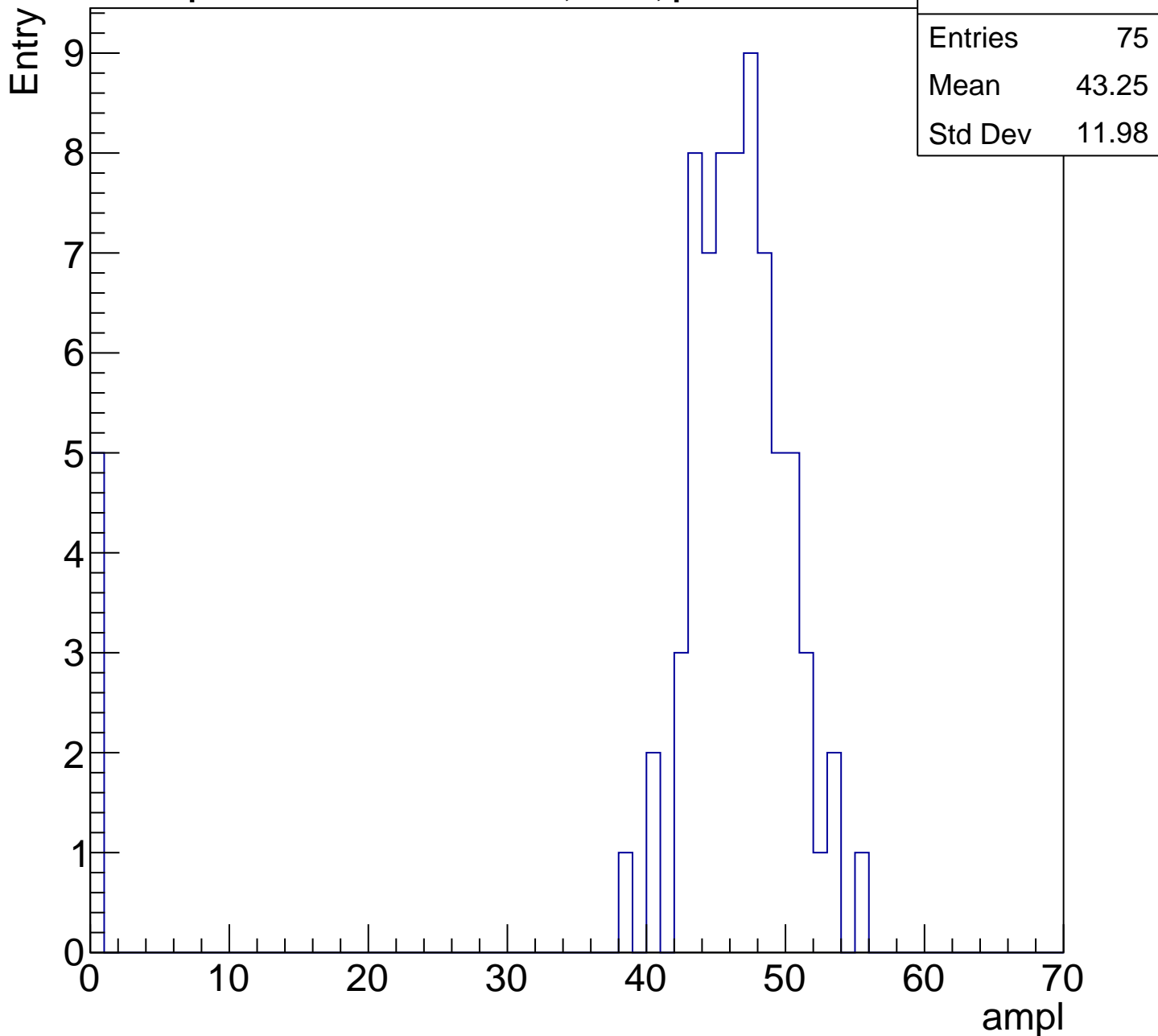
Gaus mean : 40.8005

Gaus Width: 2.4316



B1L103S, U1-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

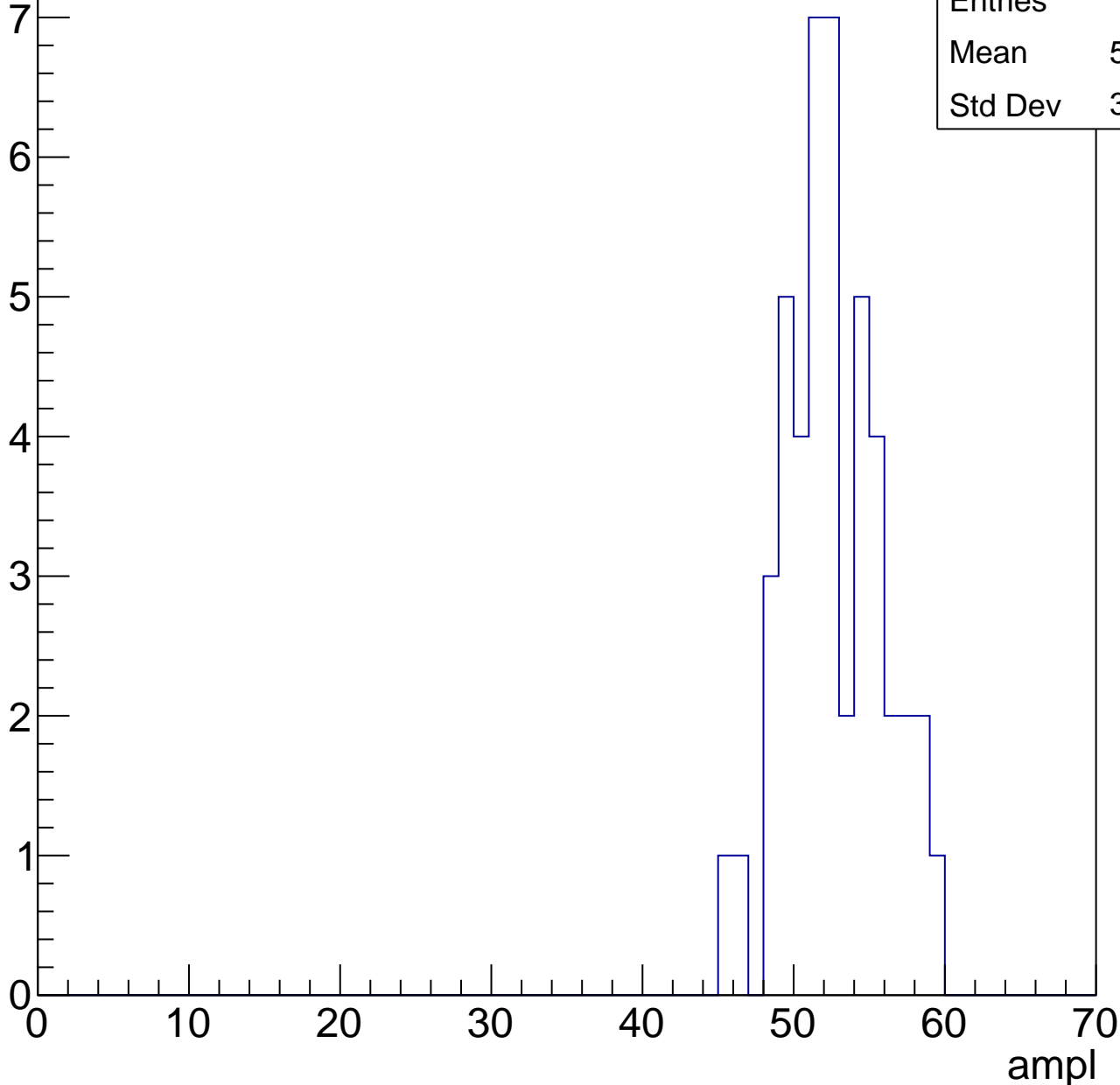


B1L103S, U1-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	52.13
Std Dev	3.166

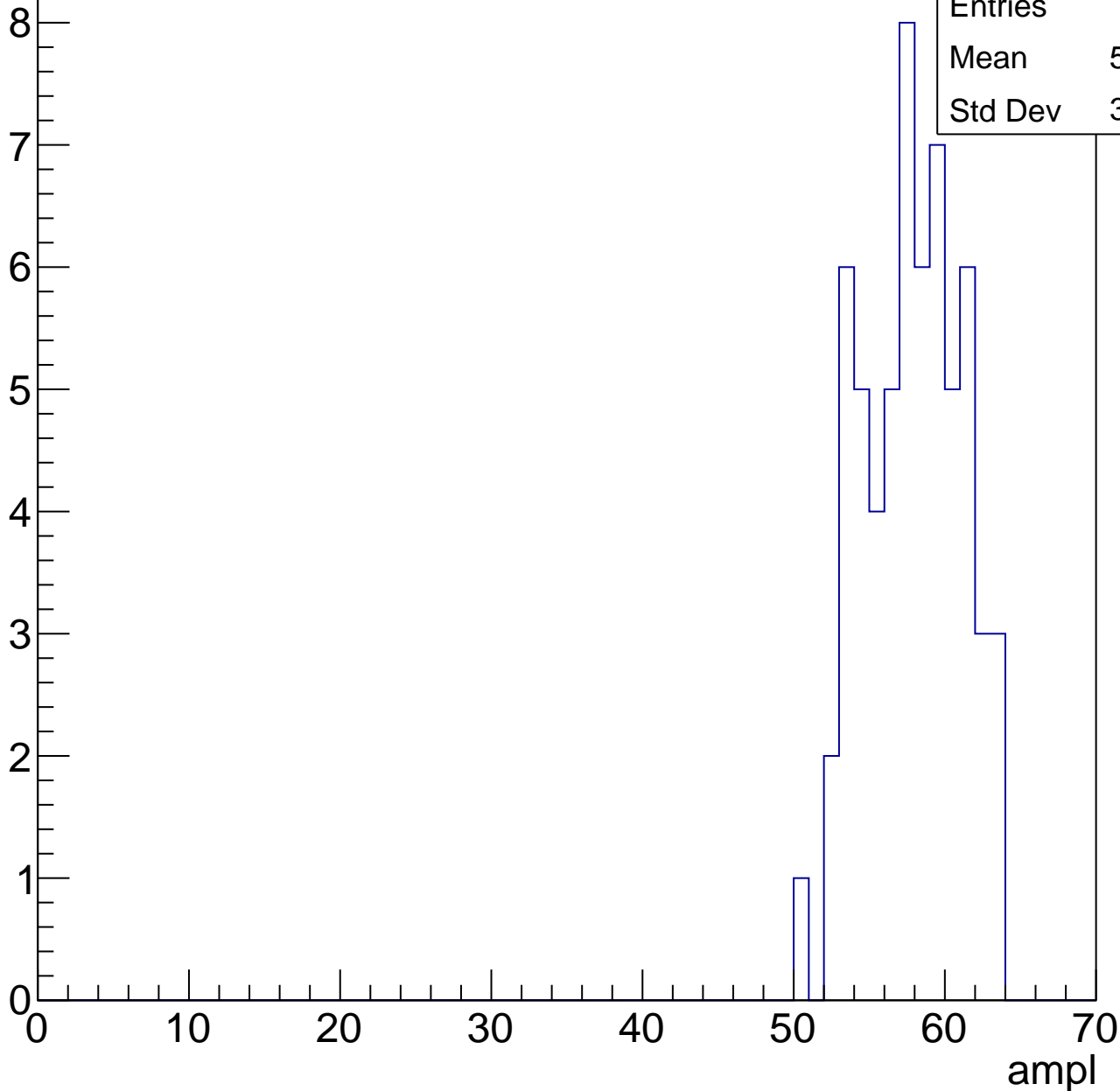


B1L103S, U1-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.38
Std Dev	3.163

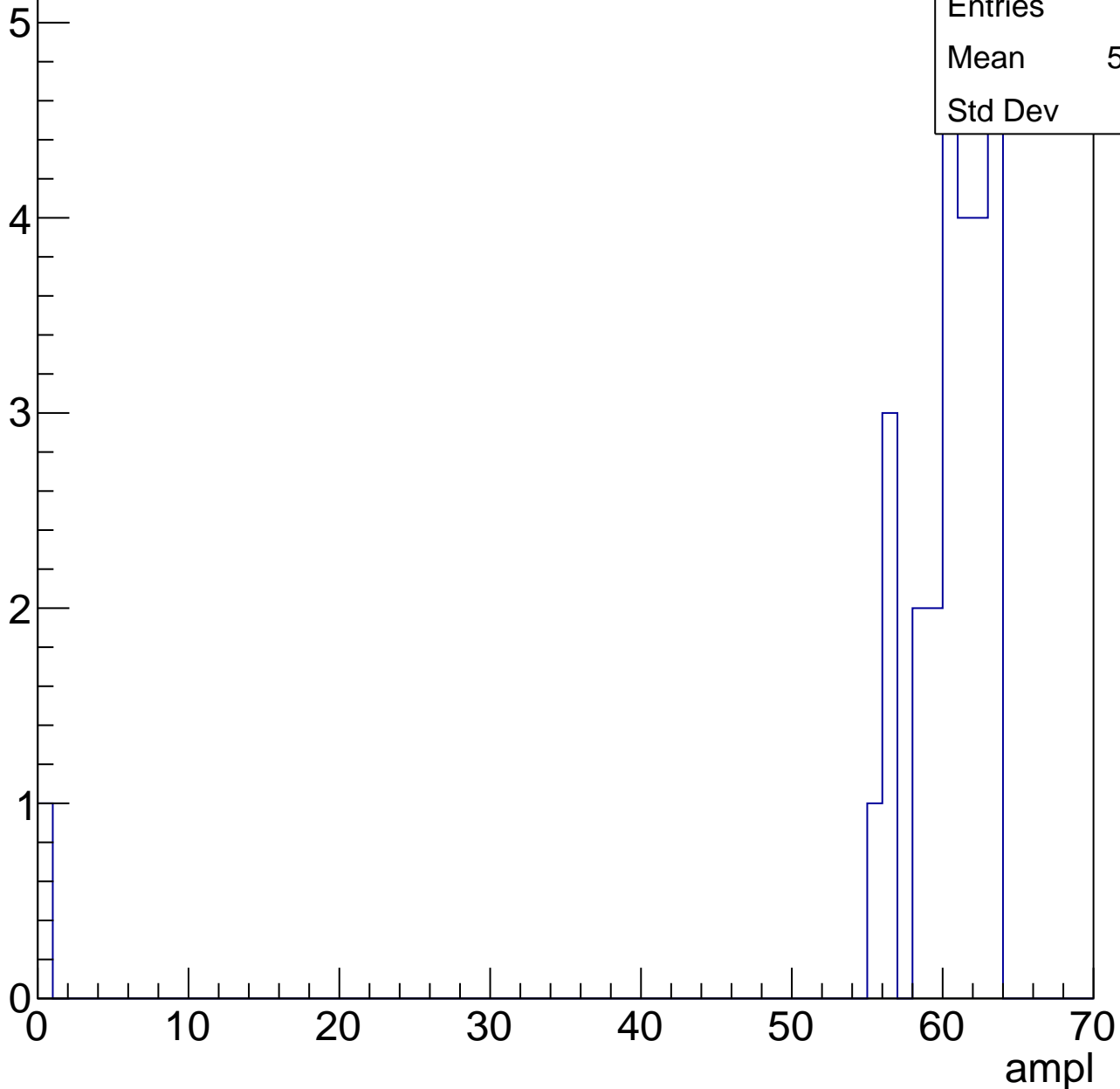


B1L103S, U1-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	57.93
Std Dev	11.6

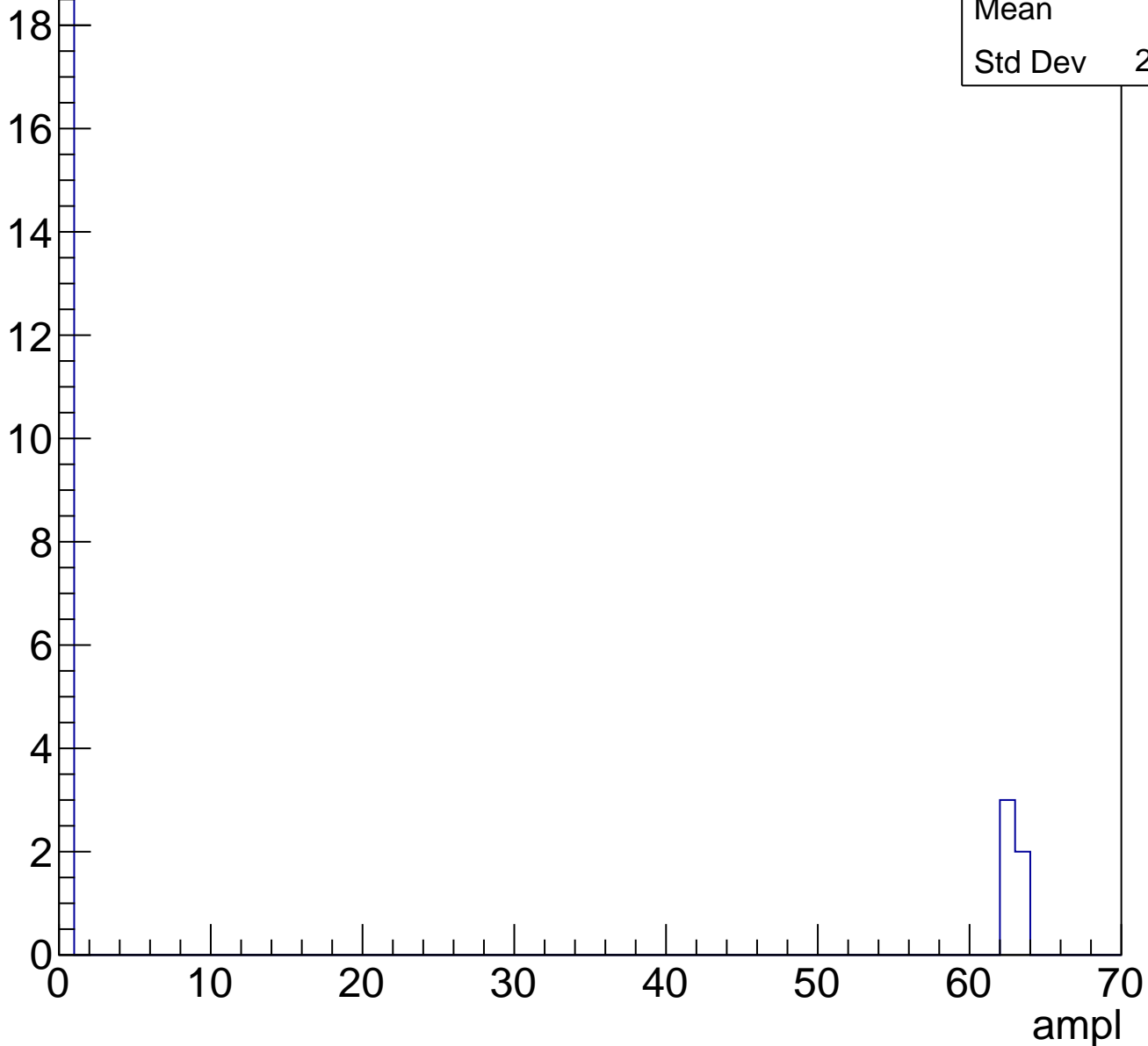


B1L103S, U1-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	13
Std Dev	25.34

Entry



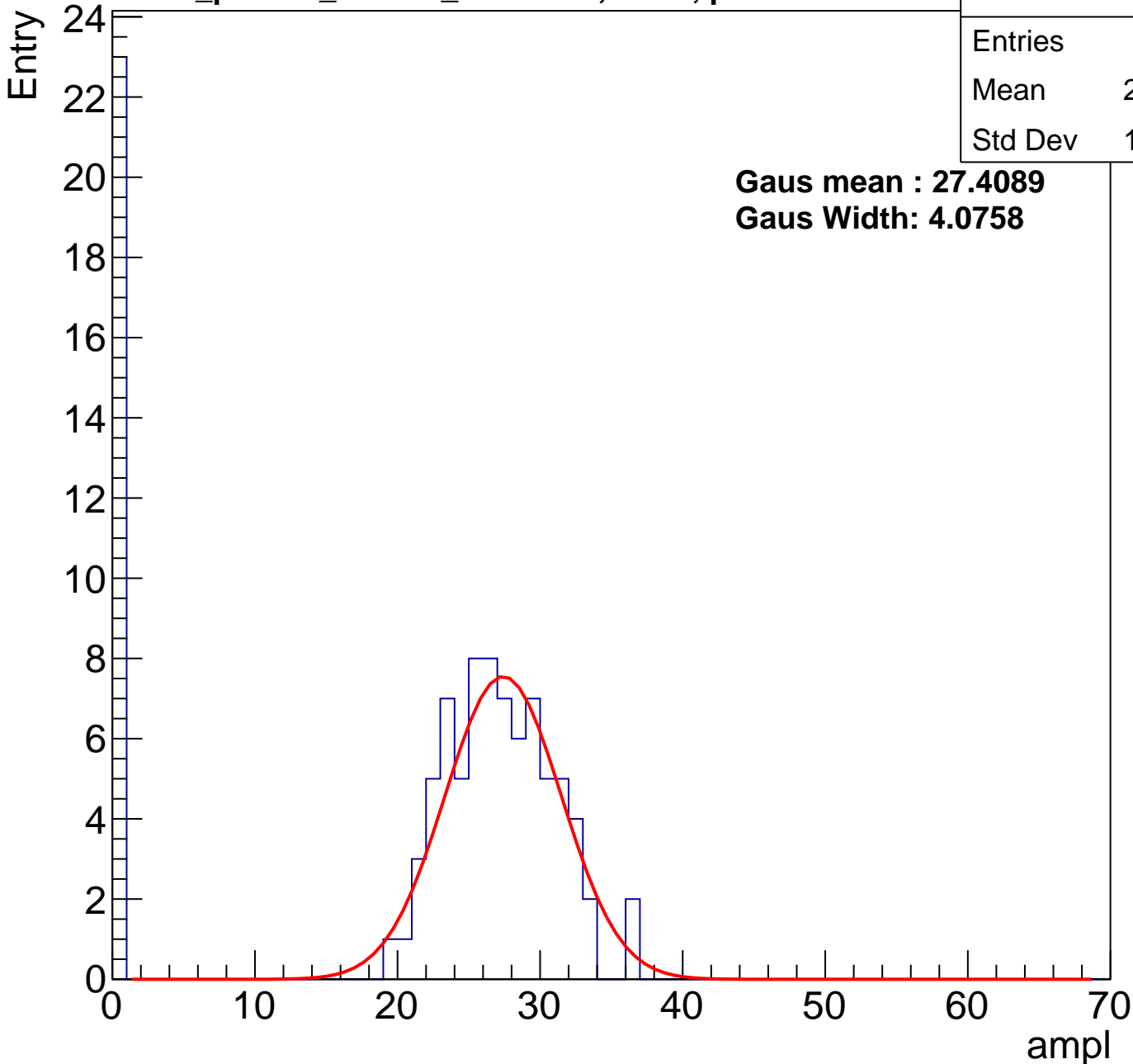
B1L103S, U1-ch95, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	20.53
Std Dev	11.74

Gaus mean : 27.4089

Gaus Width: 4.0758



B1L103S, U1-ch95, adc1

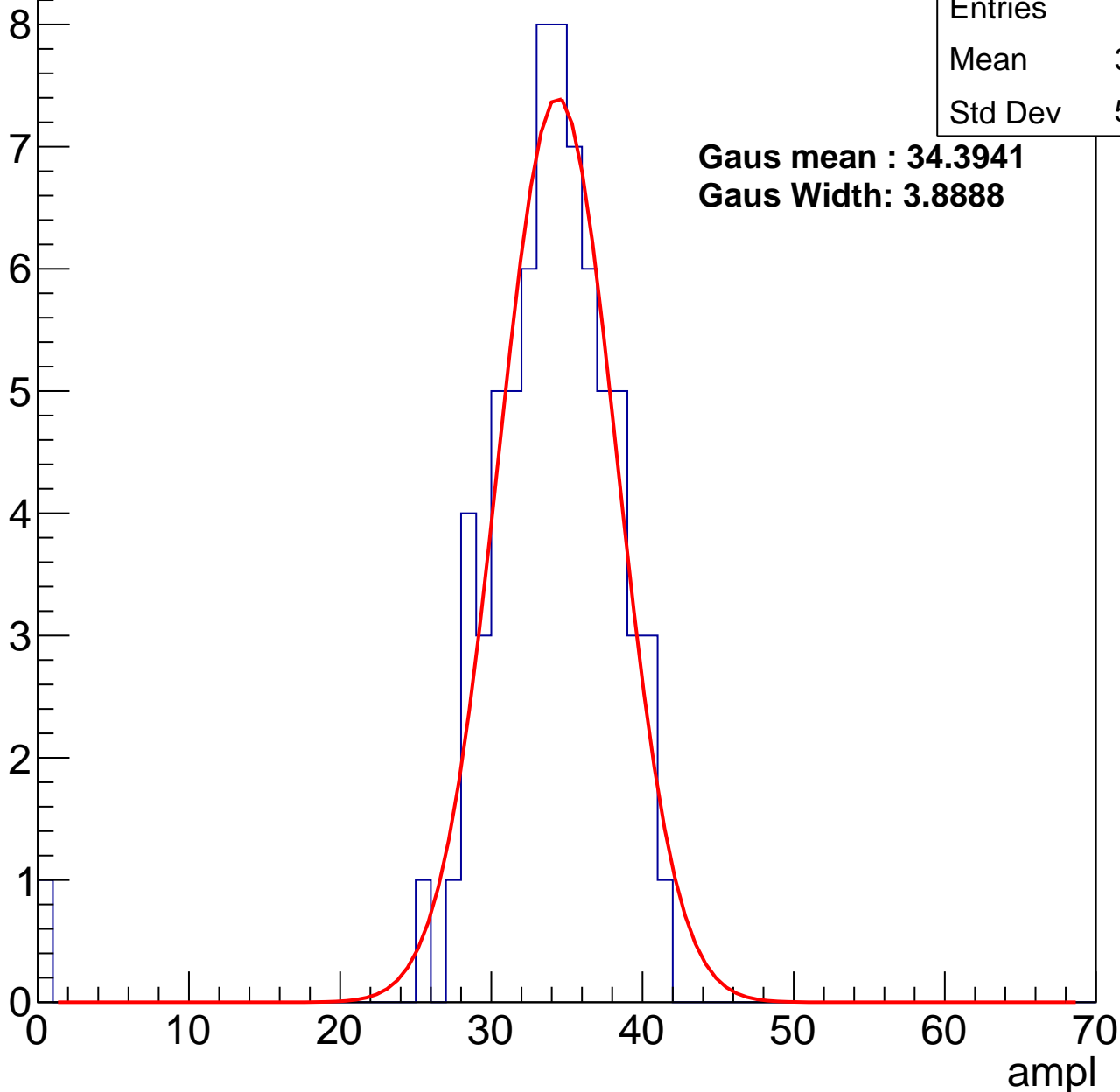
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	33.31
Std Dev	5.291

Gaus mean : 34.3941

Gaus Width: 3.8888



B1L103S, U1-ch95, adc2

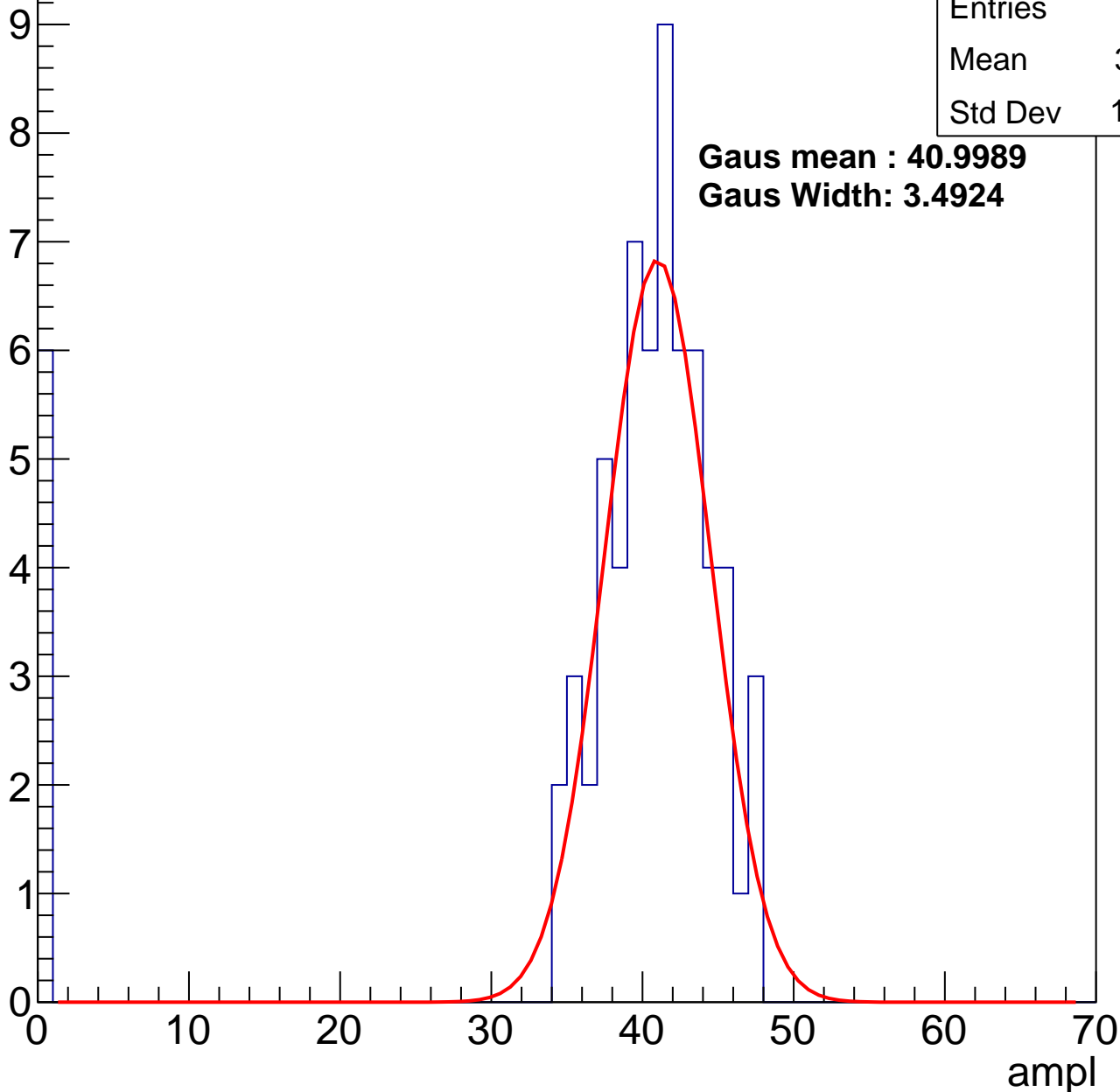
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.01
Std Dev	11.93

Gaus mean : 40.9989

Gaus Width: 3.4924

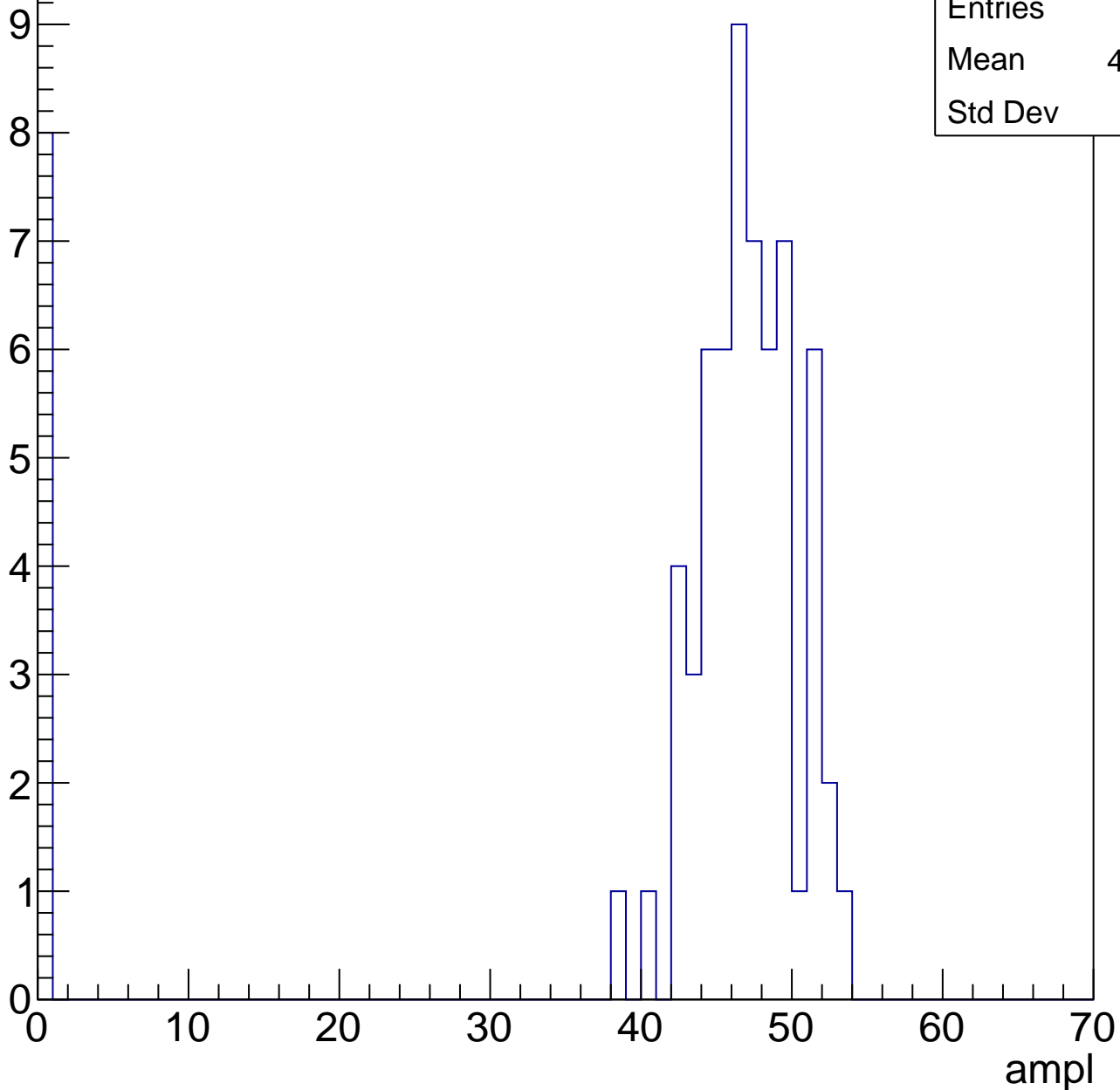


B1L103S, U1-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.12
Std Dev	15.3

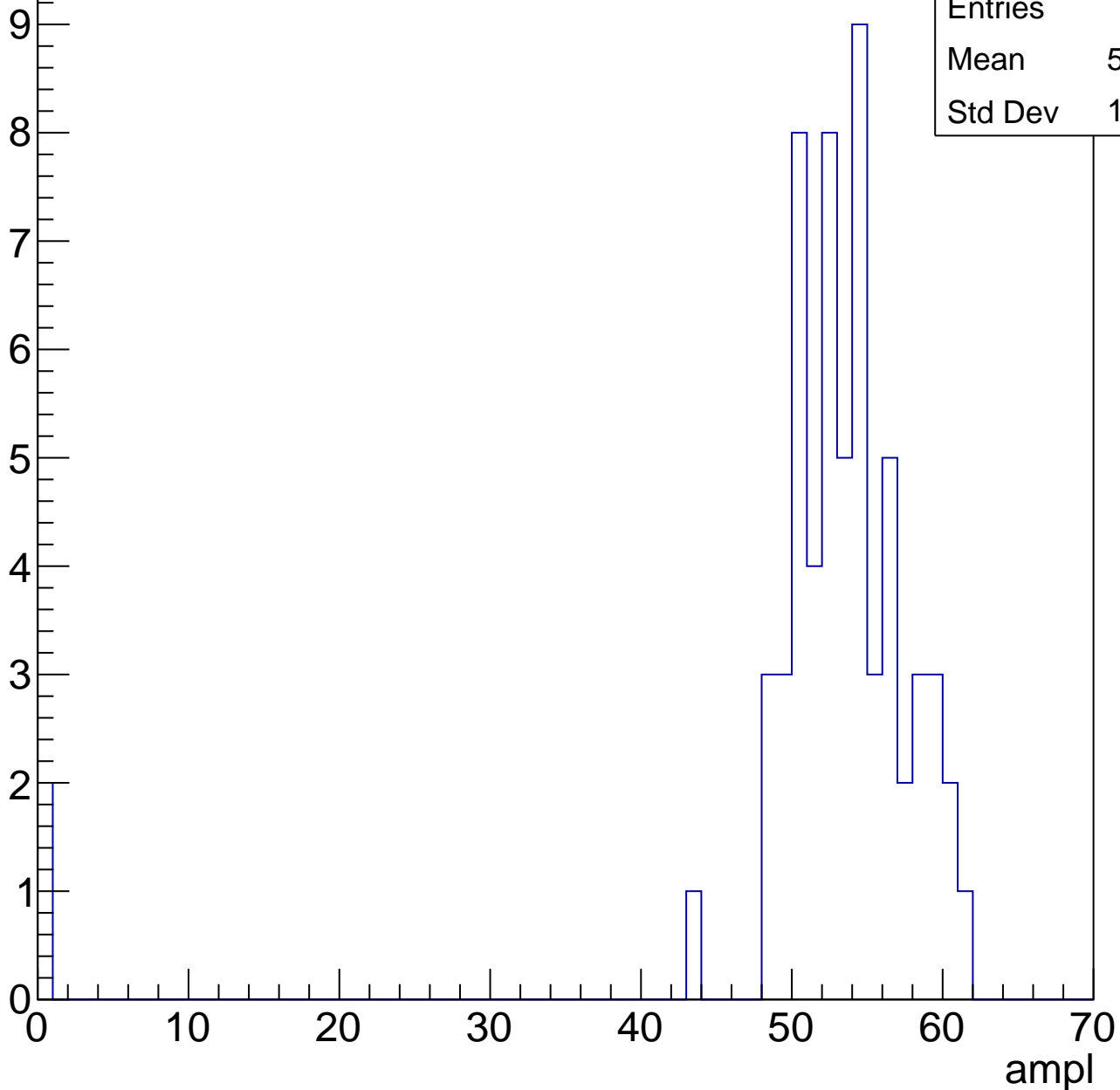


B1L103S, U1-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	51.55
Std Dev	10.04

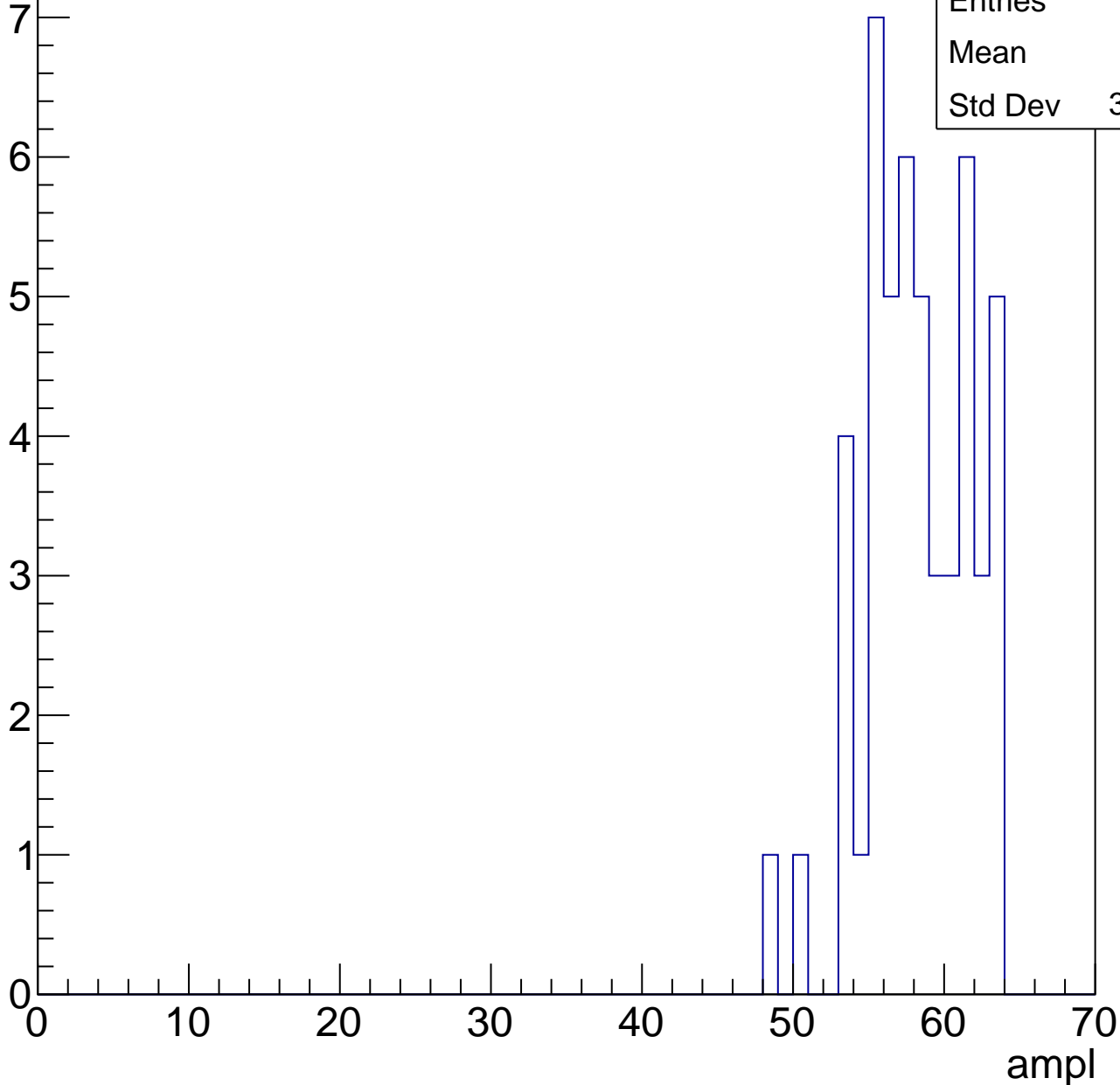


B1L103S, U1-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.7
Std Dev	3.483

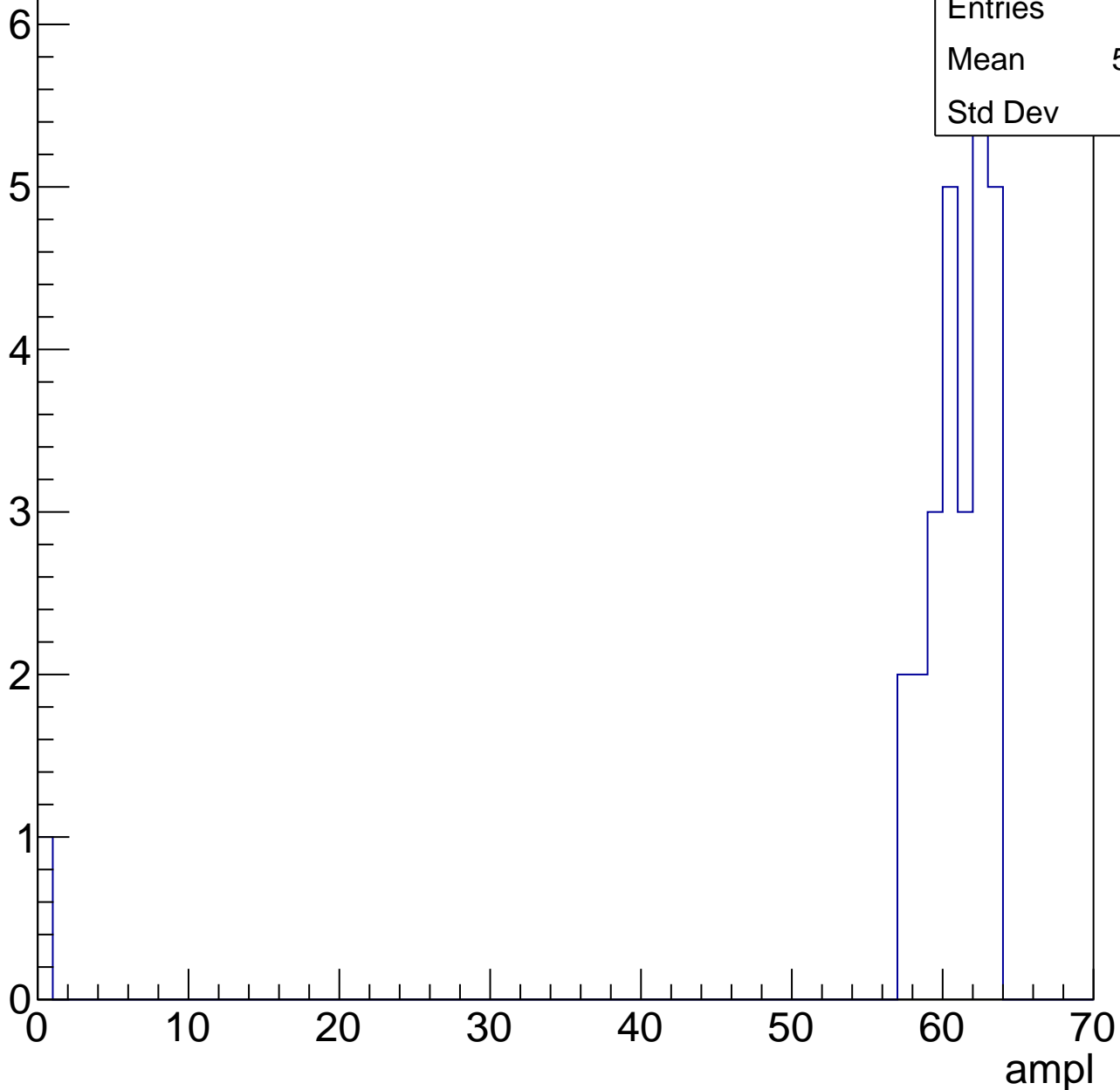


B1L103S, U1-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

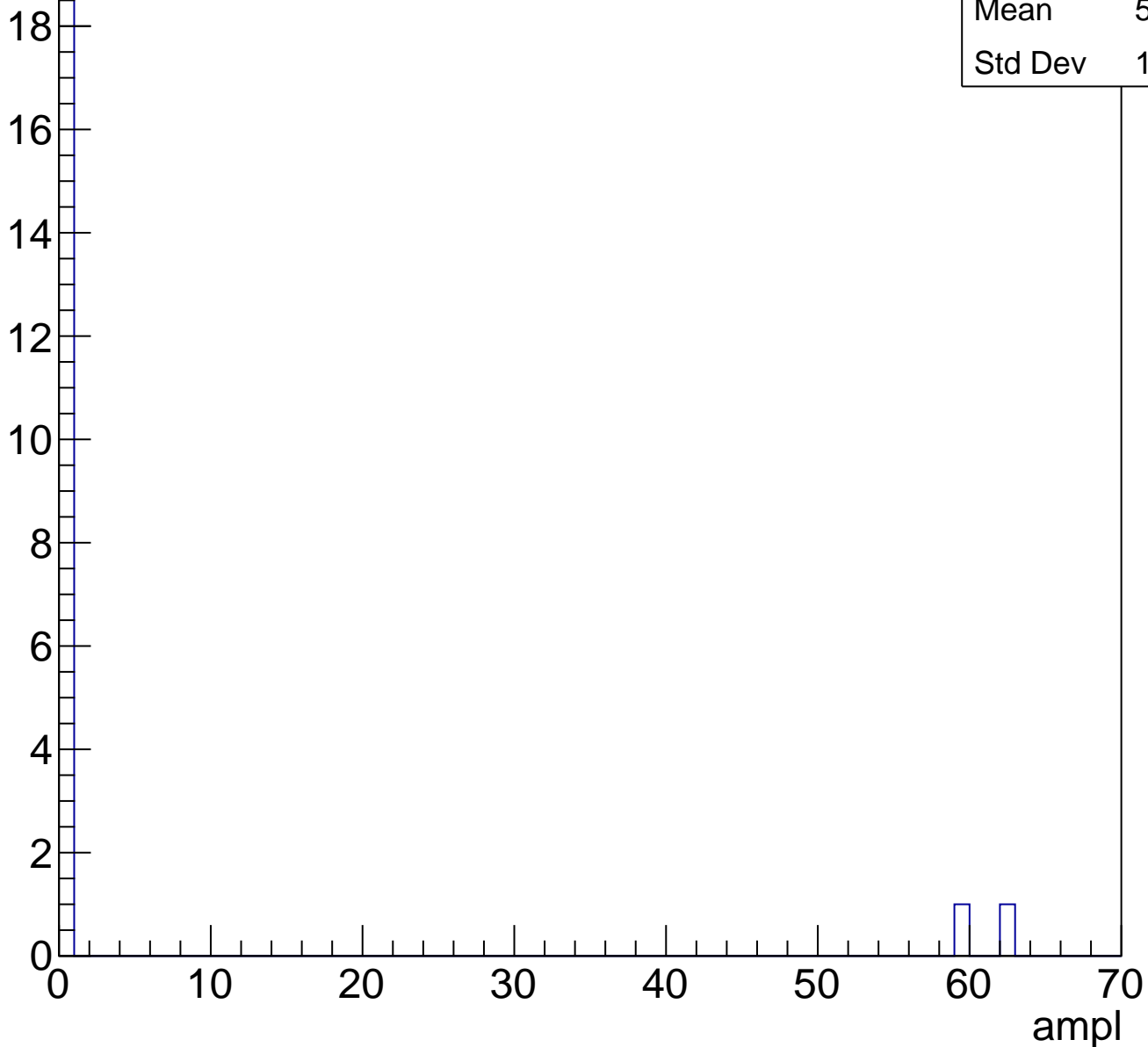
Entries	27
Mean	58.41
Std Dev	11.6



B1L103S, U1-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	5.762
Std Dev	17.77

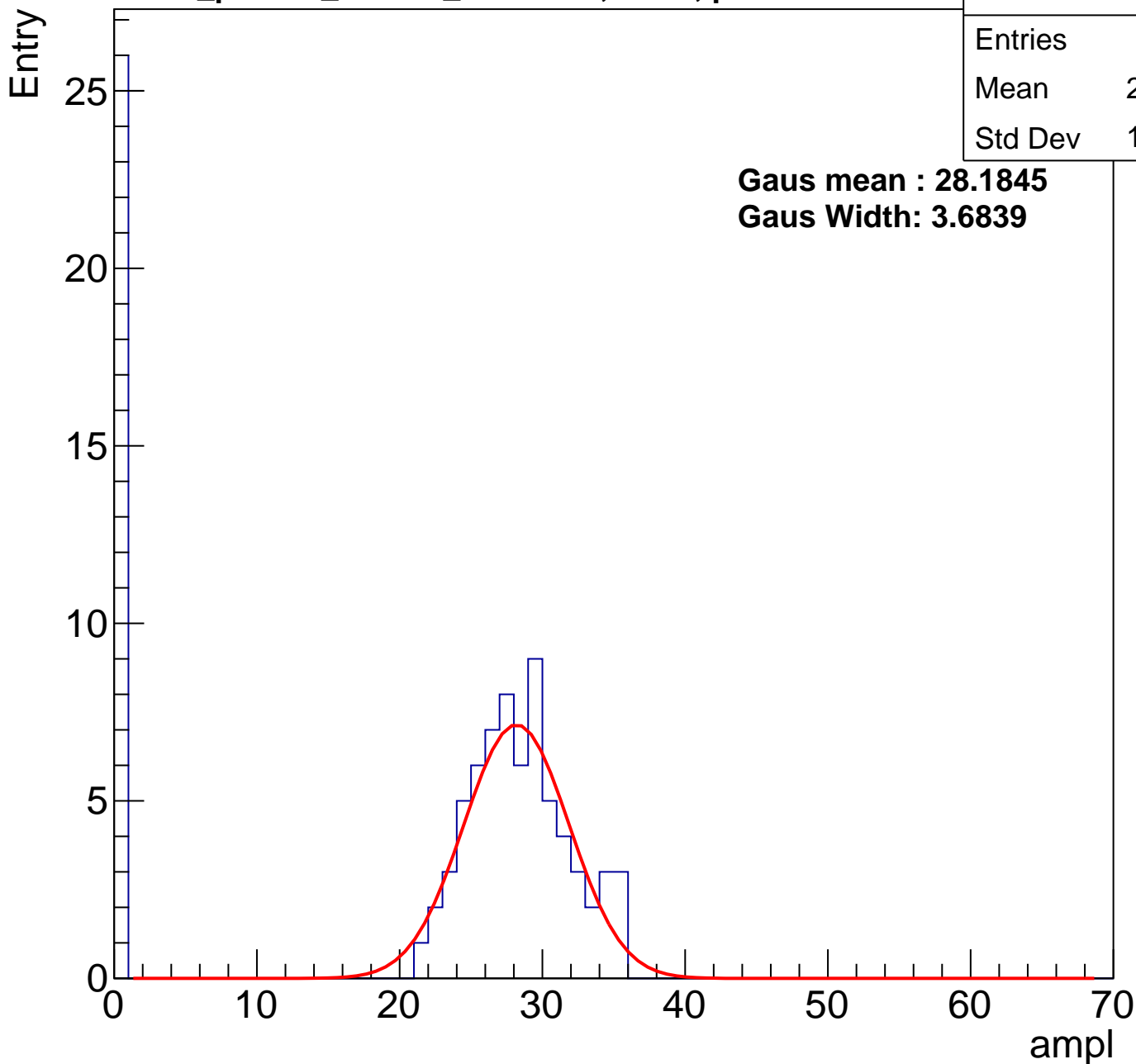
B1L103S, U1-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	20.15
Std Dev	12.88

Gaus mean : 28.1845

Gaus Width: 3.6839



B1L103S, U1-ch96, adc1

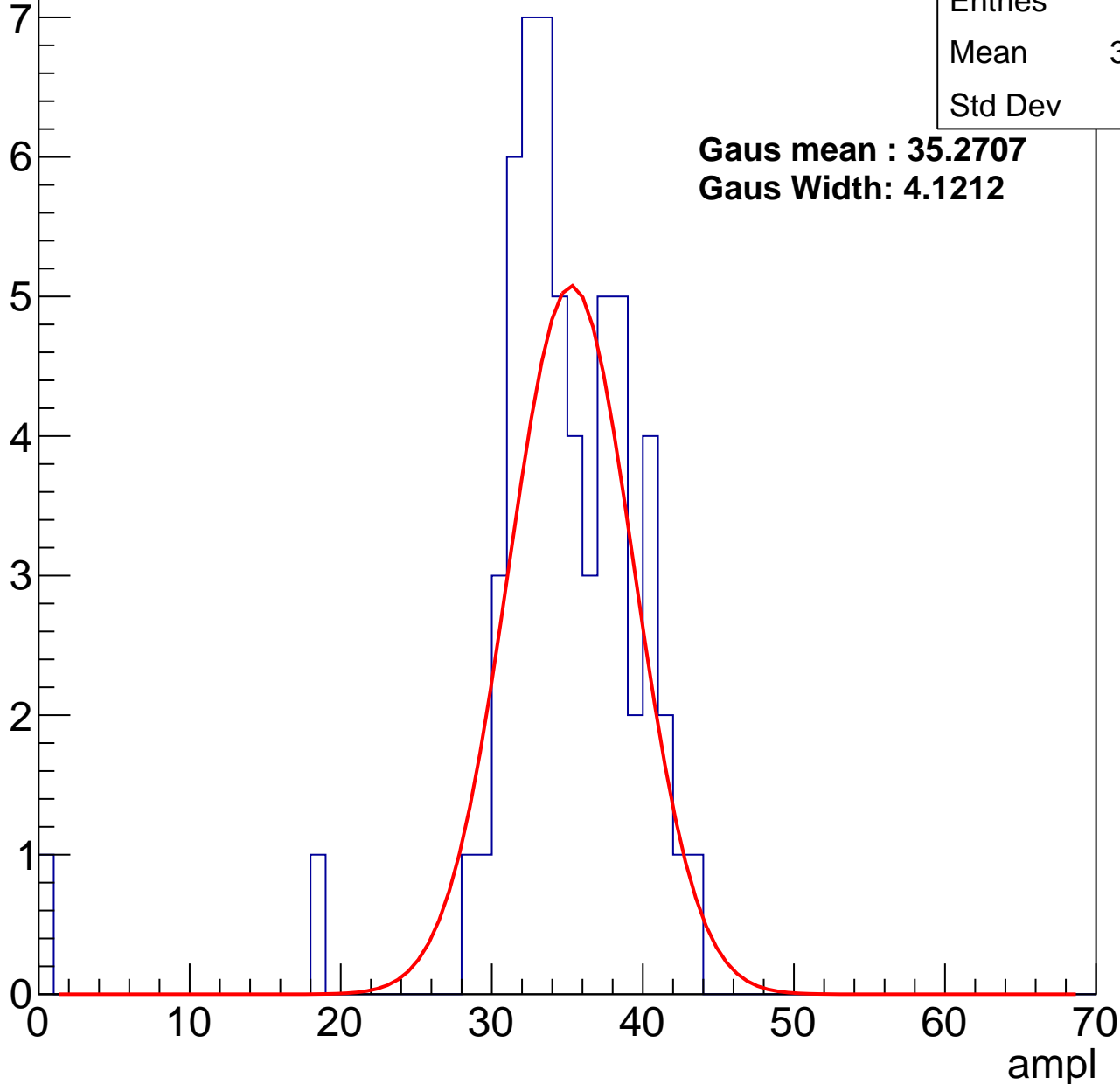
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	33.97
Std Dev	6.09

Gaus mean : 35.2707

Gaus Width: 4.1212



B1L103S, U1-ch96, adc2

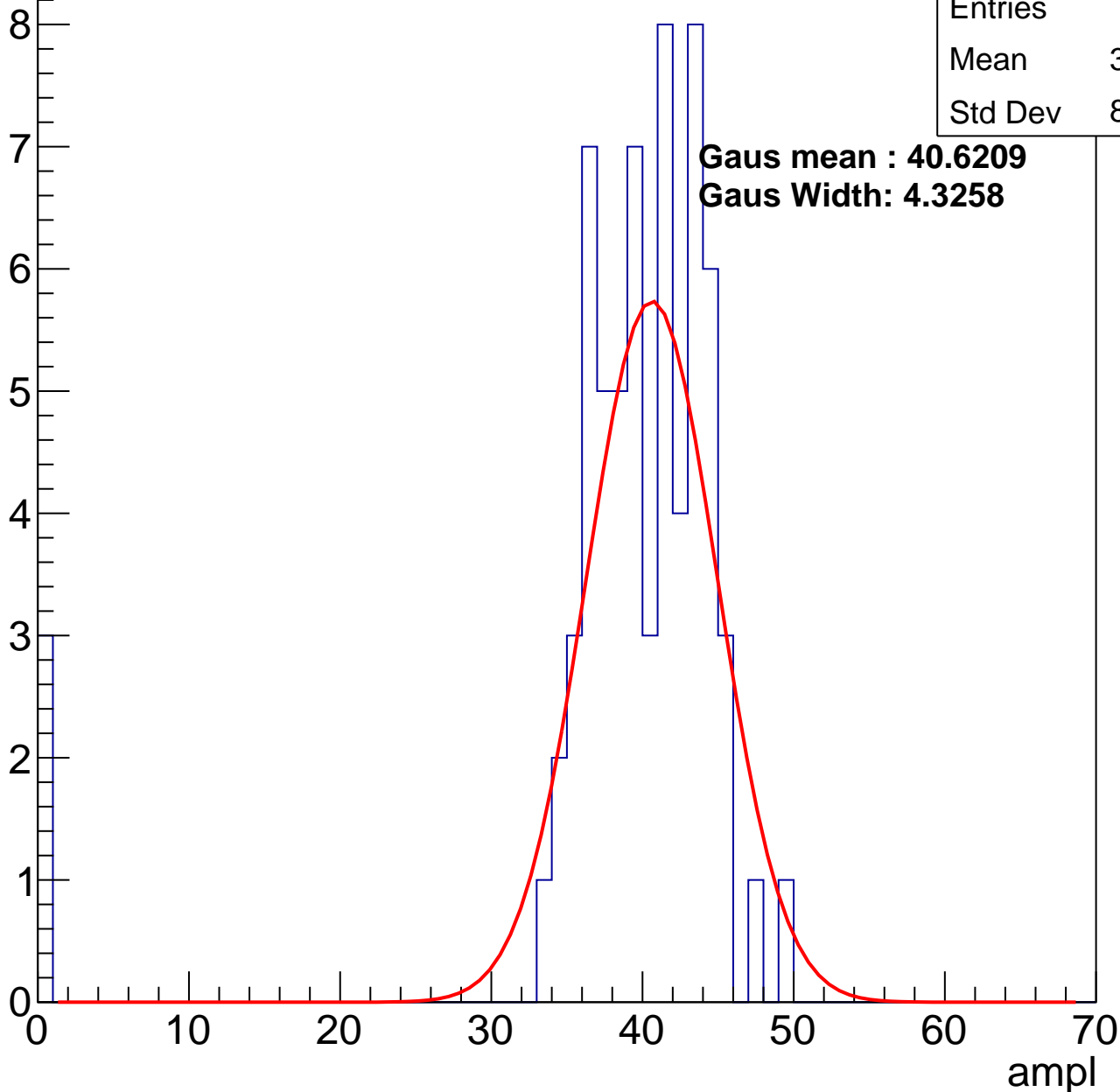
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	38.22
Std Dev	8.949

Gaus mean : 40.6209

Gaus Width: 4.3258

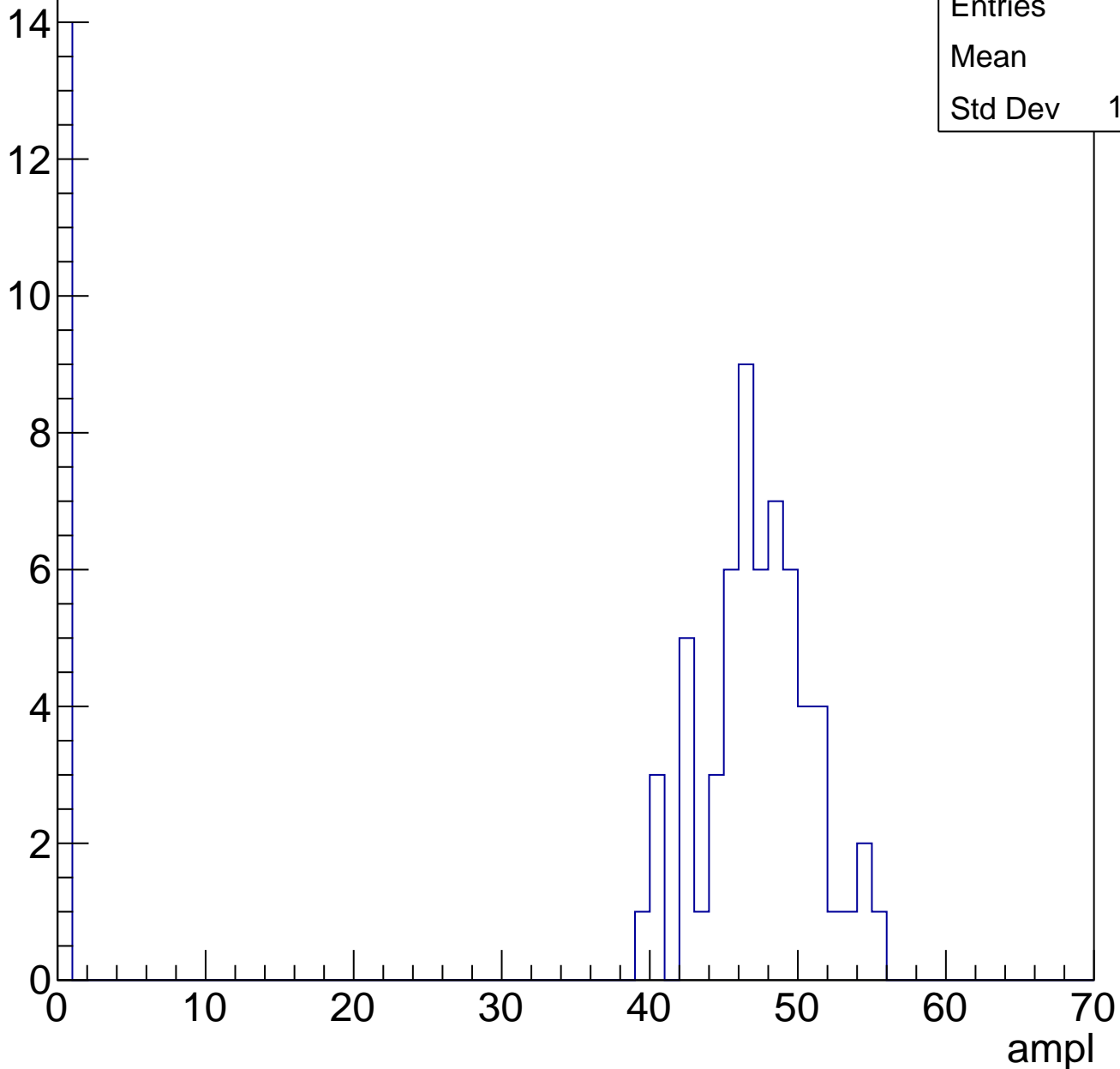


B1L103S, U1-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	38
Std Dev	18.64

Entry

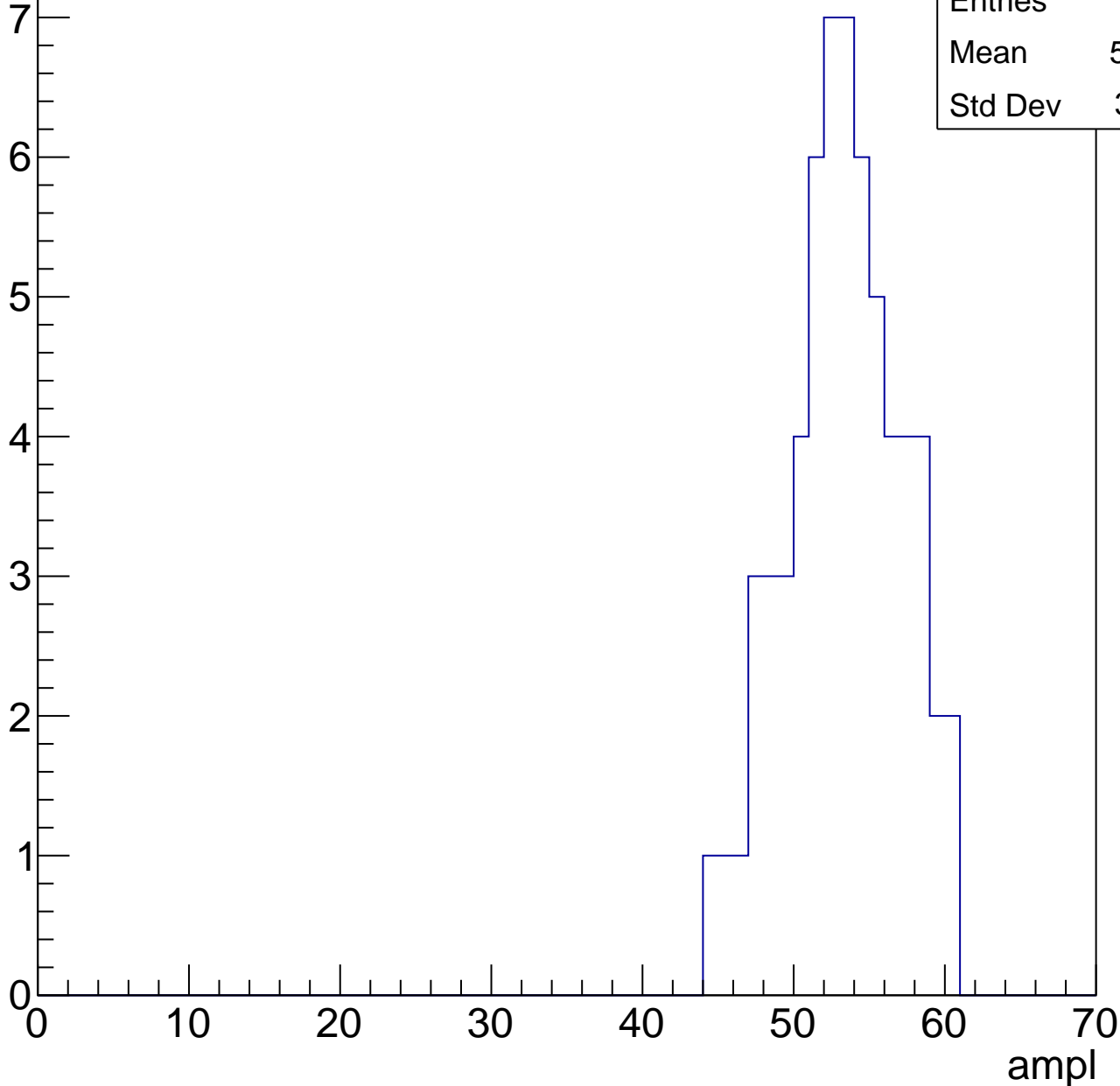


B1L103S, U1-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.84
Std Dev	3.751

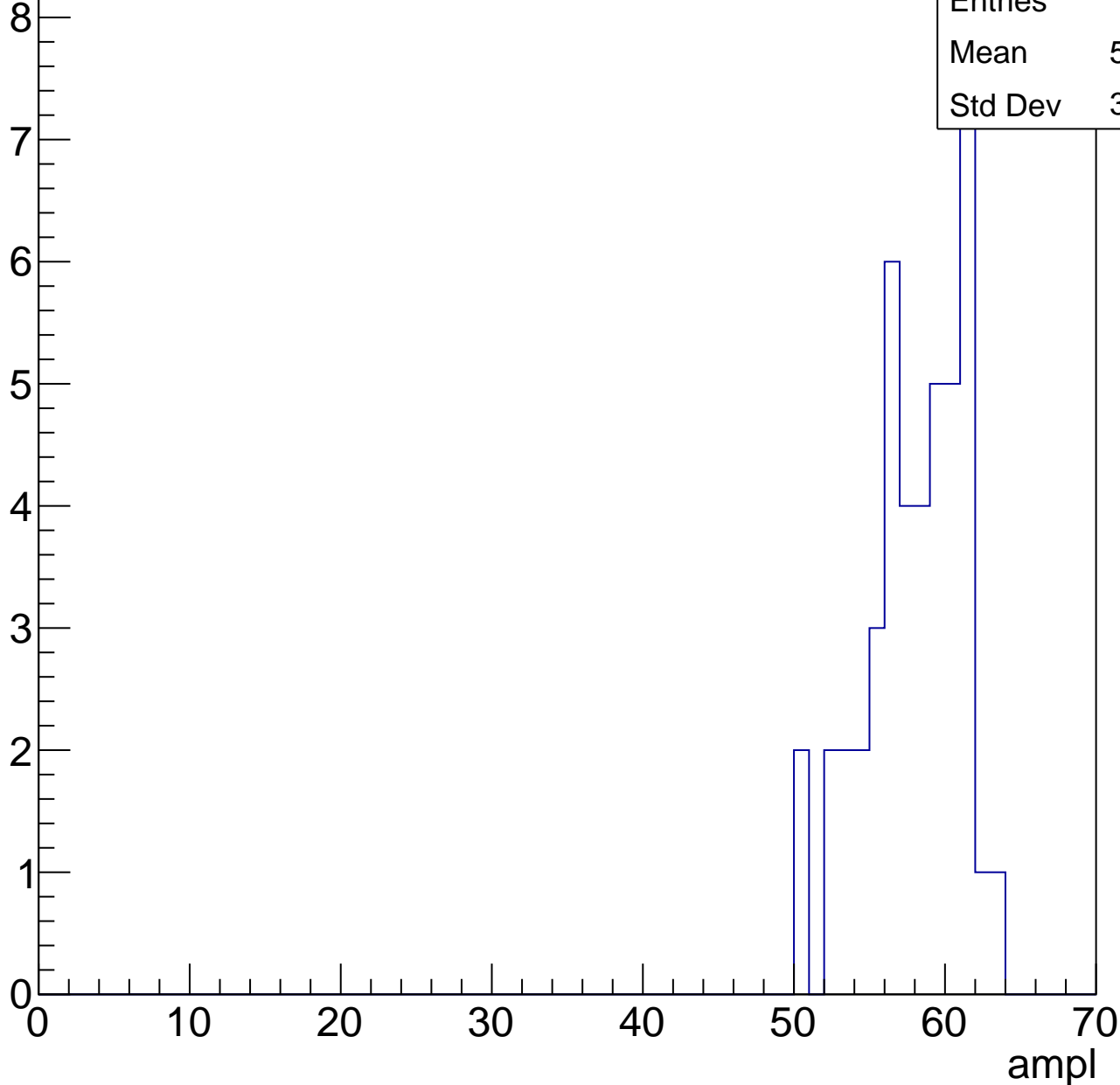


B1L103S, U1-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	57.49
Std Dev	3.208

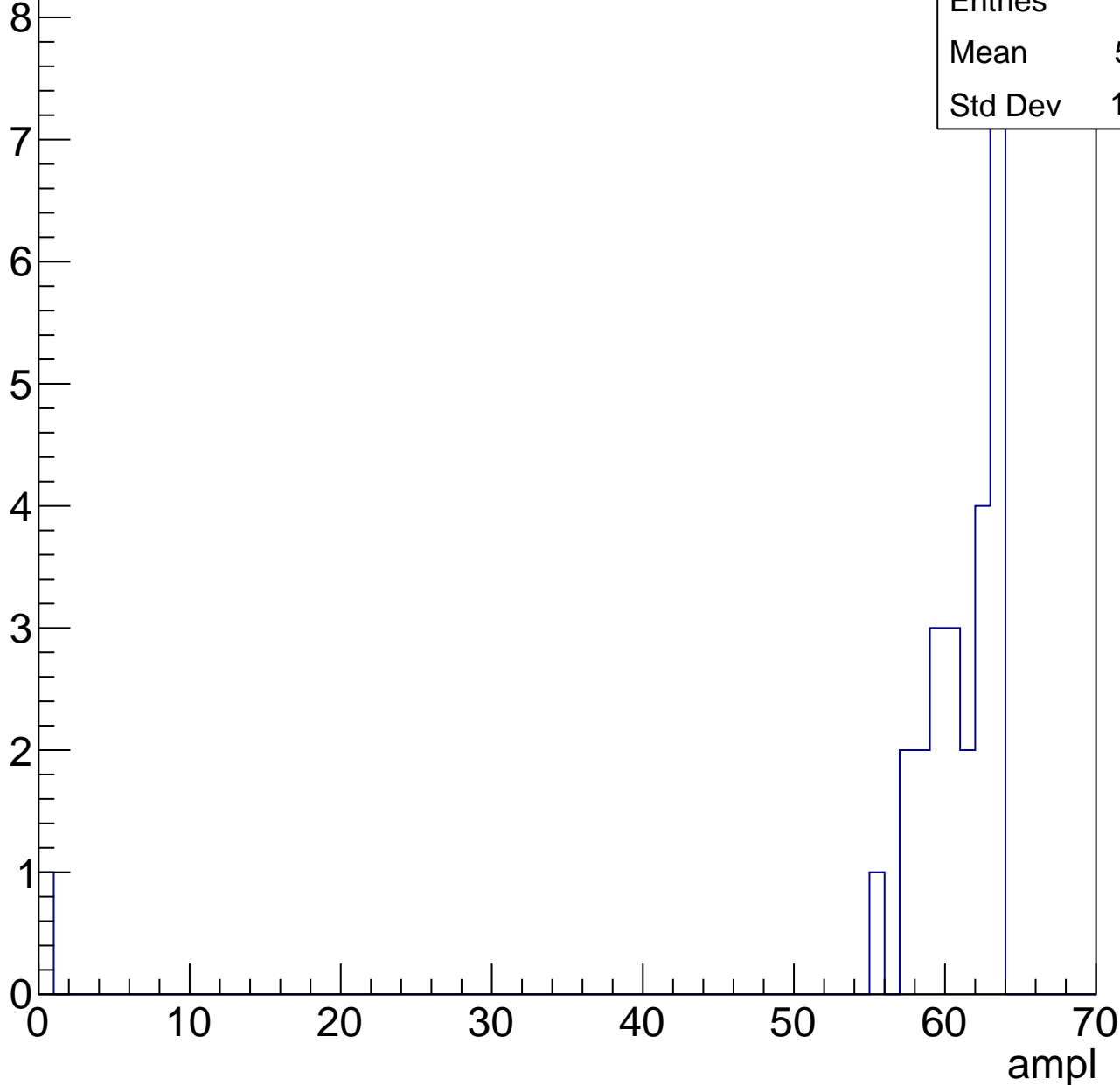


B1L103S, U1-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

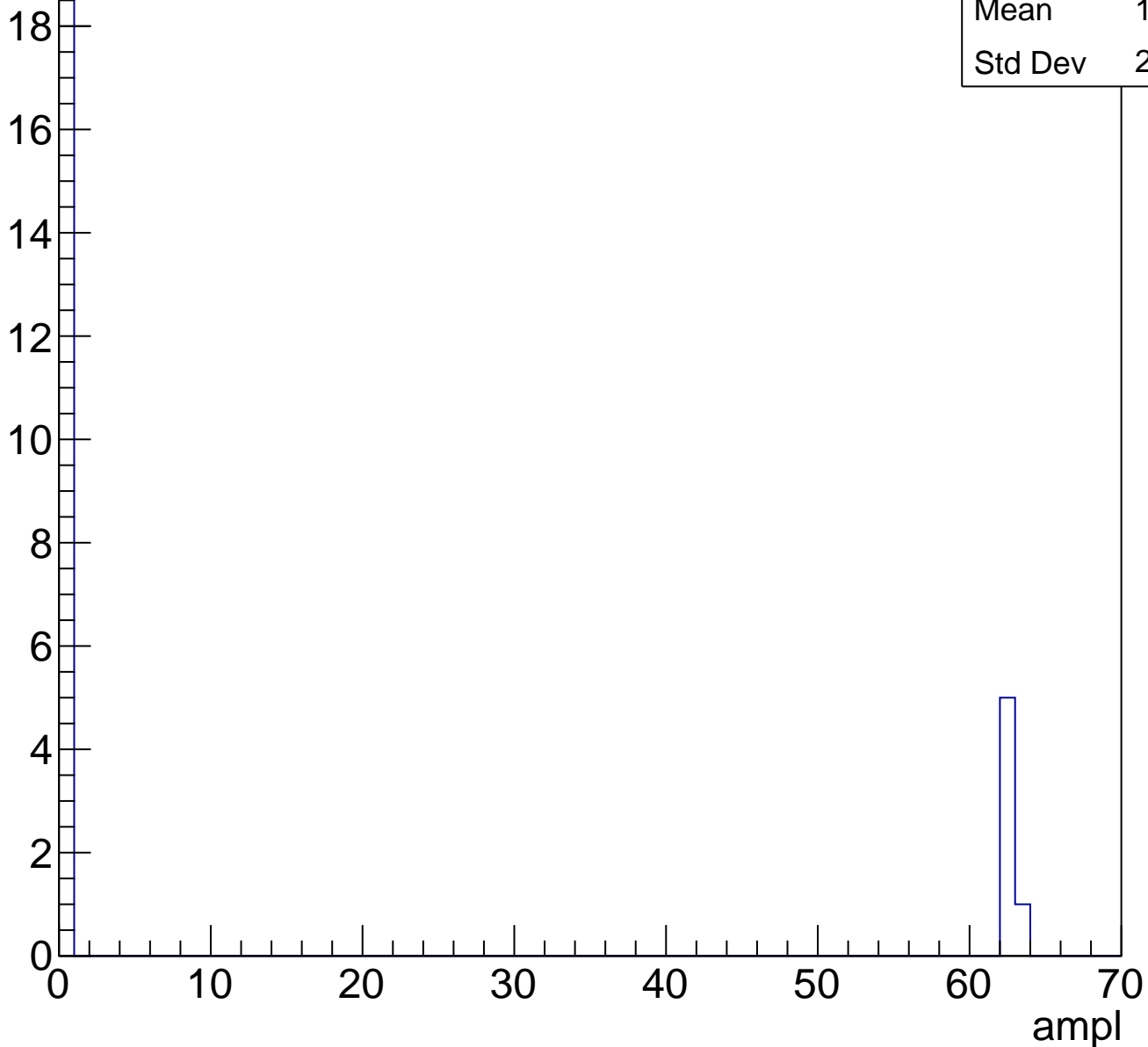
Entries	26
Mean	58.31
Std Dev	11.88



B1L103S, U1-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	25
Mean	14.92
Std Dev	26.55

B1L103S, U1-ch97, adc0

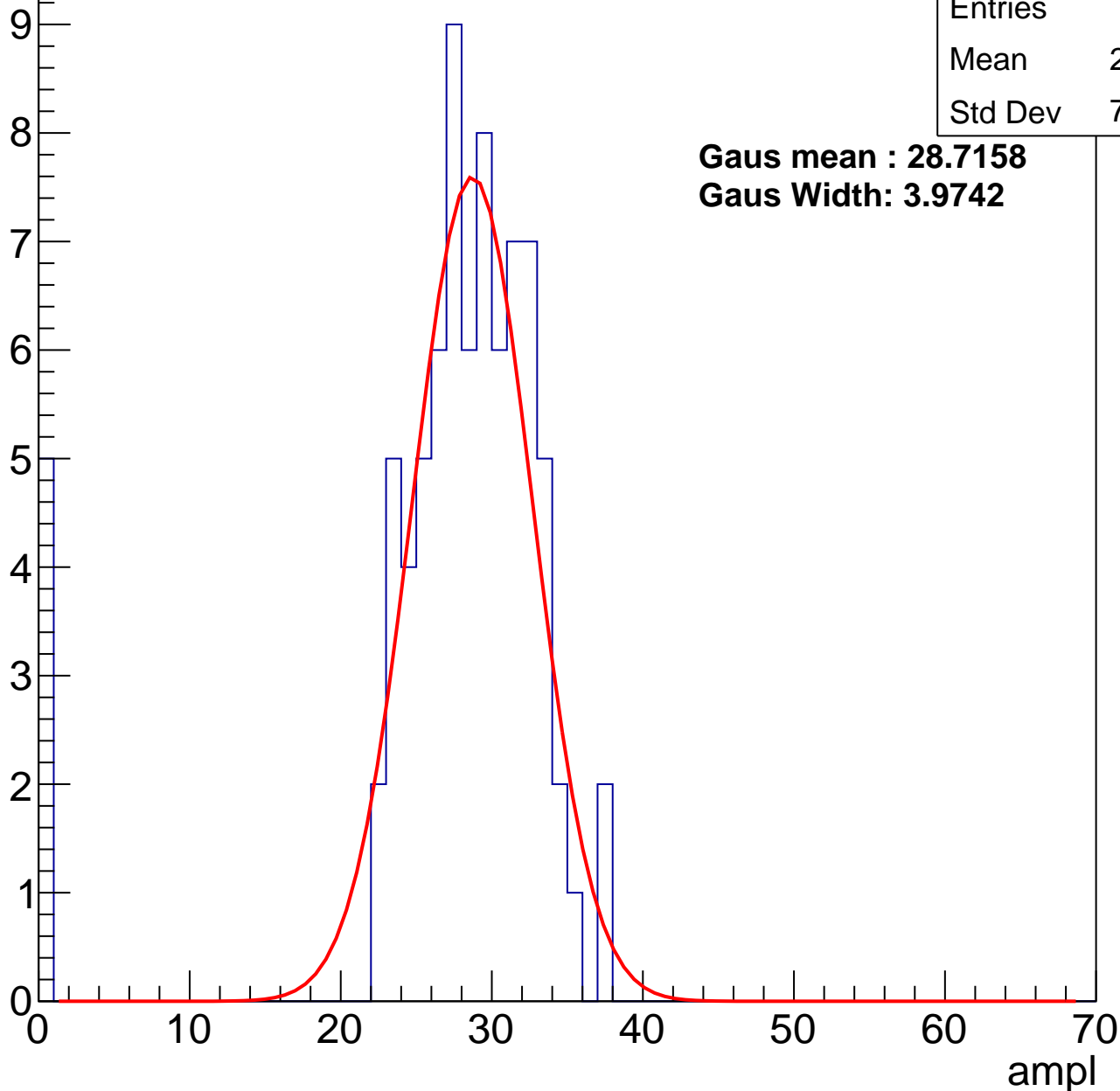
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	26.77
Std Dev	7.709

Gaus mean : 28.7158

Gaus Width: 3.9742



B1L103S, U1-ch97, adc1

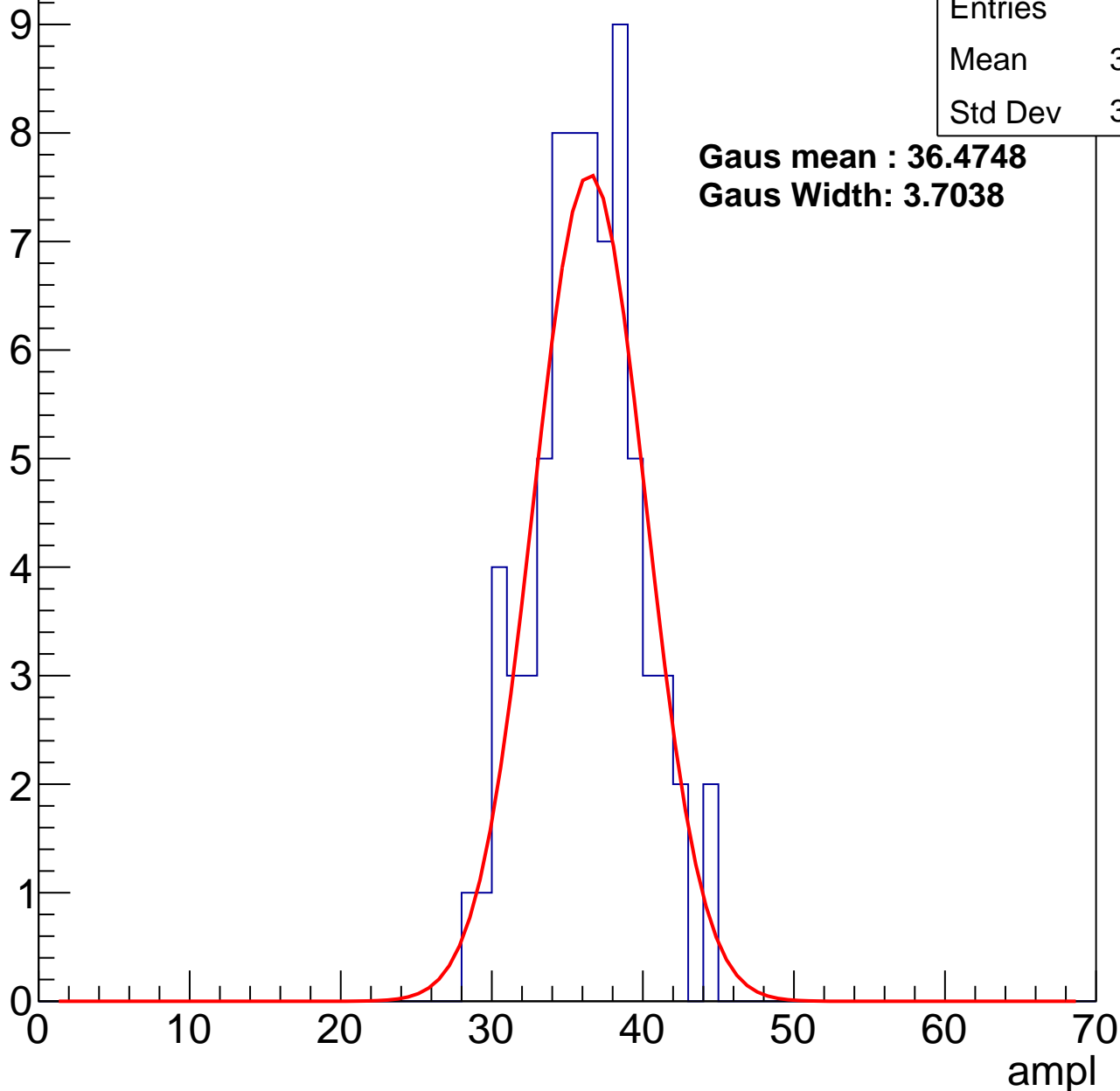
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.86
Std Dev	3.485

Gaus mean : 36.4748

Gaus Width: 3.7038



B1L103S, U1-ch97, adc2

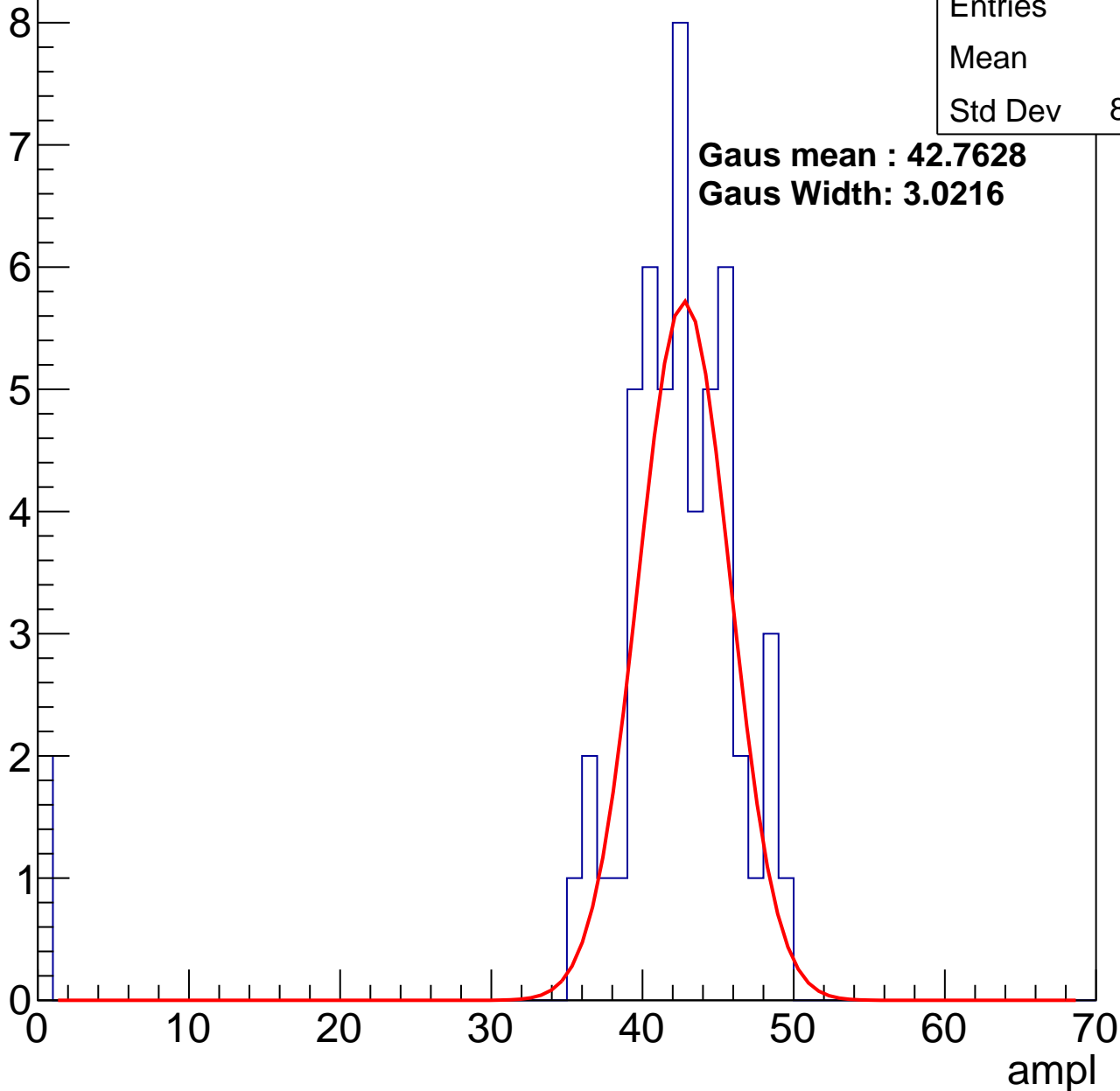
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	40.6
Std Dev	8.636

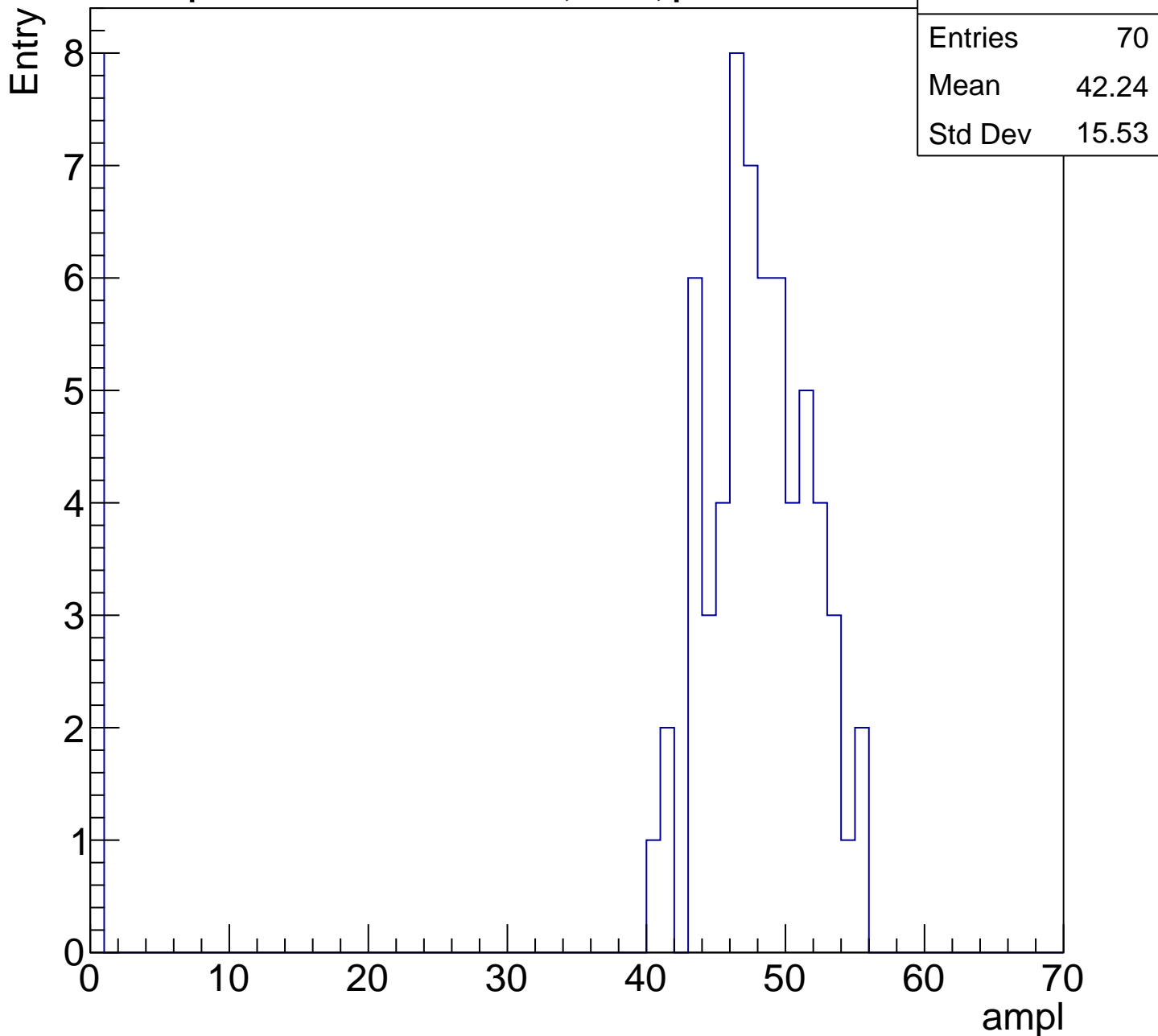
Gaus mean : 42.7628

Gaus Width: 3.0216



B1L103S, U1-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

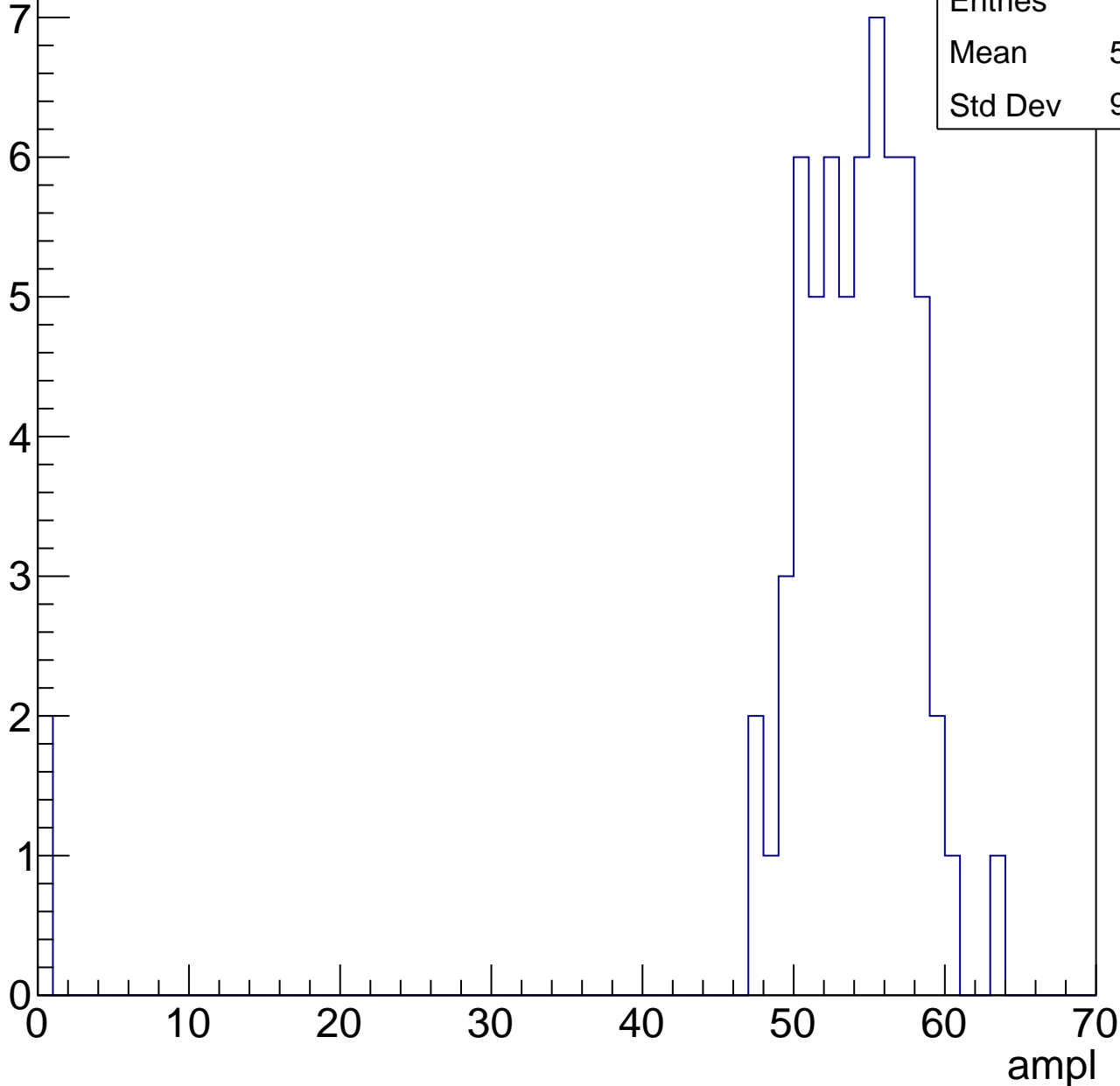


B1L103S, U1-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.17
Std Dev	9.944

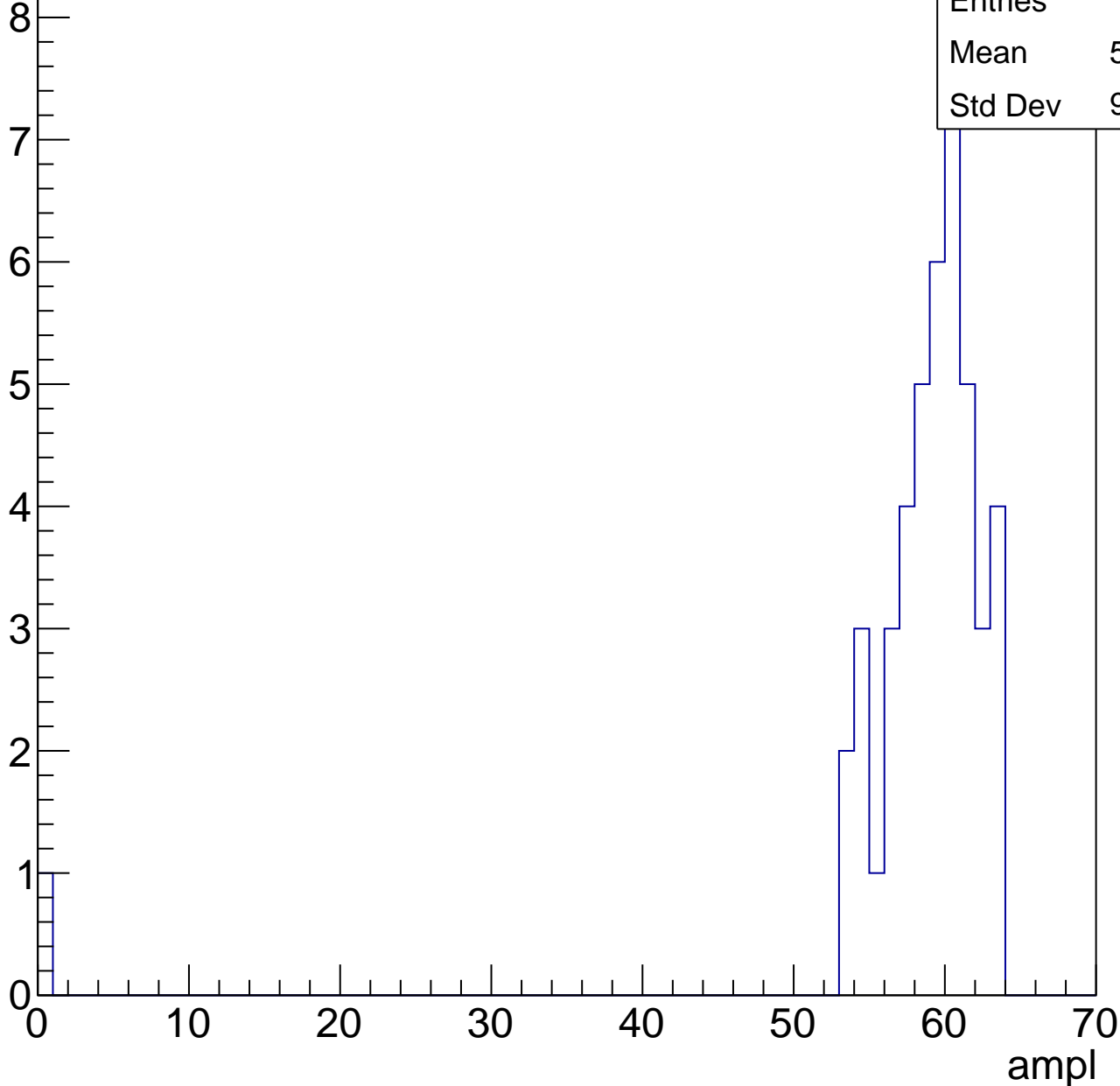


B1L103S, U1-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	57.47
Std Dev	9.074

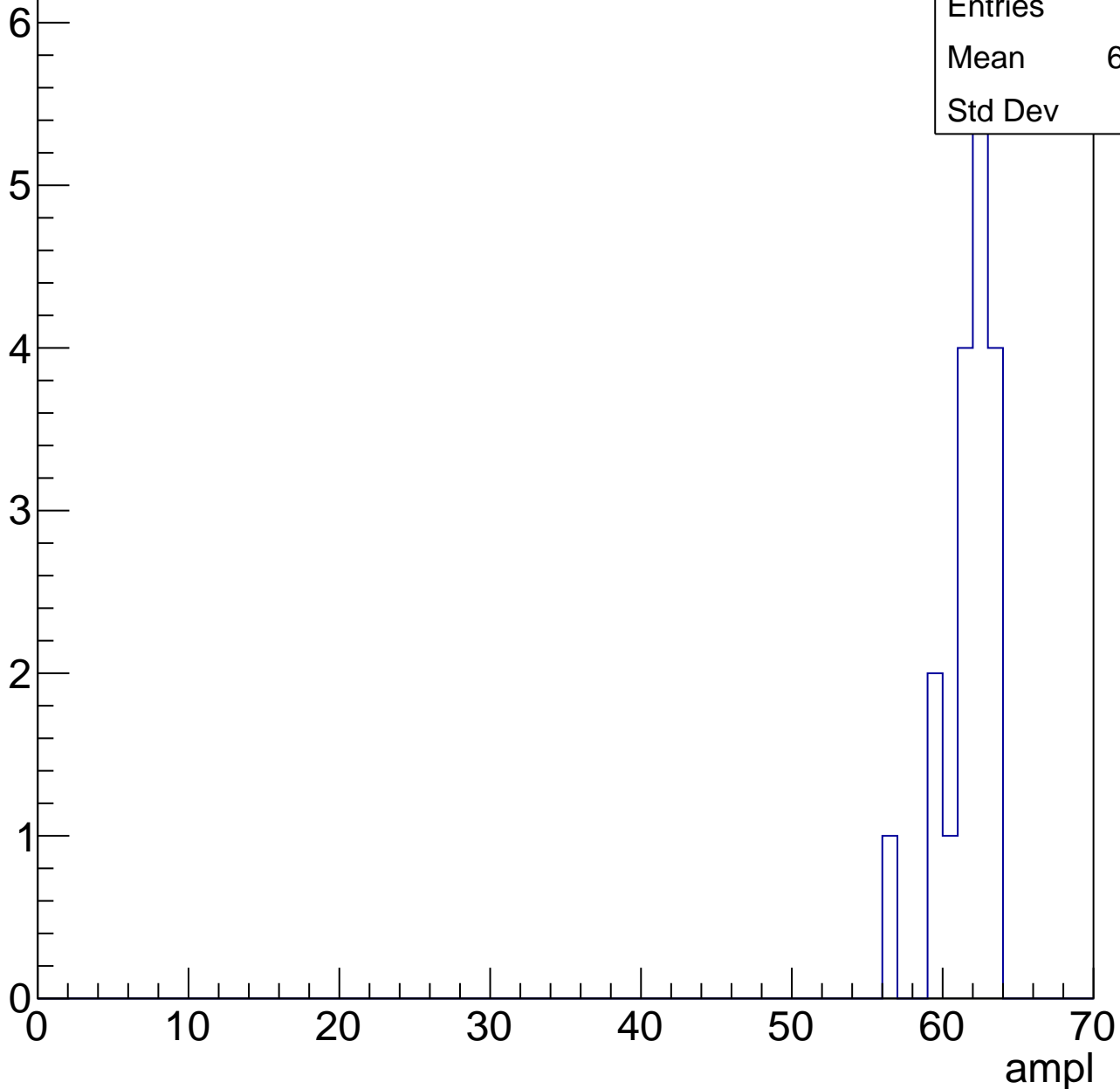


B1L103S, U1-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.22
Std Dev	1.75



B1L103S, U1-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

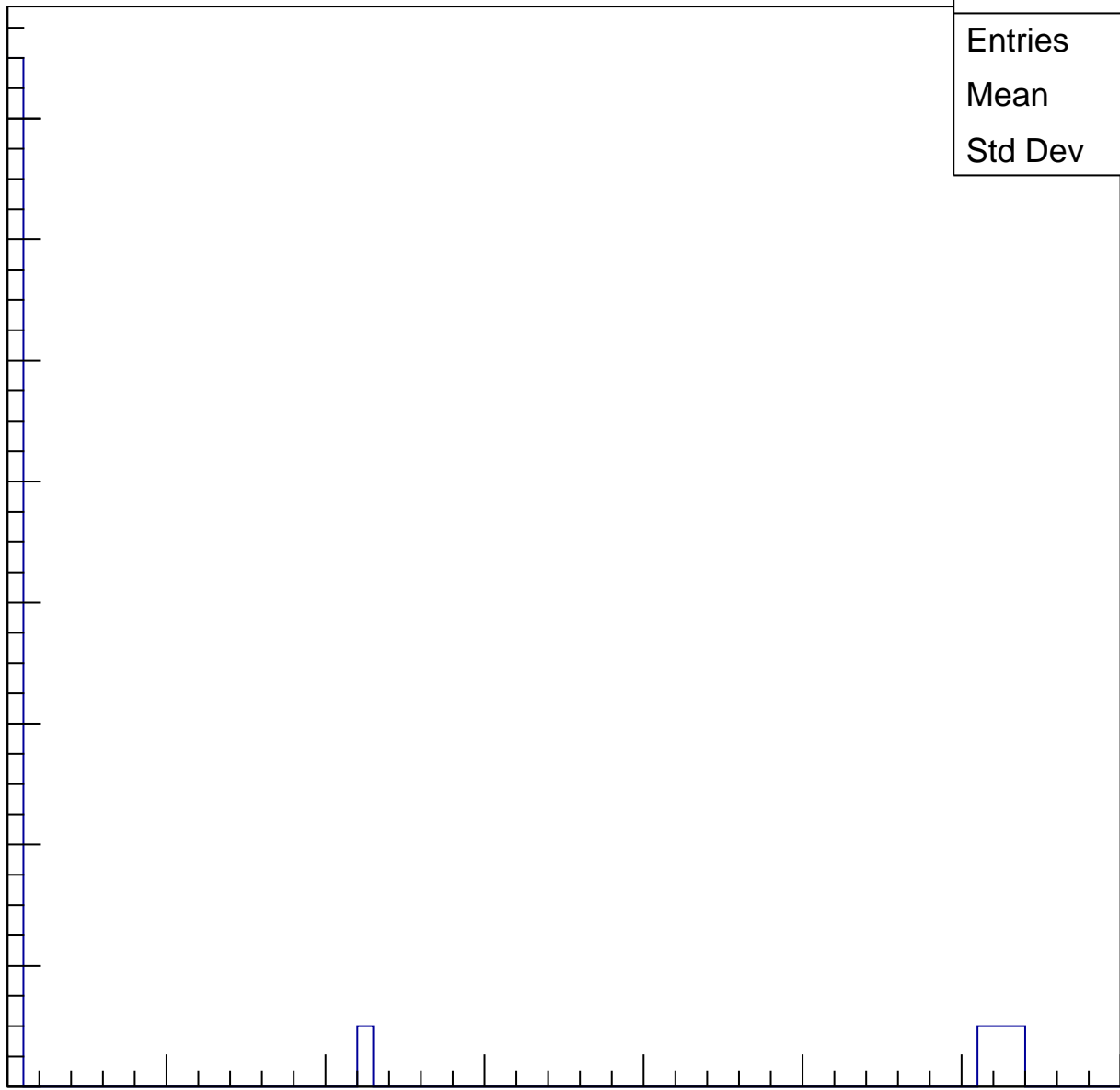
Entries	21
Mean	9.905
Std Dev	21.78

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



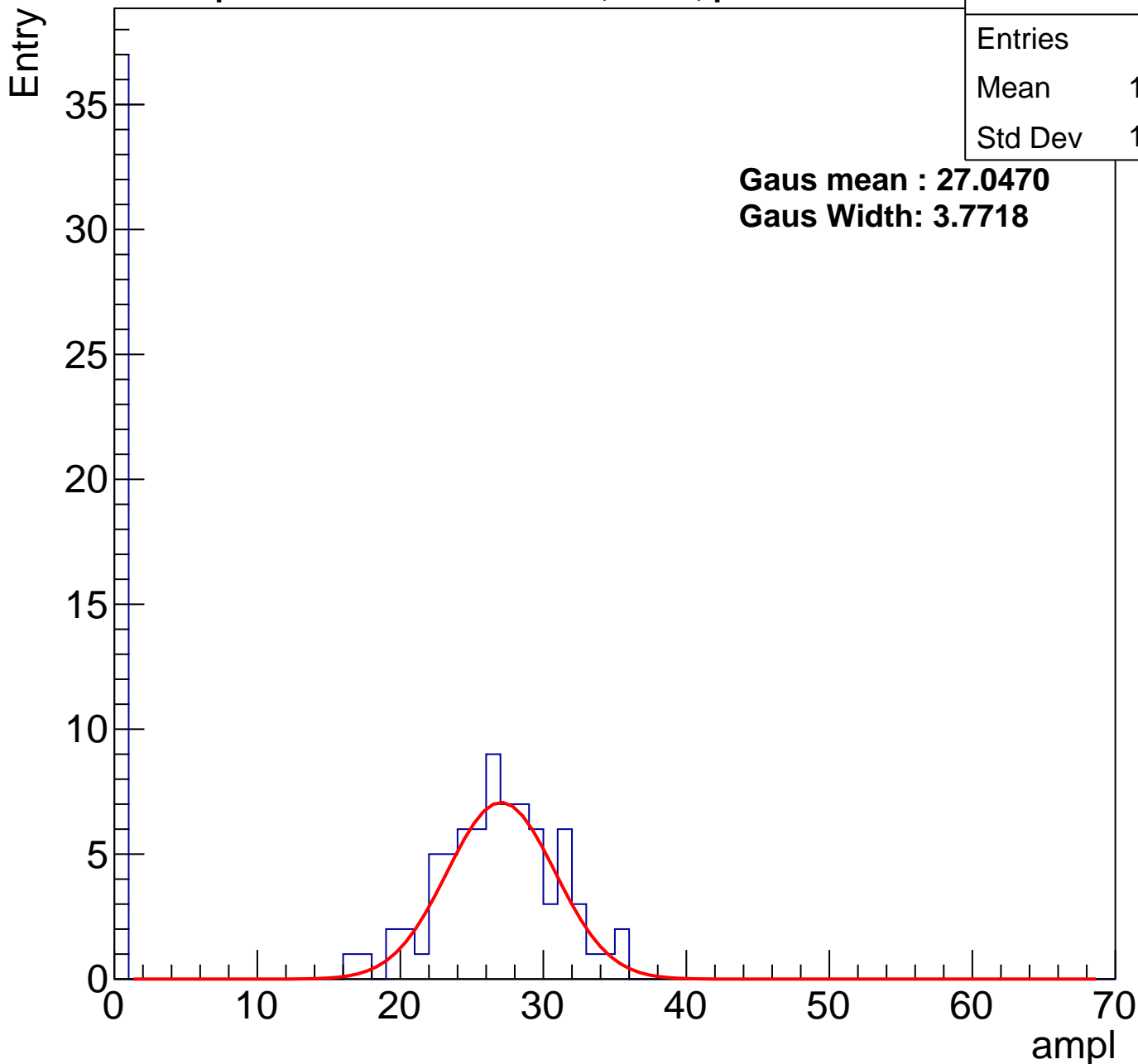
B1L103S, U1-ch98, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	17.59
Std Dev	12.86

Gaus mean : 27.0470

Gaus Width: 3.7718



B1L103S, U1-ch98, adc1

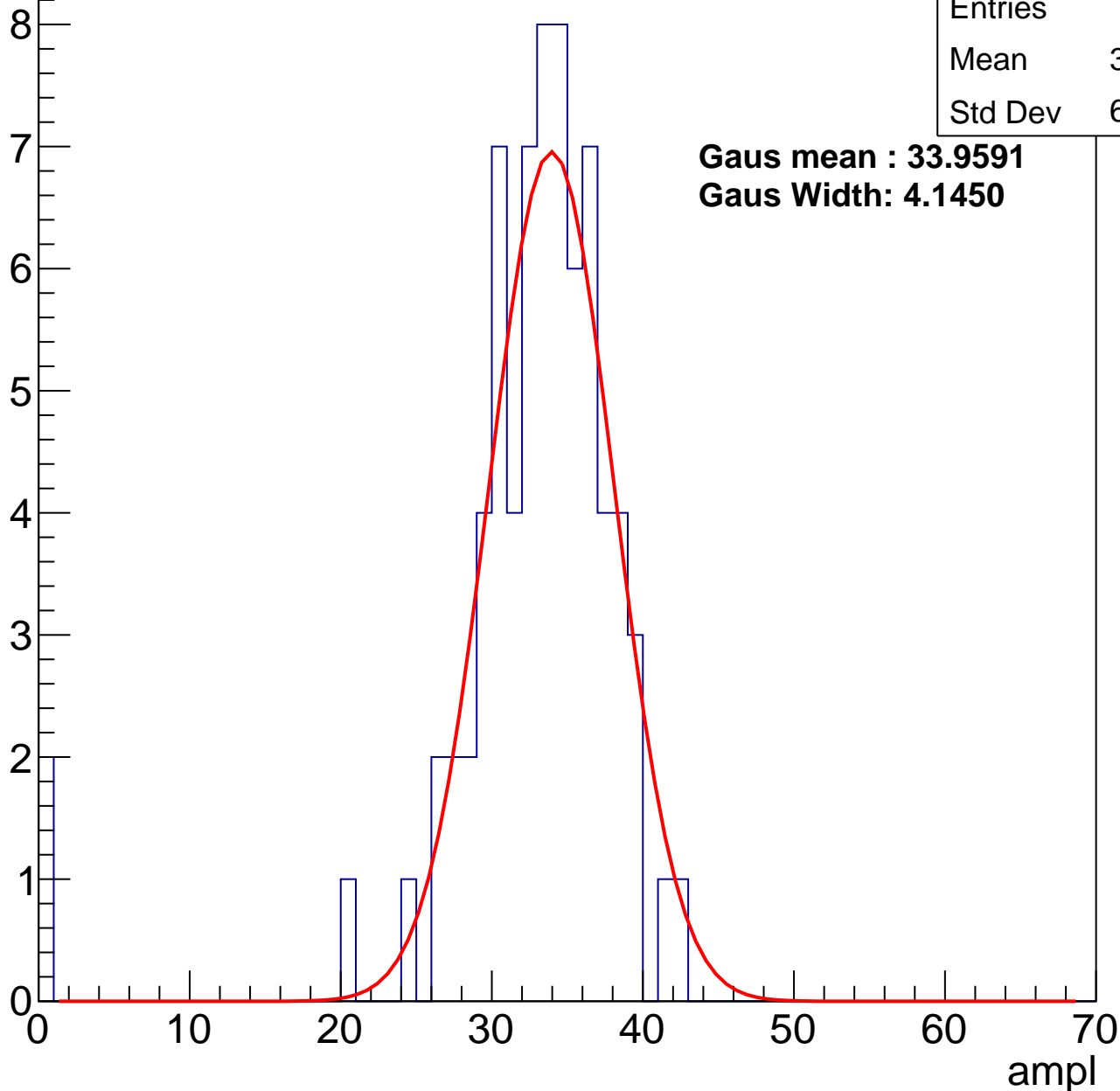
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.14
Std Dev	6.632

Gaus mean : 33.9591

Gaus Width: 4.1450



B1L103S, U1-ch98, adc2

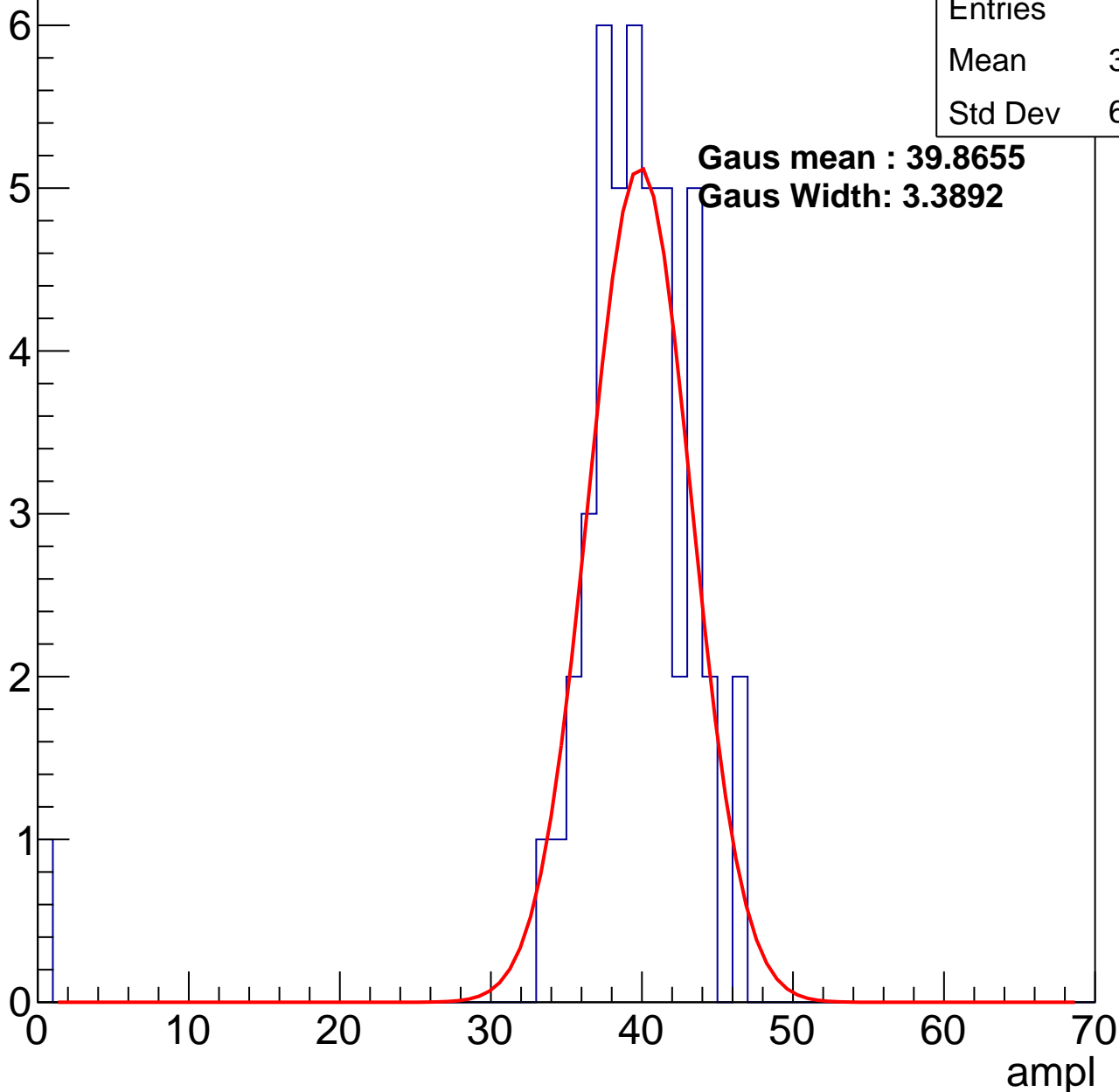
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	38.59
Std Dev	6.483

Gaus mean : 39.8655

Gaus Width: 3.3892

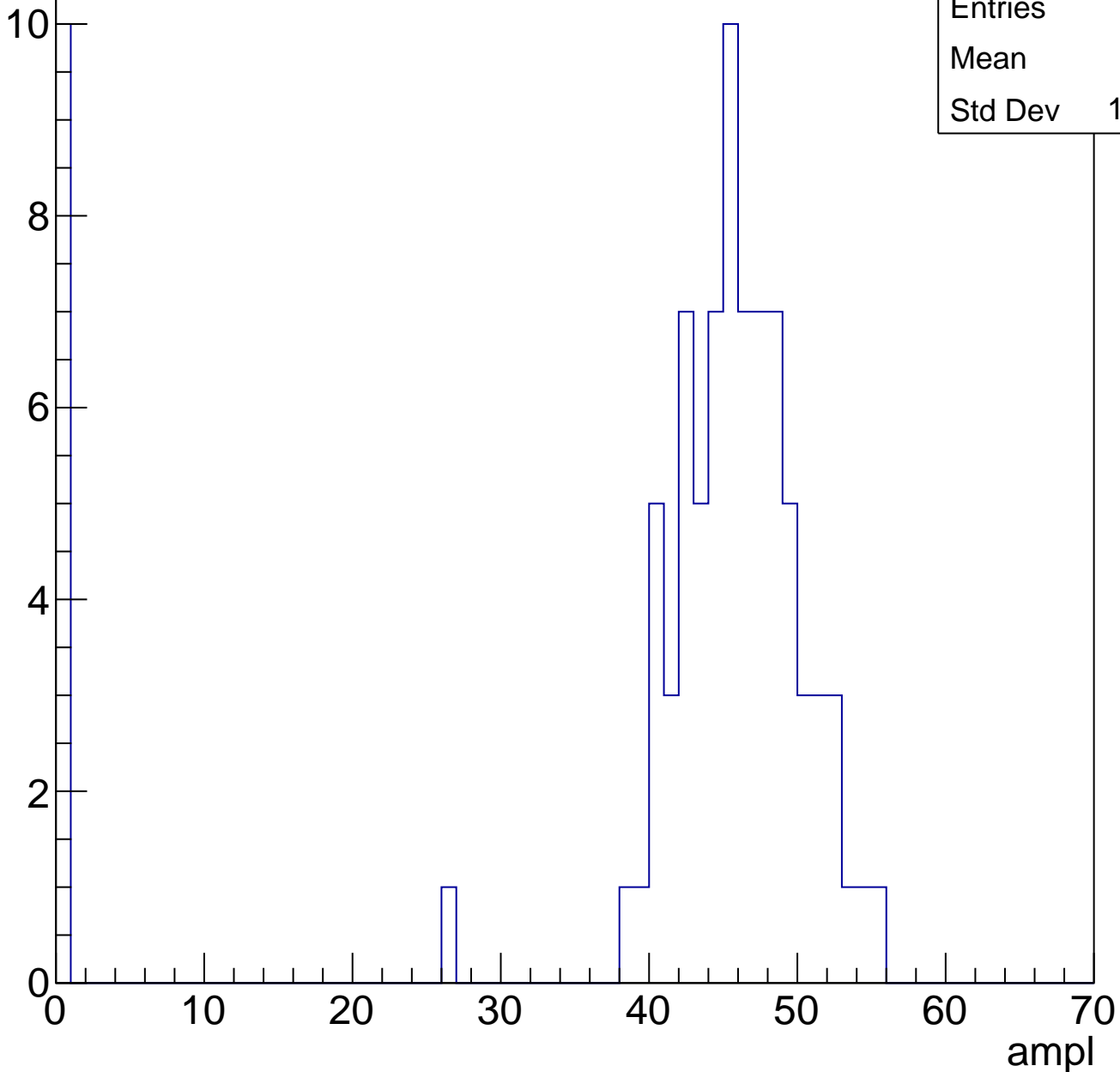


B1L103S, U1-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	40.3
Std Dev	14.99

Entry

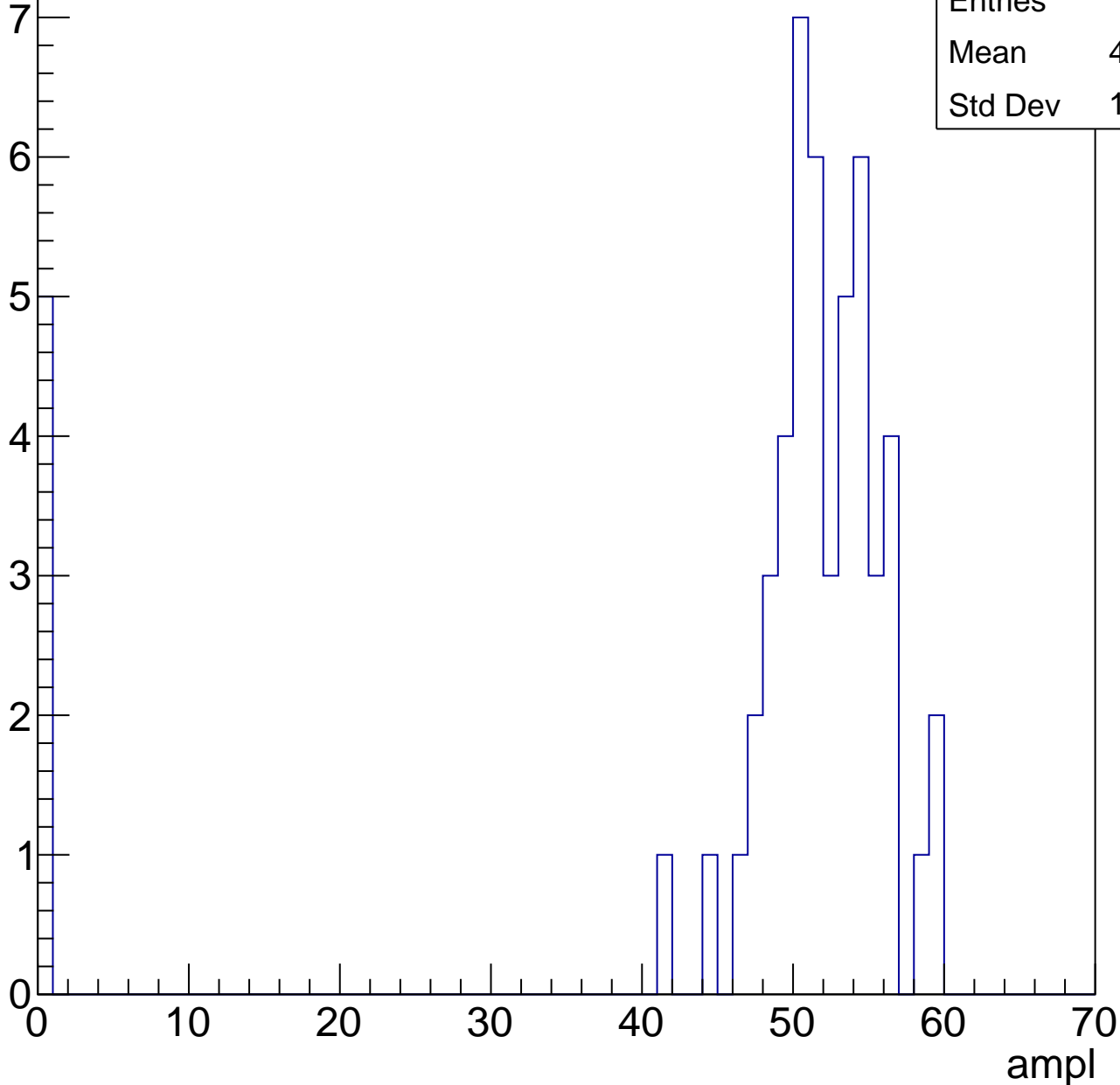


B1L103S, U1-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

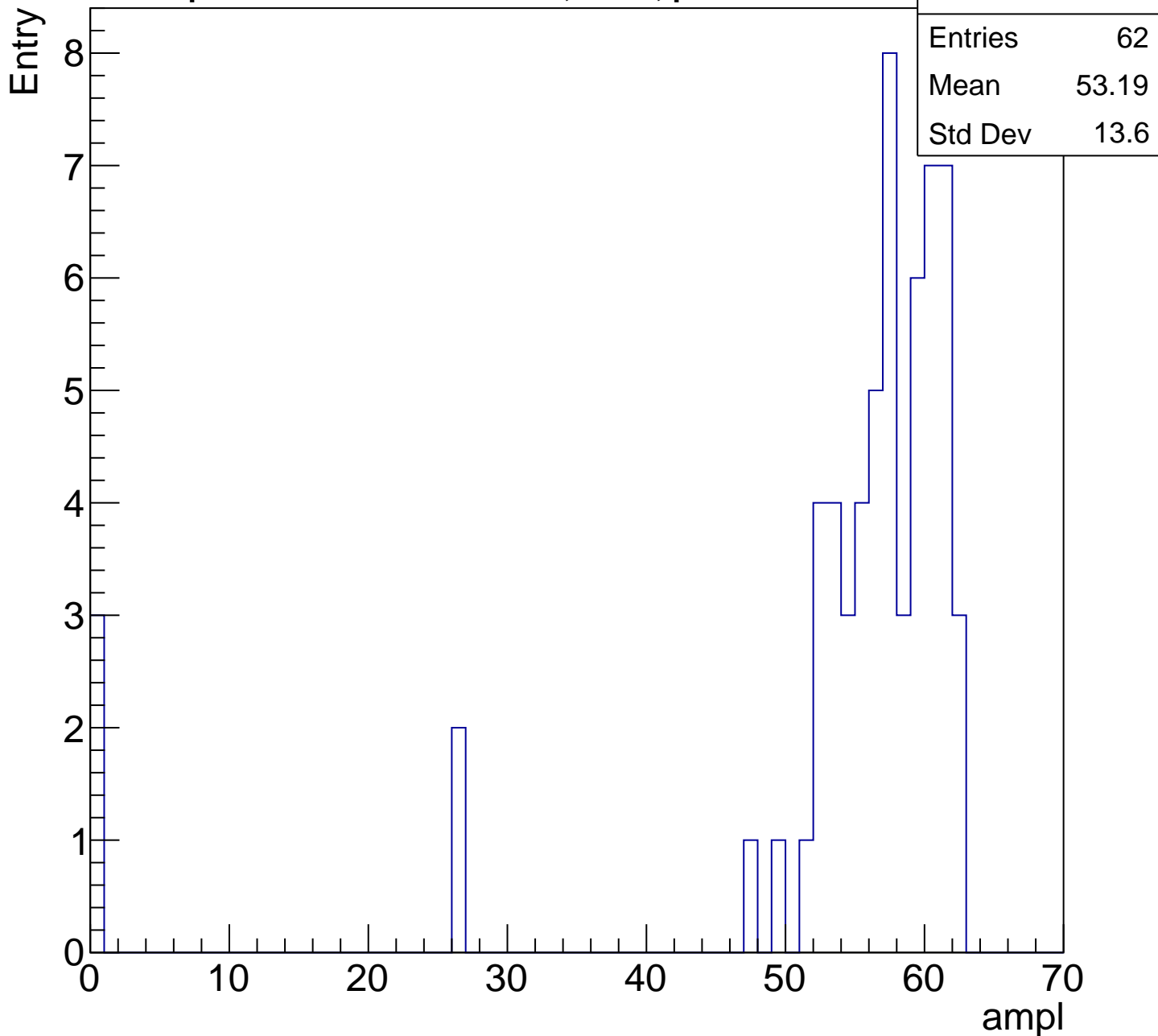
Entry

Entries	54
Mean	46.87
Std Dev	15.36



B1L103S, U1-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

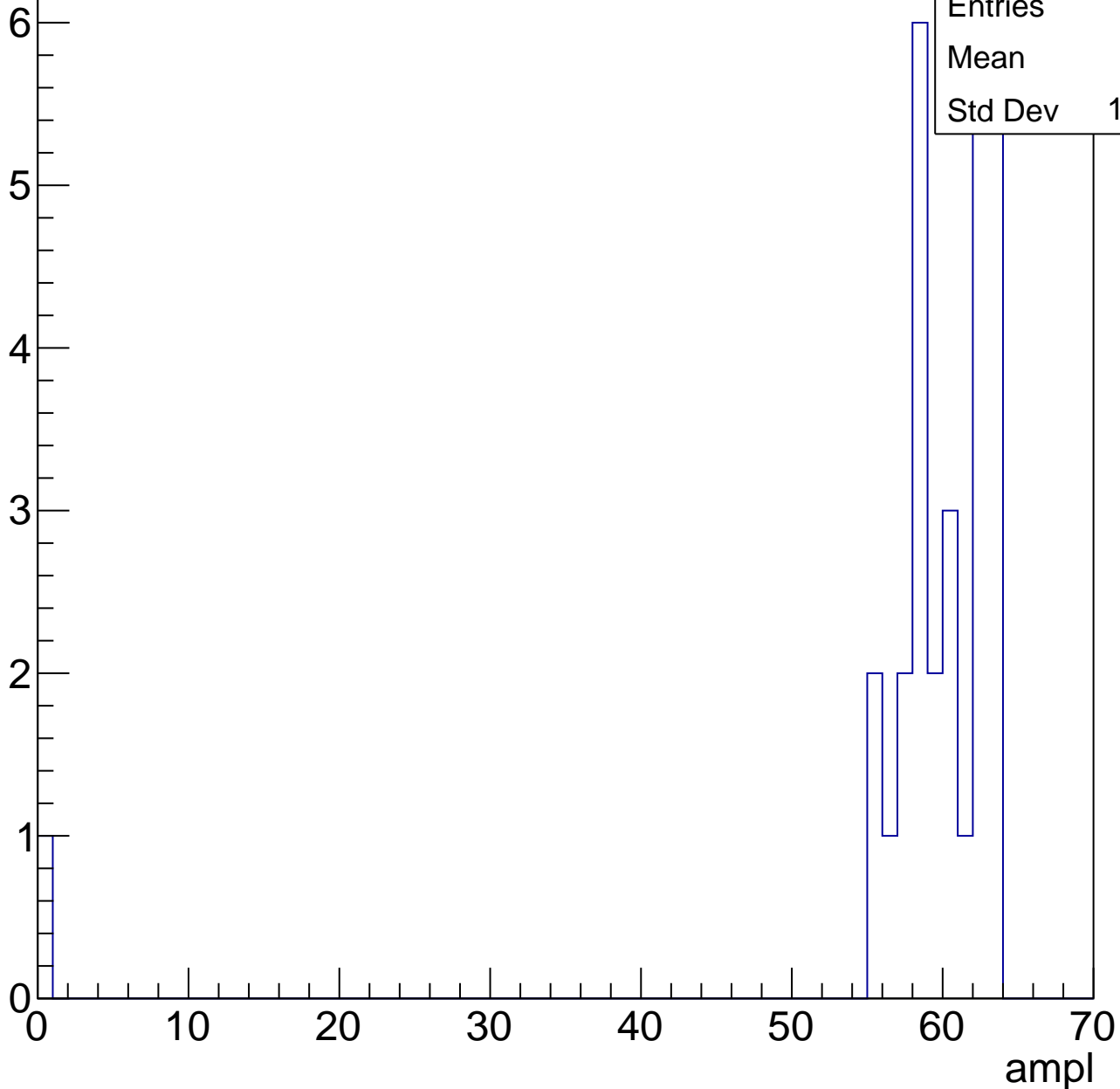


B1L103S, U1-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

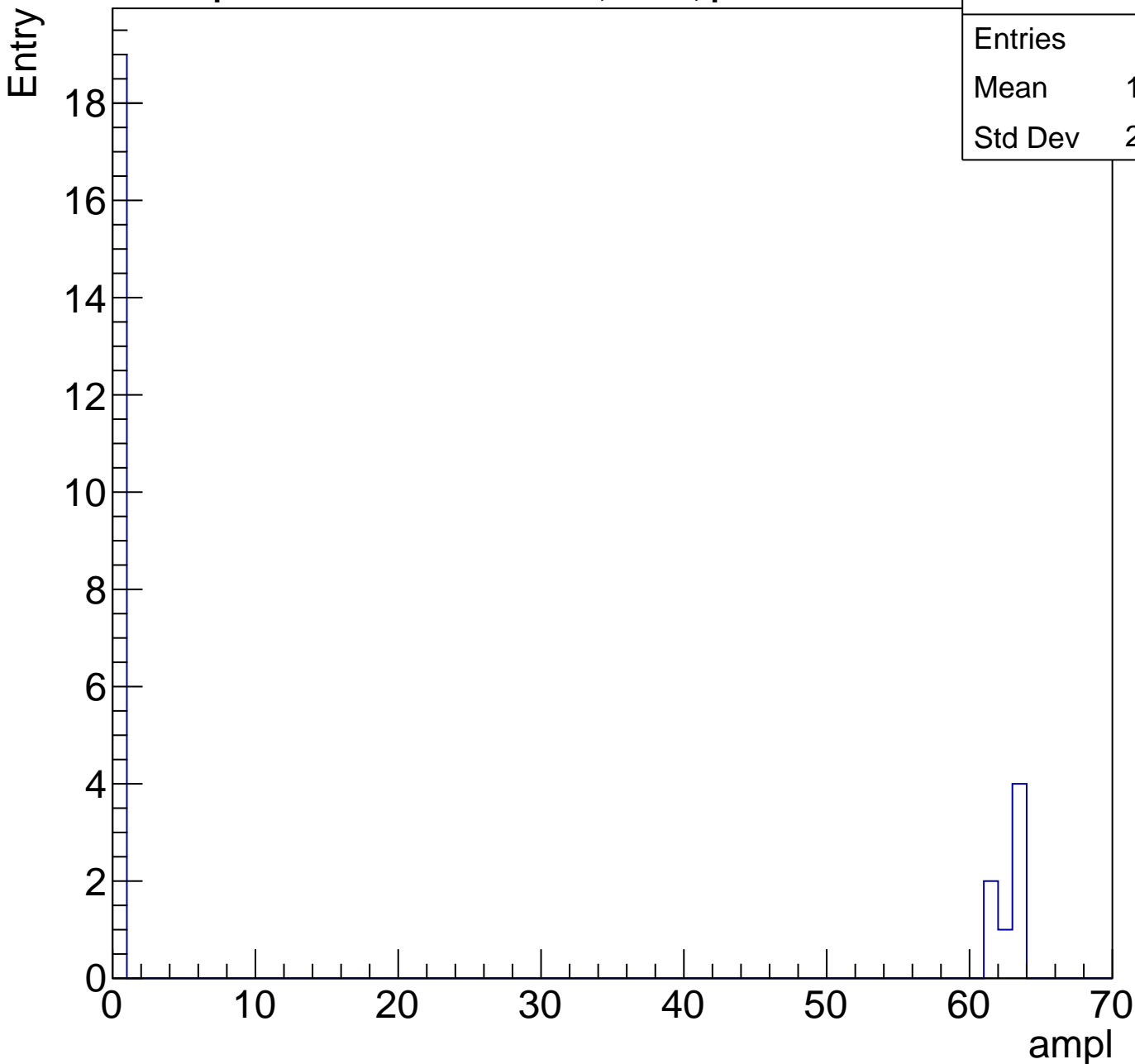
Entries	30
Mean	57.9
Std Dev	11.04



B1L103S, U1-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.77
Std Dev	27.63



B1L103S, U1-ch99, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	23.91
Std Dev	12.97

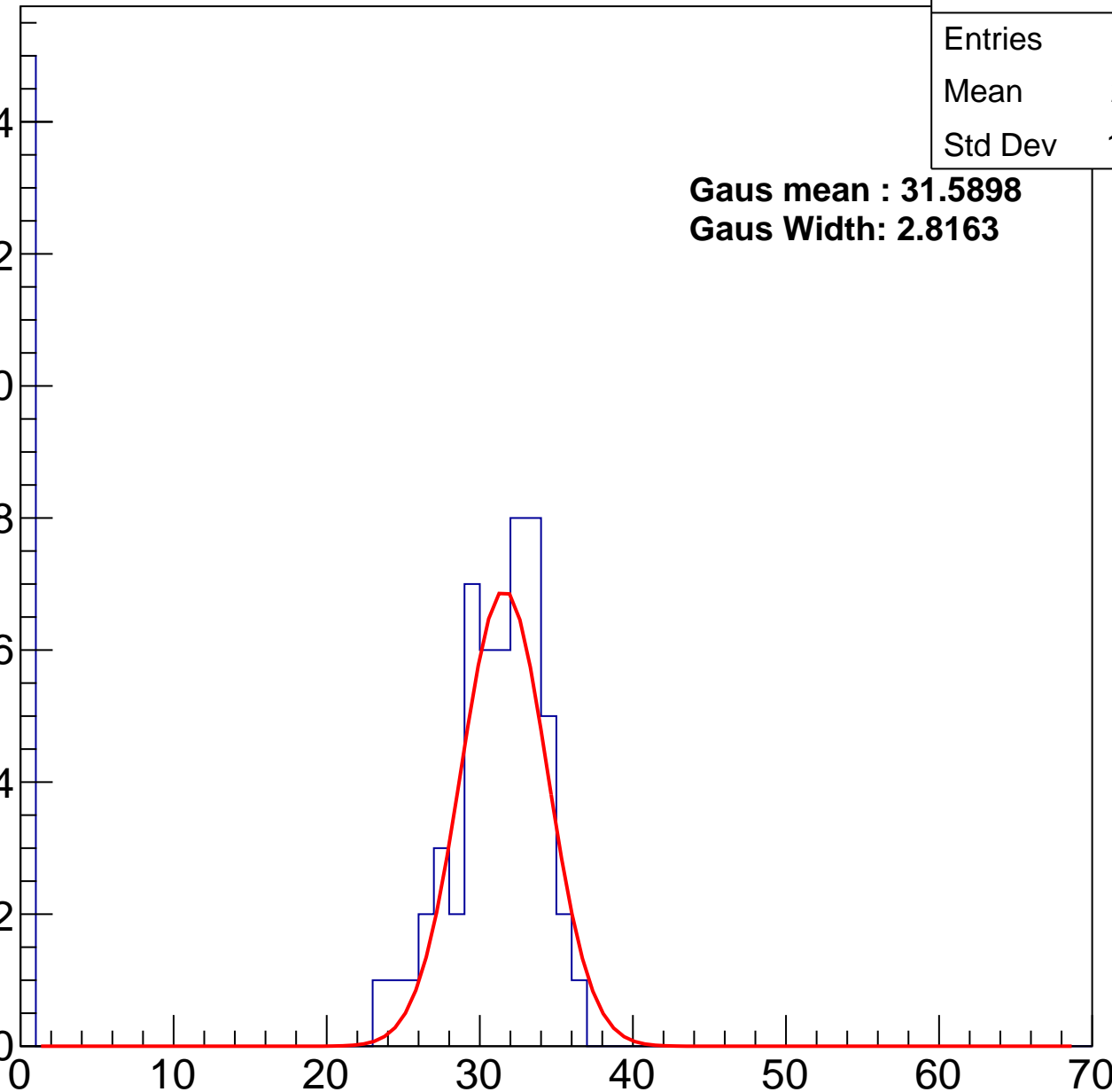
Gaus mean : 31.5898

Gaus Width: 2.8163

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U1-ch99, adc1

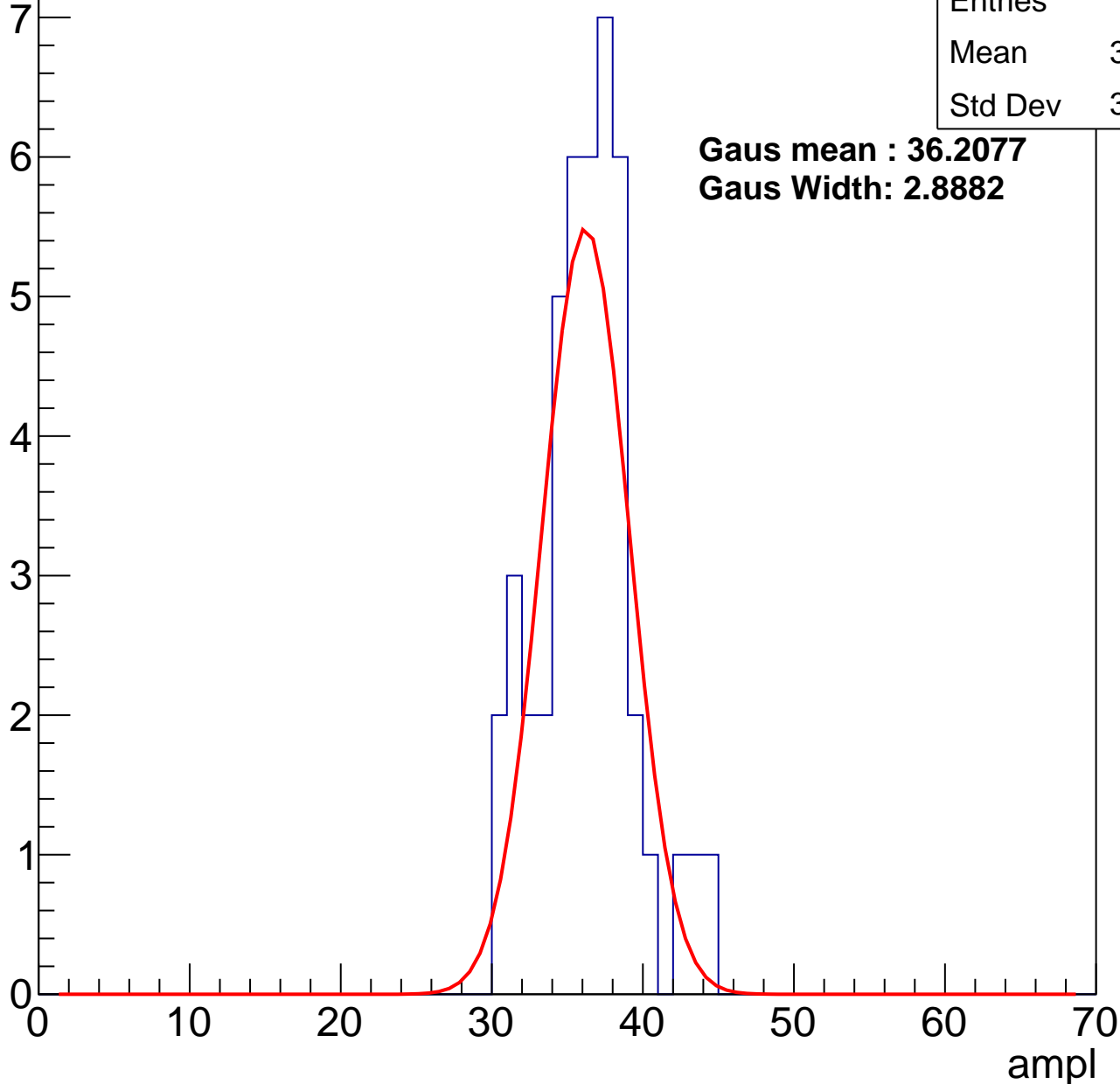
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	35.84
Std Dev	3.112

Gaus mean : 36.2077

Gaus Width: 2.8882



B1L103S, U1-ch99, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	37.4
Std Dev	12.75

Gaus mean : 41.5743

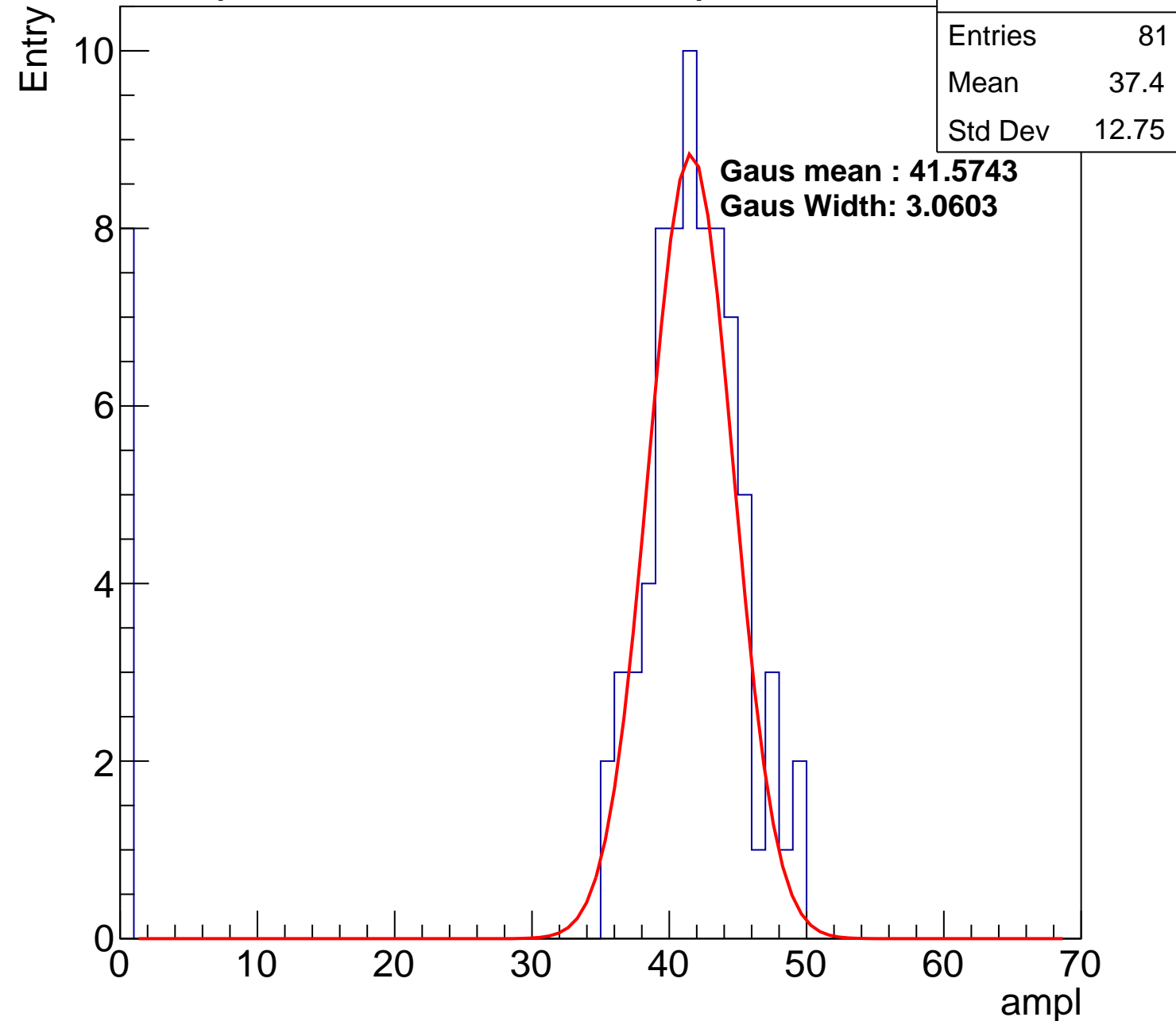
Gaus Width: 3.0603

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

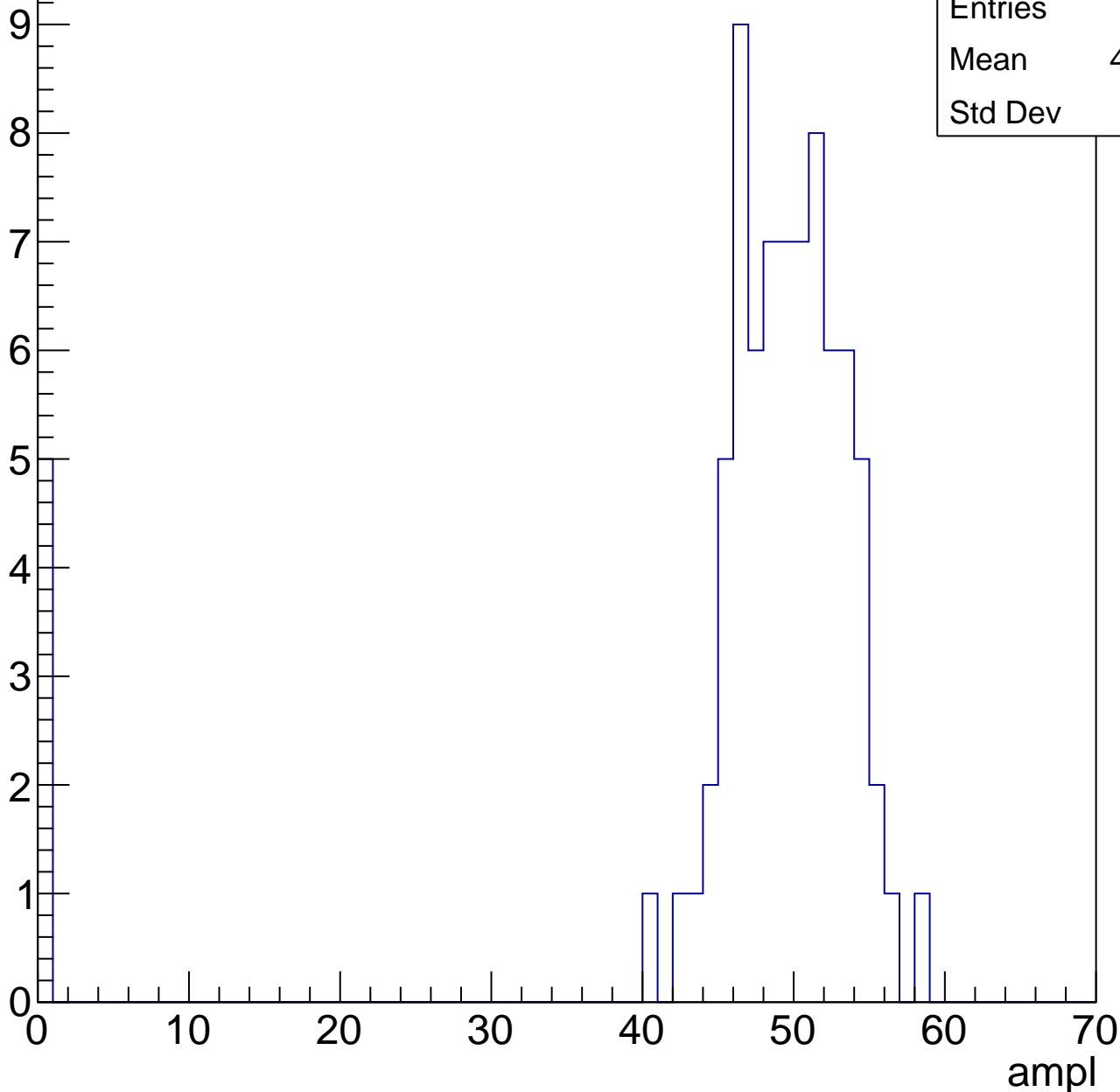


B1L103S, U1-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	46.19
Std Dev	12.4

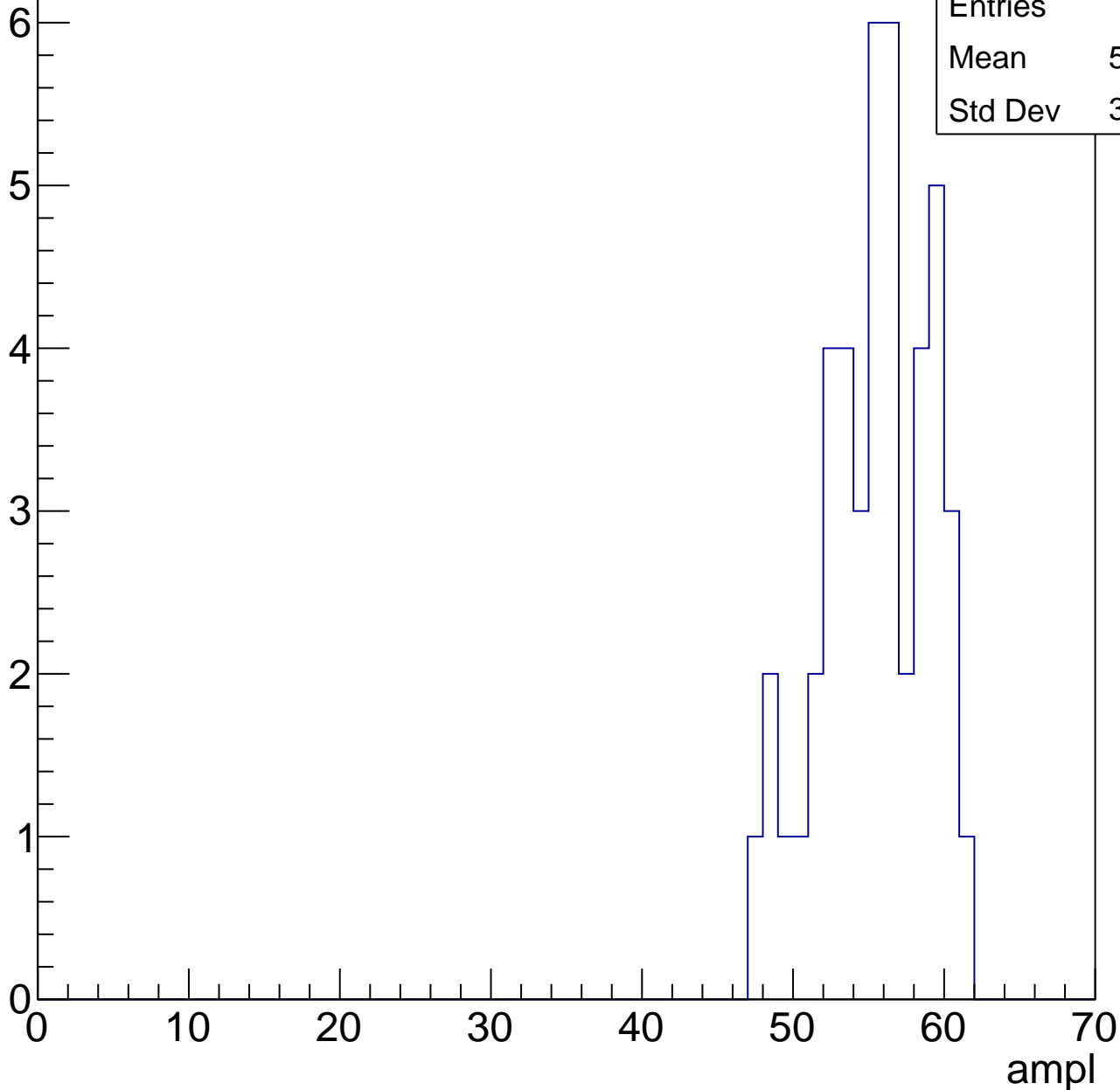


B1L103S, U1-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	54.98
Std Dev	3.499

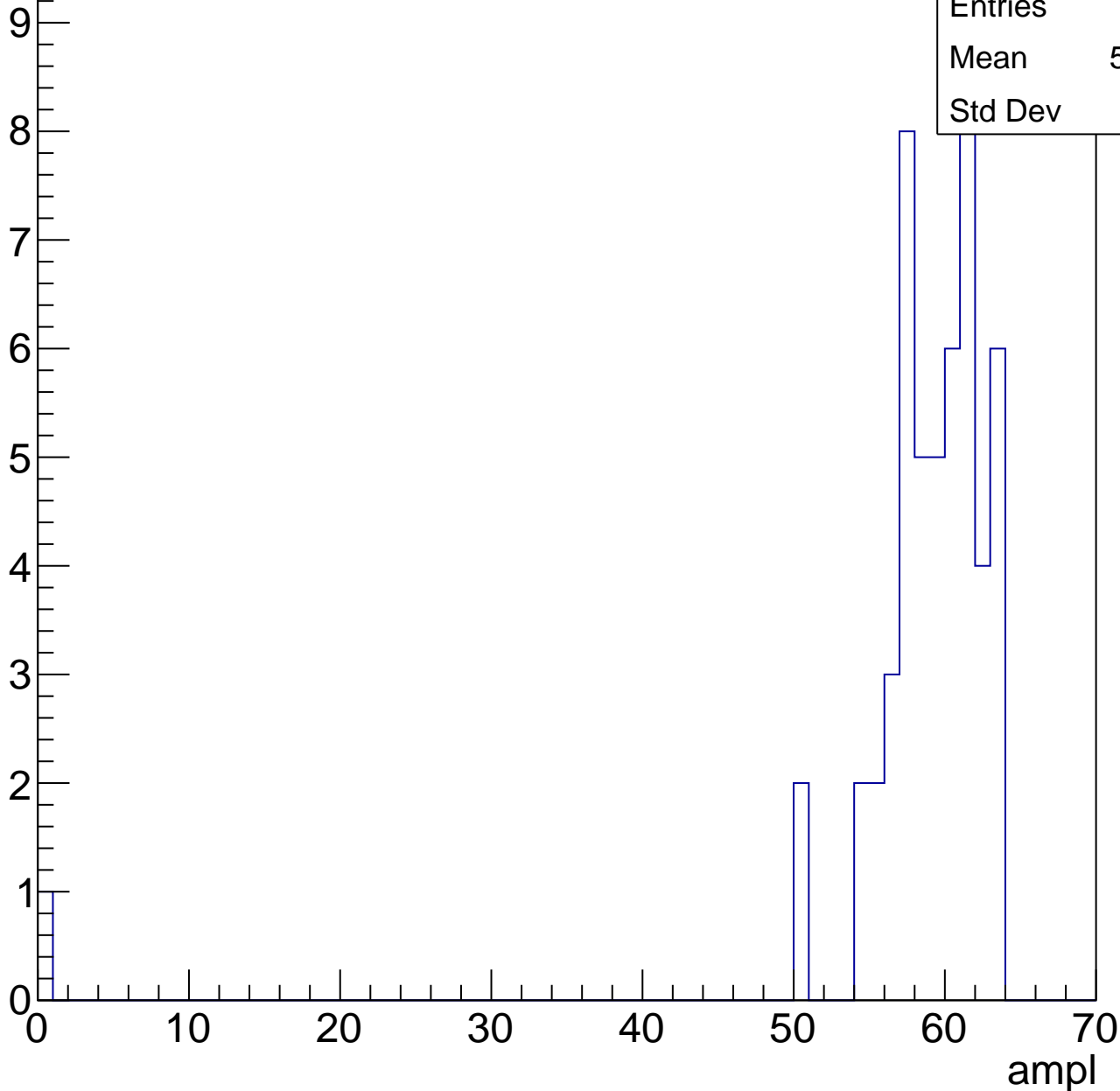


B1L103S, U1-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.77
Std Dev	8.56

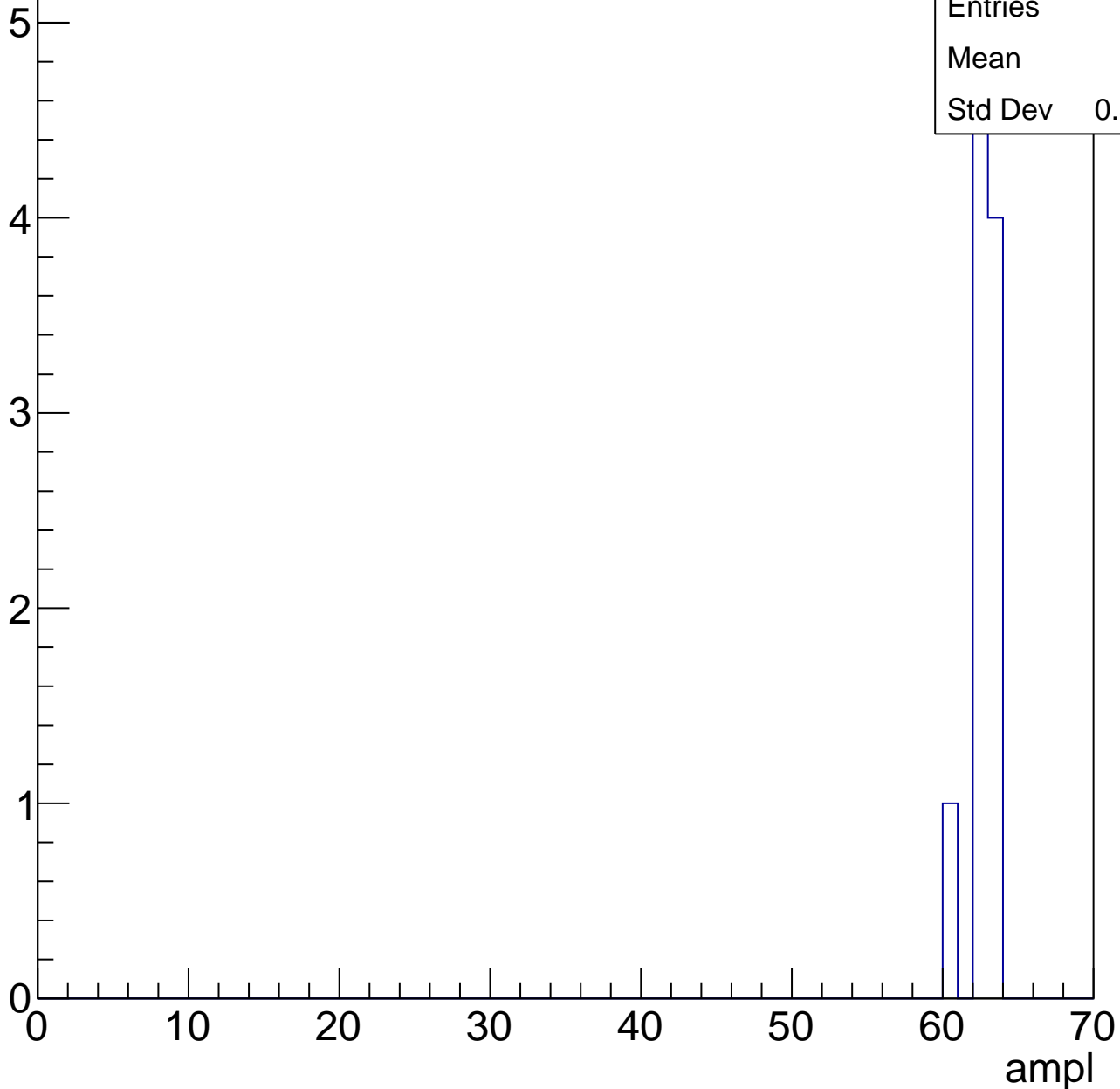


B1L103S, U1-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	62.2
Std Dev	0.8718



B1L103S, U1-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U1-ch100, adc0

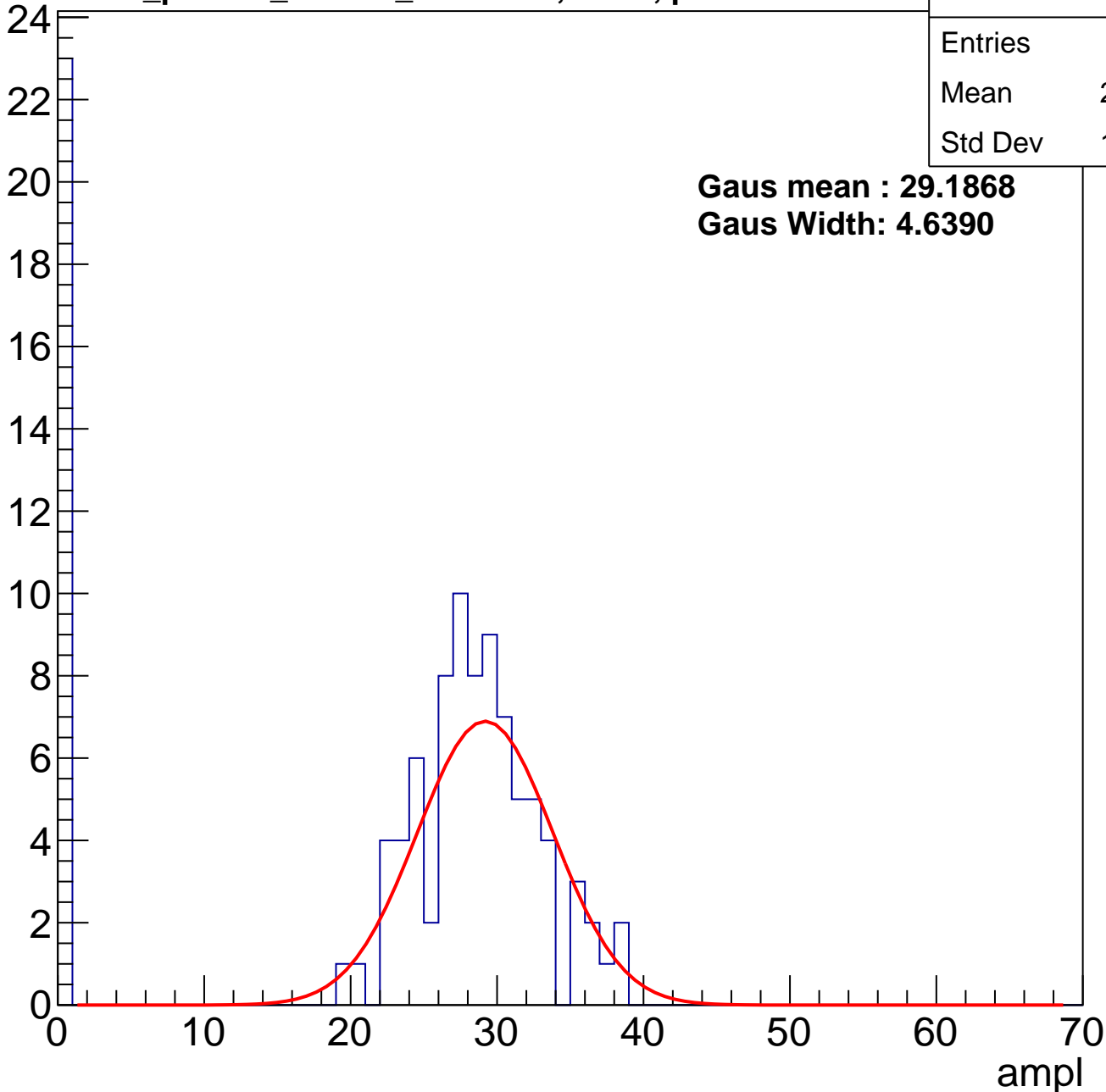
calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	22.12
Std Dev	12.25

Gaus mean : 29.1868

Gaus Width: 4.6390

Entry



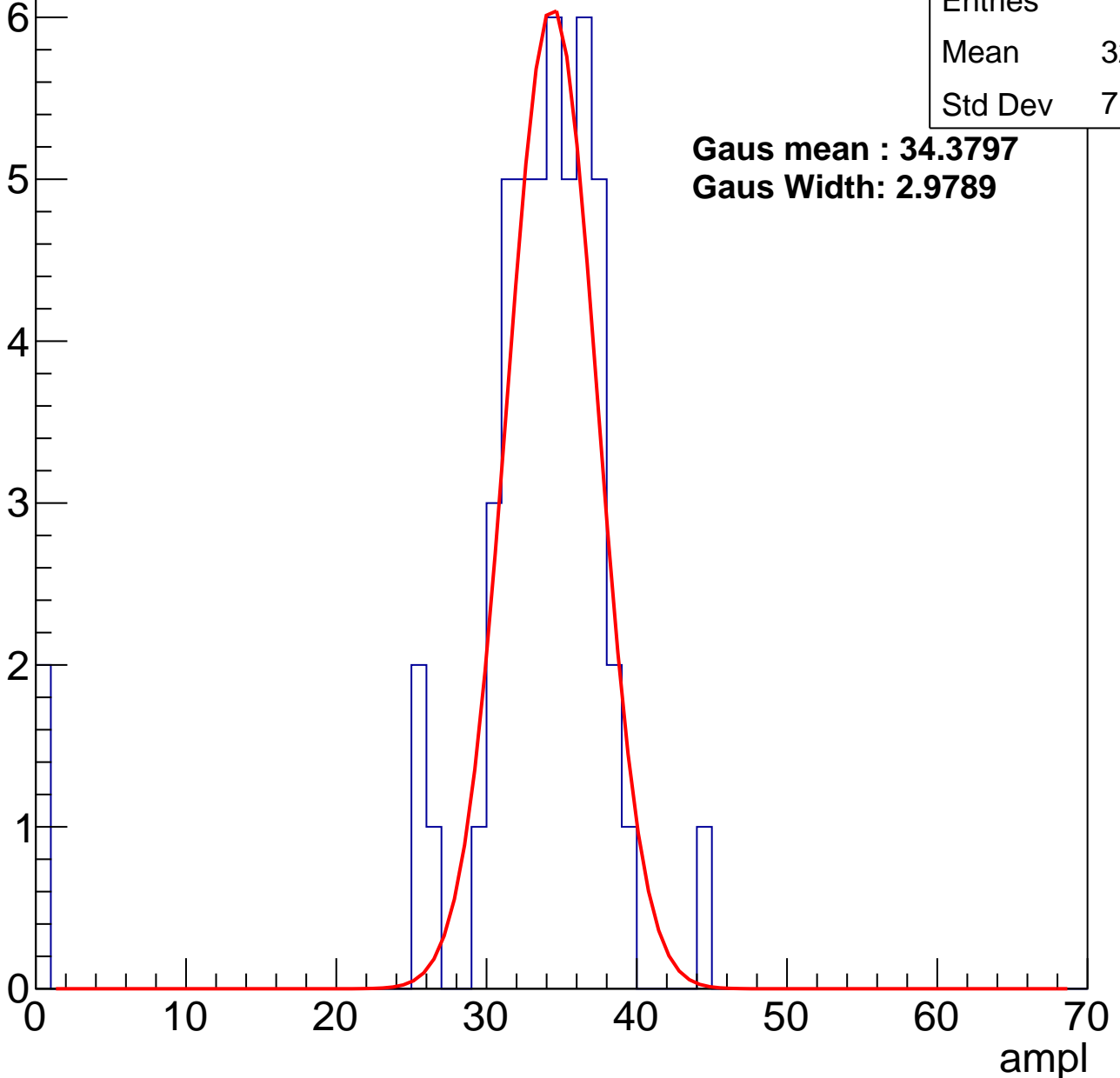
B1L103S, U1-ch100, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	32.28
Std Dev	7.435

Gaus mean : 34.3797
Gaus Width: 2.9789



B1L103S, U1-ch100, adc2

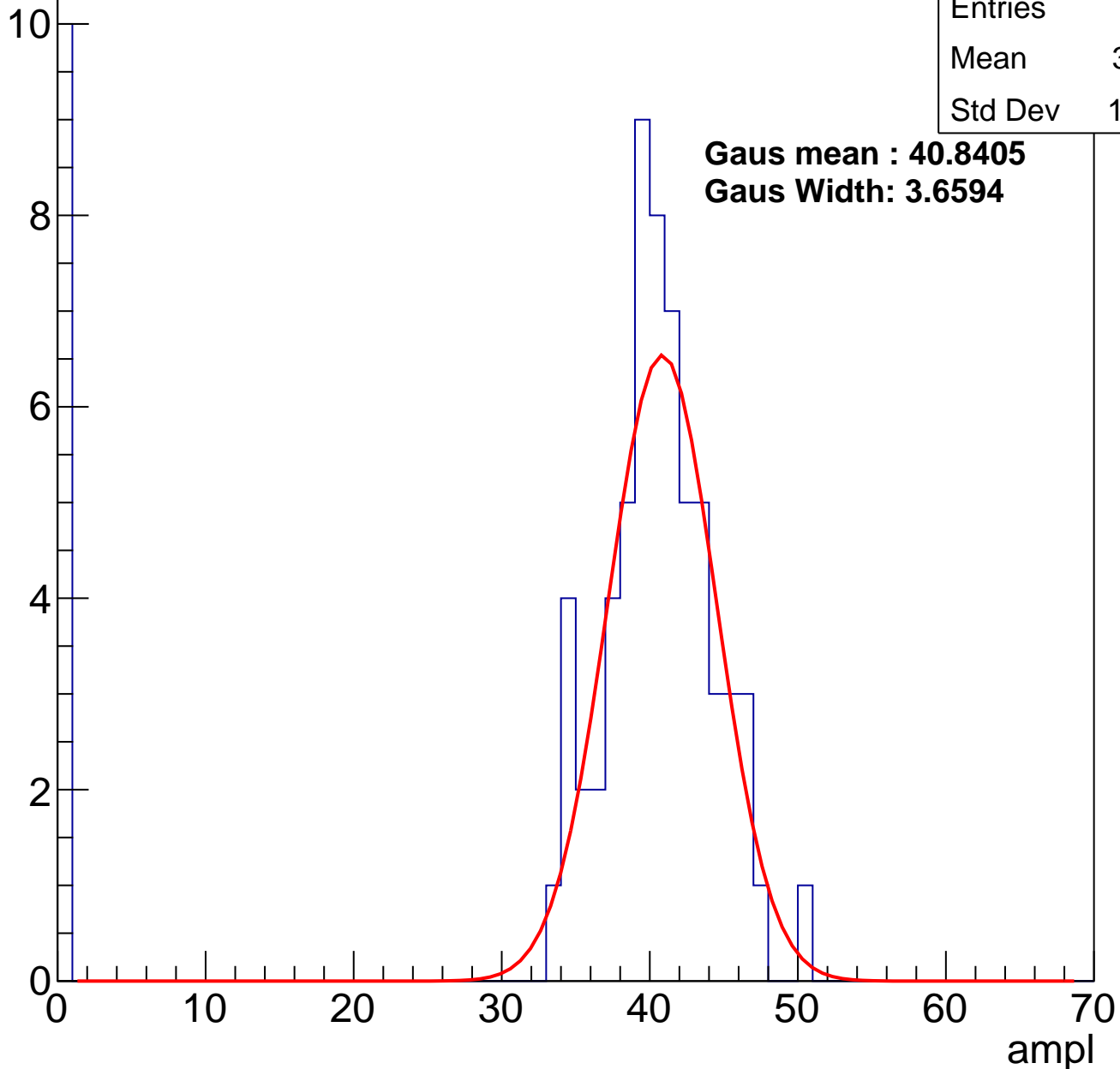
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	34.71
Std Dev	14.22

Gaus mean : 40.8405

Gaus Width: 3.6594

Entry

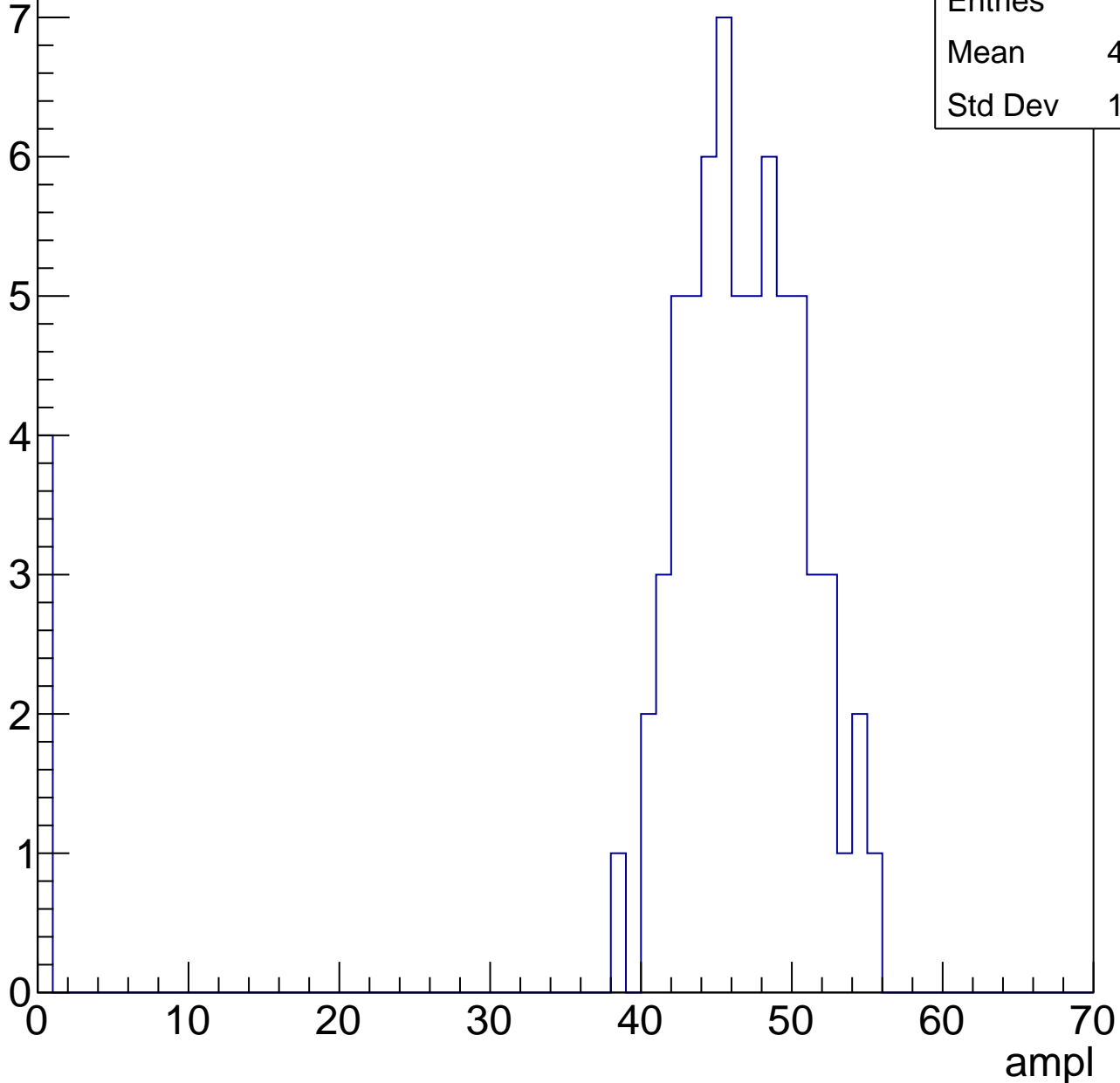


B1L103S, U1-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	43.74
Std Dev	11.47

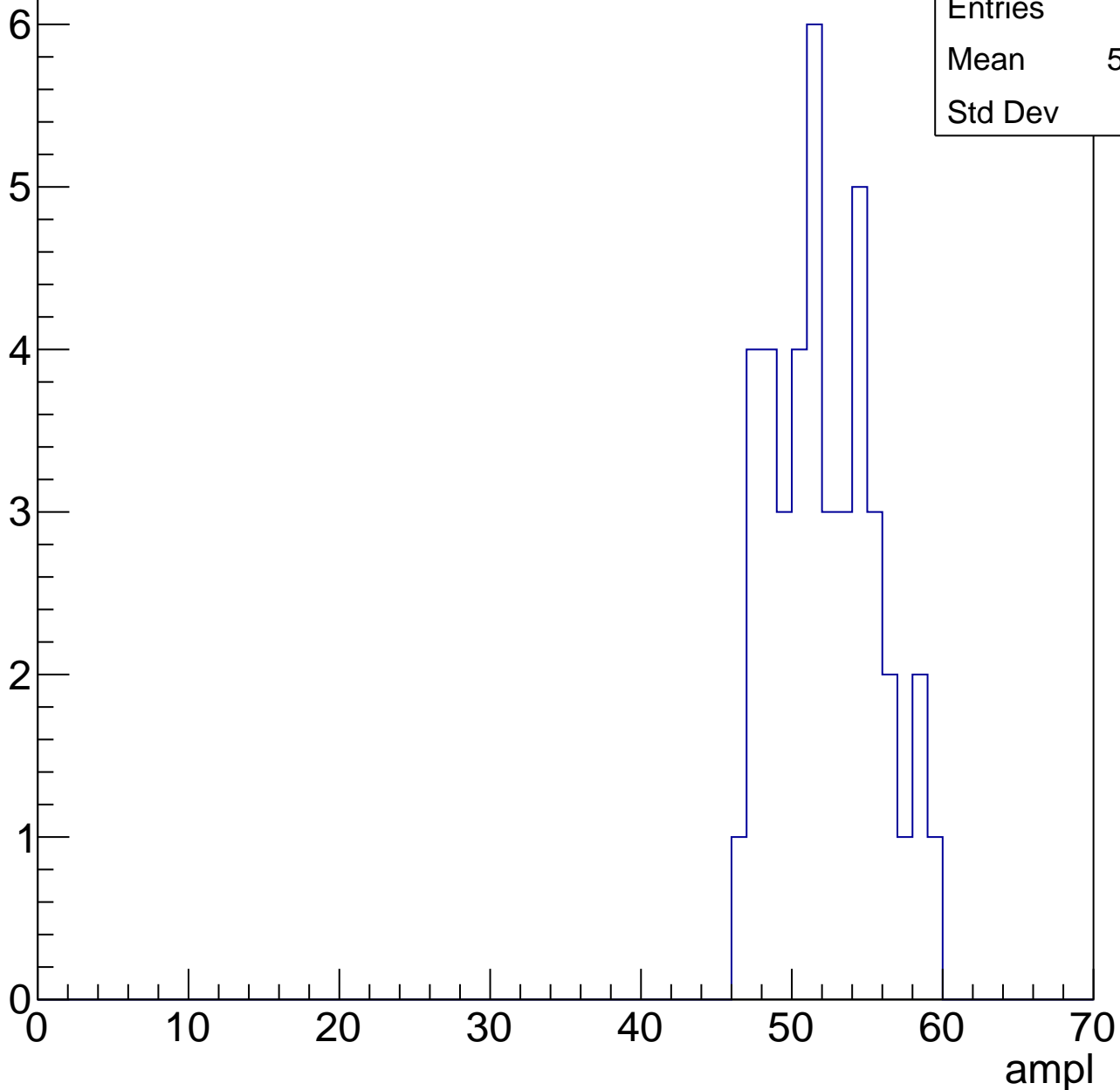


B1L103S, U1-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

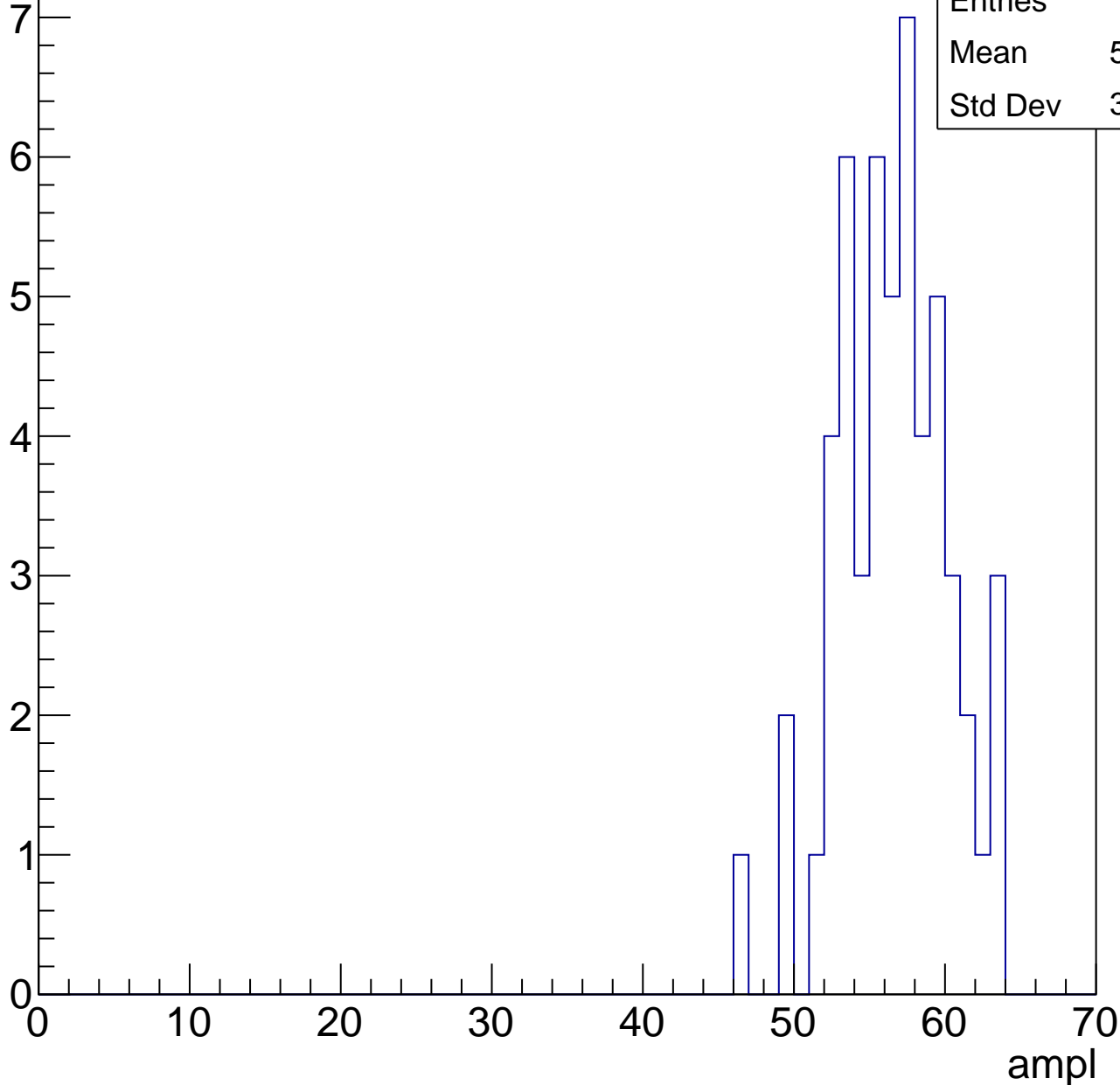
Entries	42
Mean	51.74
Std Dev	3.36



B1L103S, U1-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

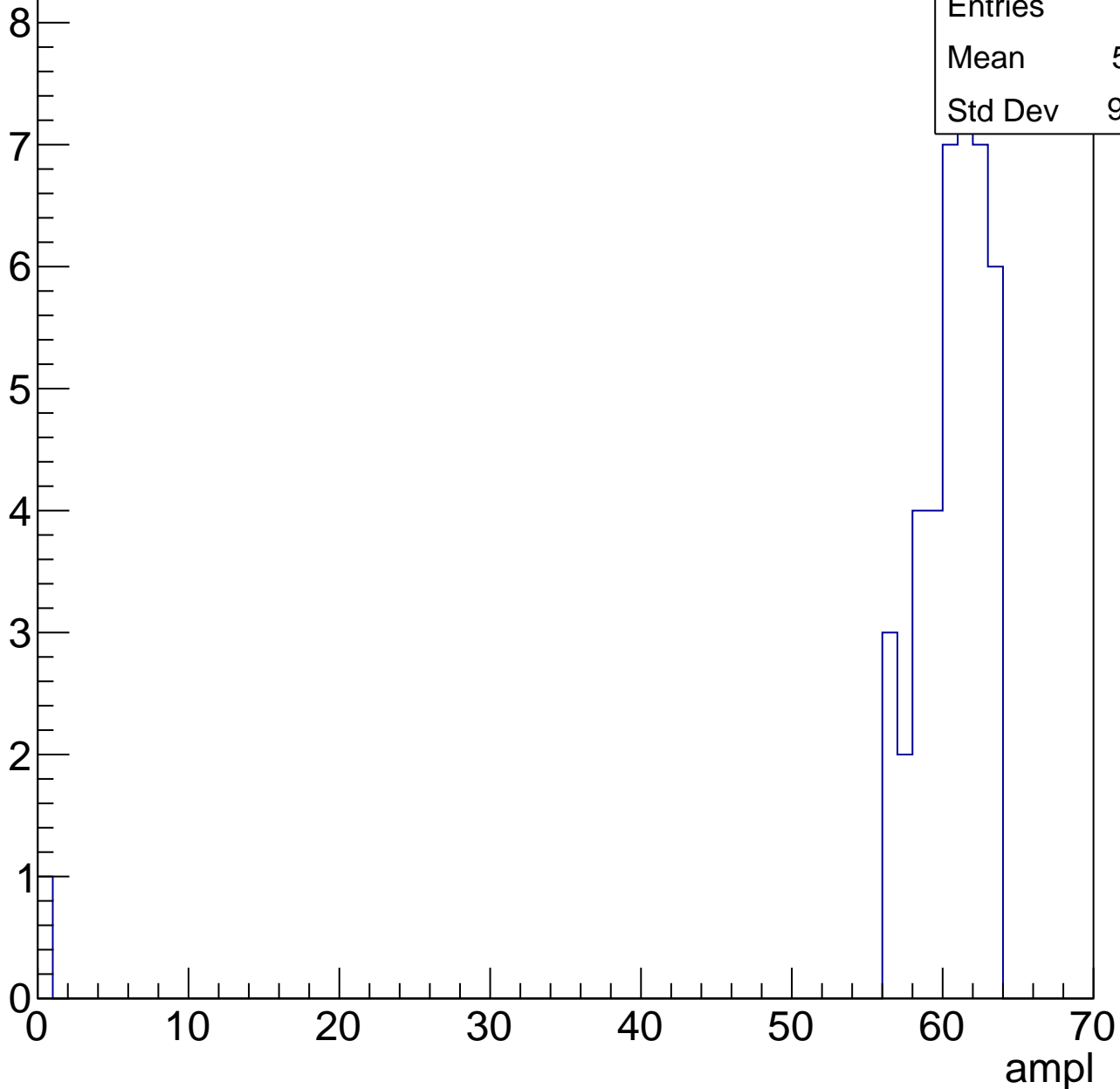


B1L103S, U1-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.81
Std Dev	9.407

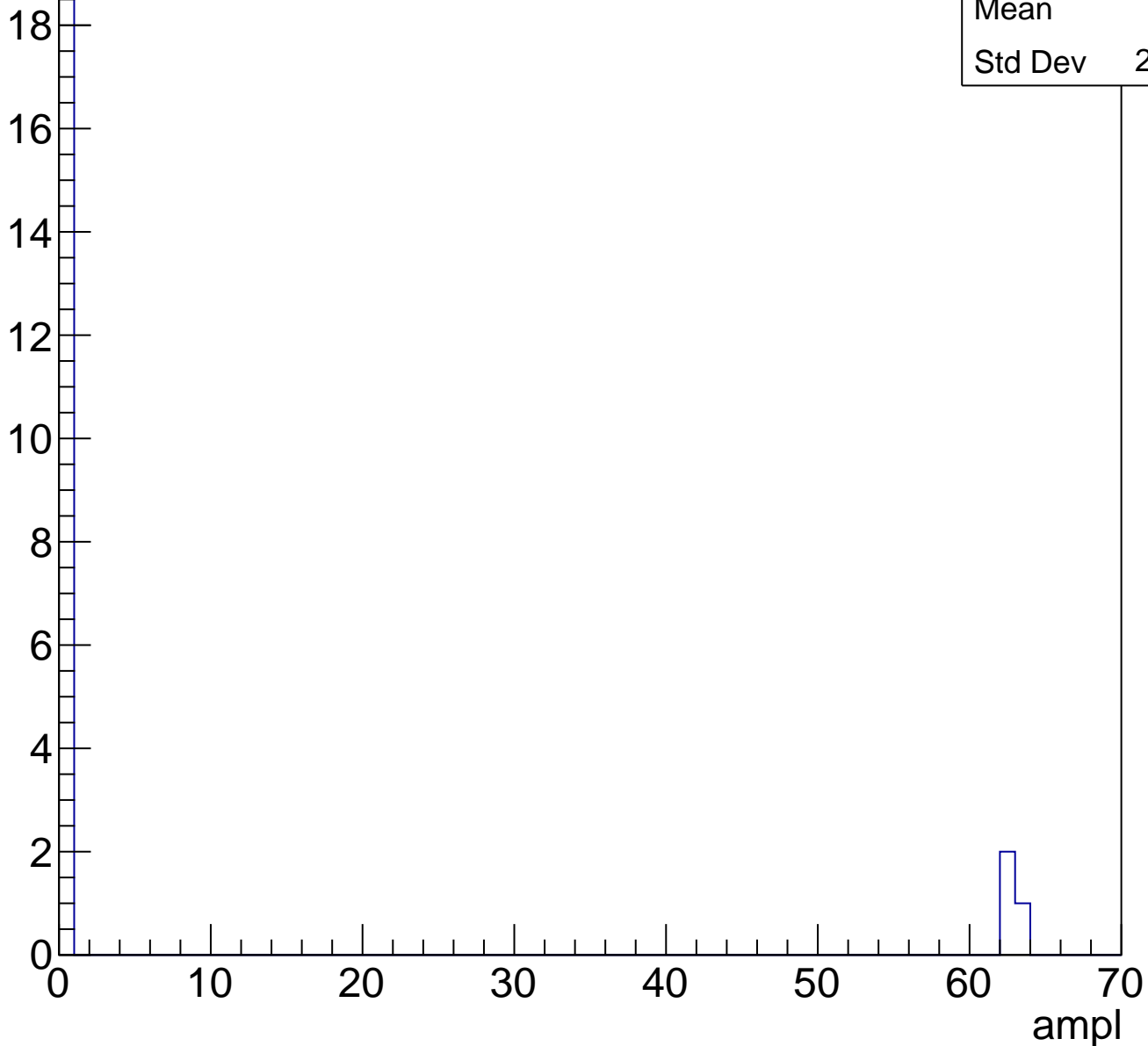


B1L103S, U1-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U1-ch101, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	19.85
Std Dev	13.27

Gaus mean : 28.6080

Gaus Width: 4.7135

Entry

25

20

15

10

5

0

0

10

20

30

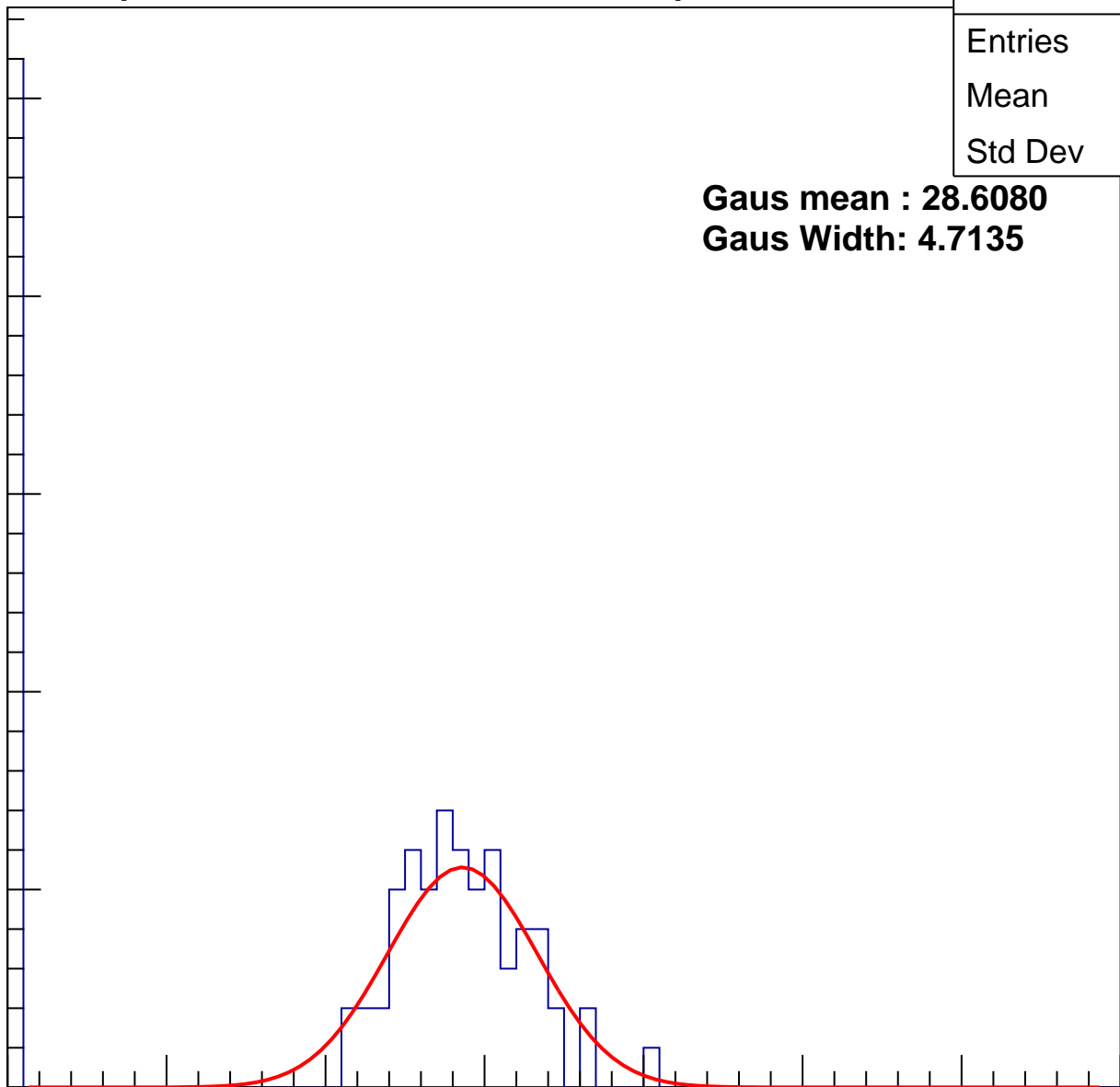
40

50

60

70

ampl



B1L103S, U1-ch101, adc1

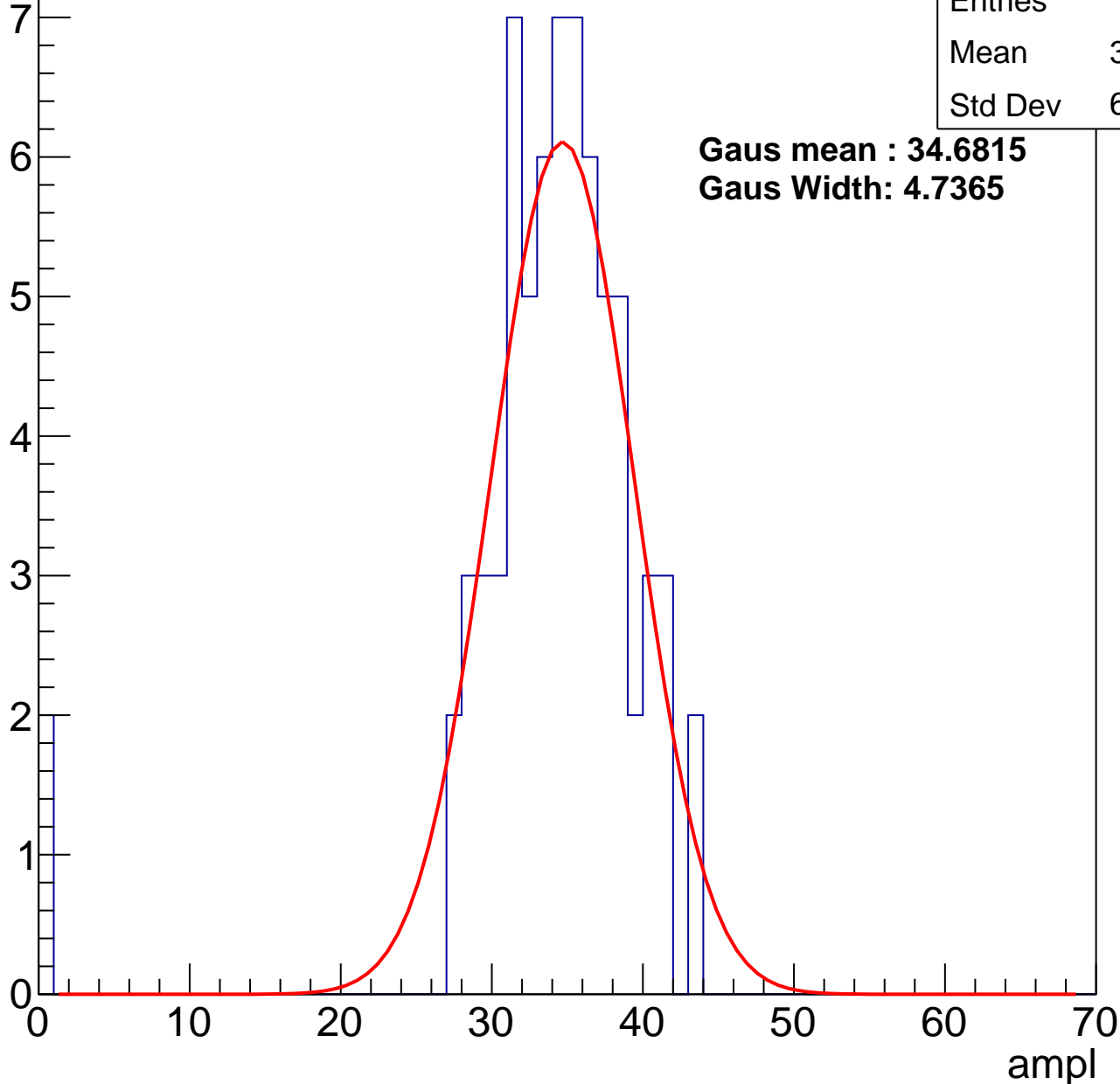
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.39
Std Dev	6.845

Gaus mean : 34.6815

Gaus Width: 4.7365



B1L103S, U1-ch101, adc2

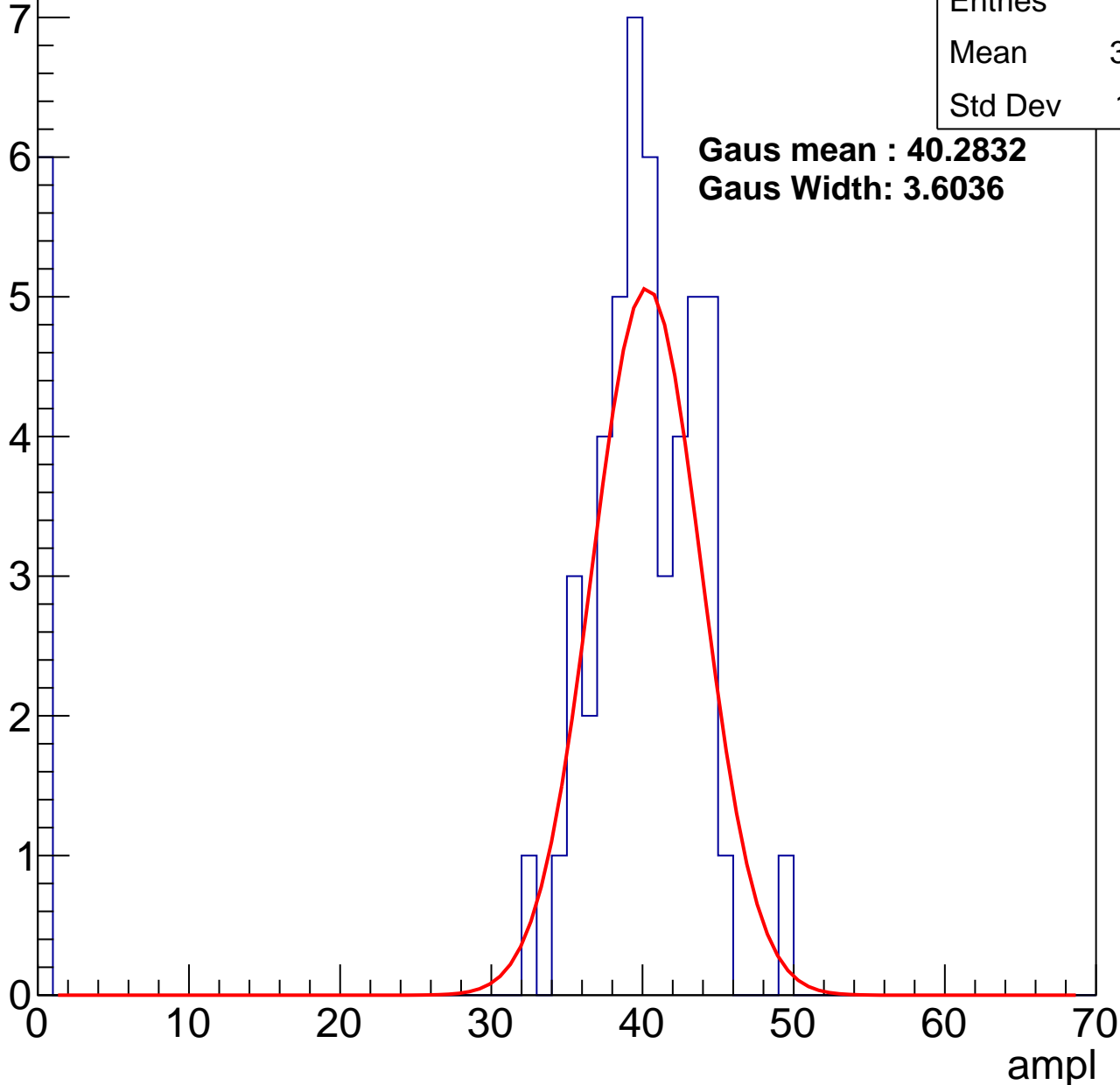
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	35.44
Std Dev	12.91

Gaus mean : 40.2832

Gaus Width: 3.6036

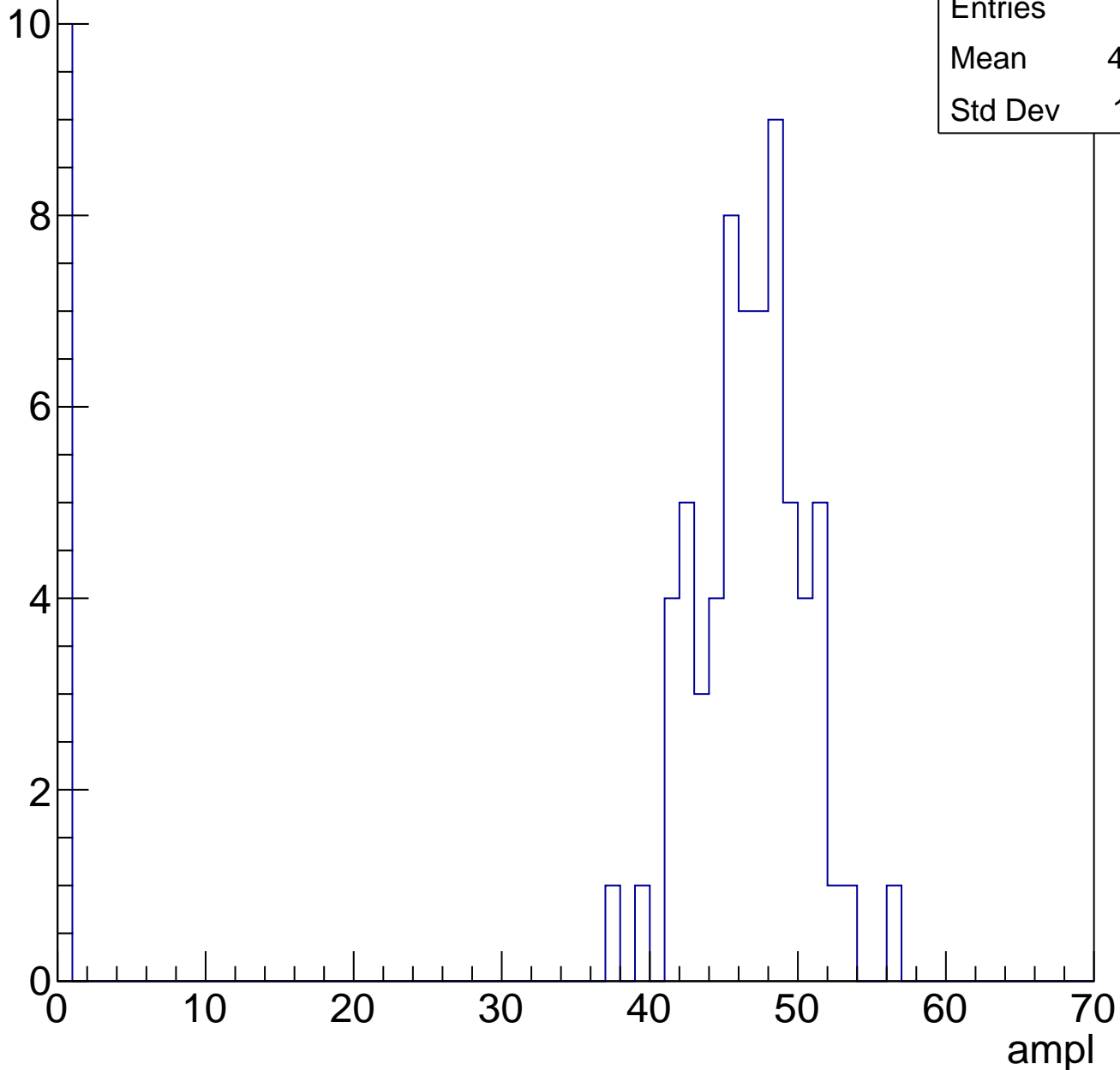


B1L103S, U1-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	40.25
Std Dev	16.01

Entry

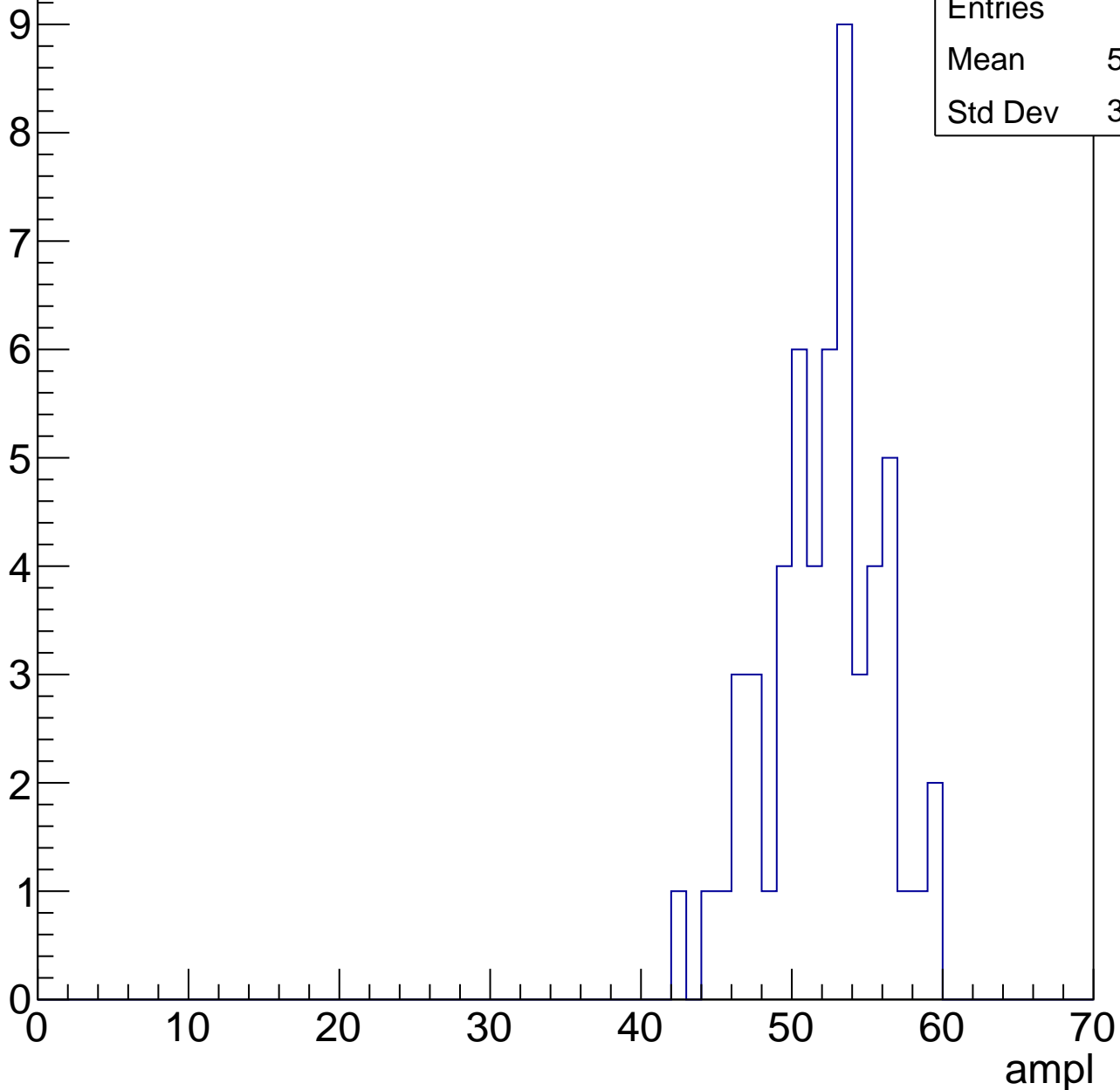


B1L103S, U1-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	51.67
Std Dev	3.732

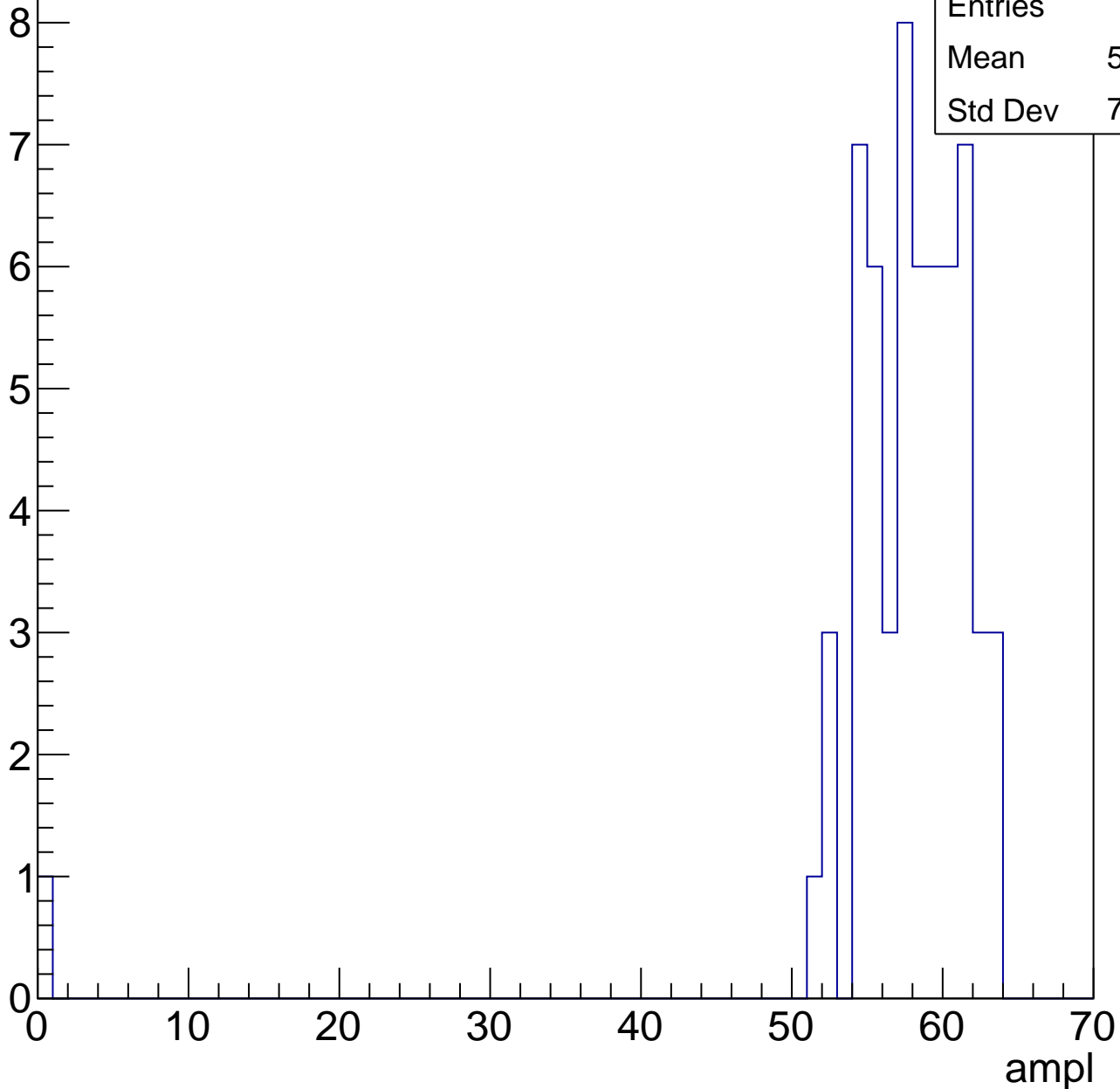


B1L103S, U1-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	56.72
Std Dev	7.984

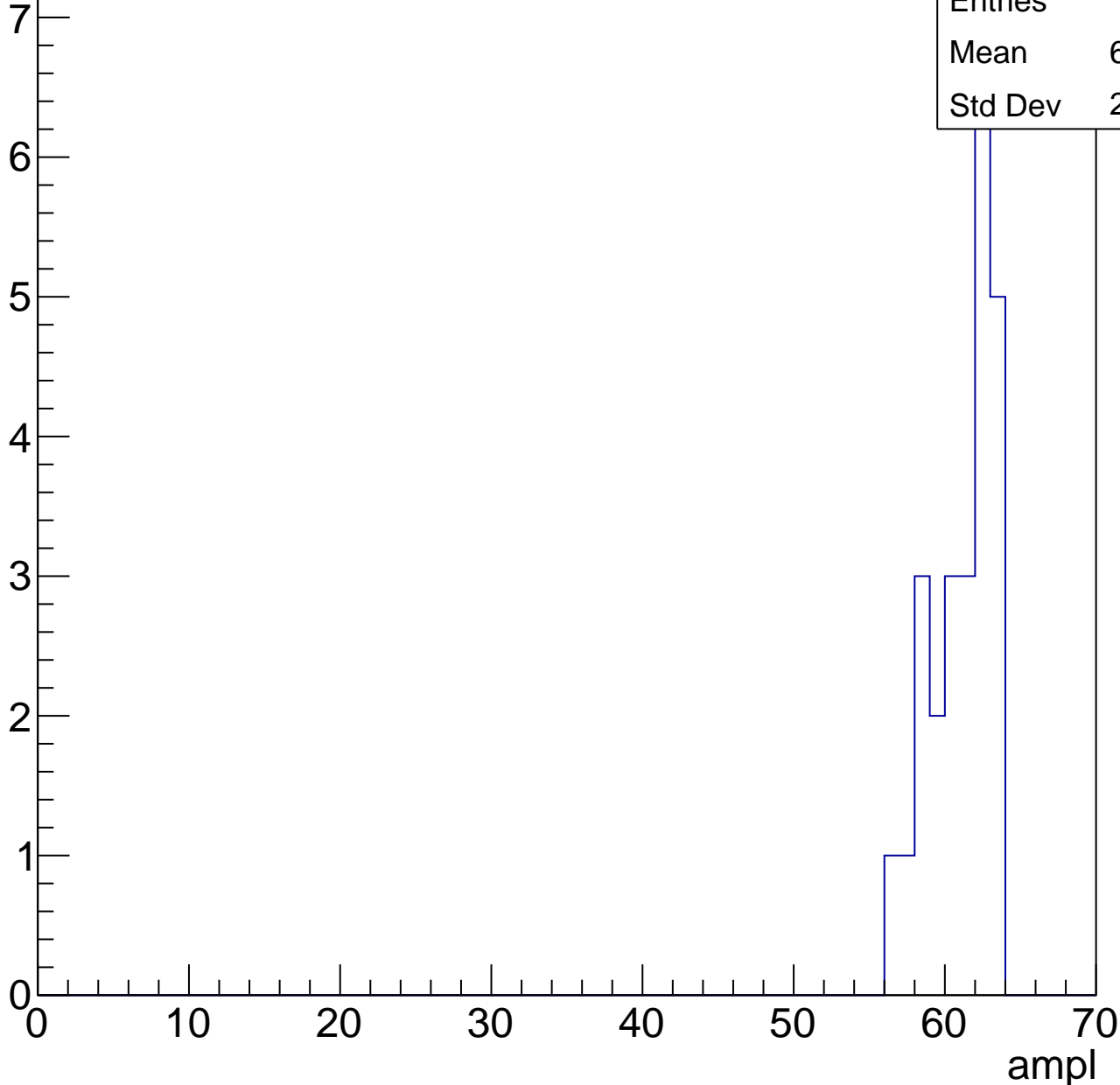


B1L103S, U1-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	60.68
Std Dev	2.034

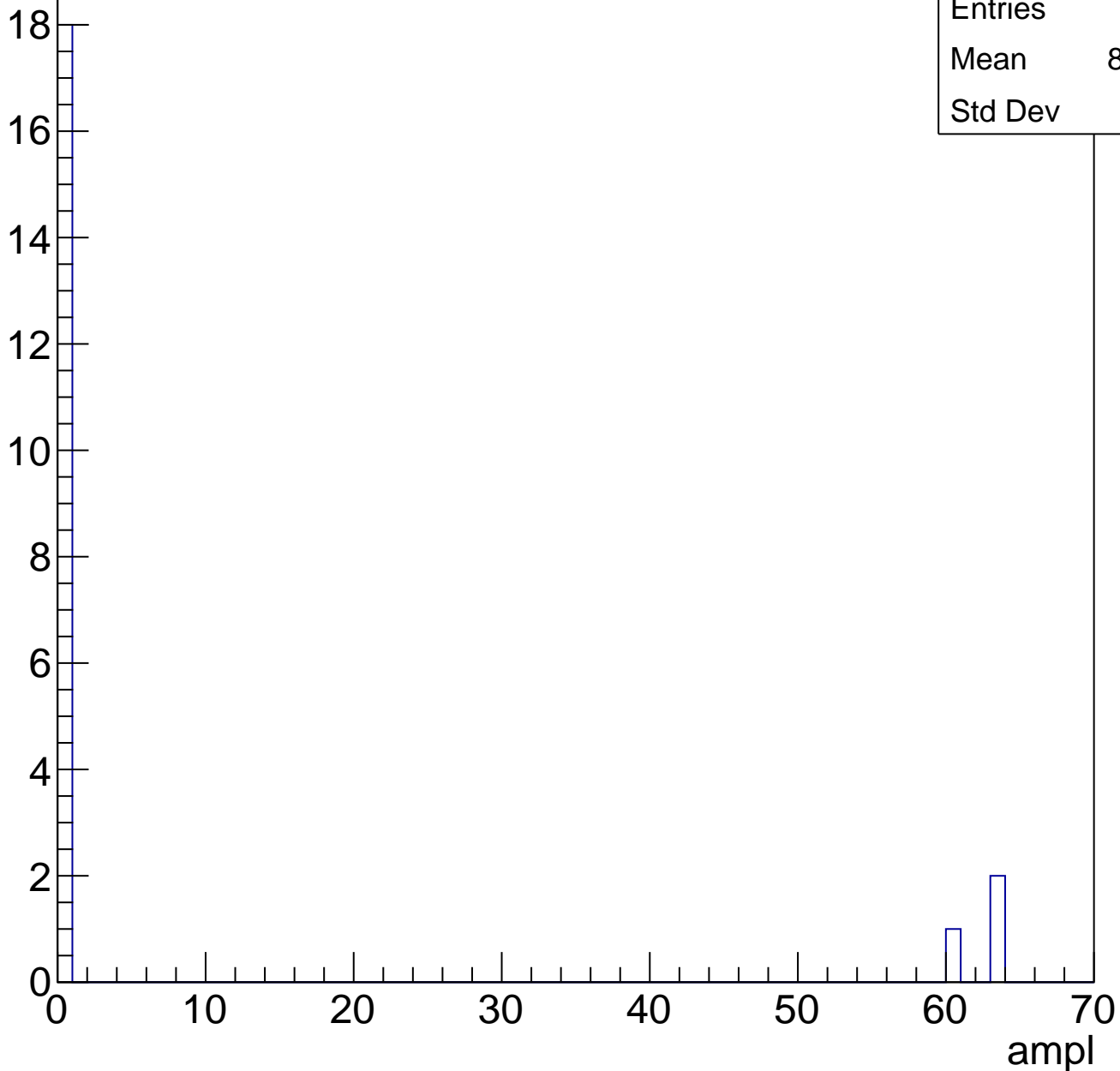


B1L103S, U1-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.857
Std Dev	21.7

Entry



B1L103S, U1-ch102, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	17.51
Std Dev	12.21

Gaus mean : 25.9851

Gaus Width: 4.0643

Entry

25

20

15

10

5

0

0

10

20

30

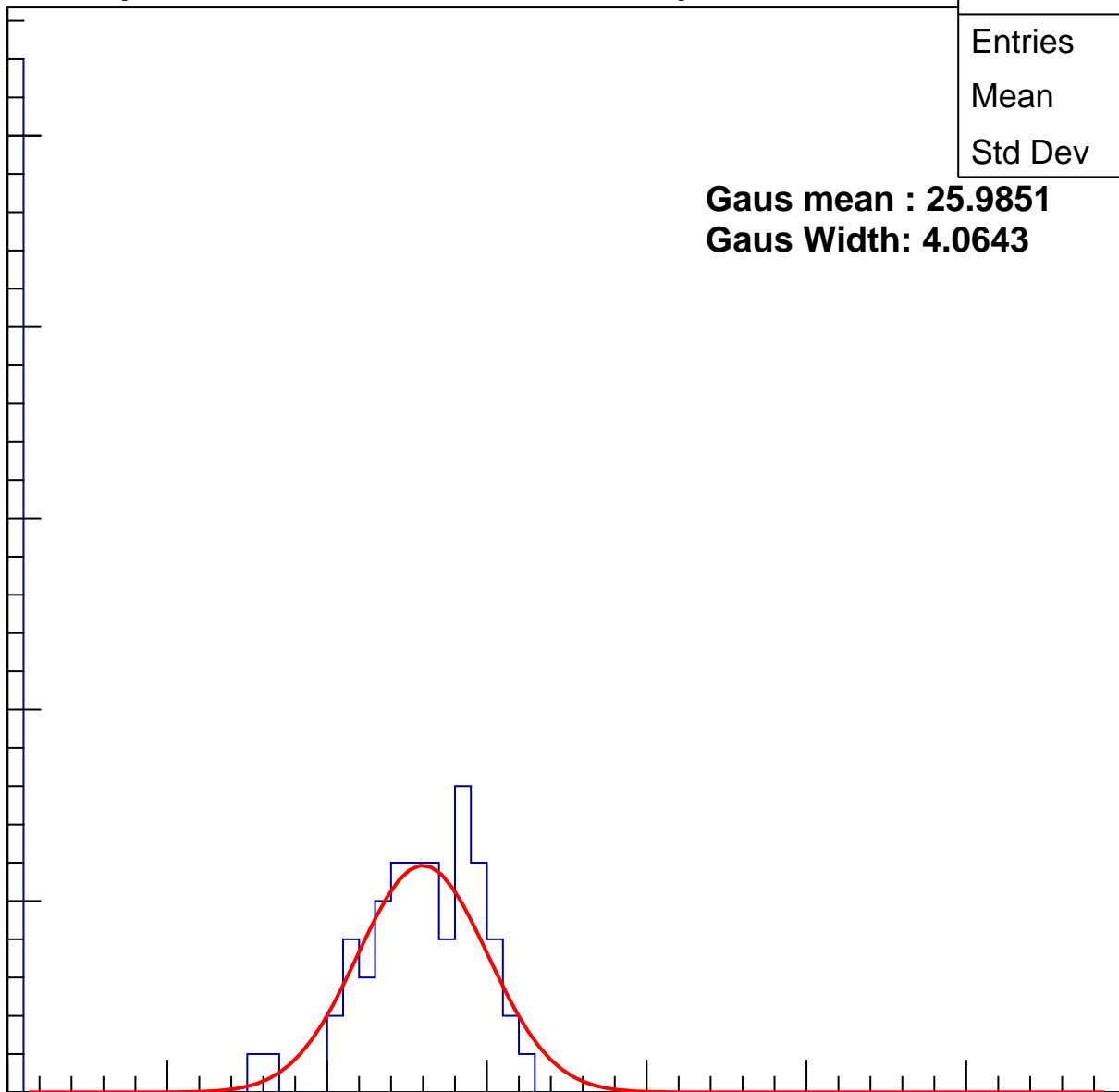
40

50

60

70

ampl



B1L103S, U1-ch102, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	27.29
Std Dev	11.98

Gaus mean : 32.8595

Gaus Width: 3.5932

Entry

10

8

6

4

2

0

0

10

20

30

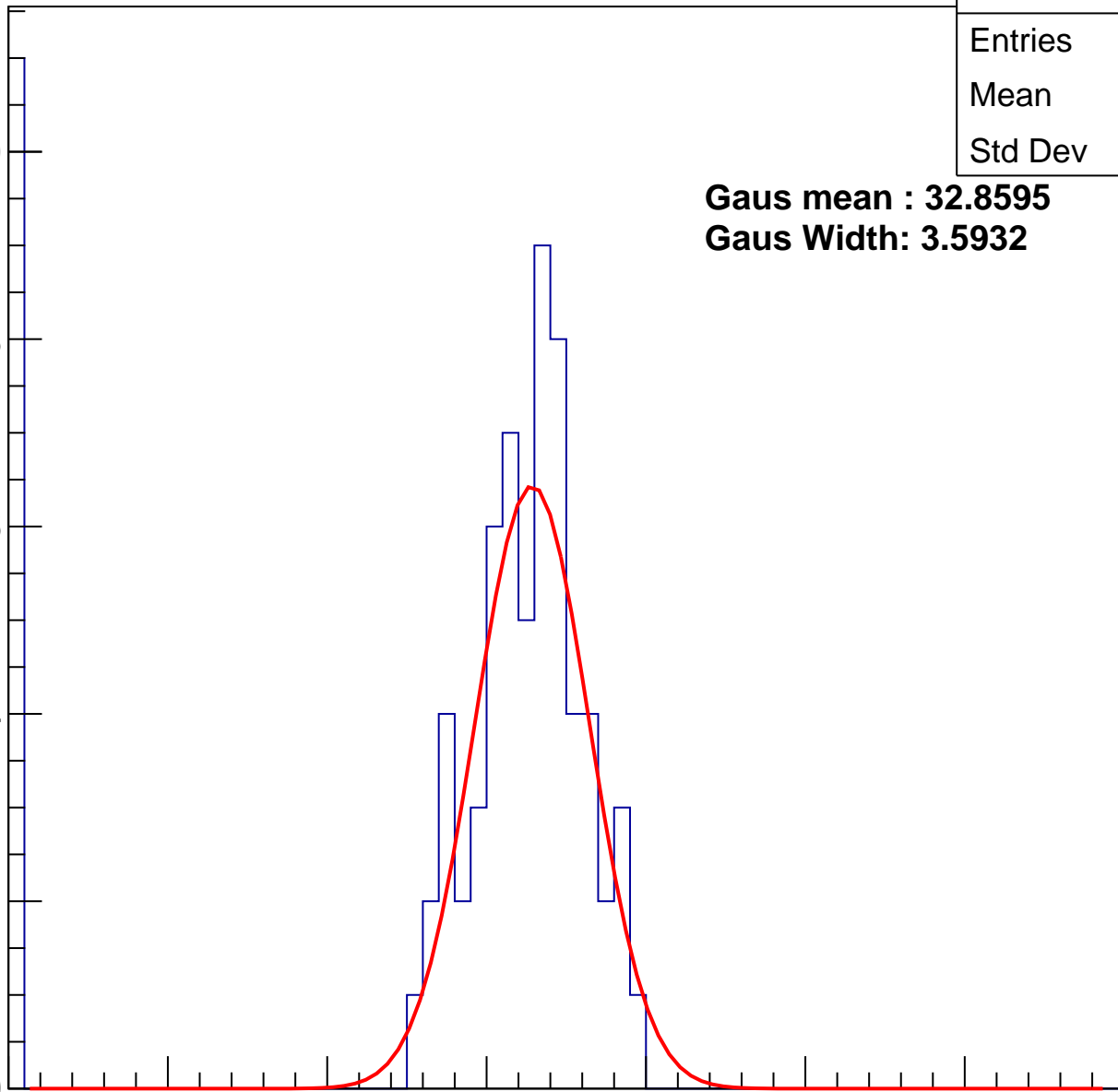
40

50

60

70

ampl



B1L103S, U1-ch102, adc2

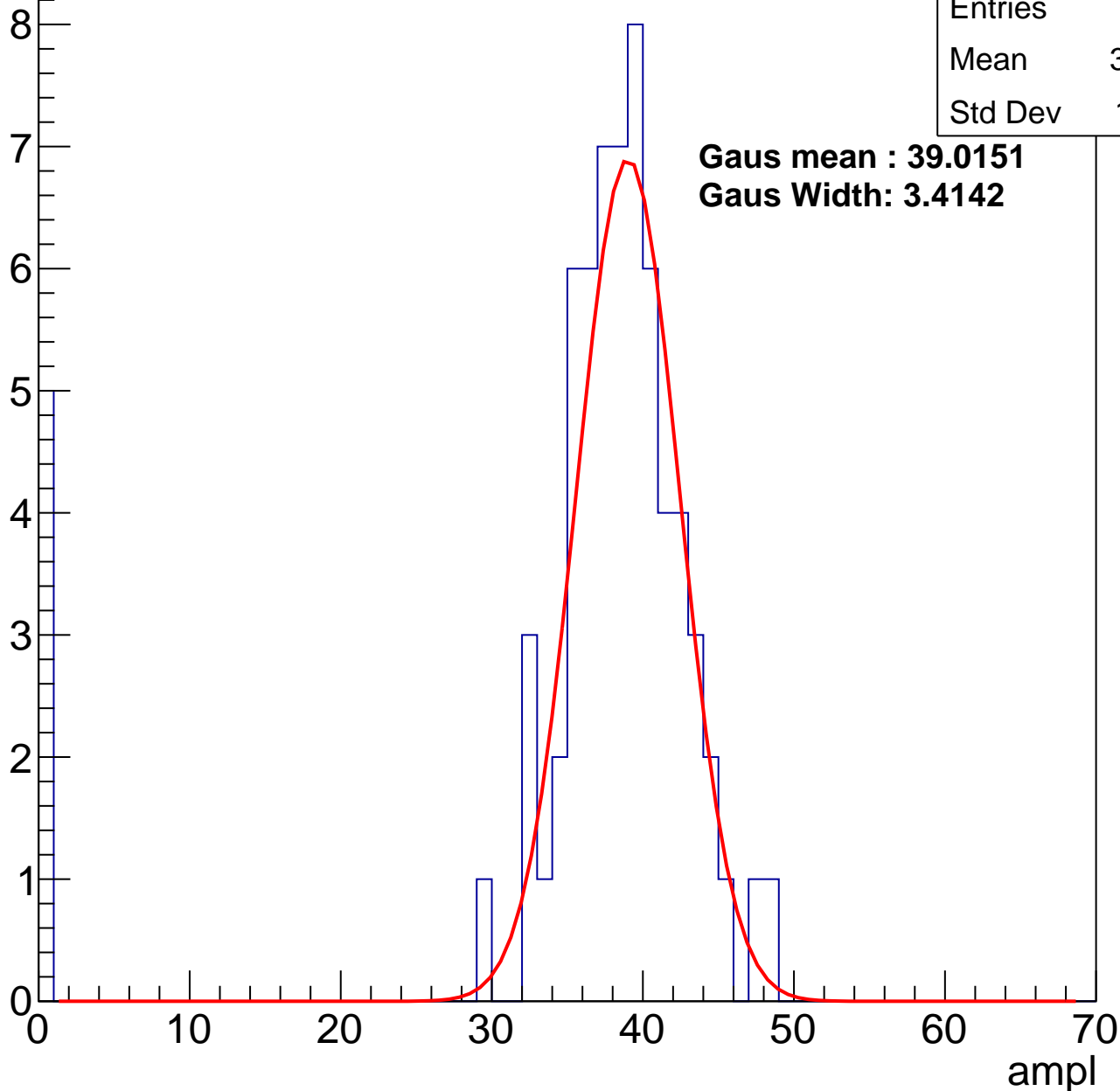
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.56
Std Dev	10.61

Gaus mean : 39.0151

Gaus Width: 3.4142

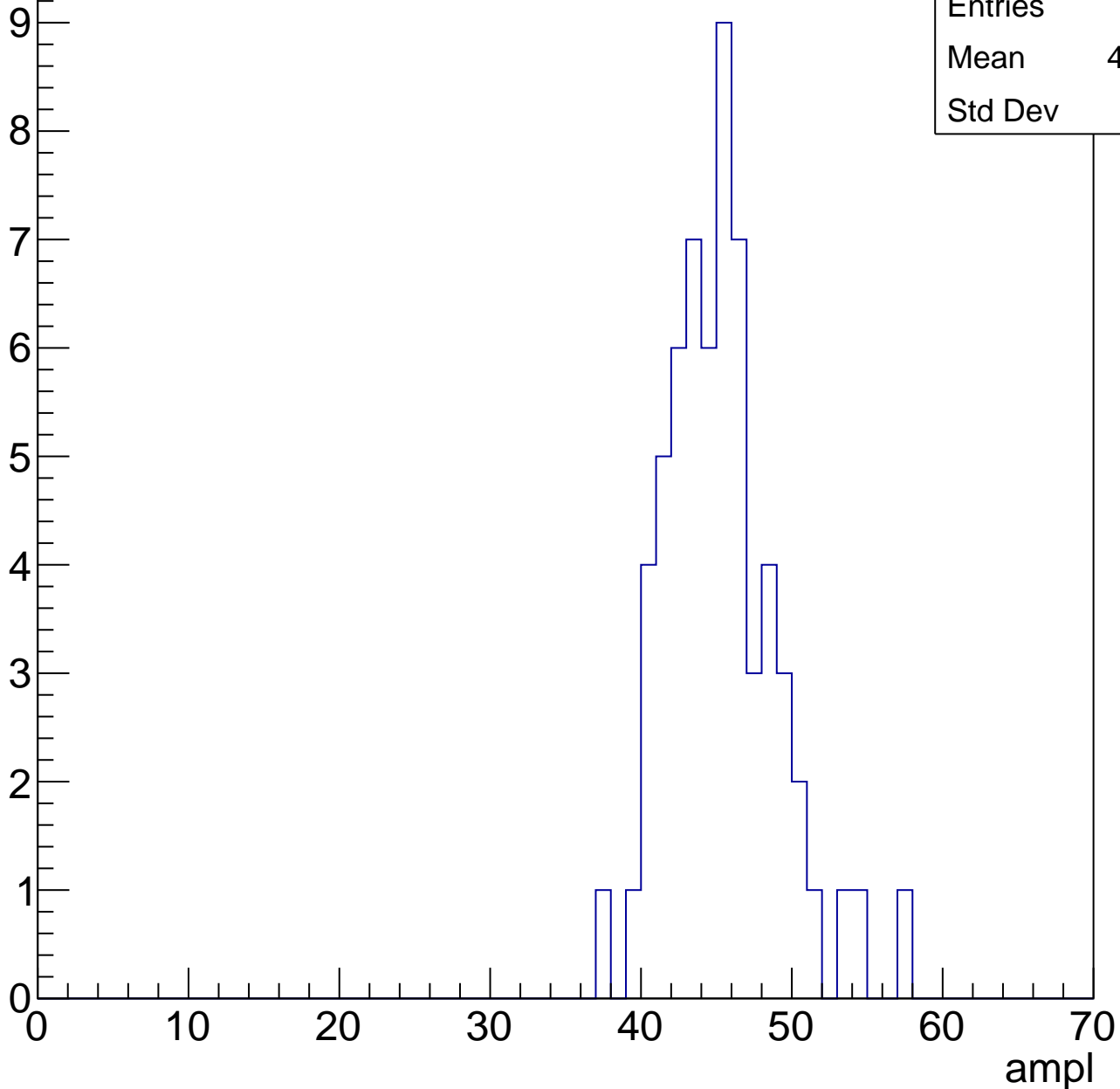


B1L103S, U1-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	44.84
Std Dev	3.69

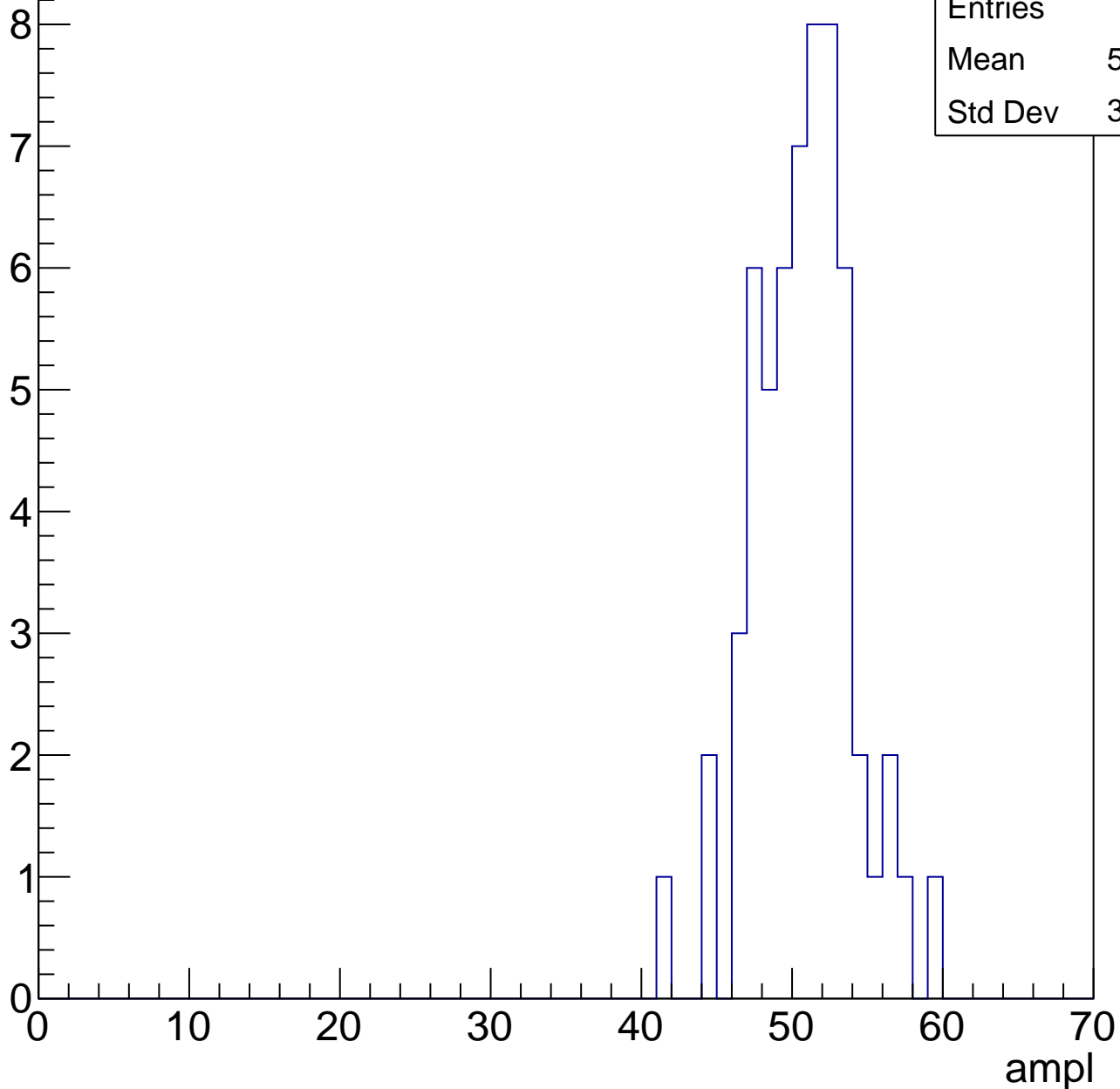


B1L103S, U1-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	50.27
Std Dev	3.267

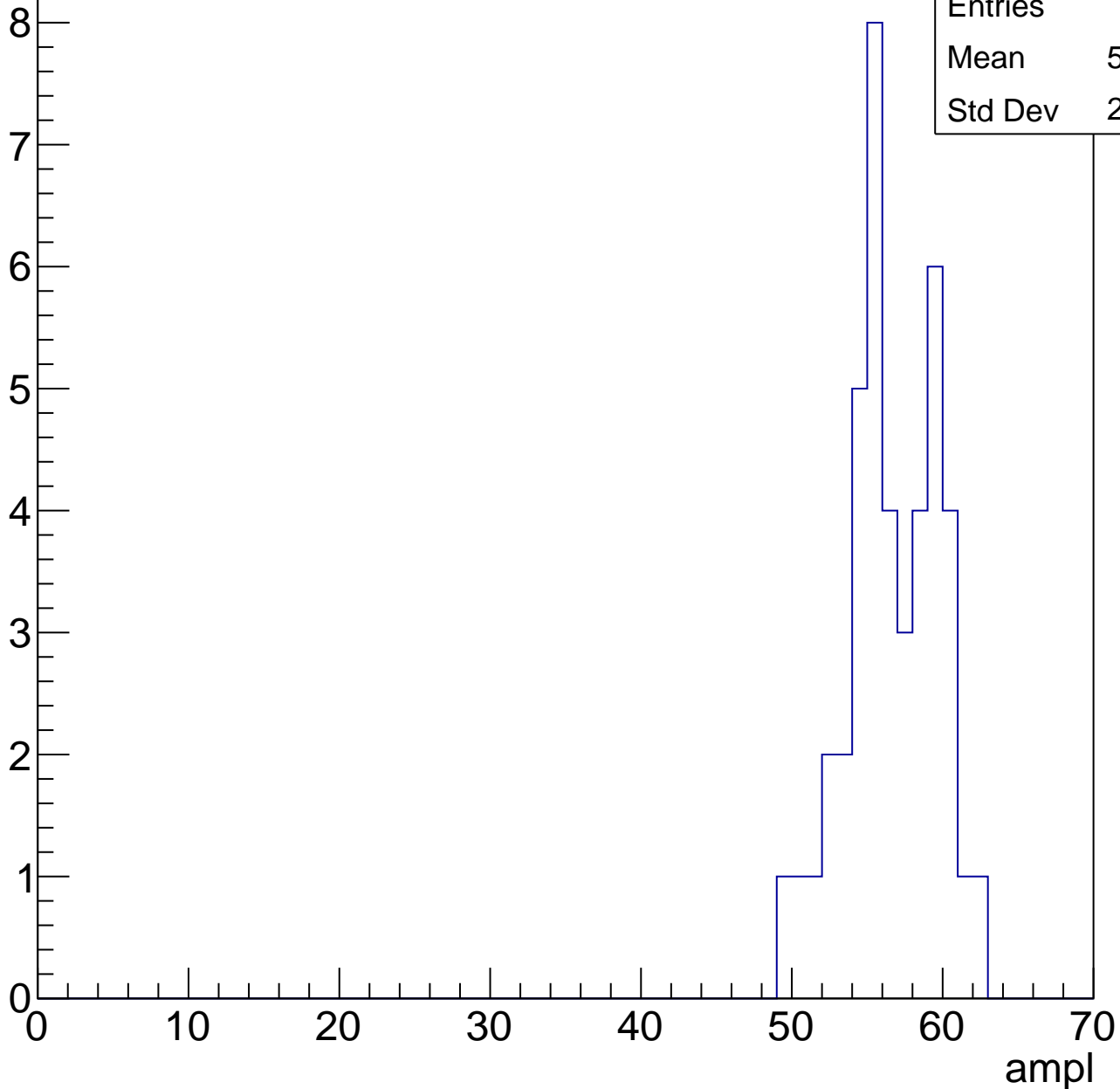


B1L103S, U1-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	56.14
Std Dev	2.993

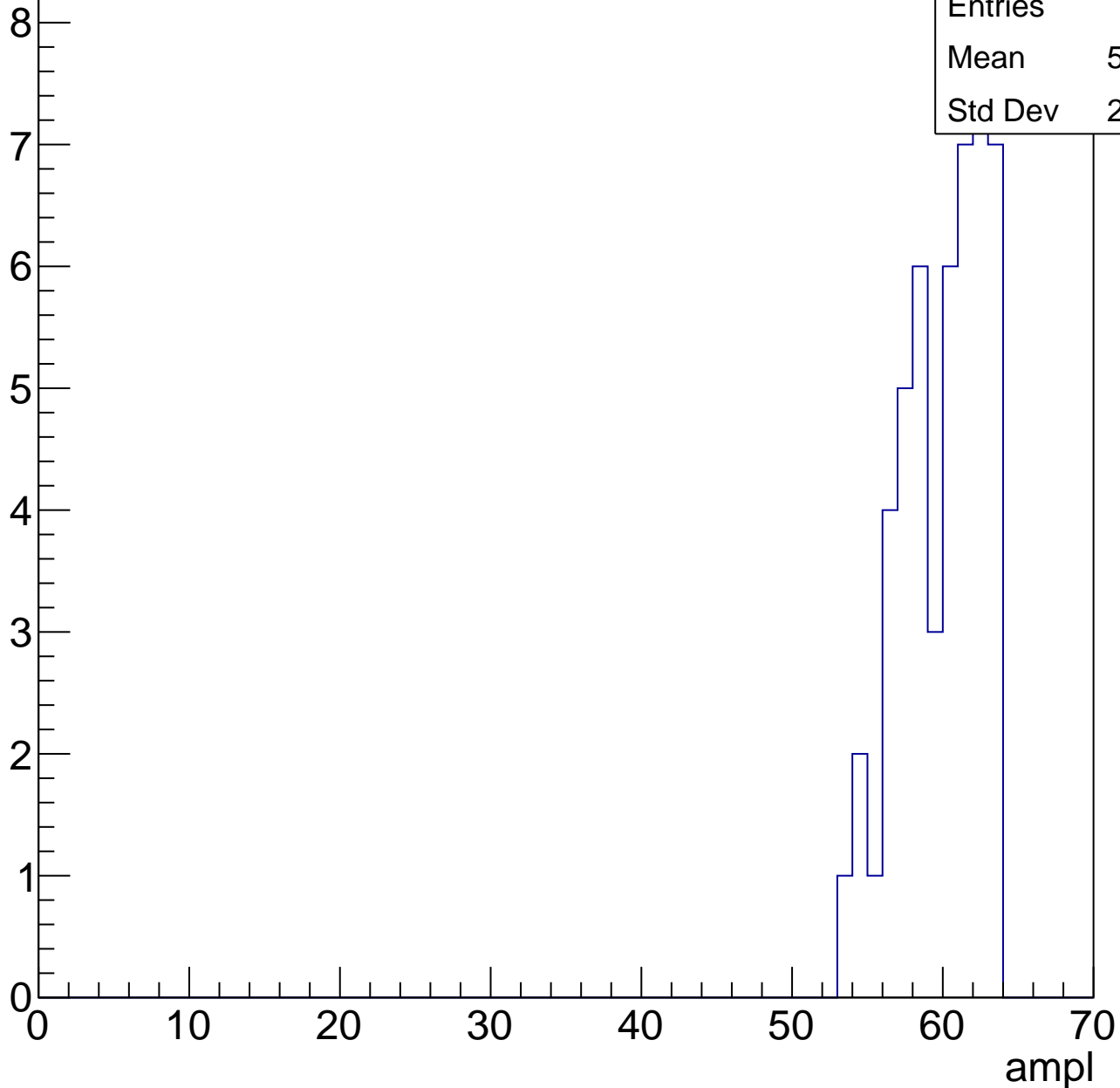


B1L103S, U1-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	59.48
Std Dev	2.722



B1L103S, U1-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

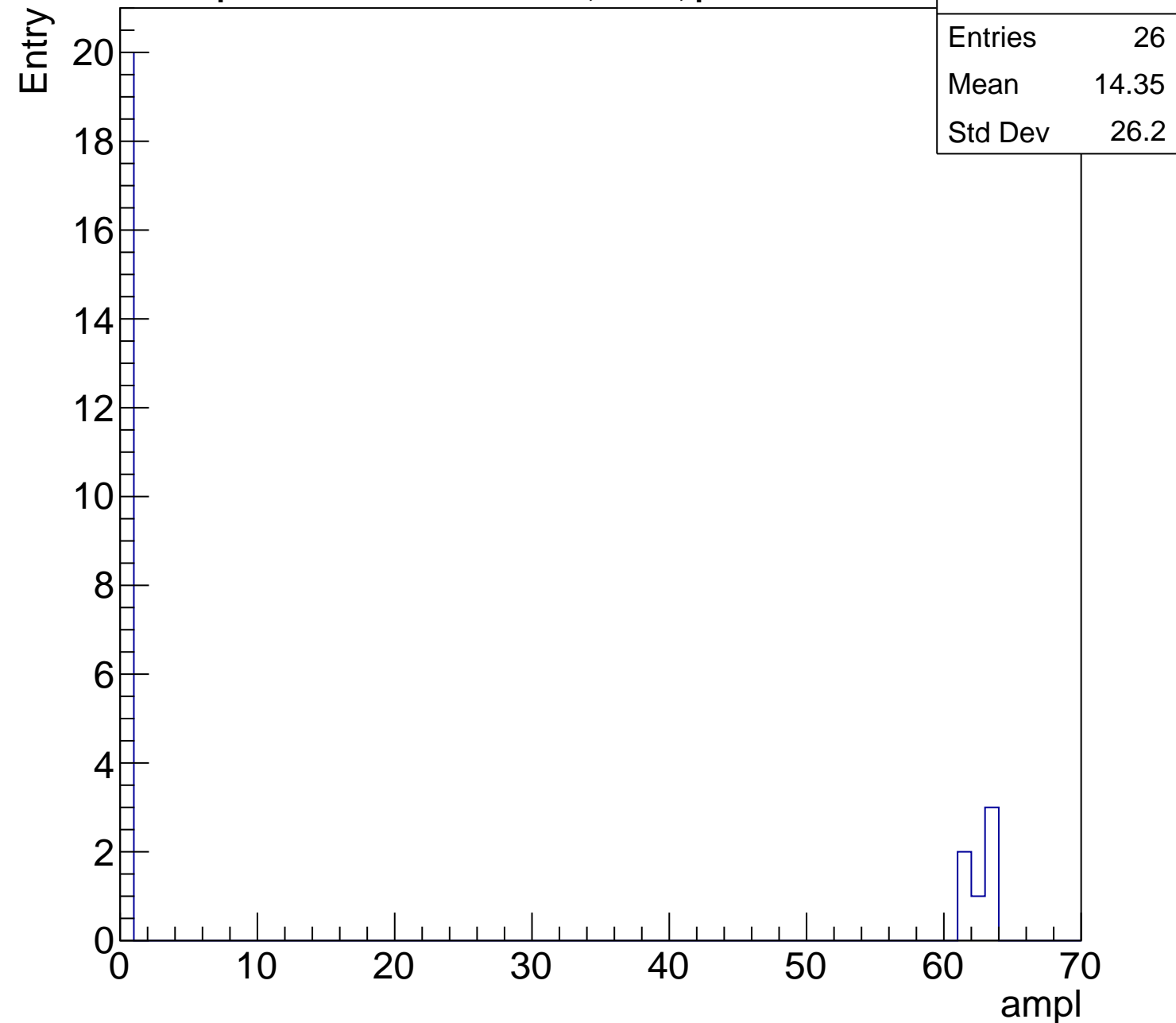
Entries	26
Mean	14.35
Std Dev	26.2

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch103, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	24.69
Std Dev	10.24

Gaus mean : 28.6963

Gaus Width: 4.3085

Entry

12

10

8

6

4

2

0

0

10

20

30

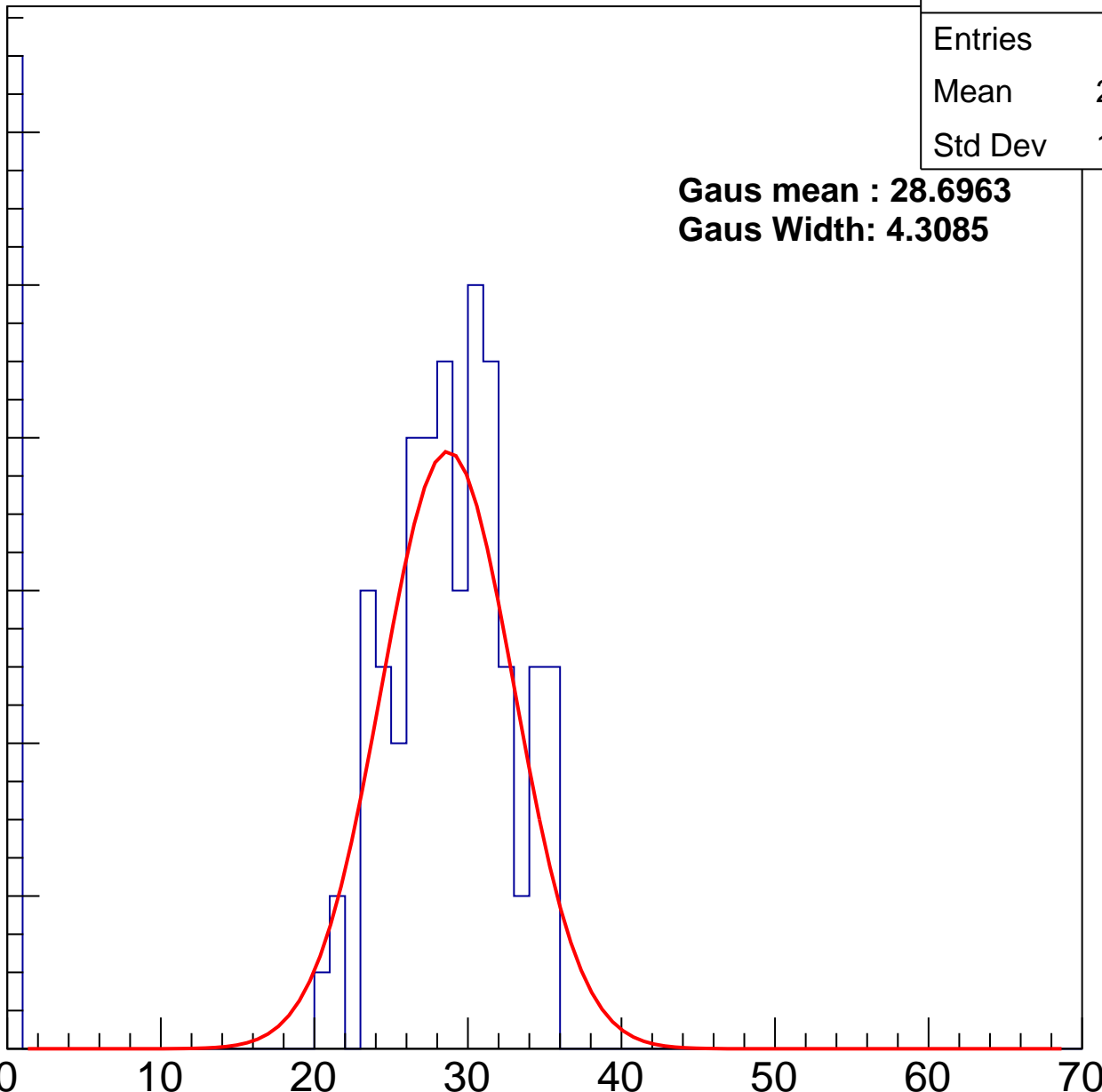
40

50

60

70

ampl



B1L103S, U1-ch103, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	59
Mean	35.98
Std Dev	3.51

Gaus mean : 36.4058

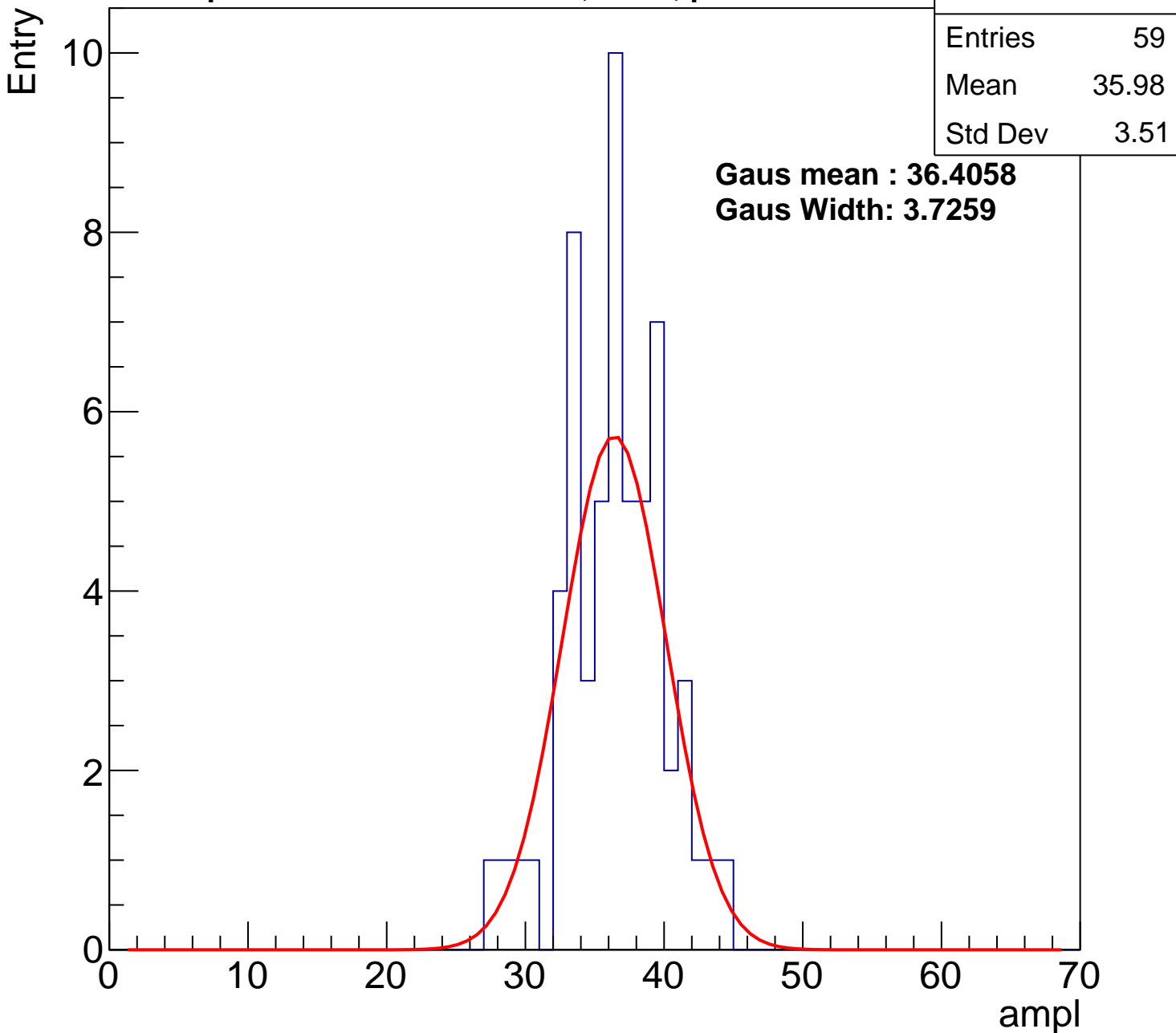
Gaus Width: 3.7259

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch103, adc2

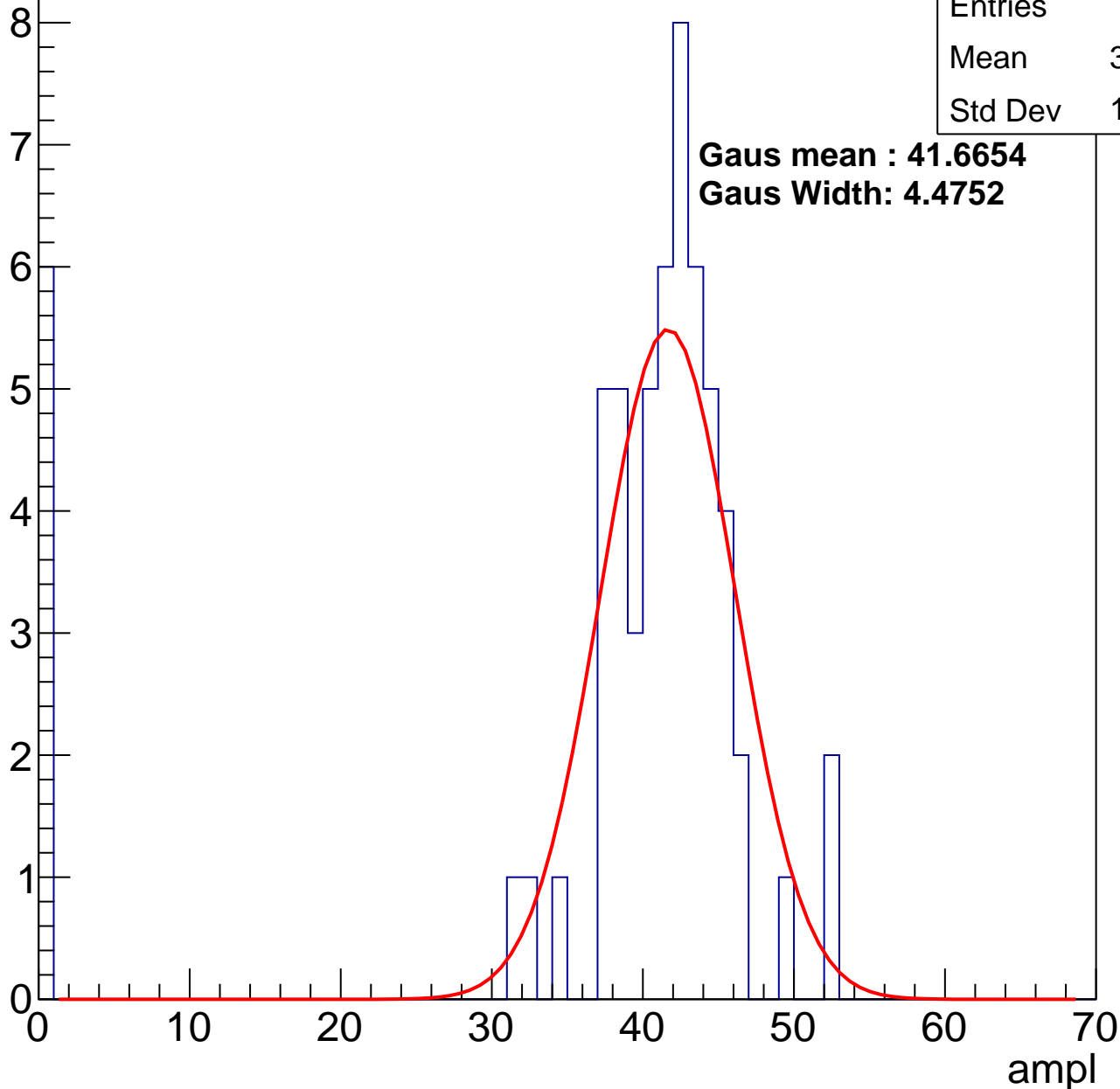
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	37.28
Std Dev	12.87

Gaus mean : 41.6654

Gaus Width: 4.4752



B1L103S, U1-ch103, adc3

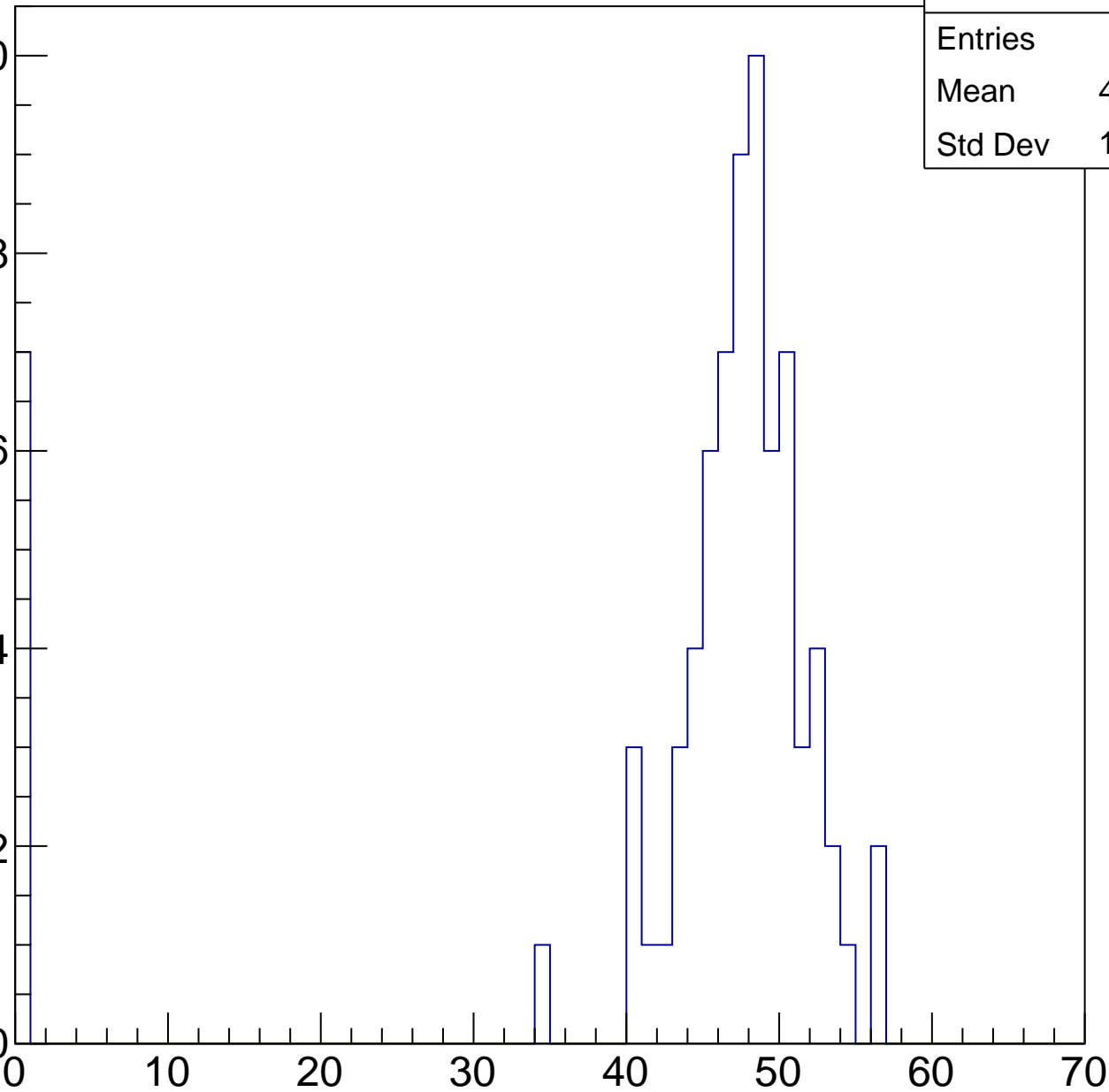
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	43.04
Std Dev	14.08

Entry

10
8
6
4
2
0

ampl

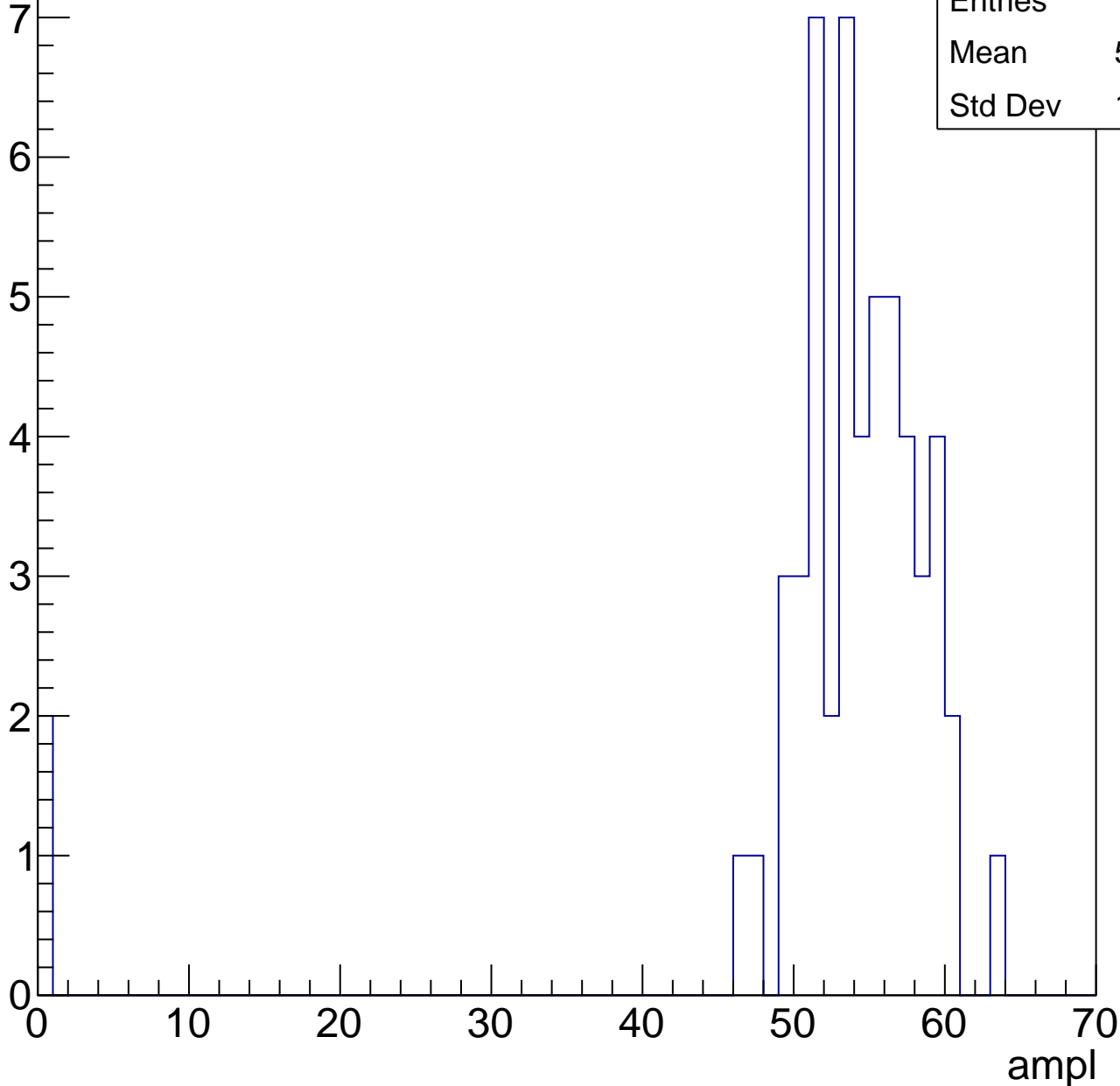


B1L103S, U1-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	52.11
Std Dev	10.81

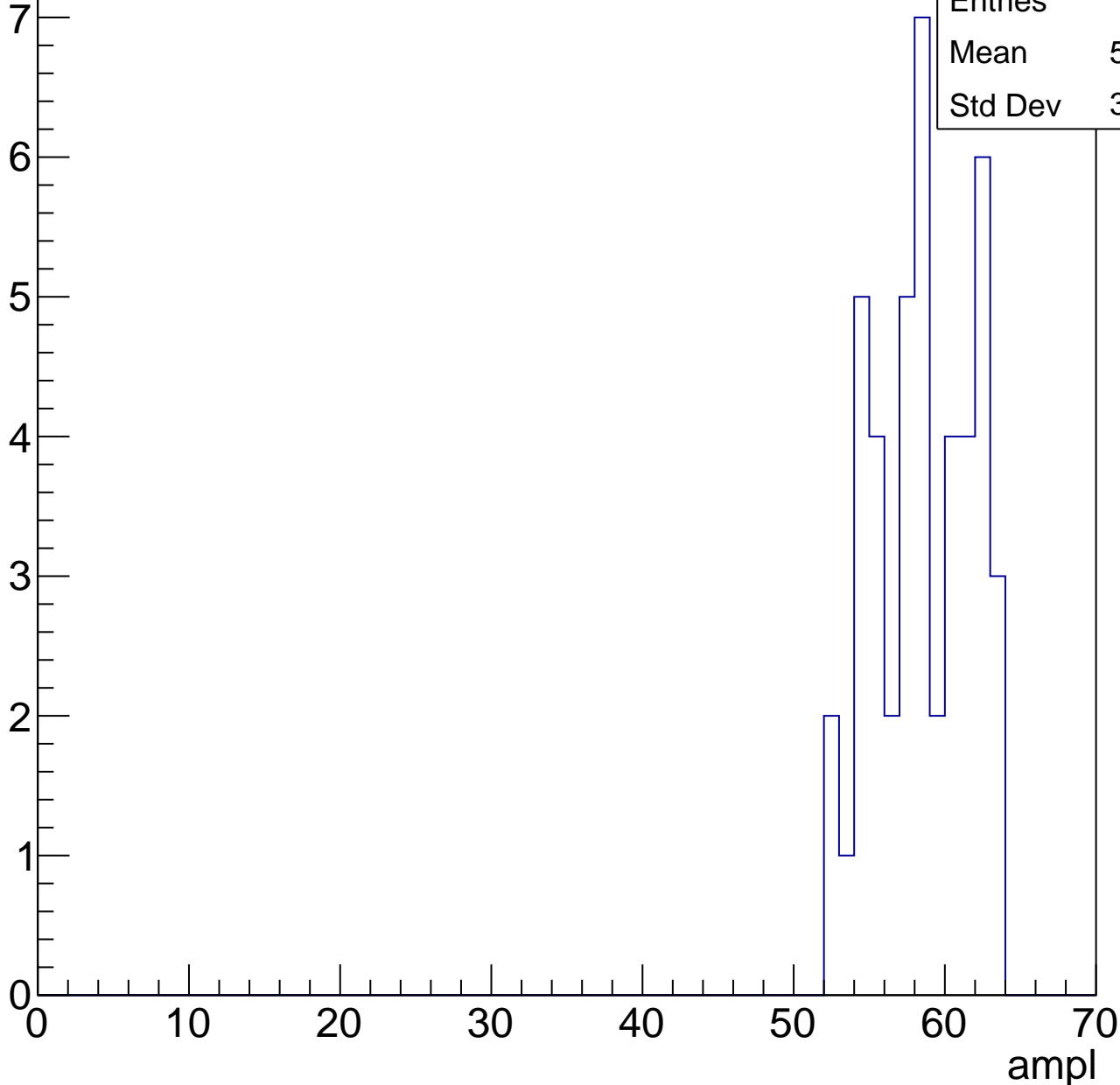


B1L103S, U1-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.07
Std Dev	3.165

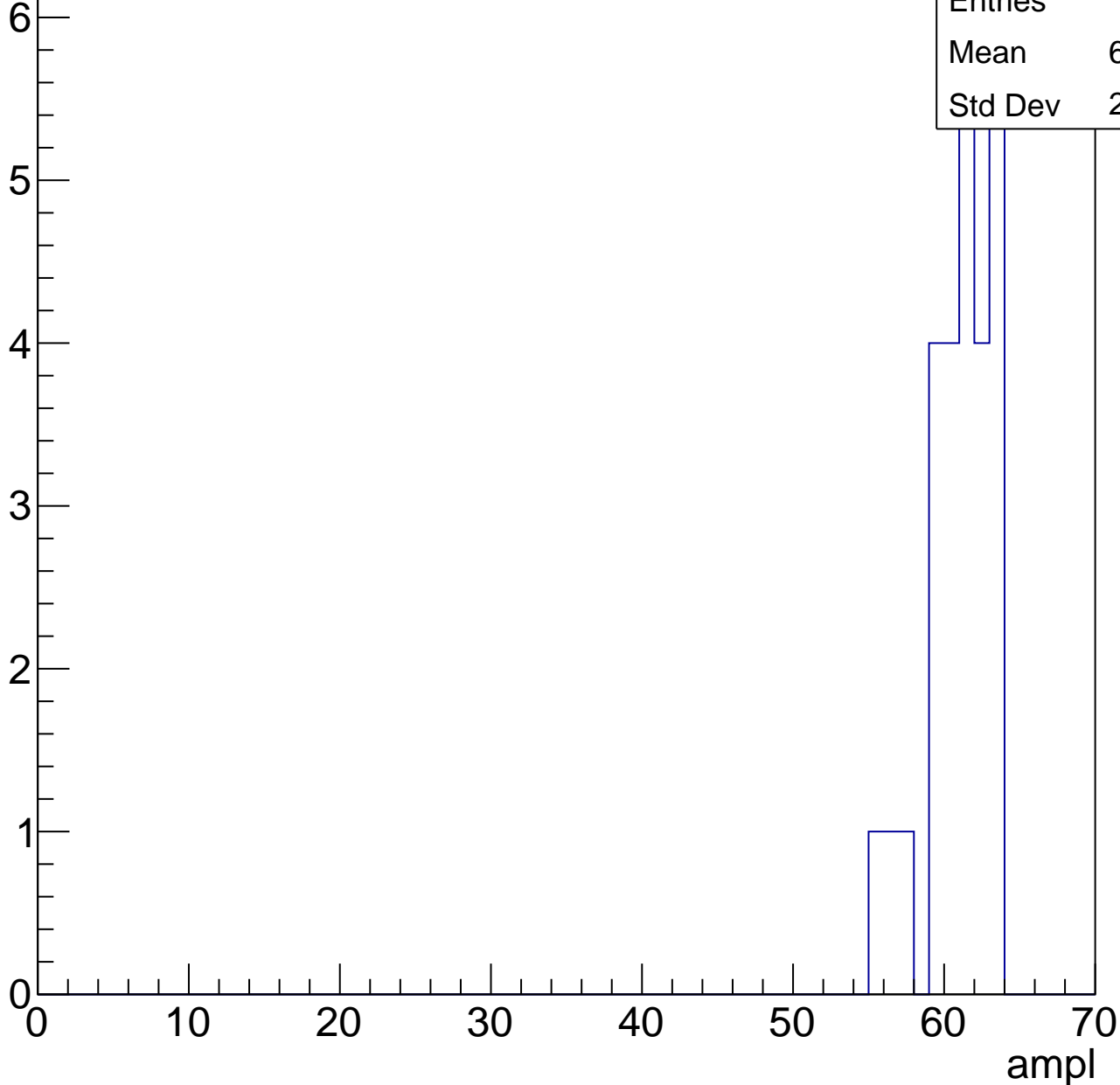


B1L103S, U1-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

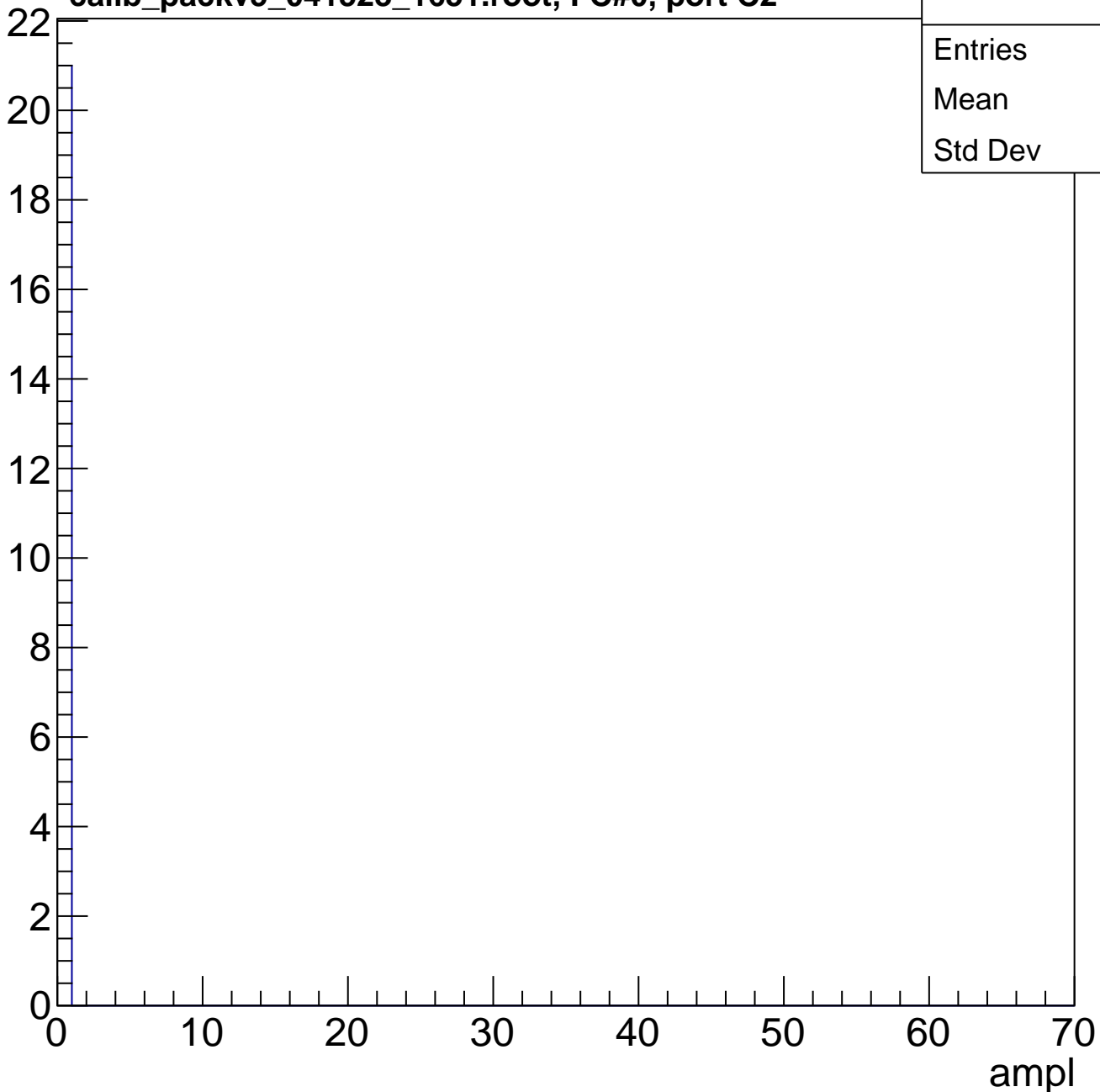
Entries	27
Mean	60.59
Std Dev	2.113



B1L103S, U1-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	21
Mean	0
Std Dev	0

B1L103S, U1-ch104, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	18.23
Std Dev	13.24

Gaus mean : 27.7517

Gaus Width: 3.2061

Entry

30

25

20

15

10

5

0

0

10

20

30

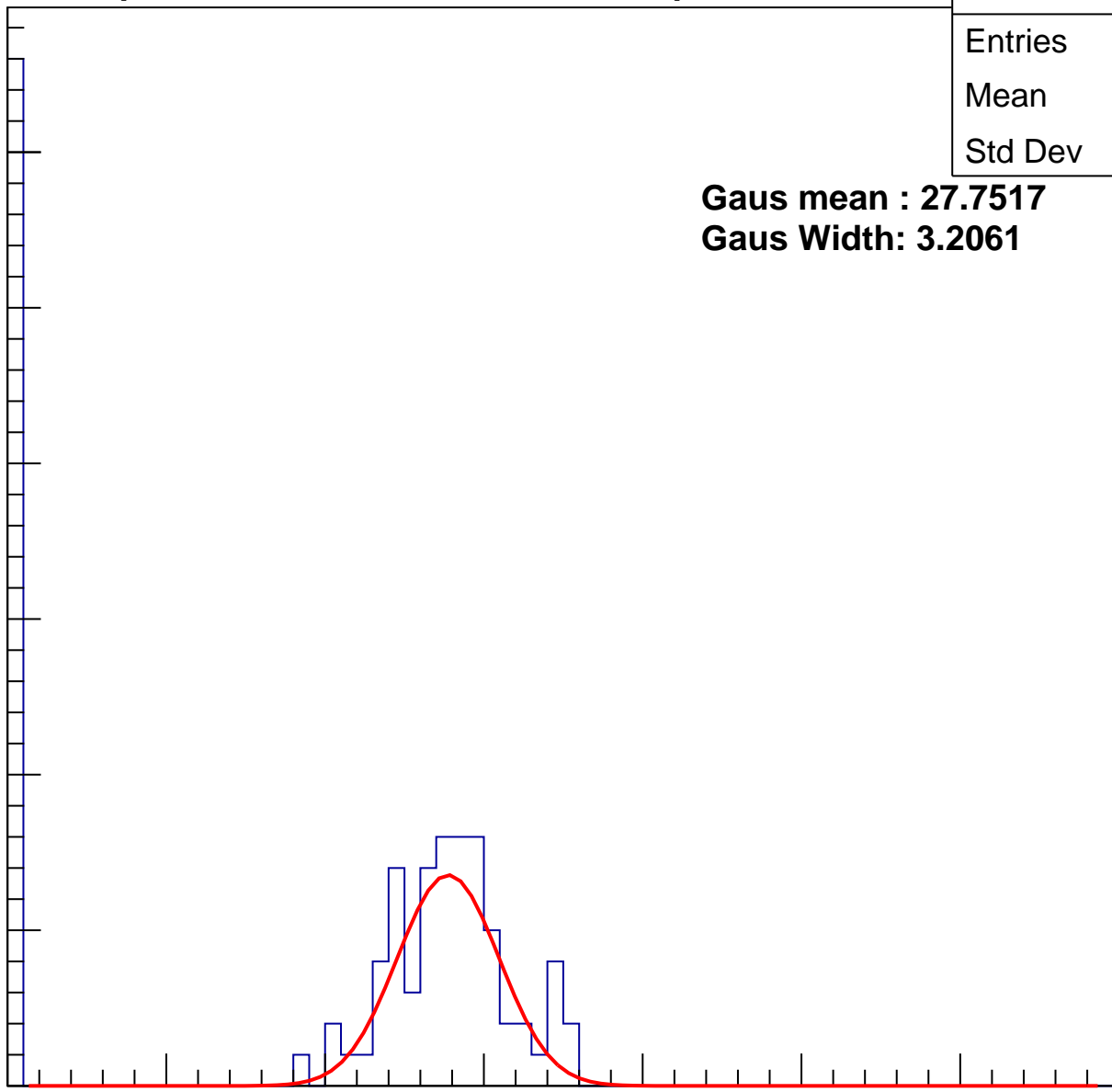
40

50

60

70

ampl



B1L103S, U1-ch104, adc1

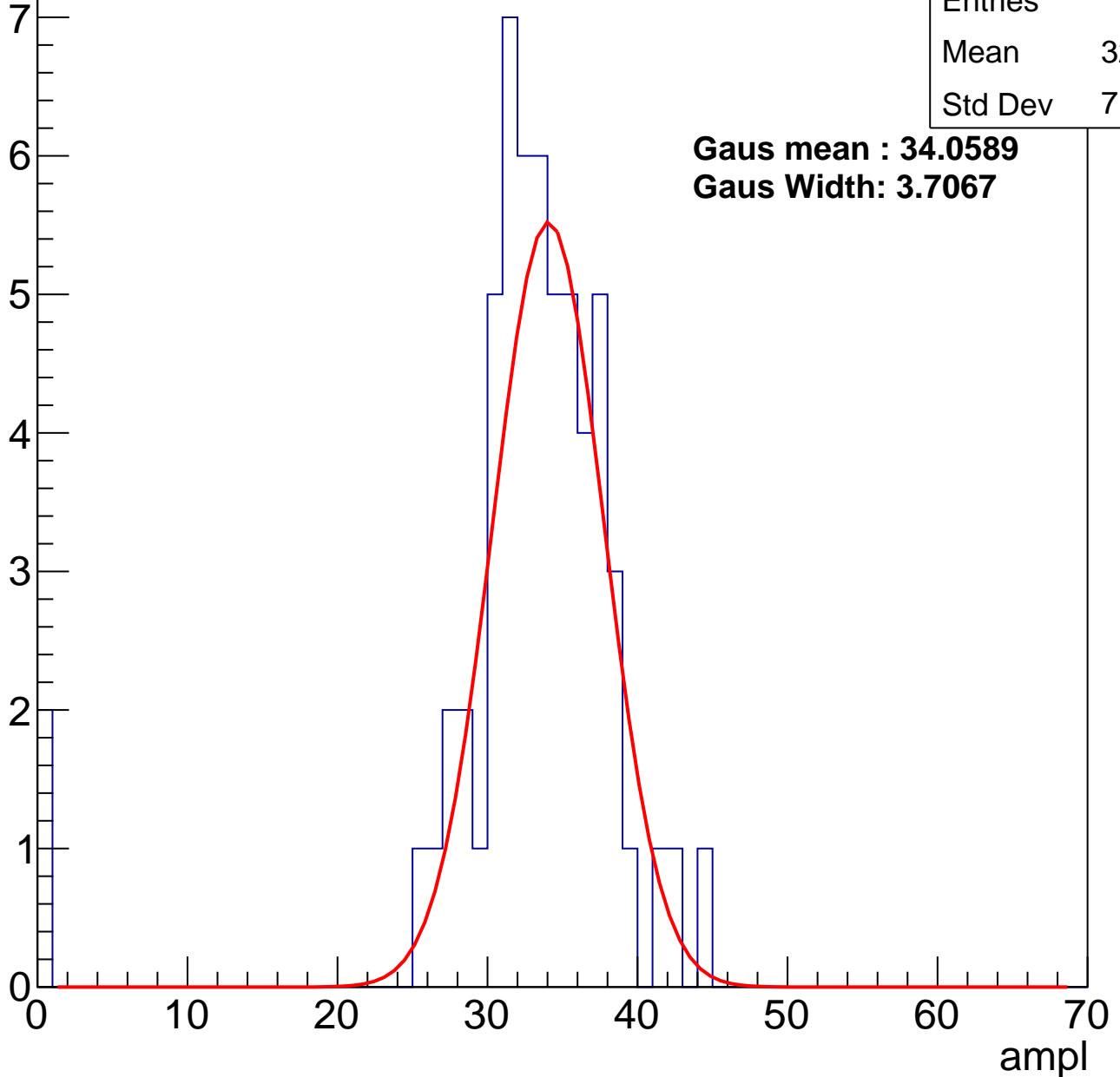
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	32.22
Std Dev	7.119

Gaus mean : 34.0589

Gaus Width: 3.7067



B1L103S, U1-ch104, adc2

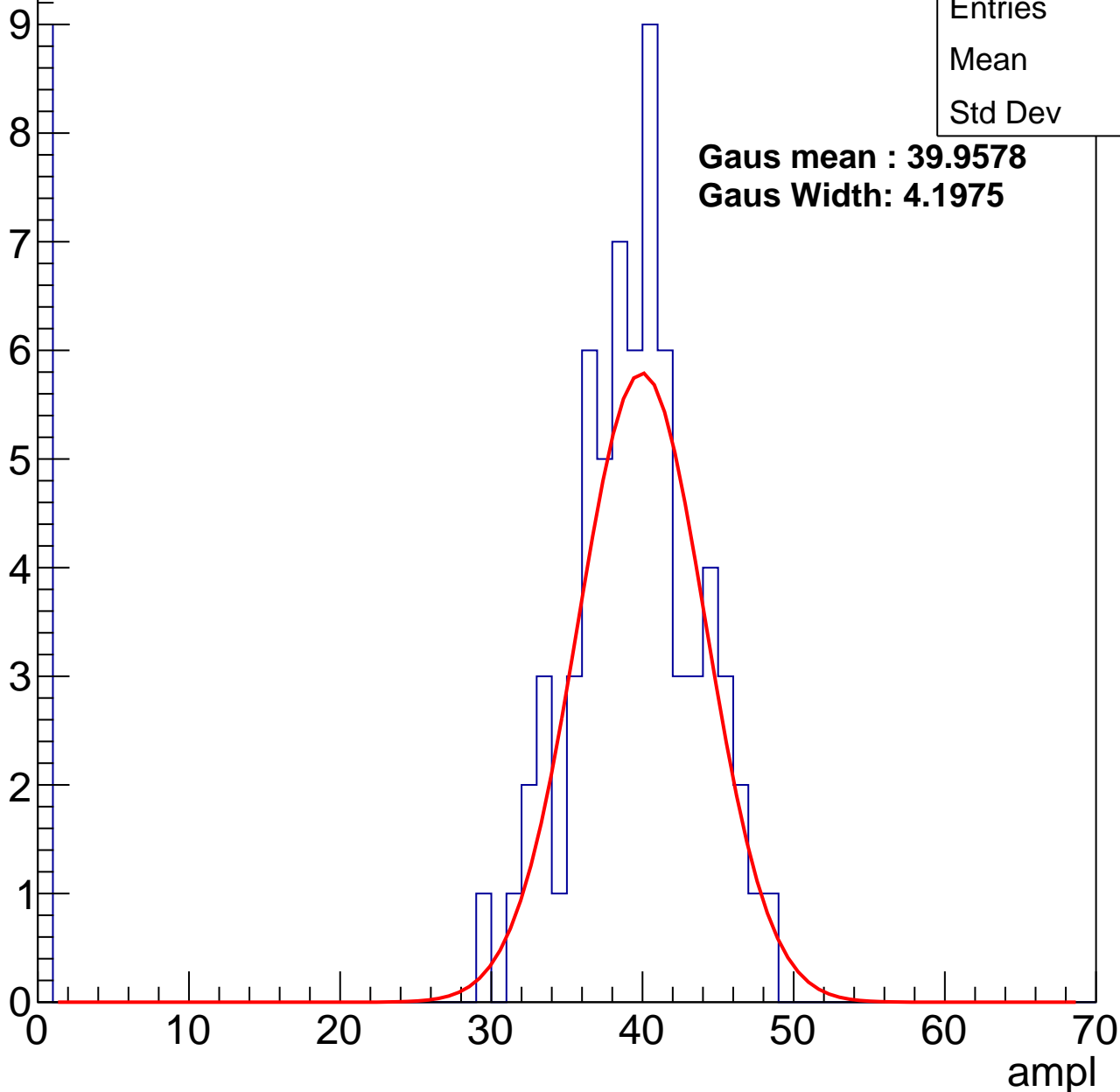
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.5
Std Dev	13.2

Gaus mean : 39.9578

Gaus Width: 4.1975

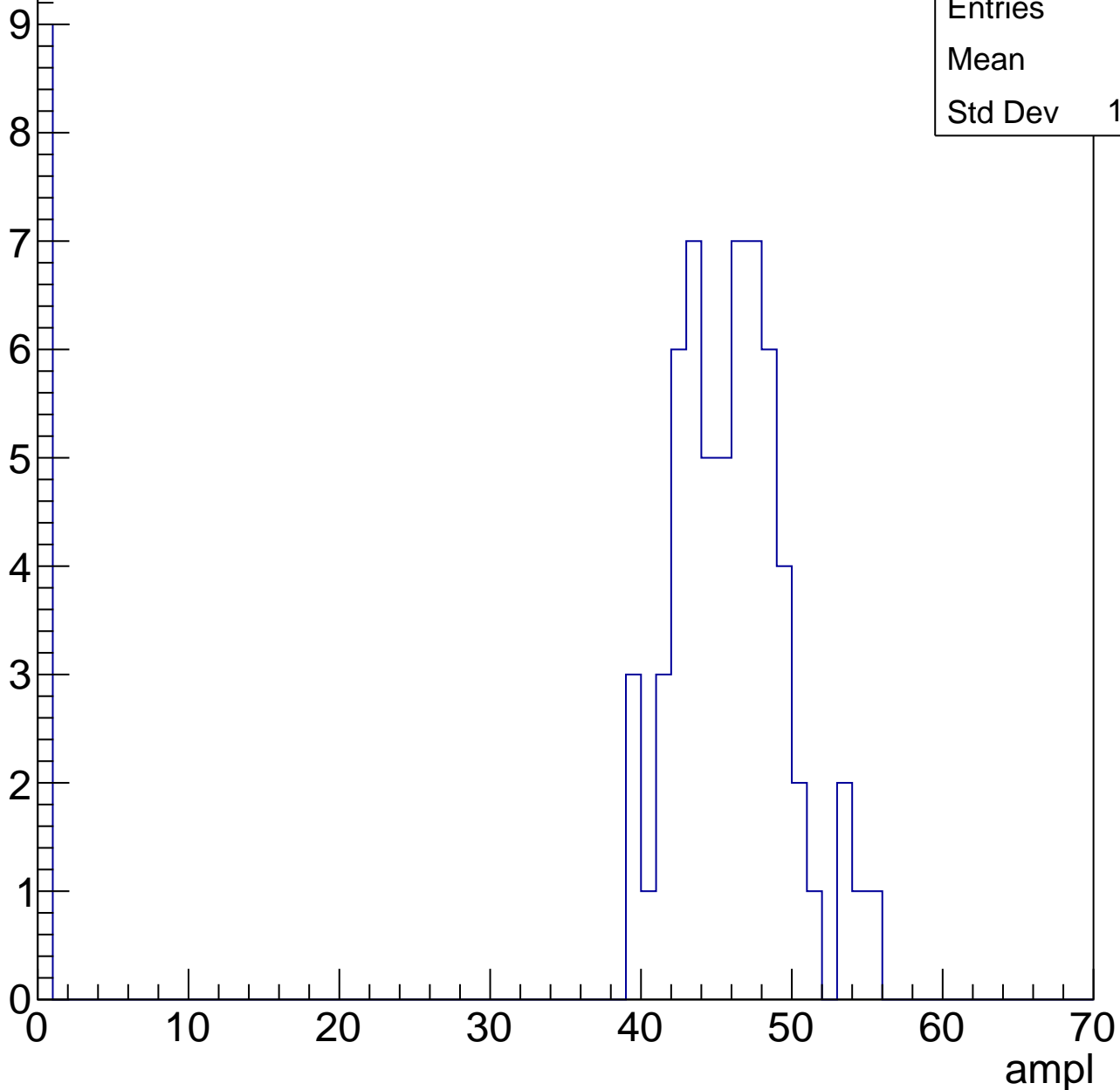


B1L103S, U1-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

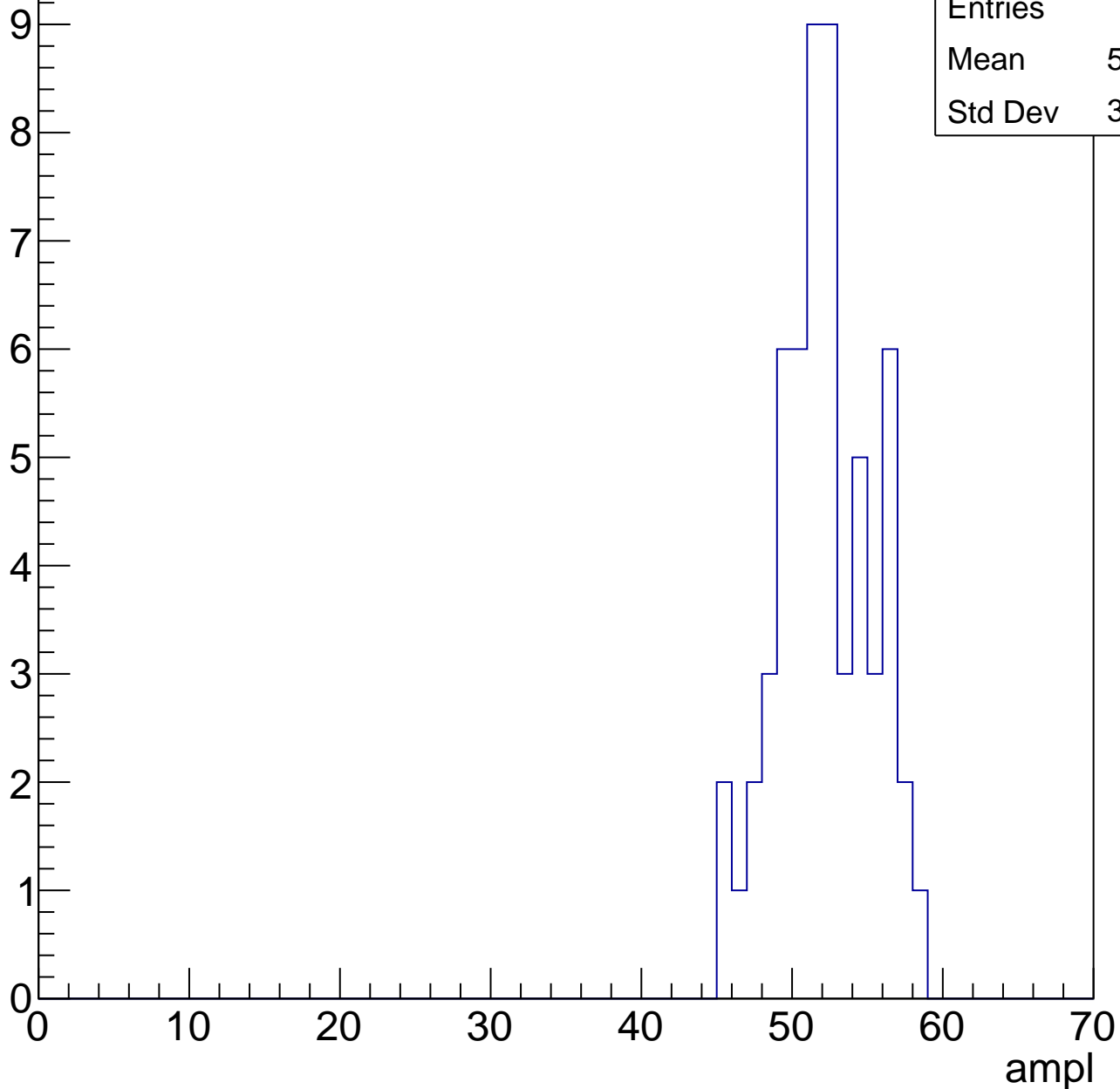
Entries	70
Mean	39.7
Std Dev	15.62



B1L103S, U1-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

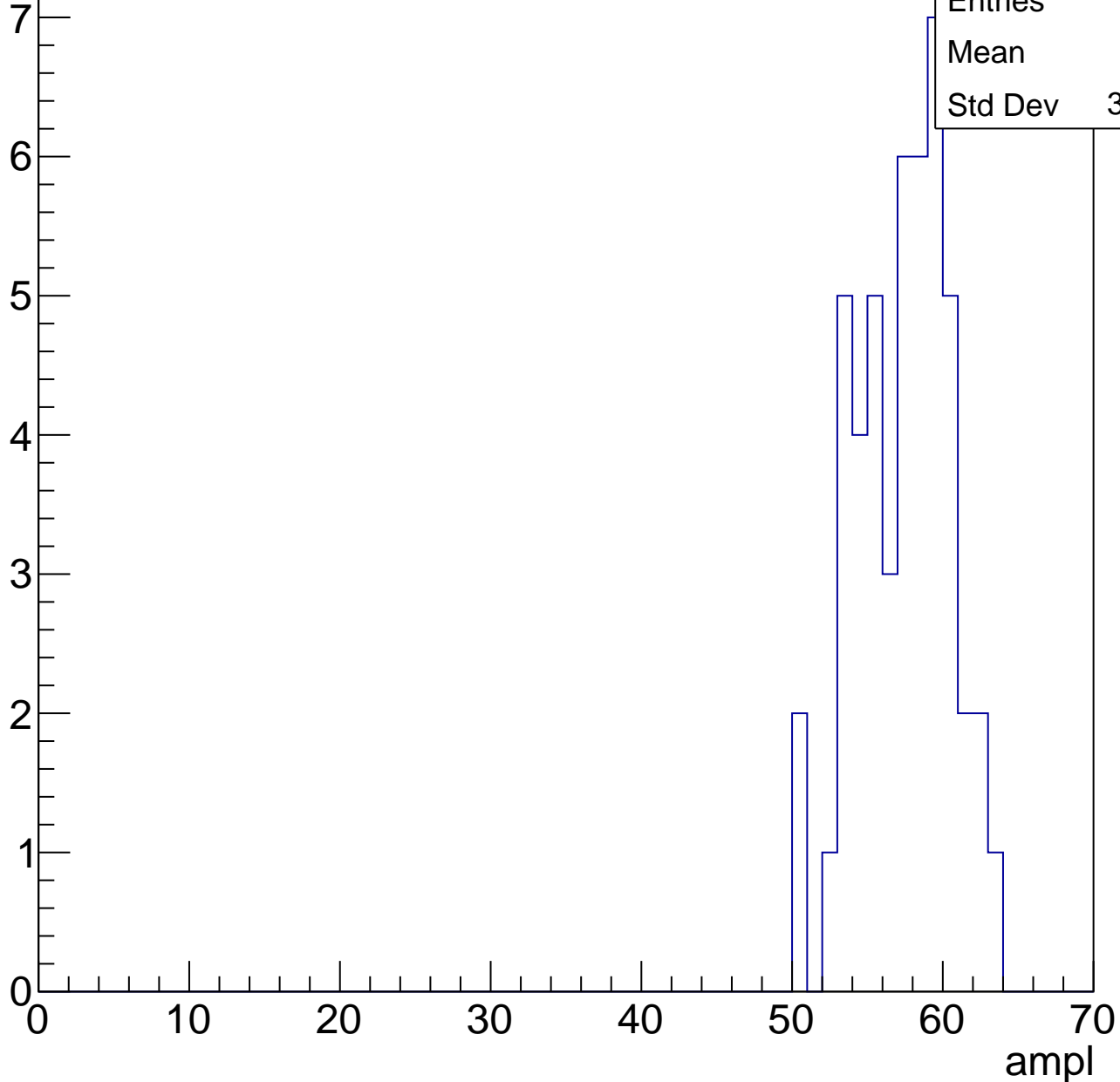


Entries	58
Mean	51.67
Std Dev	3.065

B1L103S, U1-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

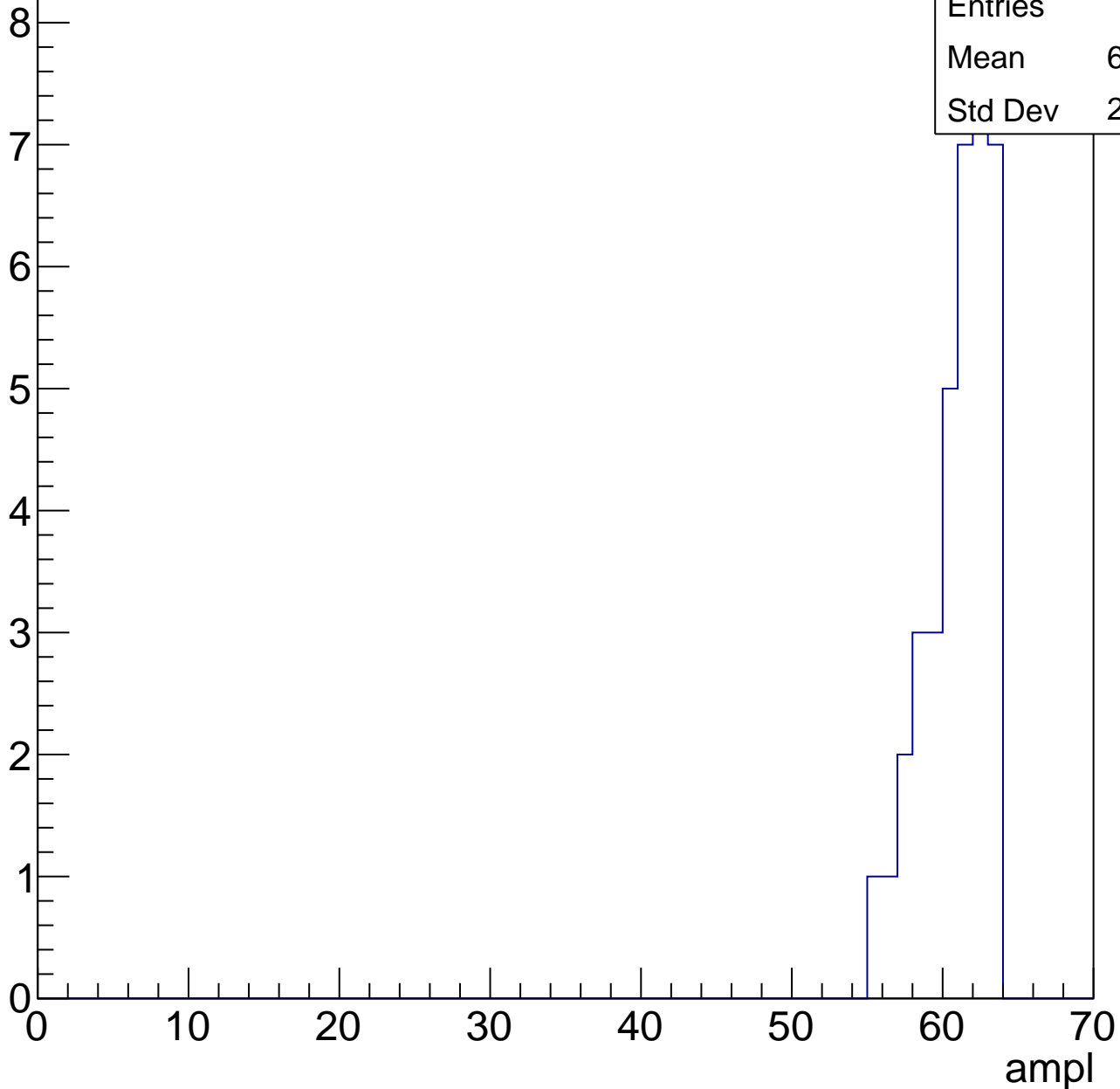


B1L103S, U1-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	60.54
Std Dev	2.113

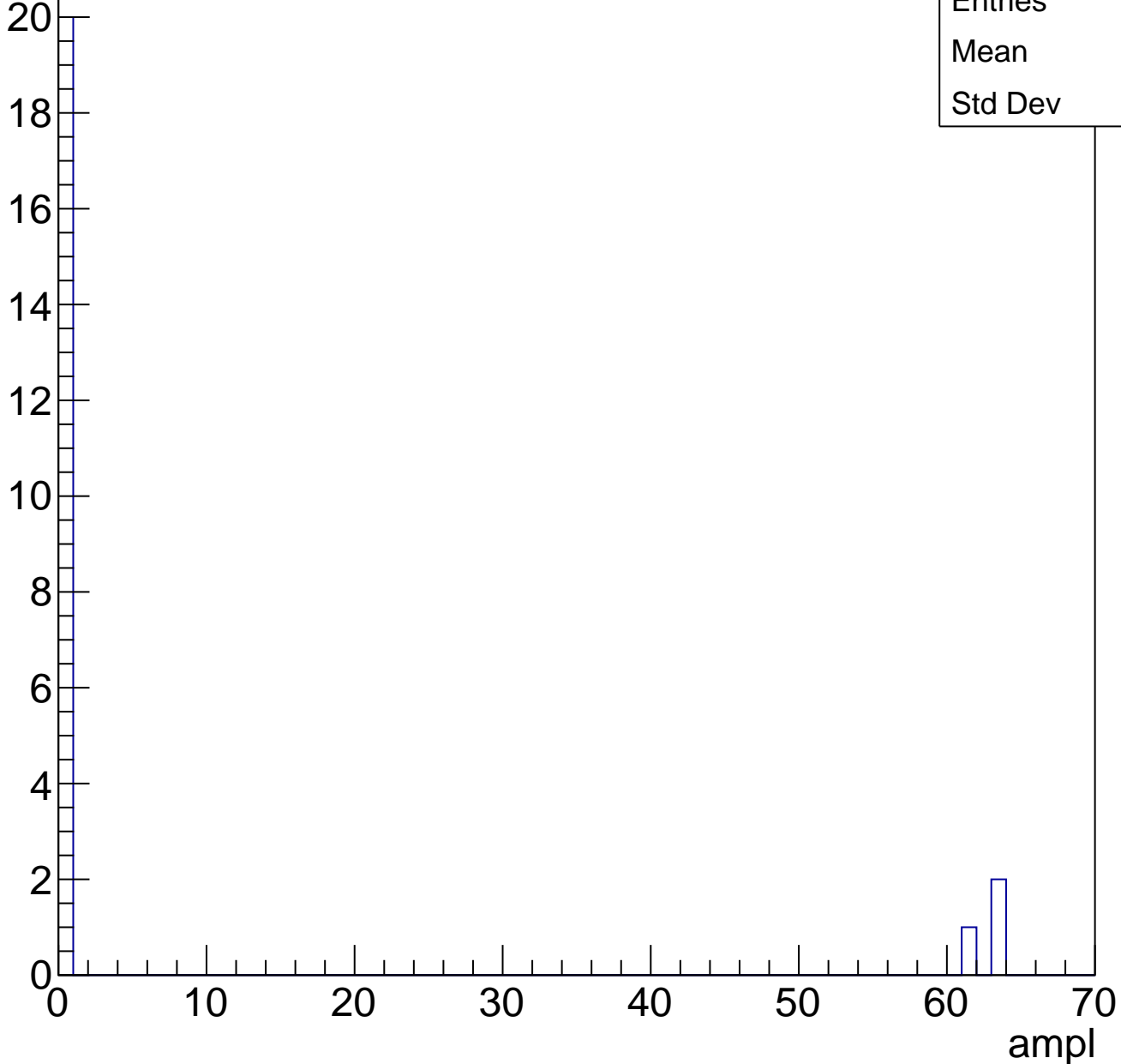


B1L103S, U1-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.13
Std Dev	21

Entry



B1L103S, U1-ch105, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	22.91
Std Dev	11.38

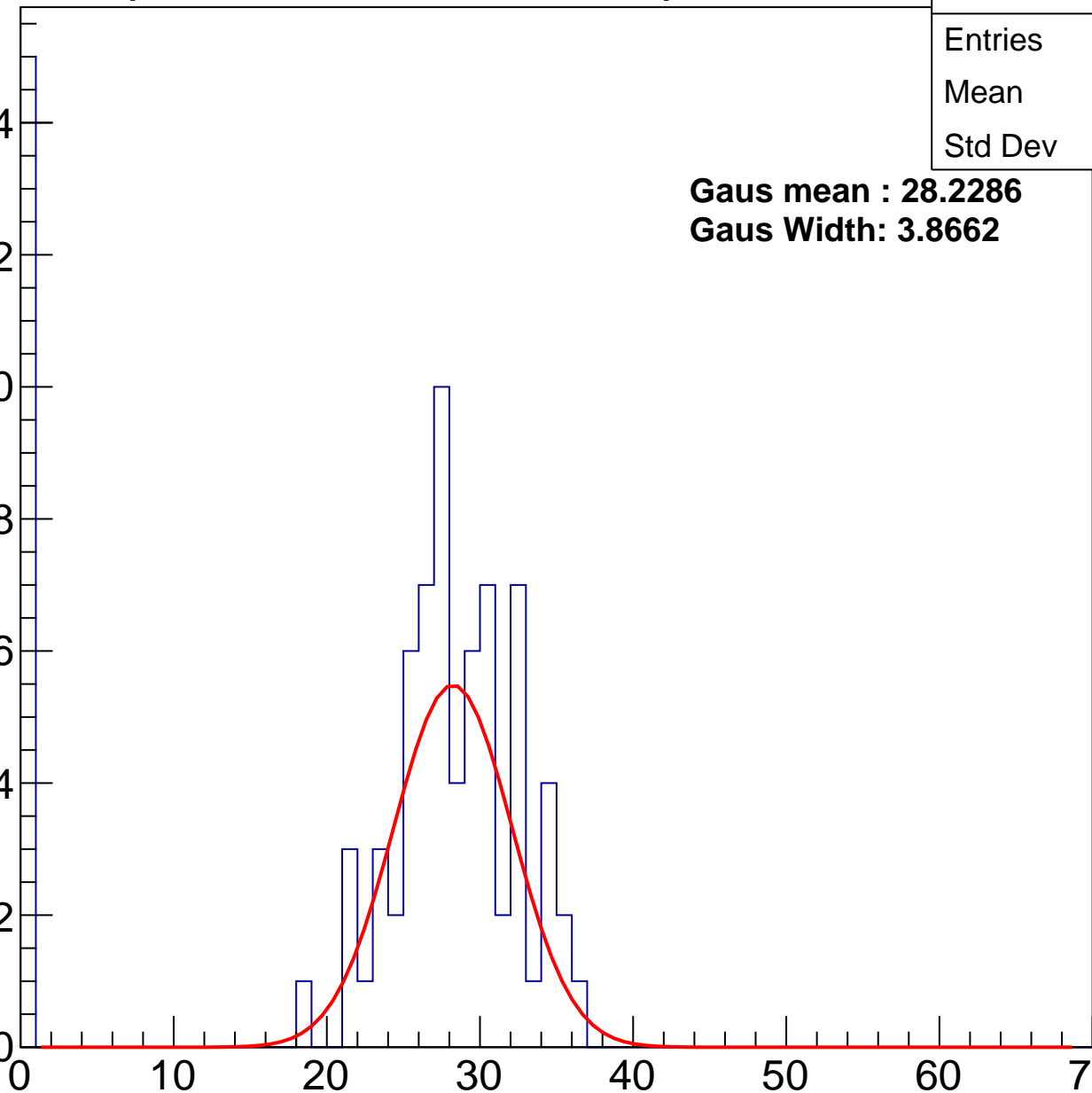
Gaus mean : 28.2286

Gaus Width: 3.8662

Entry

14
12
10
8
6
4
2
0

ampl



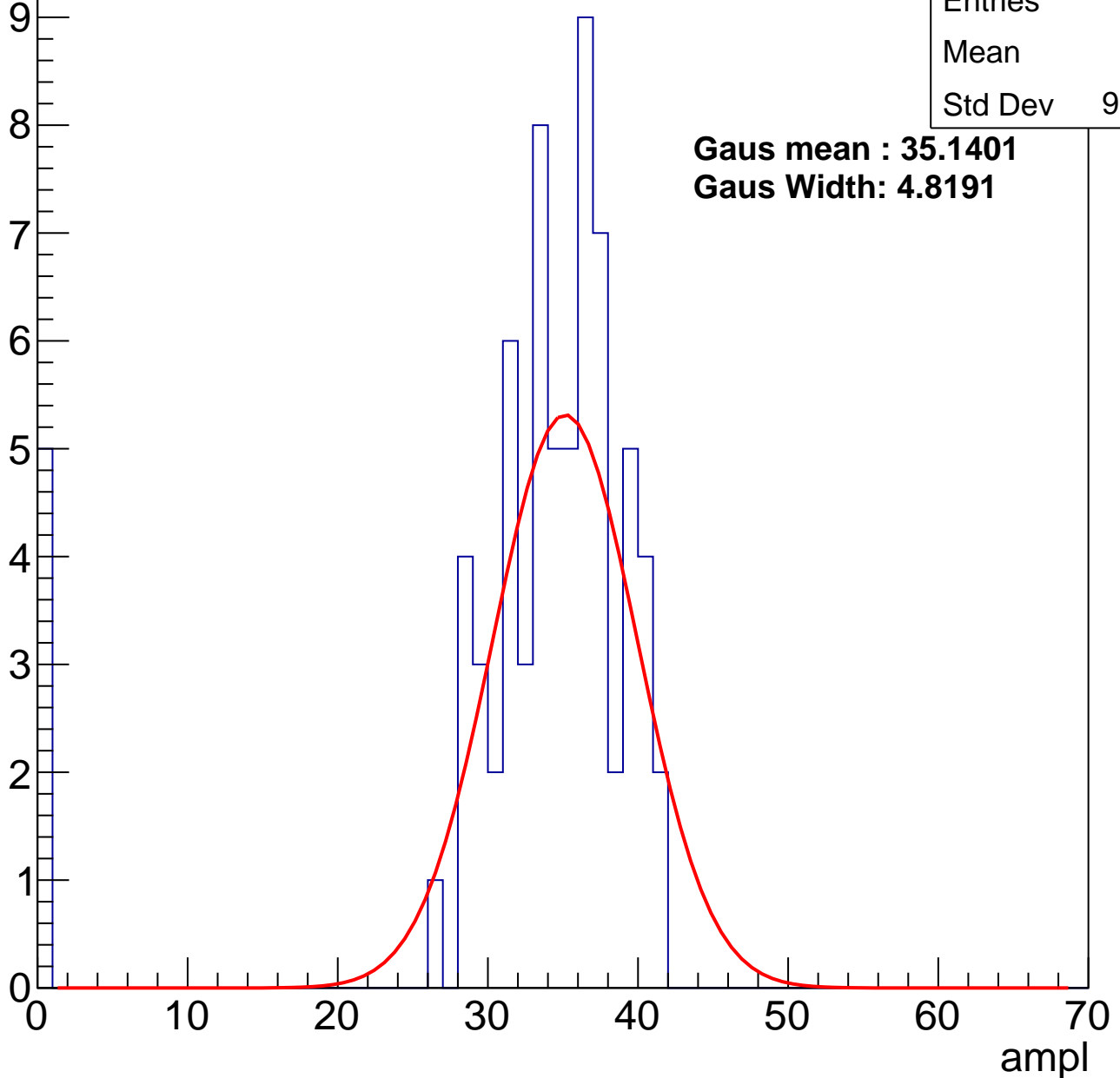
B1L103S, U1-ch105, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	32
Std Dev	9.485

Gaus mean : 35.1401
Gaus Width: 4.8191



B1L103S, U1-ch105, adc2

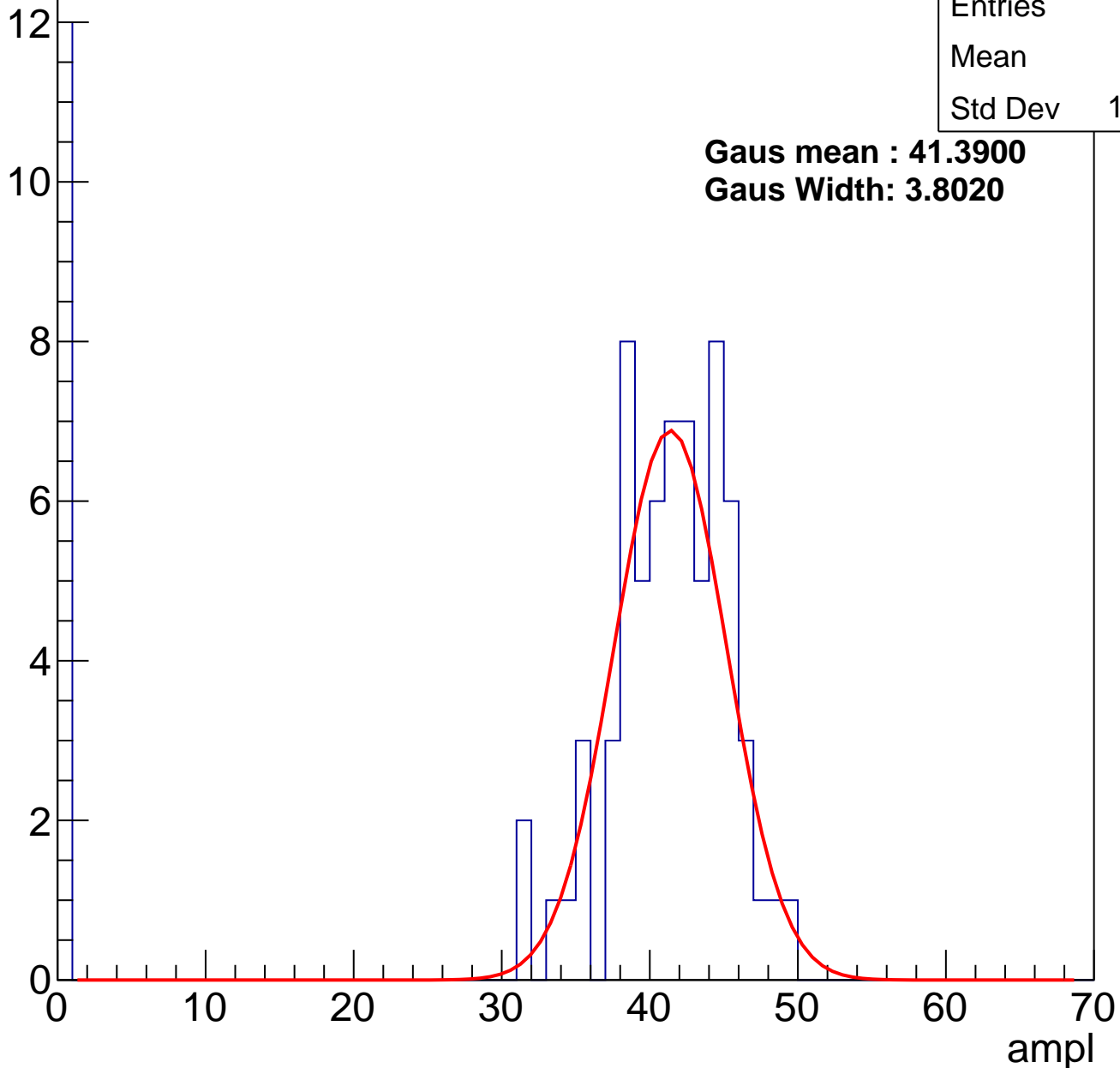
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	34.8
Std Dev	15.03

Gaus mean : 41.3900

Gaus Width: 3.8020

Entry

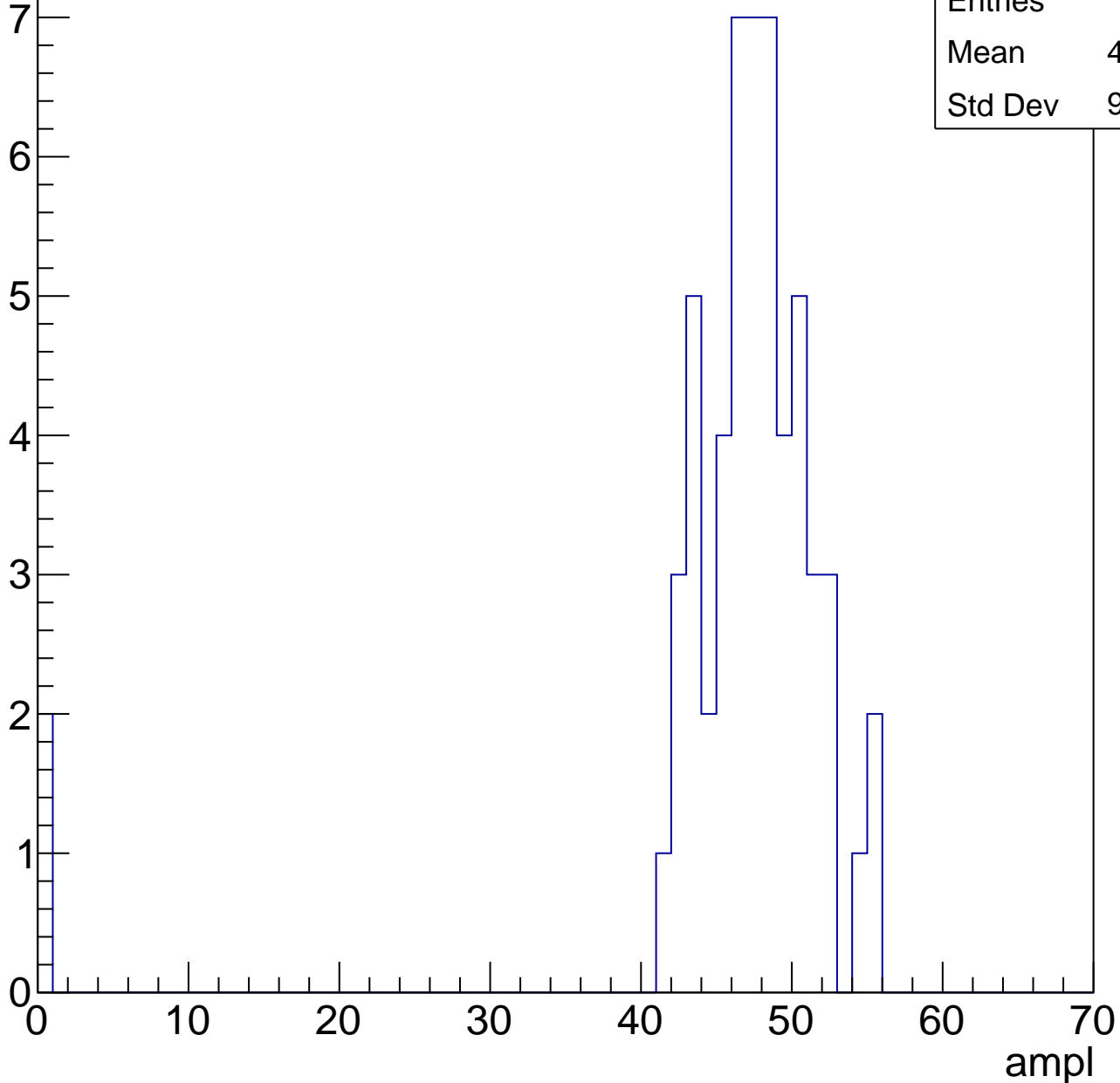


B1L103S, U1-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	45.64
Std Dev	9.368

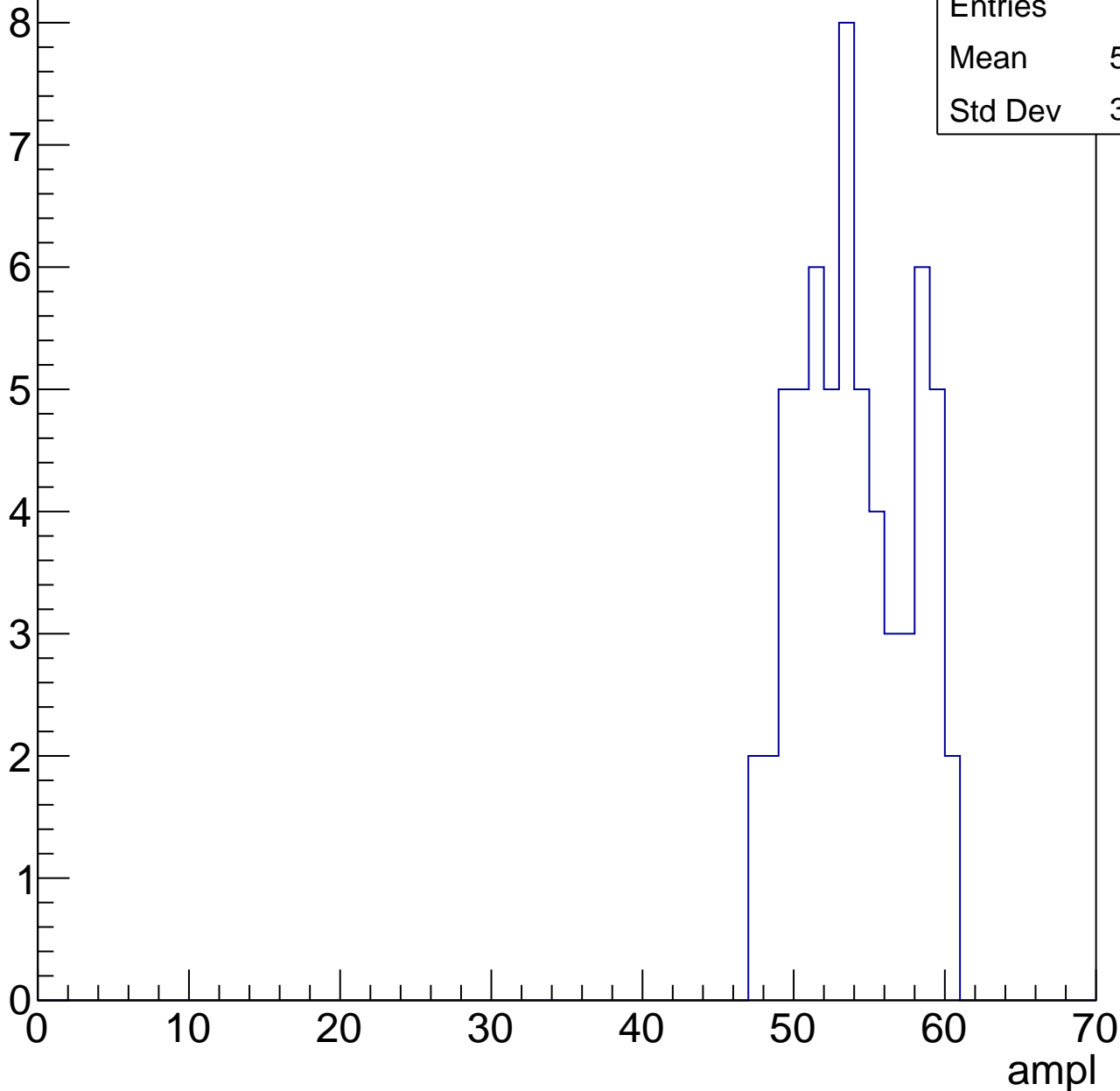


B1L103S, U1-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

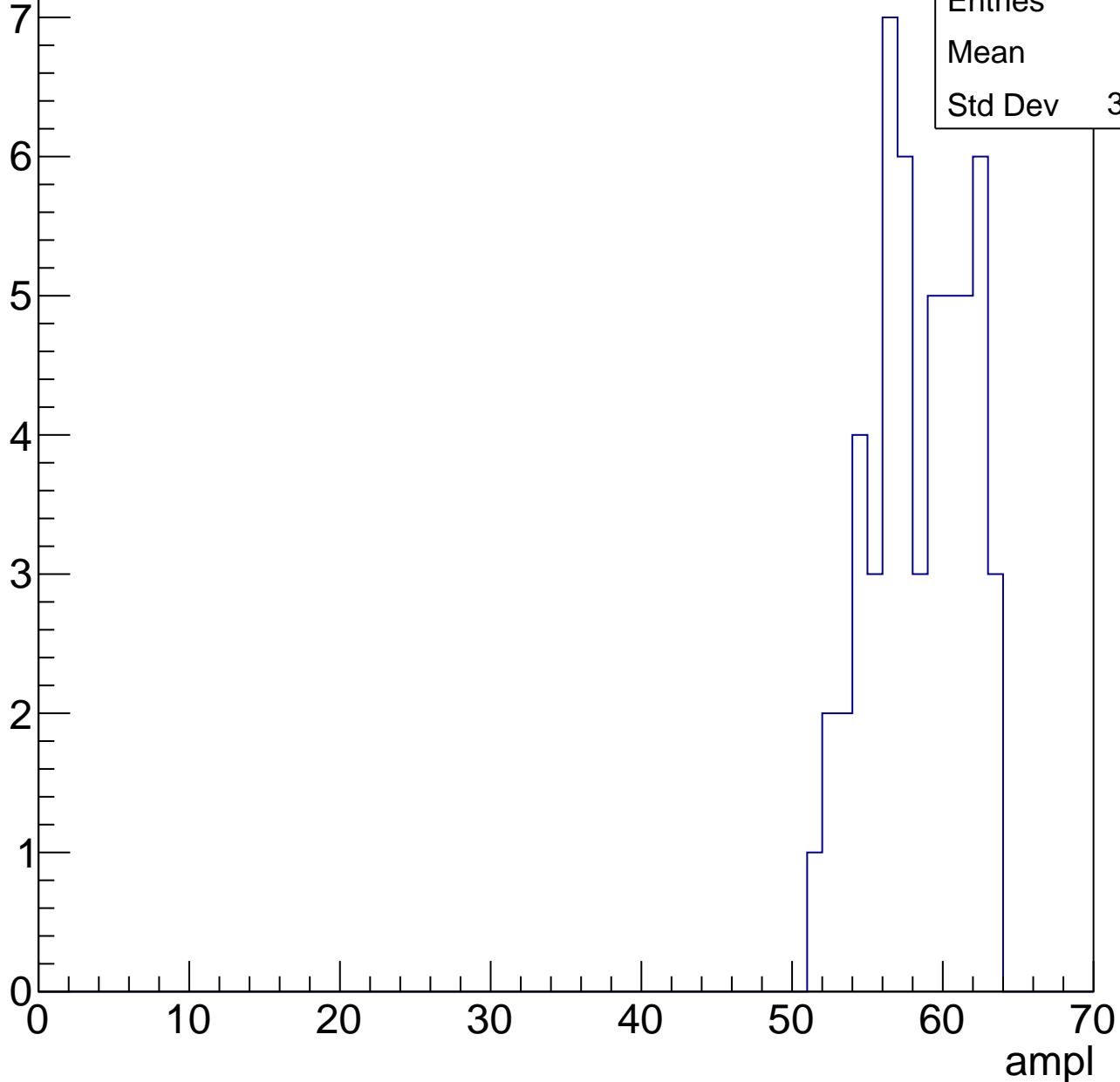
Entries	61
Mean	53.56
Std Dev	3.578



B1L103S, U1-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



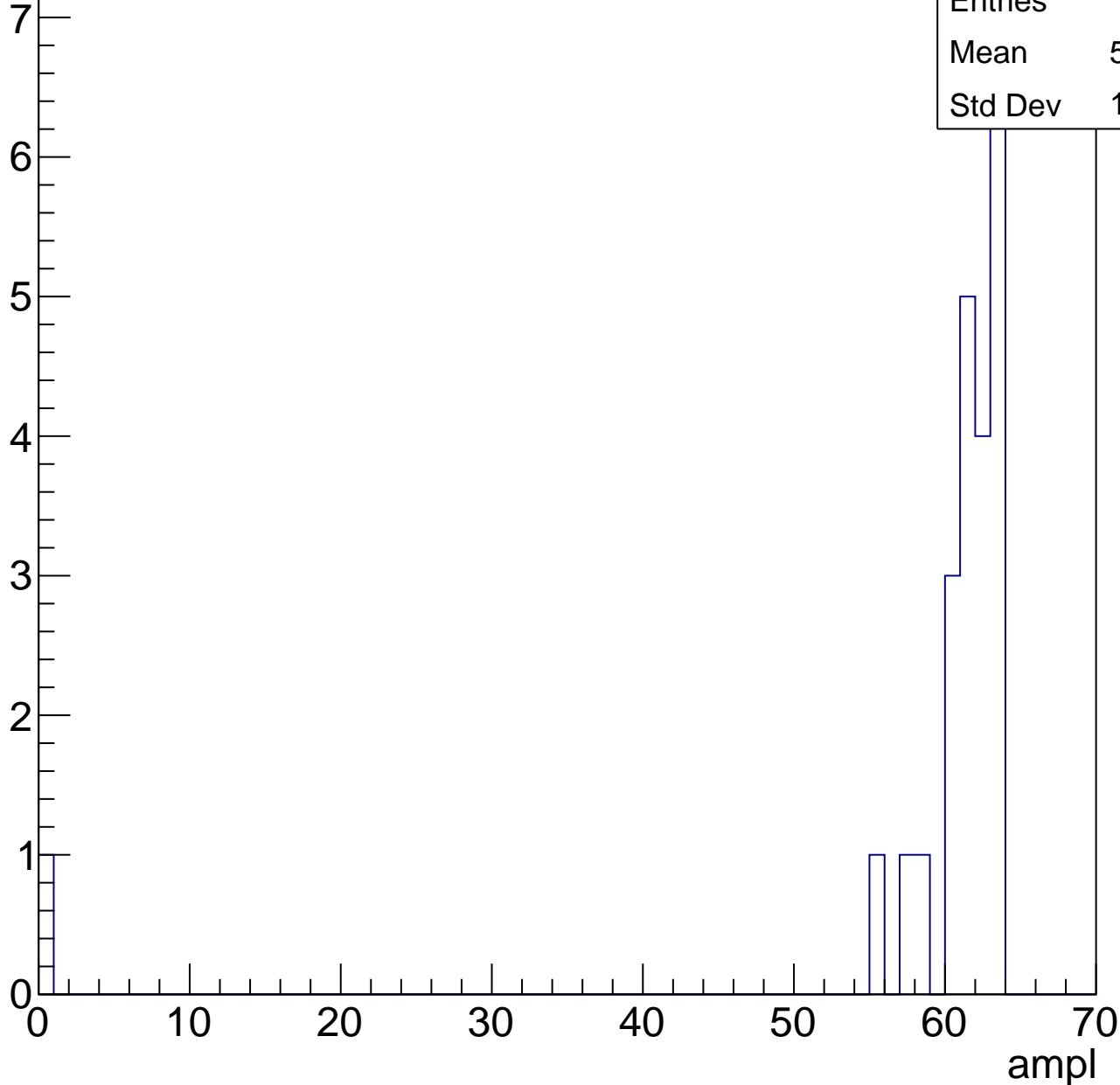
Entries	52
Mean	57.9
Std Dev	3.212

B1L103S, U1-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.43
Std Dev	12.62



B1L103S, U1-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U1-ch106, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	19.06
Std Dev	12.43

Gaus mean : 26.1338

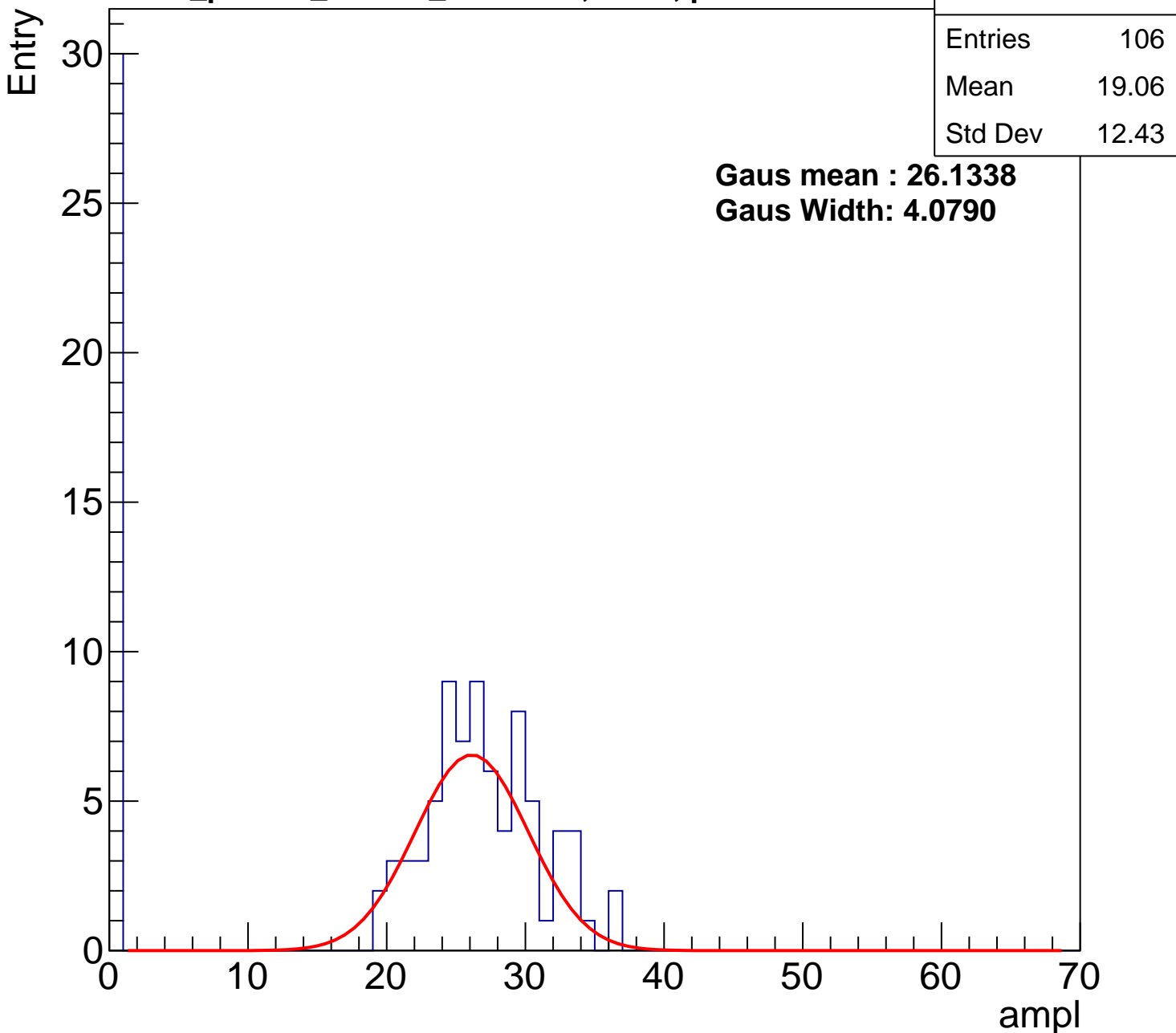
Gaus Width: 4.0790

Entry

30
25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch106, adc1

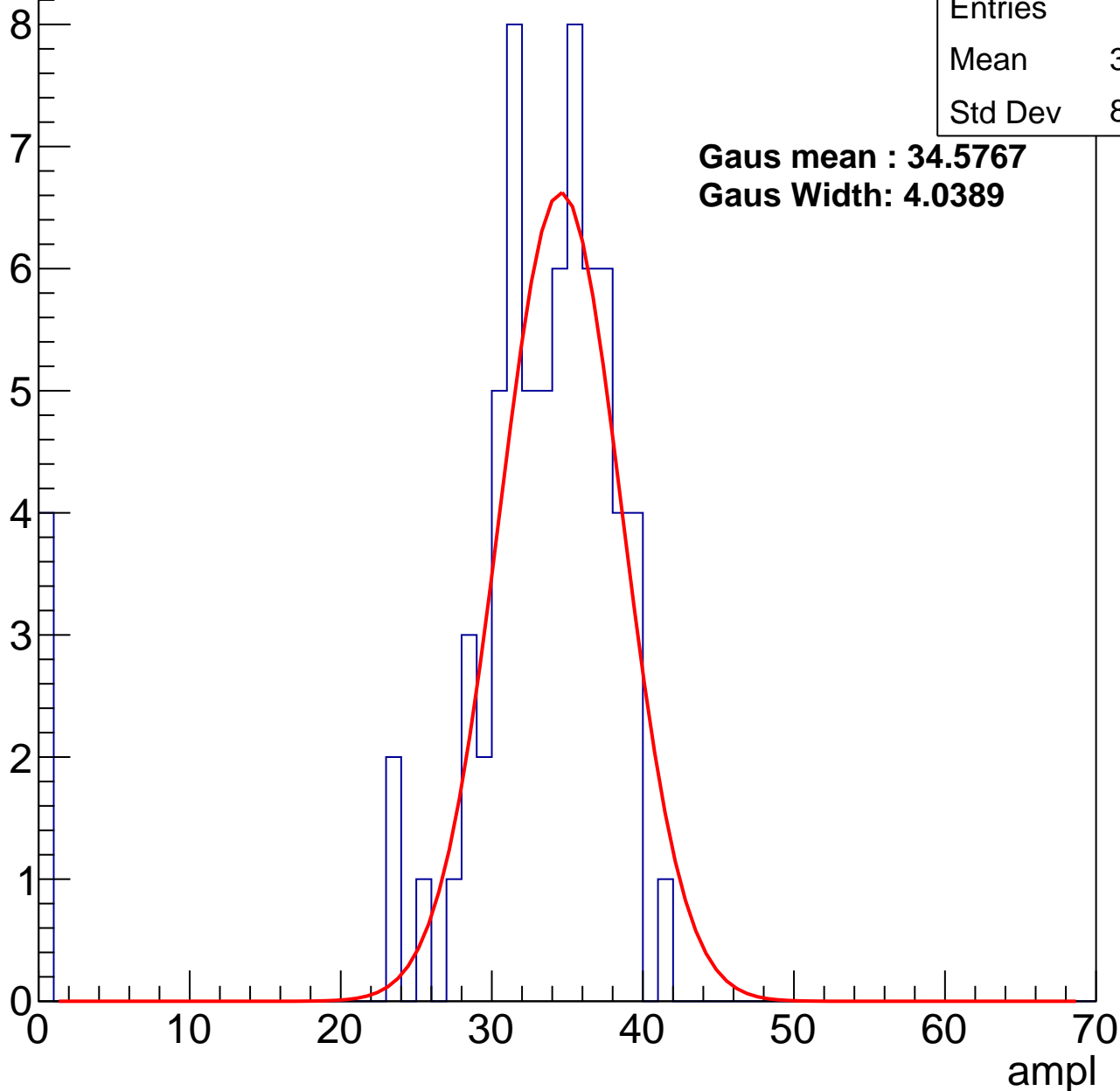
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.46
Std Dev	8.536

Gaus mean : 34.5767

Gaus Width: 4.0389



B1L103S, U1-ch106, adc2

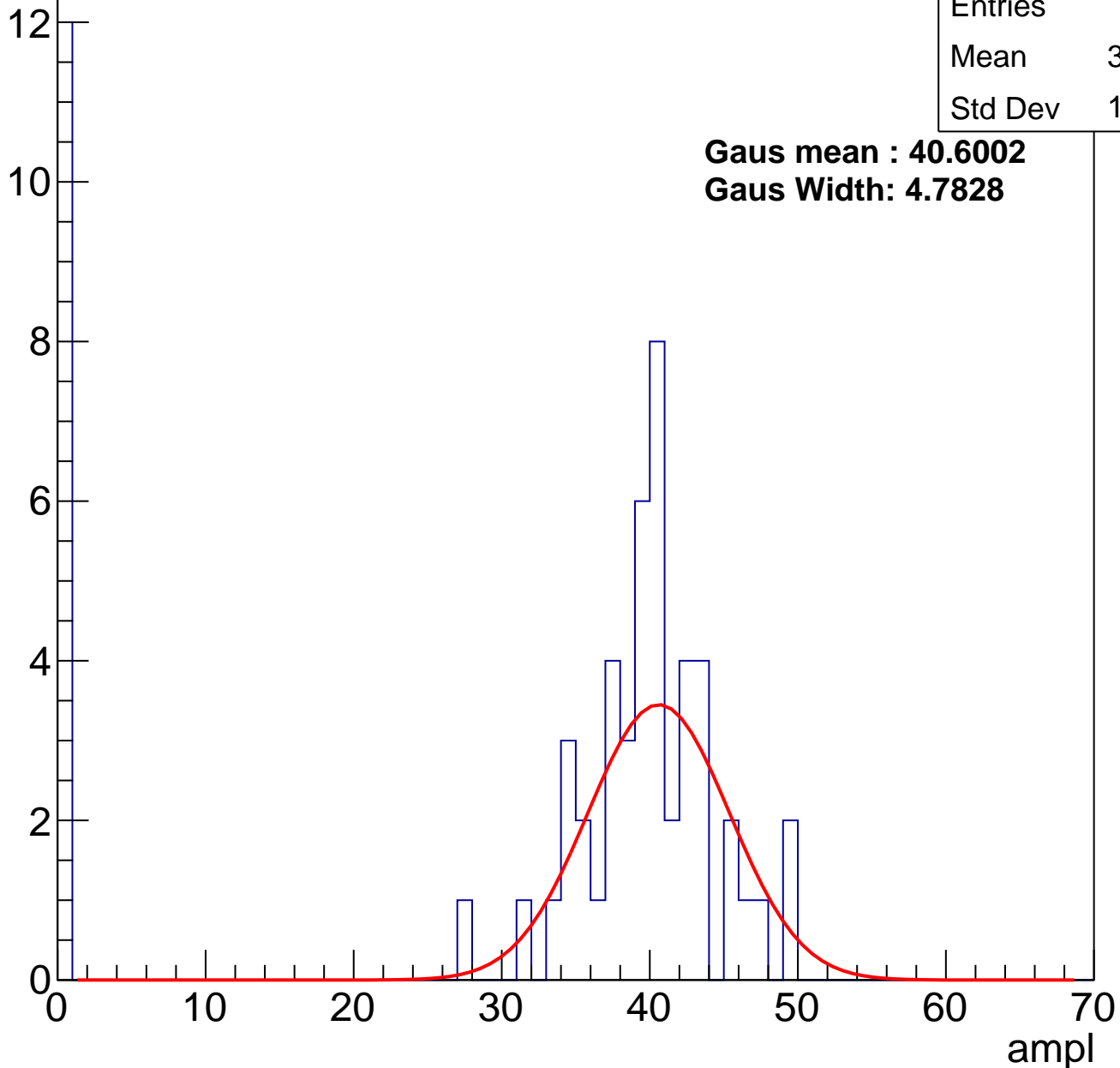
calib_packv5_041523_1651.root, FC#0, port C2

Entries	58
Mean	31.34
Std Dev	16.47

Gaus mean : 40.6002

Gaus Width: 4.7828

Entry

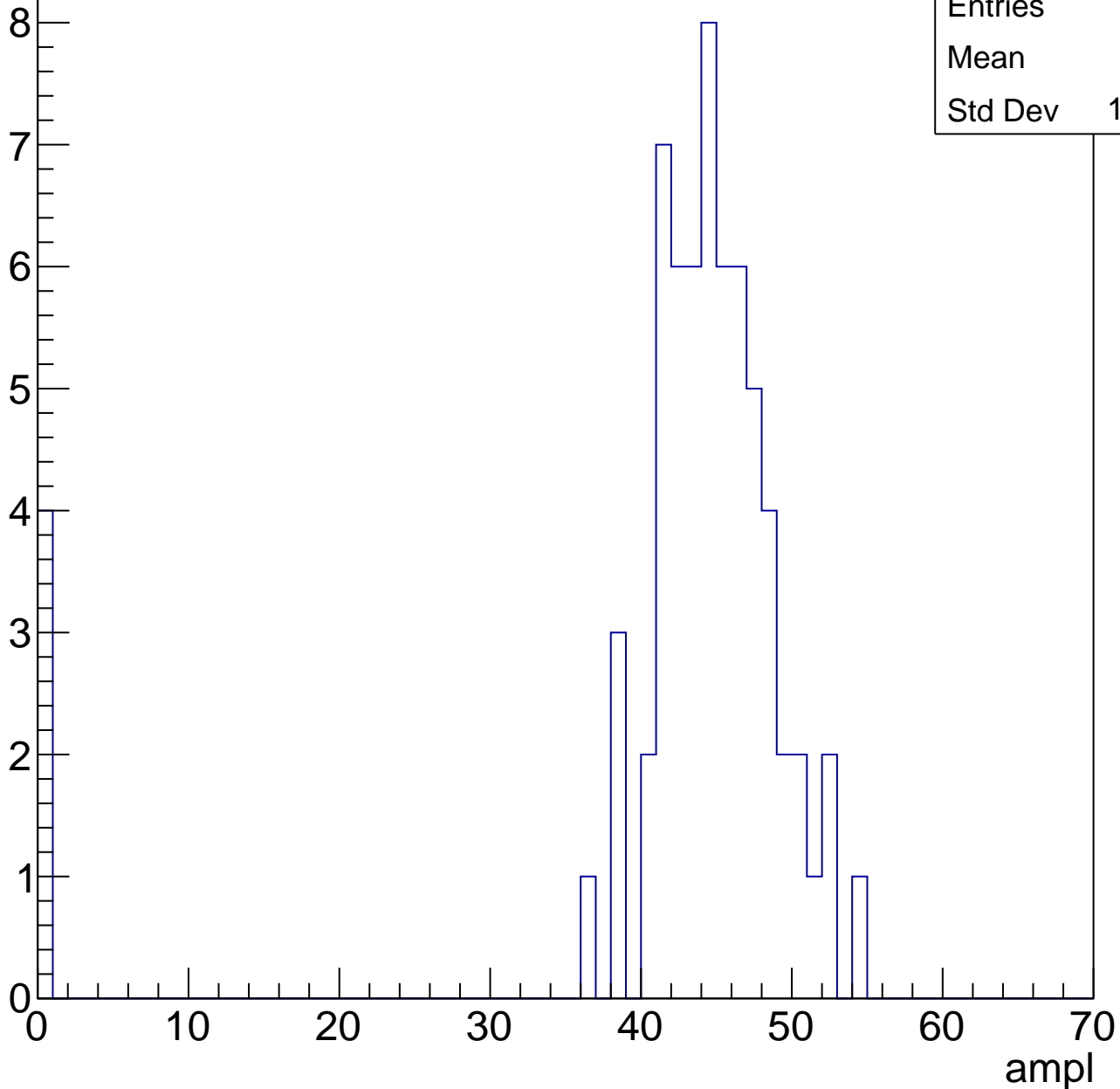


B1L103S, U1-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.8
Std Dev	11.19

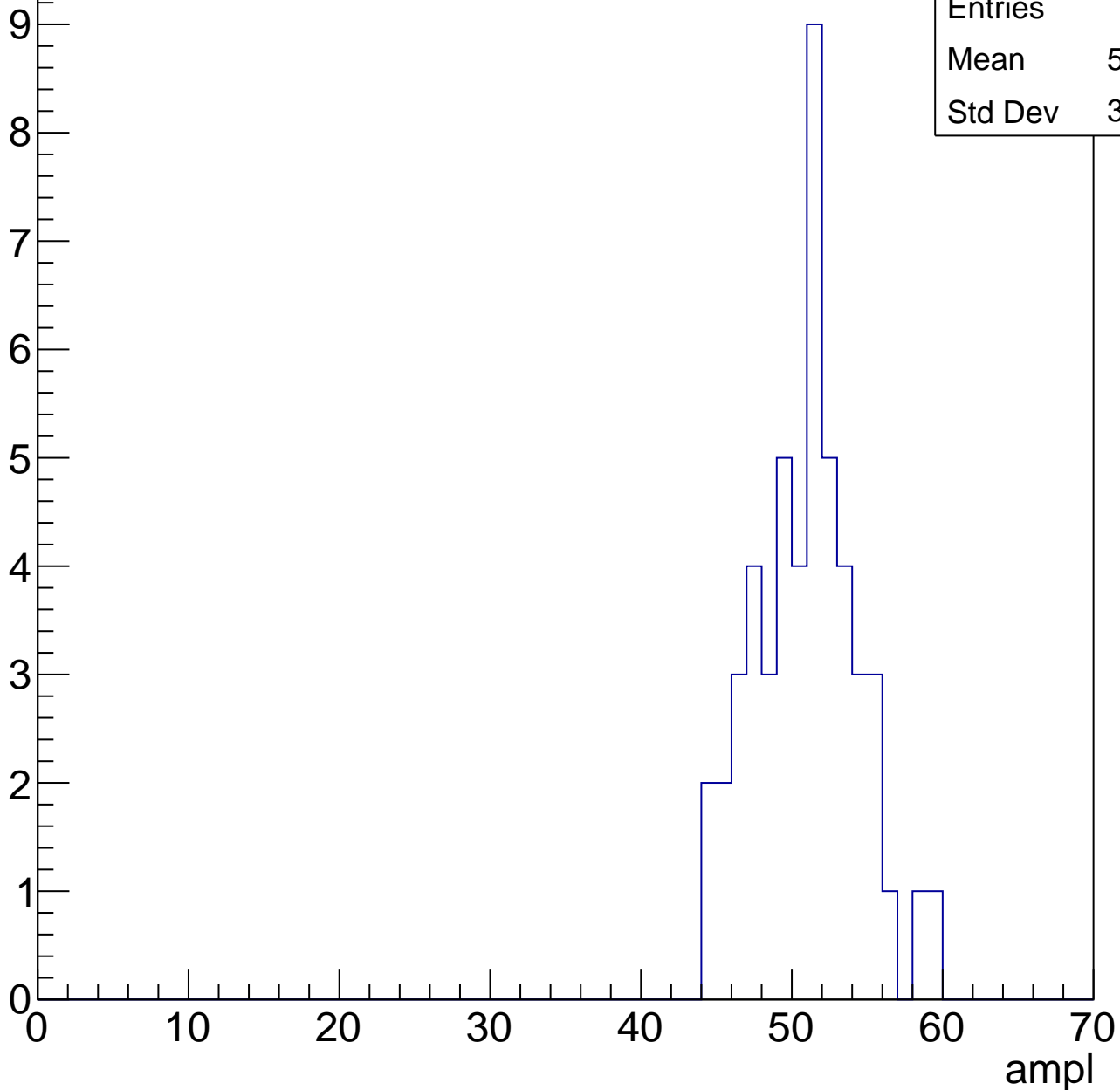


B1L103S, U1-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	50.48
Std Dev	3.407

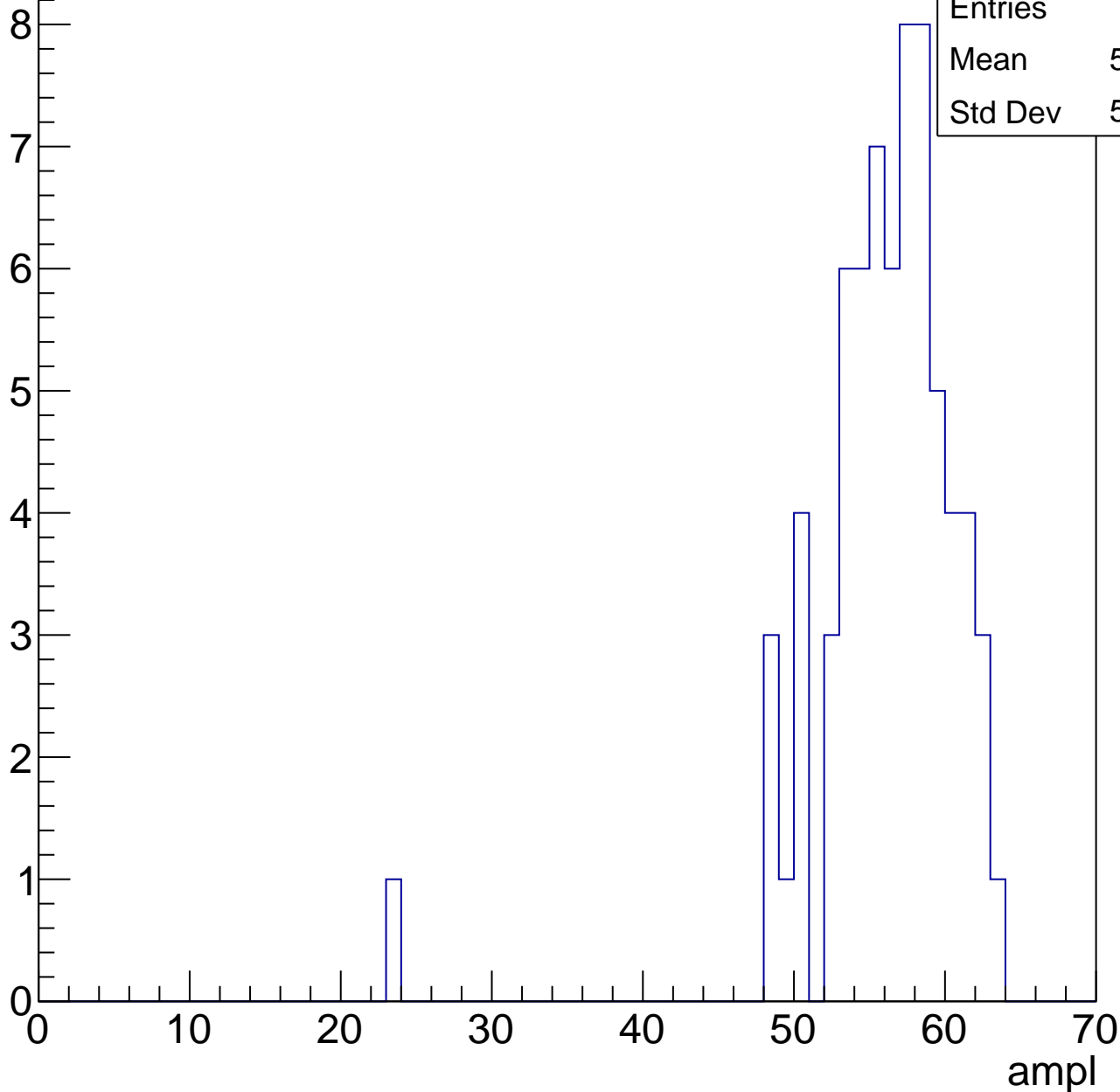


B1L103S, U1-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	55.47
Std Dev	5.336

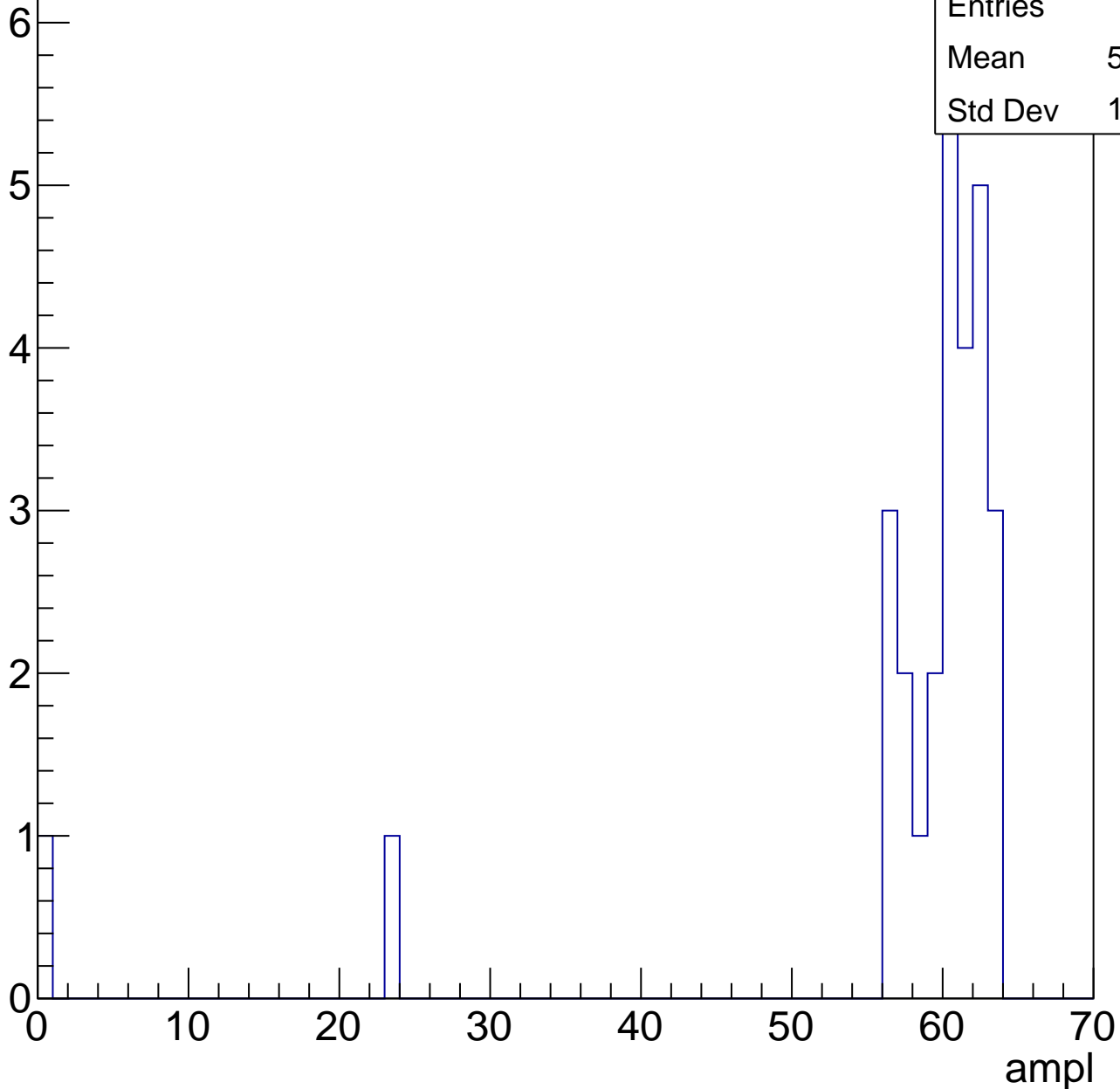


B1L103S, U1-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	56.57
Std Dev	13.04

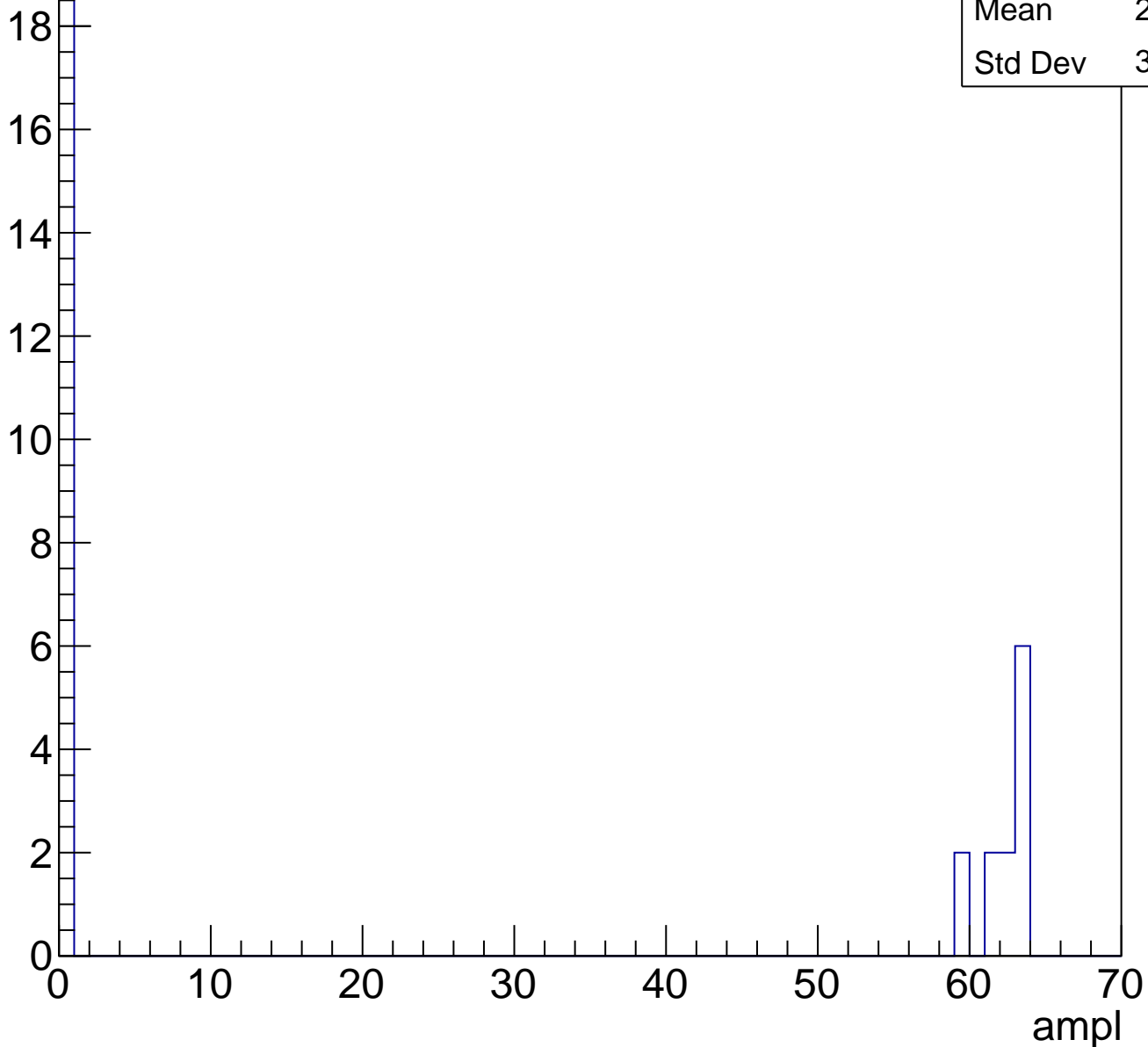


B1L103S, U1-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	31
Mean	23.94
Std Dev	30.13

Entry



B1L103S, U1-ch107, adc0

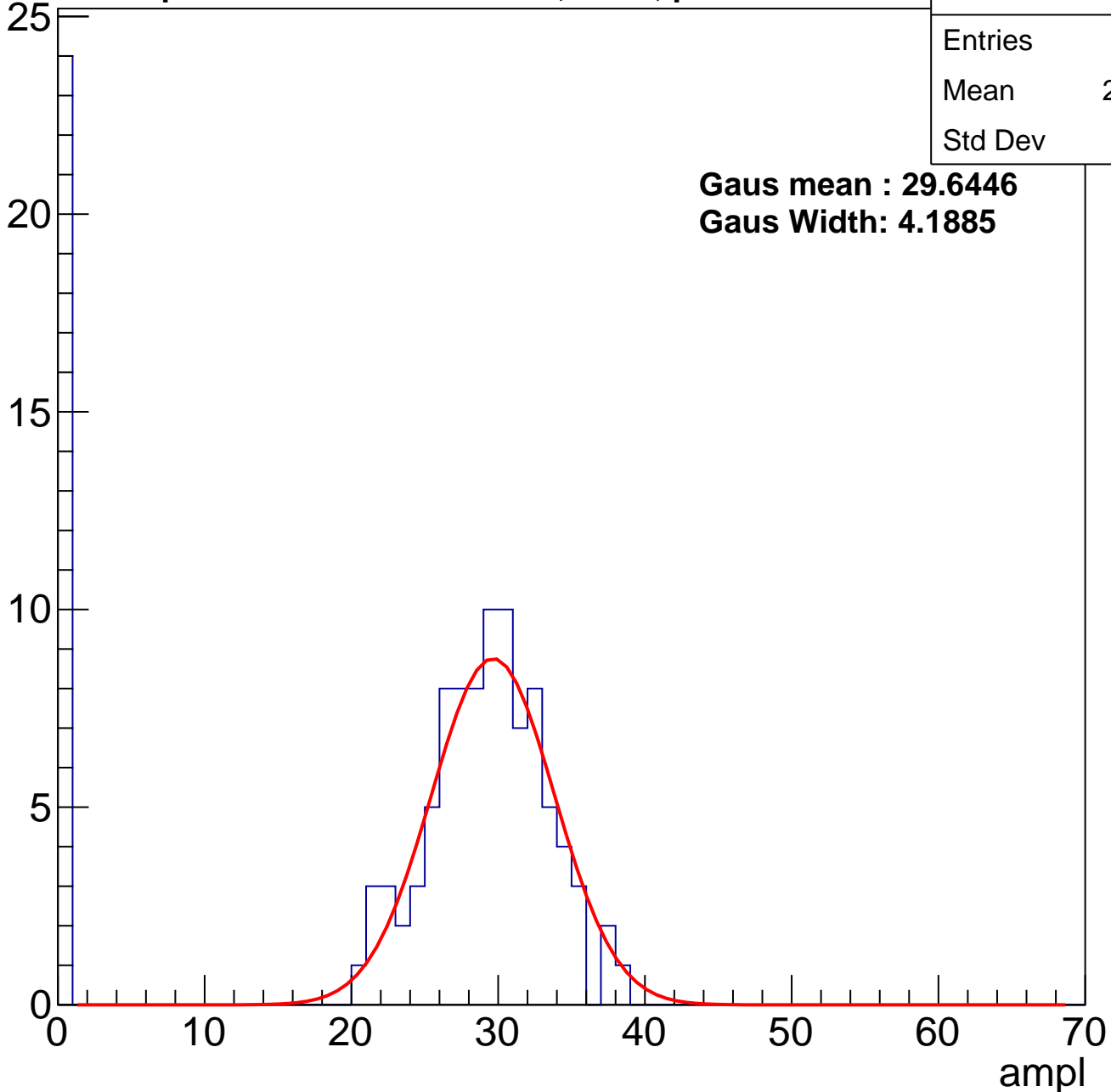
calib_packv5_041523_1651.root, FC#0, port C2

Entries	115
Mean	22.79
Std Dev	12.2

Gaus mean : 29.6446

Gaus Width: 4.1885

Entry



B1L103S, U1-ch107, adc1

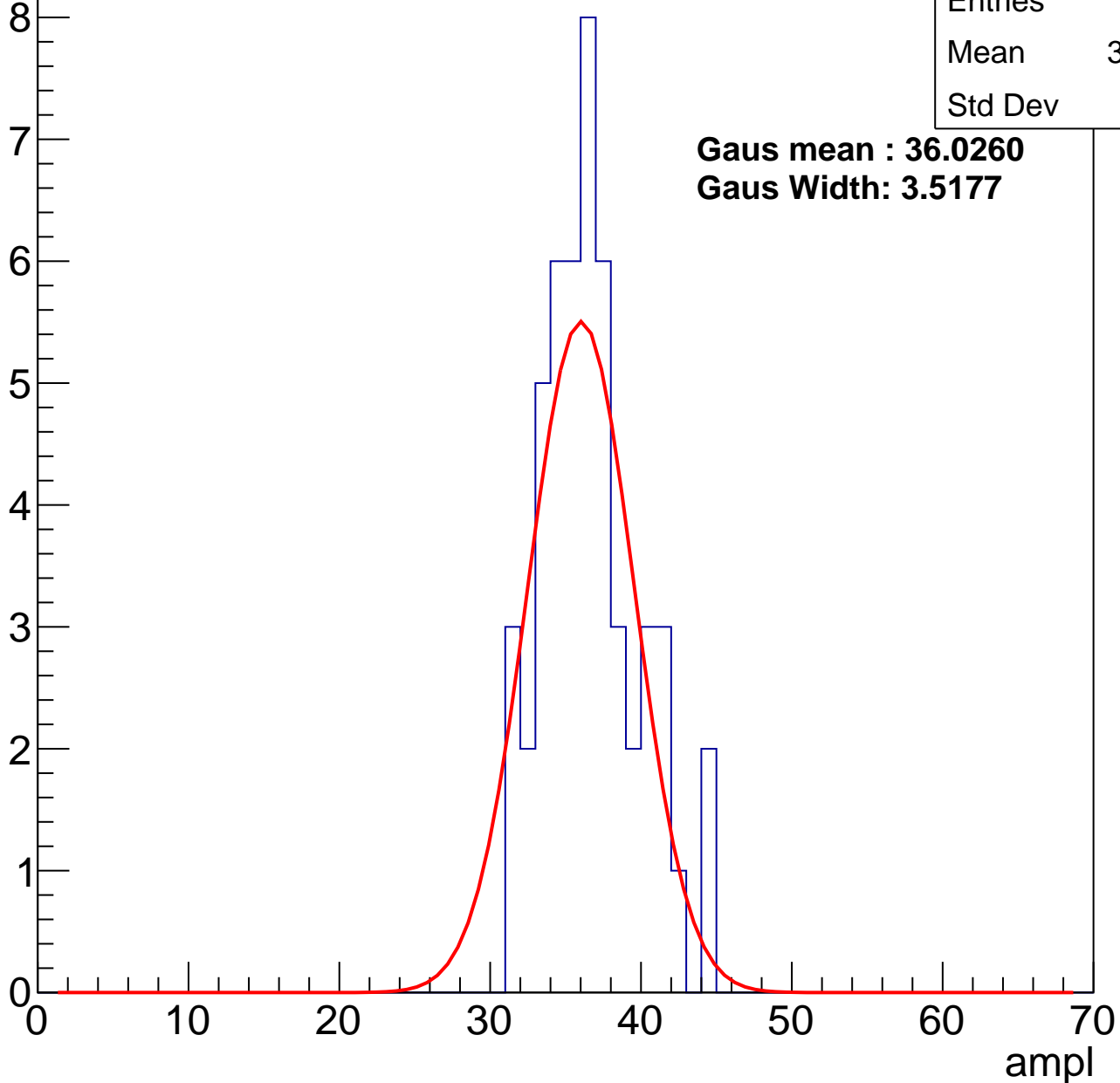
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	36.22
Std Dev	3.17

Gaus mean : 36.0260

Gaus Width: 3.5177



B1L103S, U1-ch107, adc2

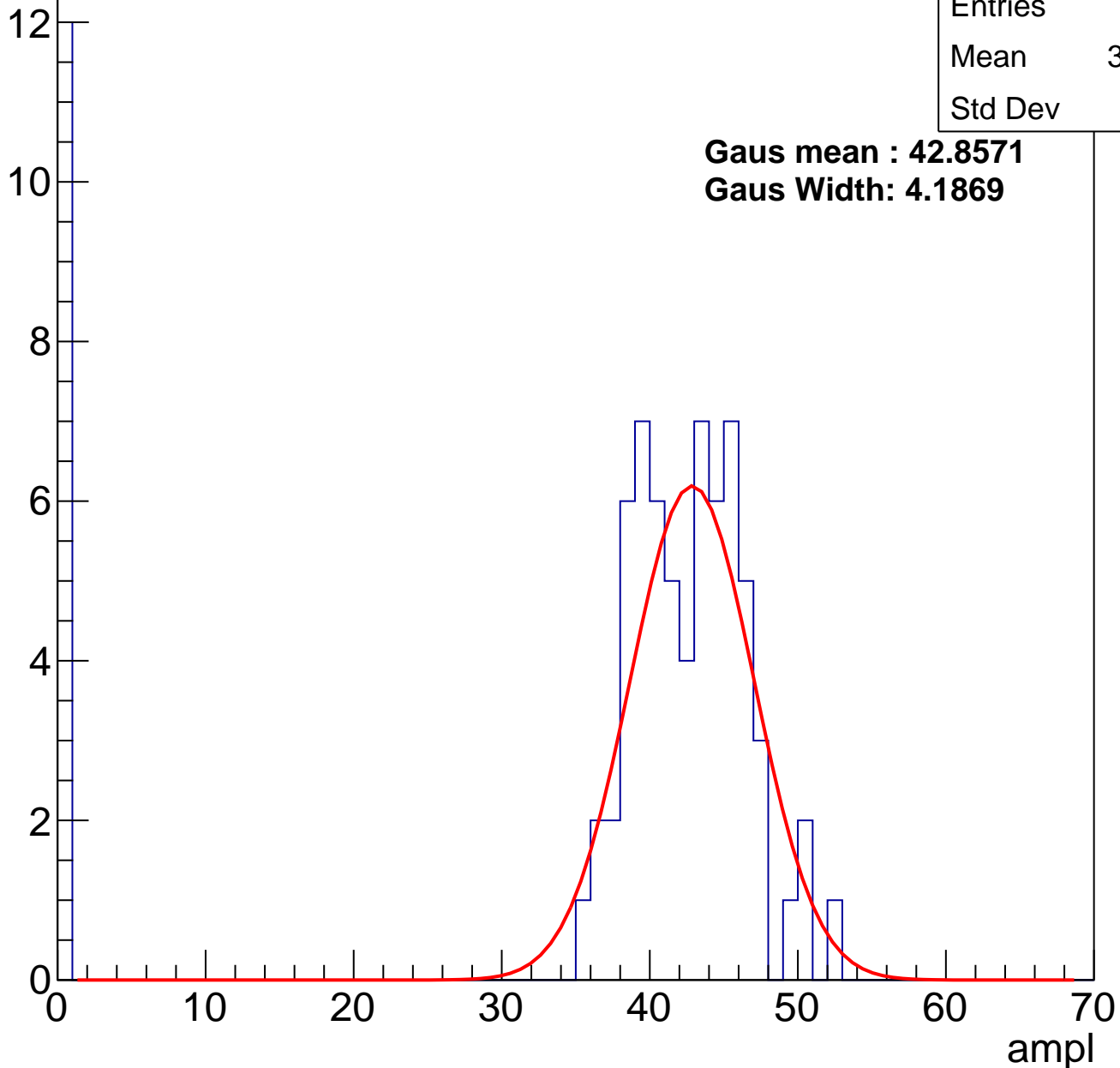
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	35.68
Std Dev	15.7

Gaus mean : 42.8571

Gaus Width: 4.1869

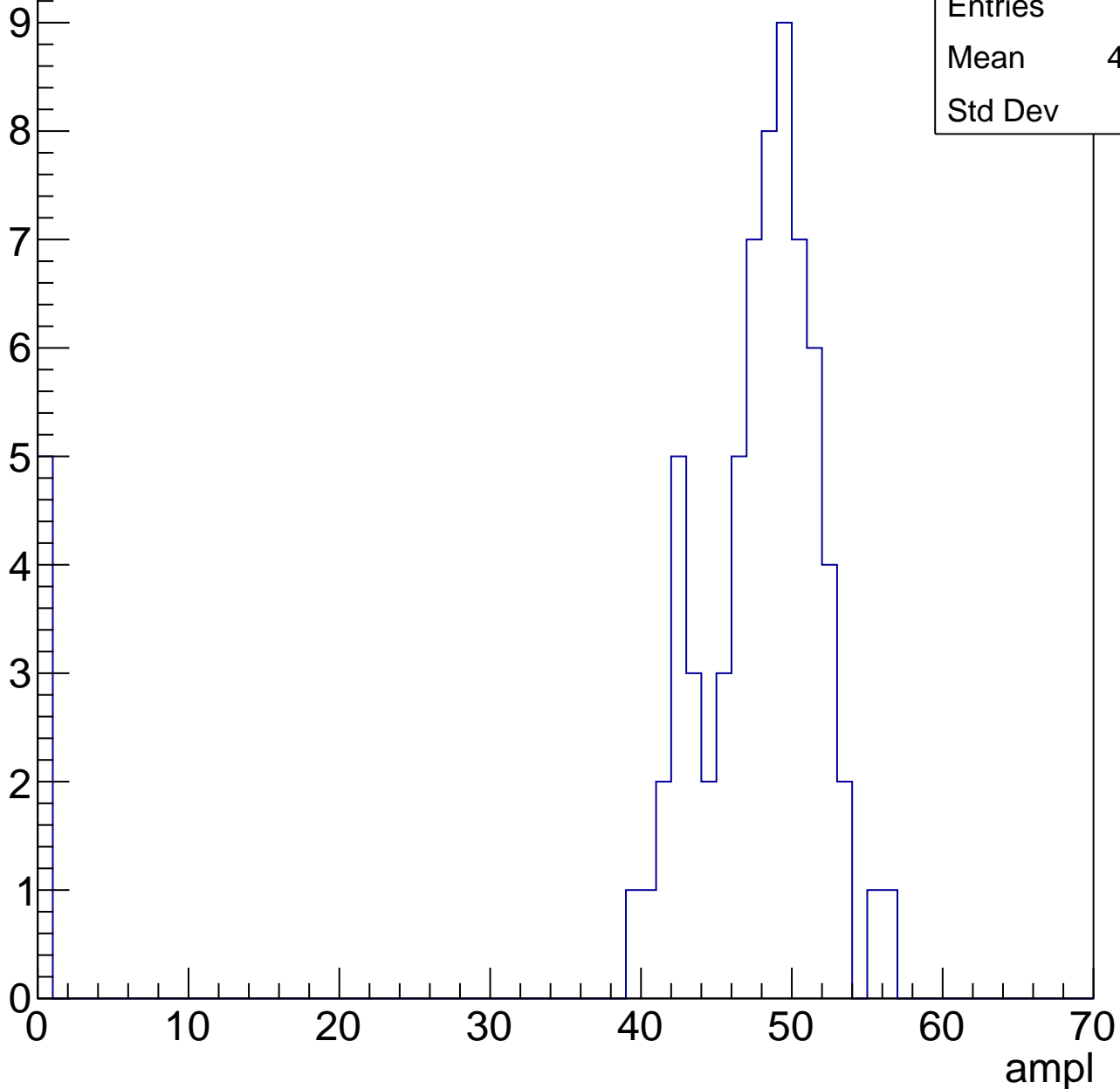
Entry



B1L103S, U1-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U1-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	56
Mean	54.45
Std Dev	2.685

Entry

10

8

6

4

2

0

0

10

20

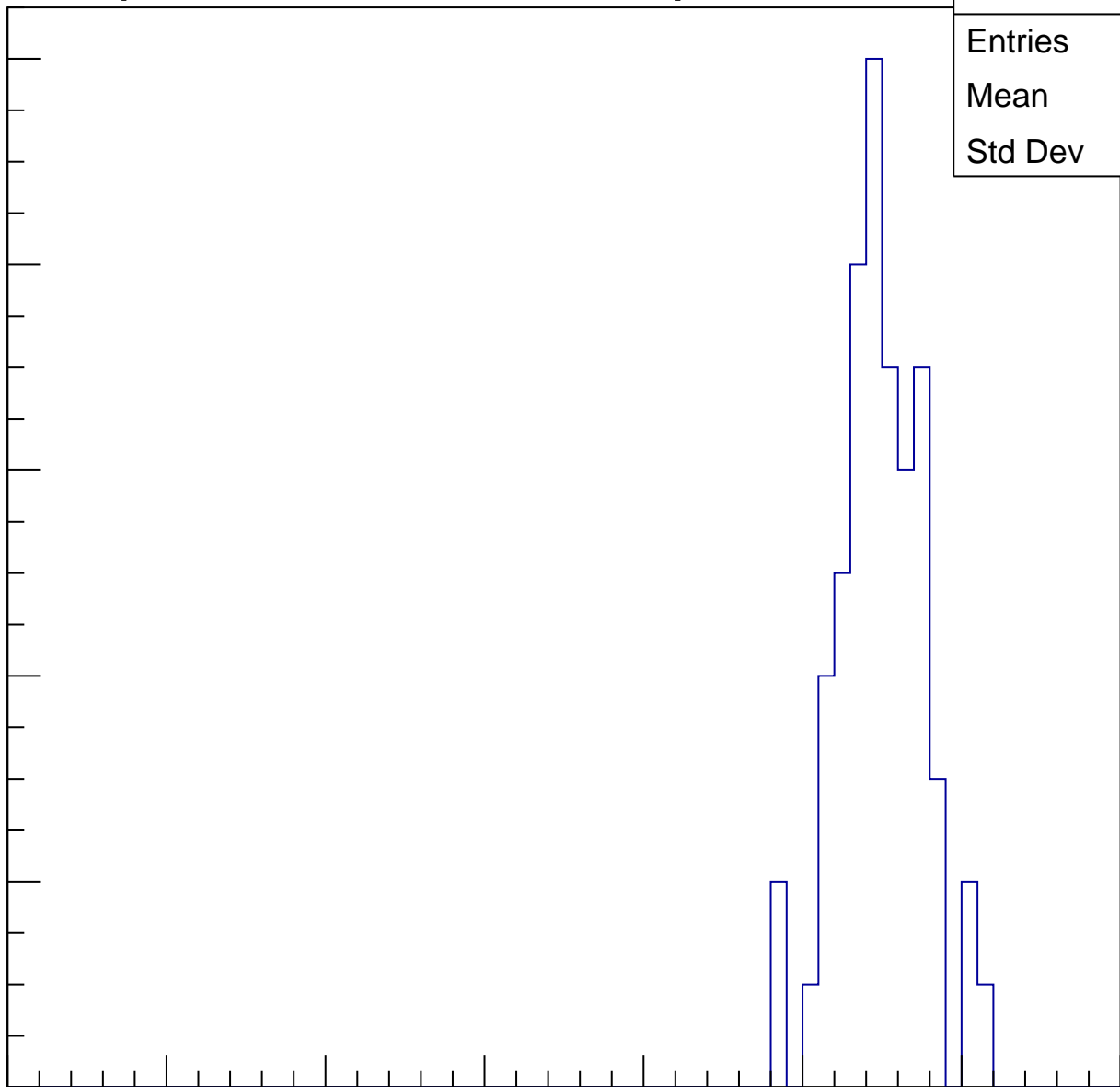
30

40

50

60

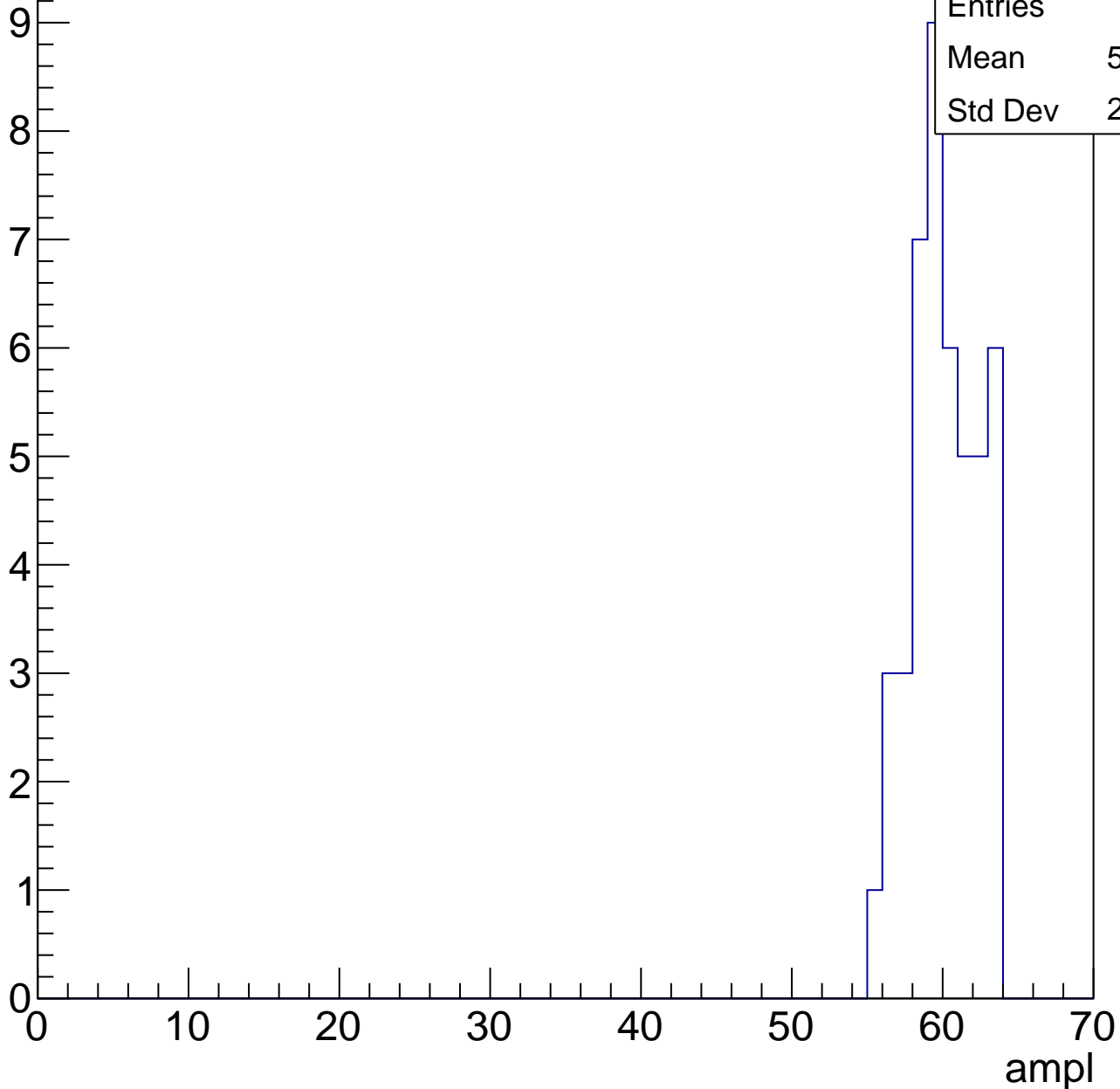
ampl



B1L103S, U1-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



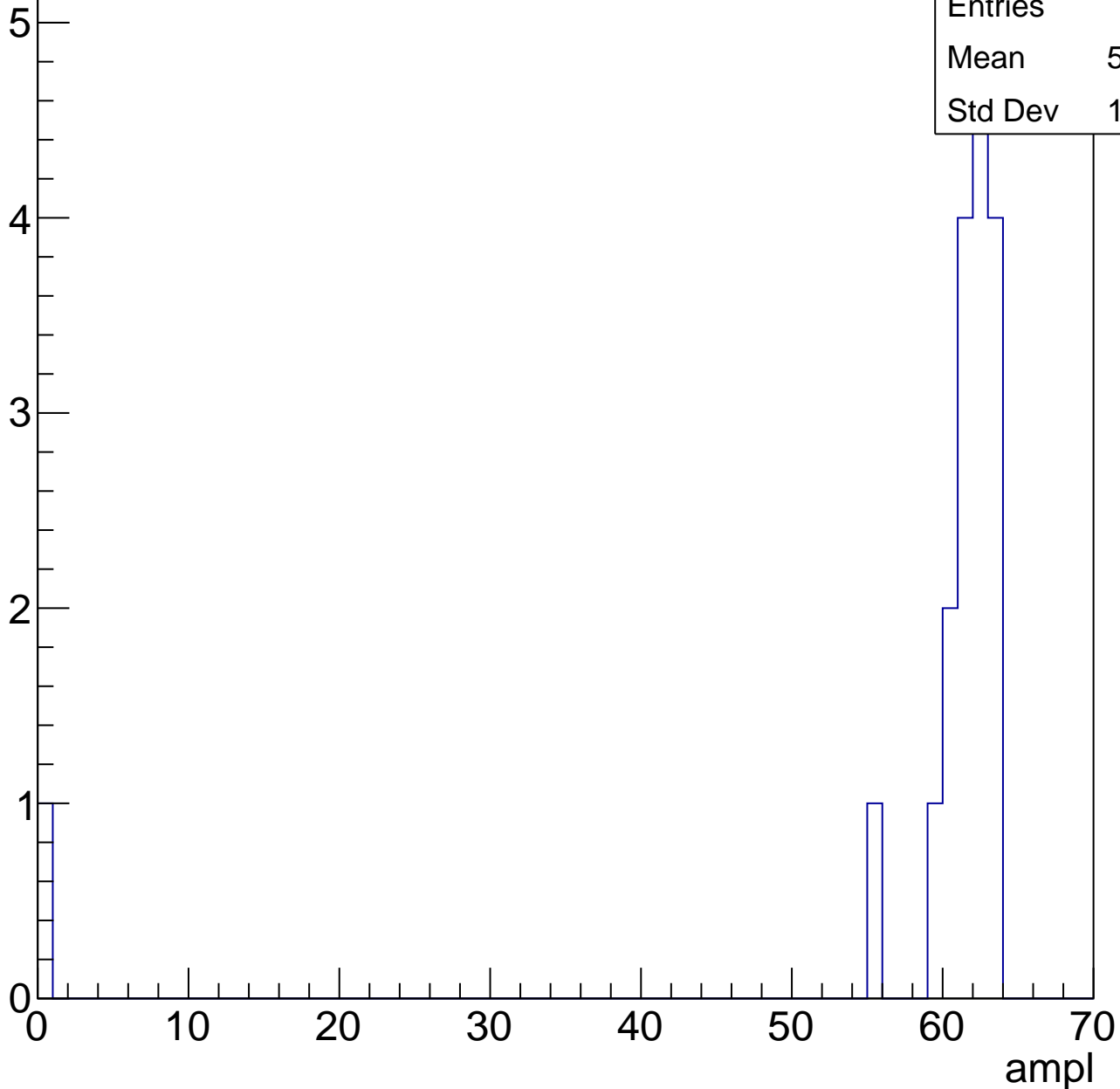
Entries	45
Mean	59.64
Std Dev	2.162

B1L103S, U1-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

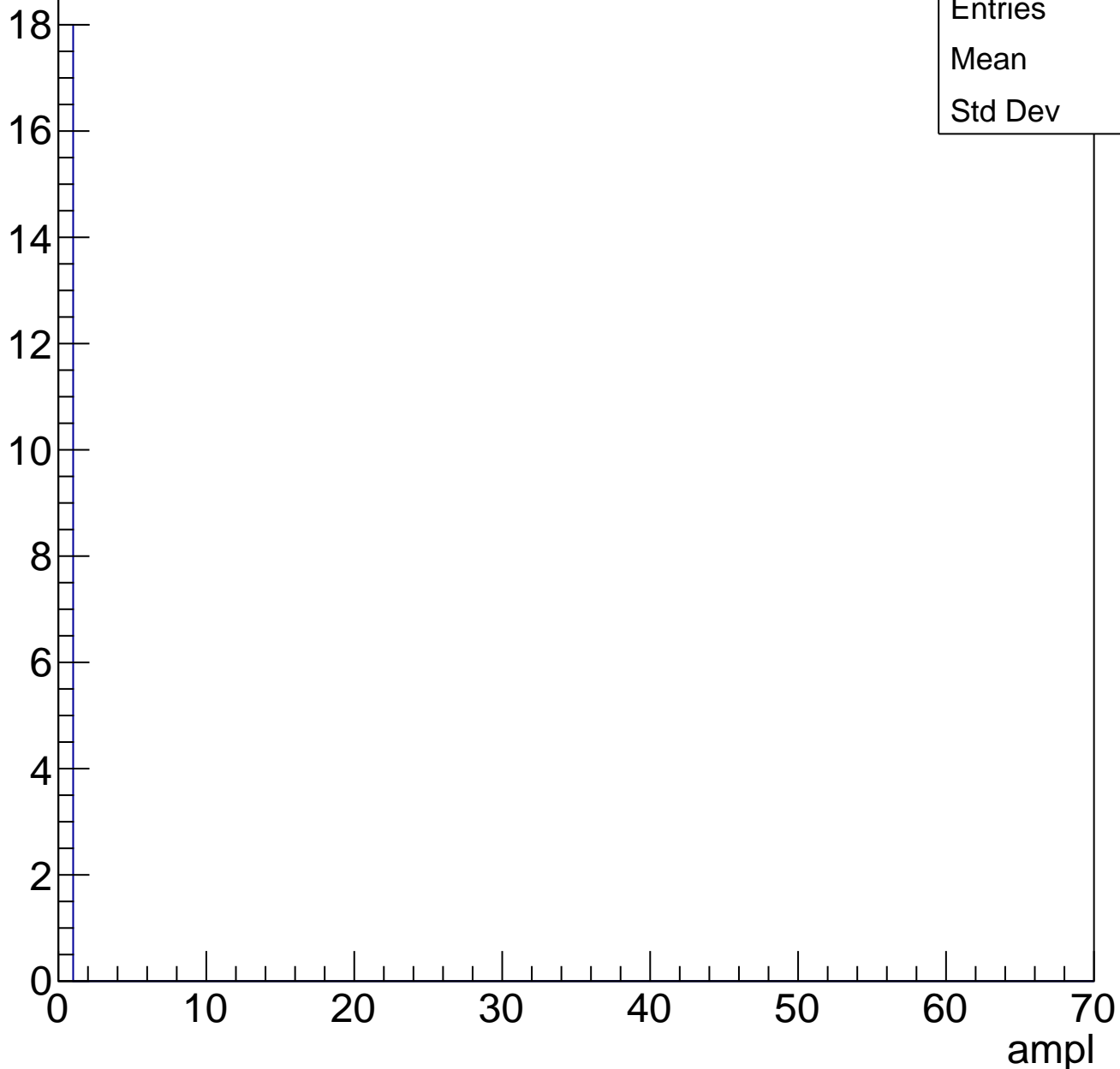
Entries	18
Mean	57.78
Std Dev	14.14



B1L103S, U1-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

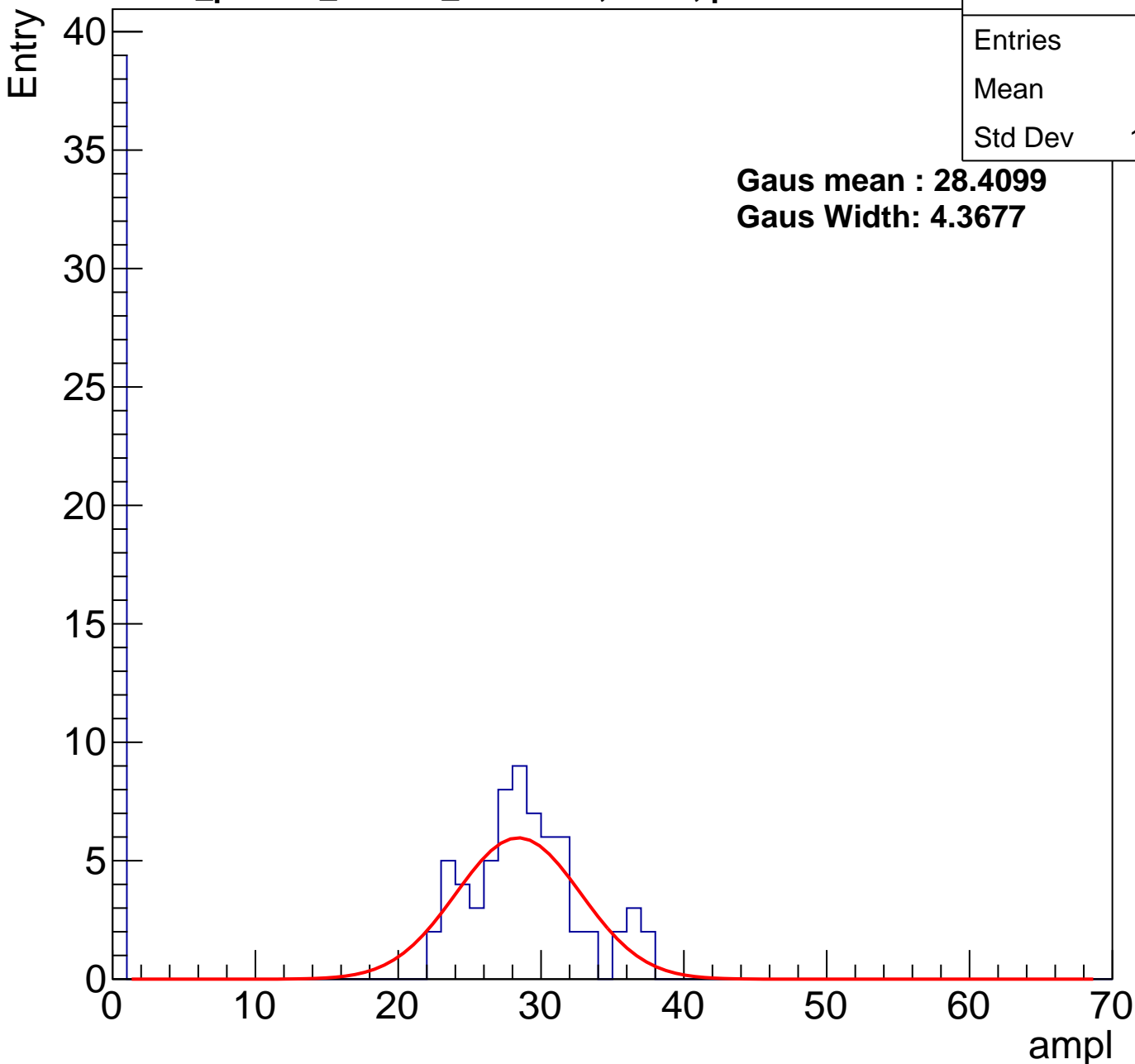
B1L103S, U1-ch108, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	105
Mean	17.9
Std Dev	14.07

Gaus mean : 28.4099

Gaus Width: 4.3677



B1L103S, U1-ch108, adc1

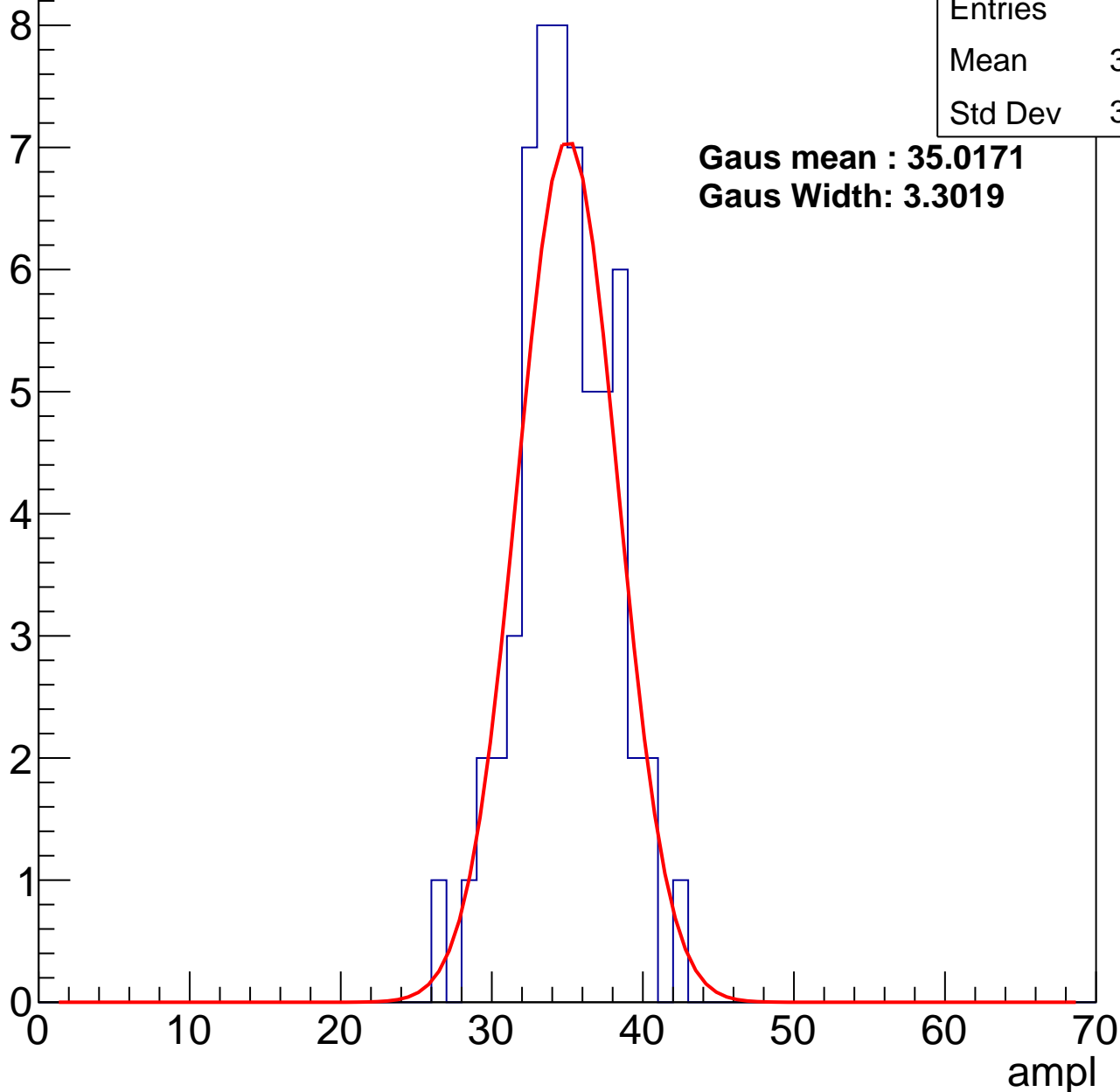
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	34.38
Std Dev	3.136

Gaus mean : 35.0171

Gaus Width: 3.3019



B1L103S, U1-ch108, adc2

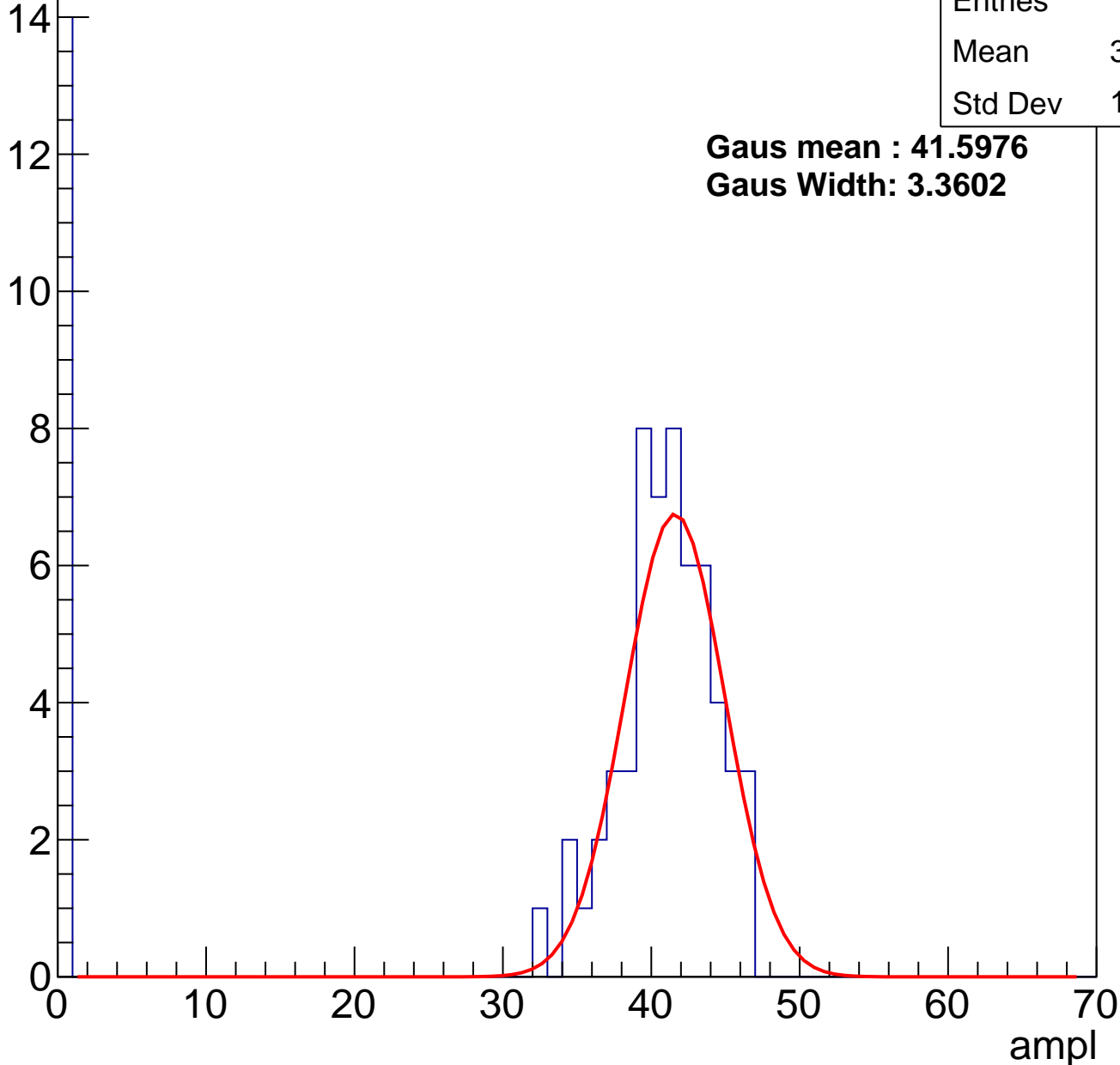
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	32.55
Std Dev	16.37

Gaus mean : 41.5976

Gaus Width: 3.3602

Entry

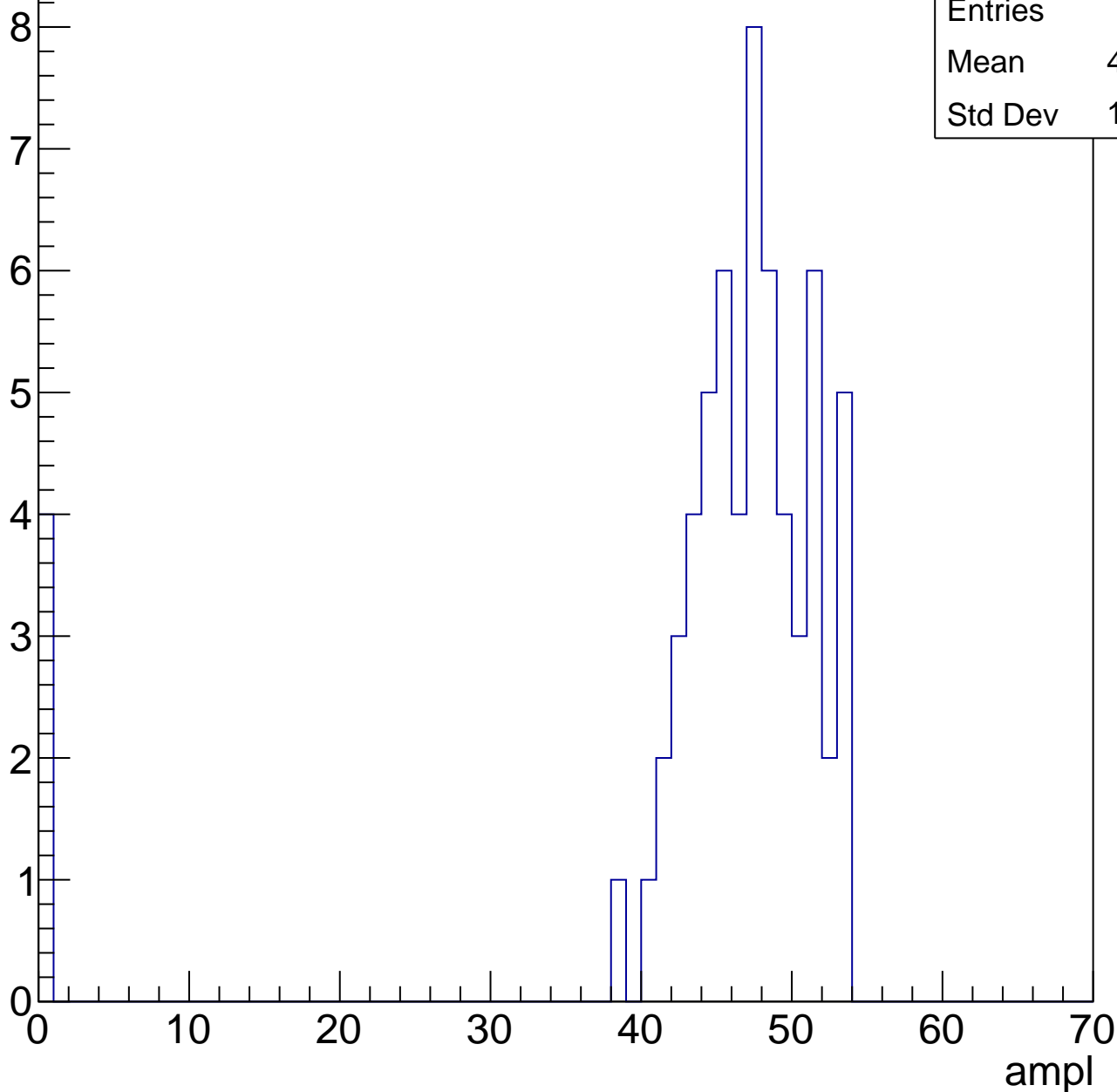


B1L103S, U1-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	44.02
Std Dev	11.89

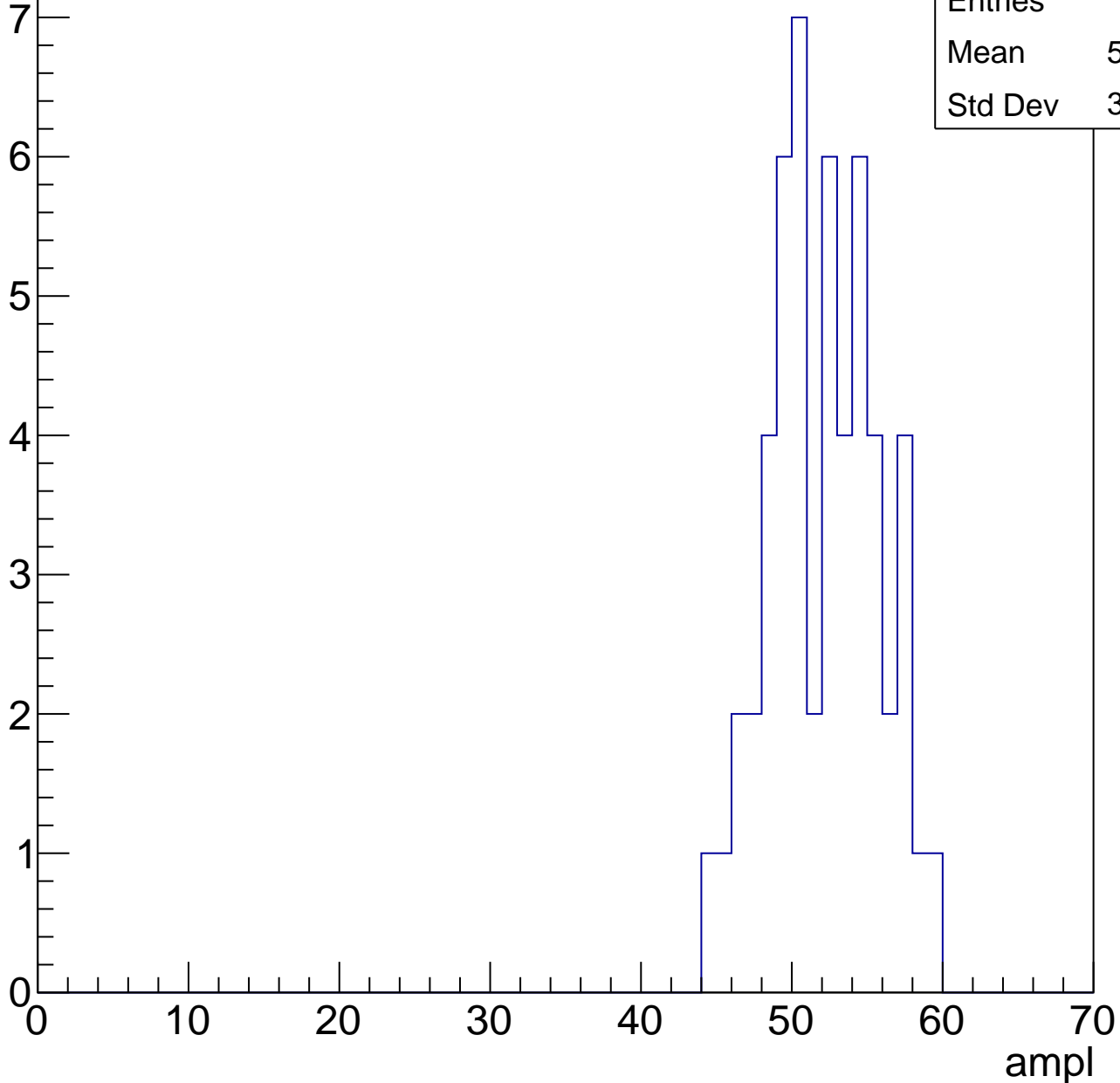


B1L103S, U1-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	51.66
Std Dev	3.523

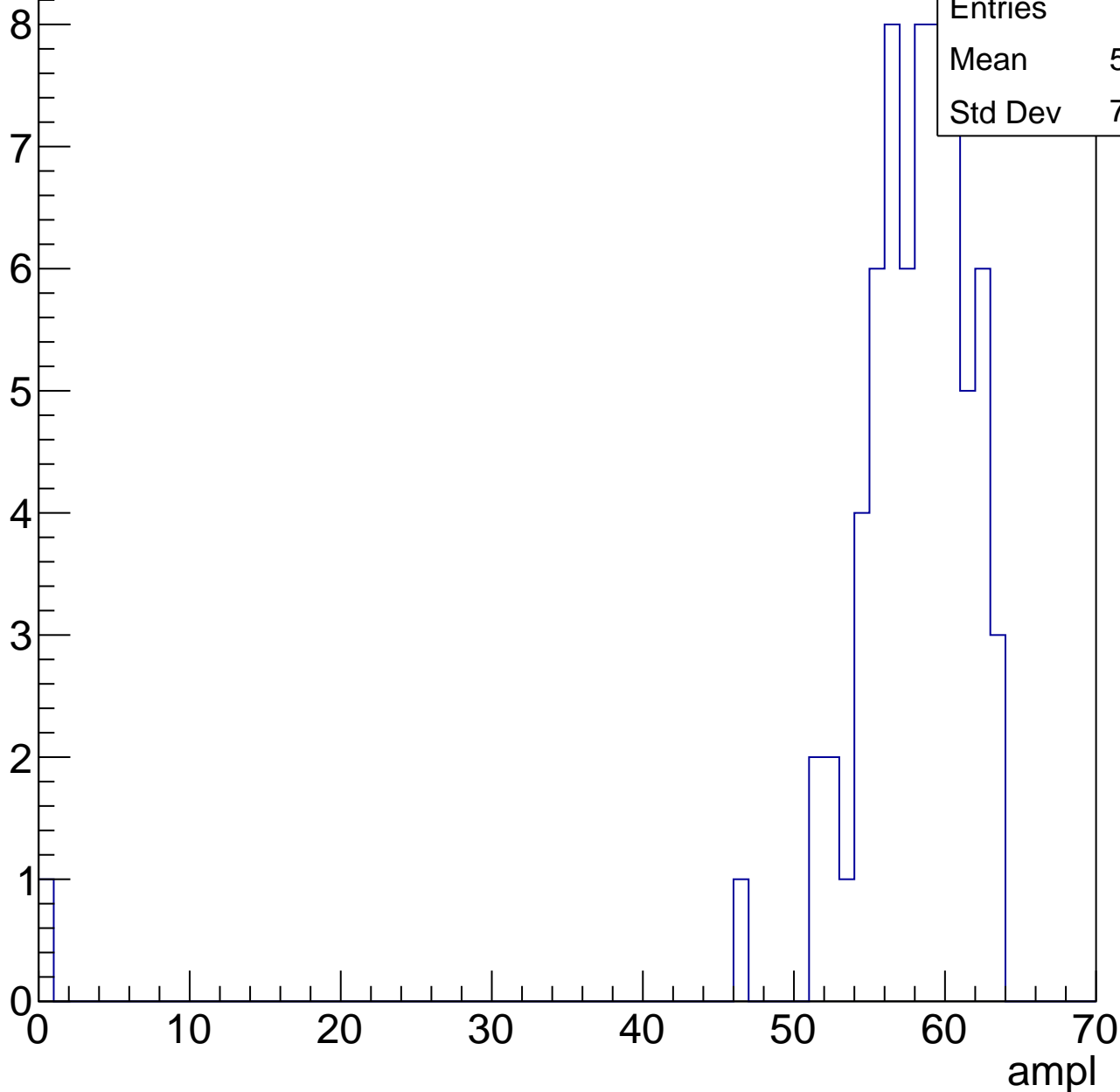


B1L103S, U1-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	56.86
Std Dev	7.637

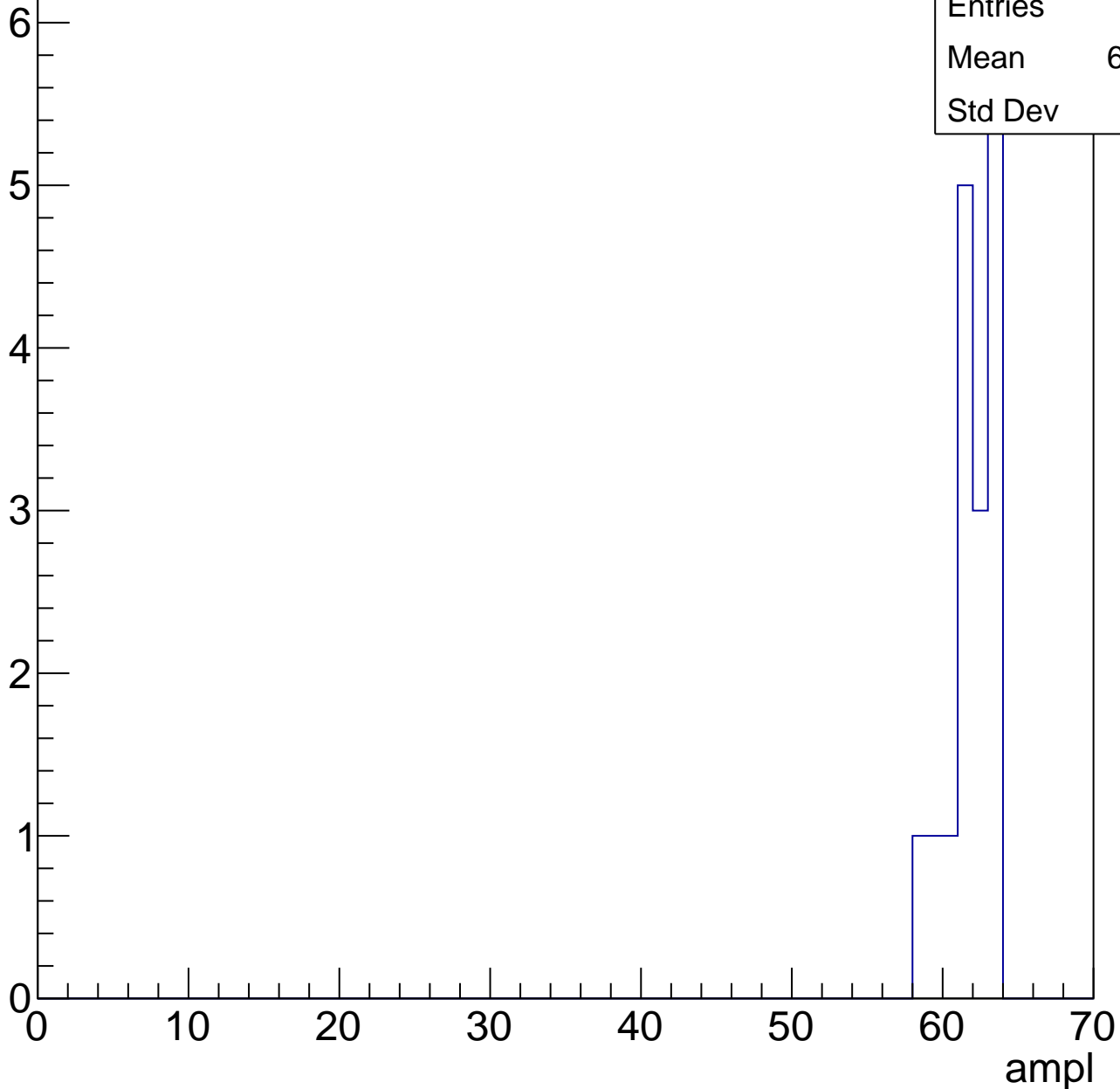


B1L103S, U1-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.53
Std Dev	1.46

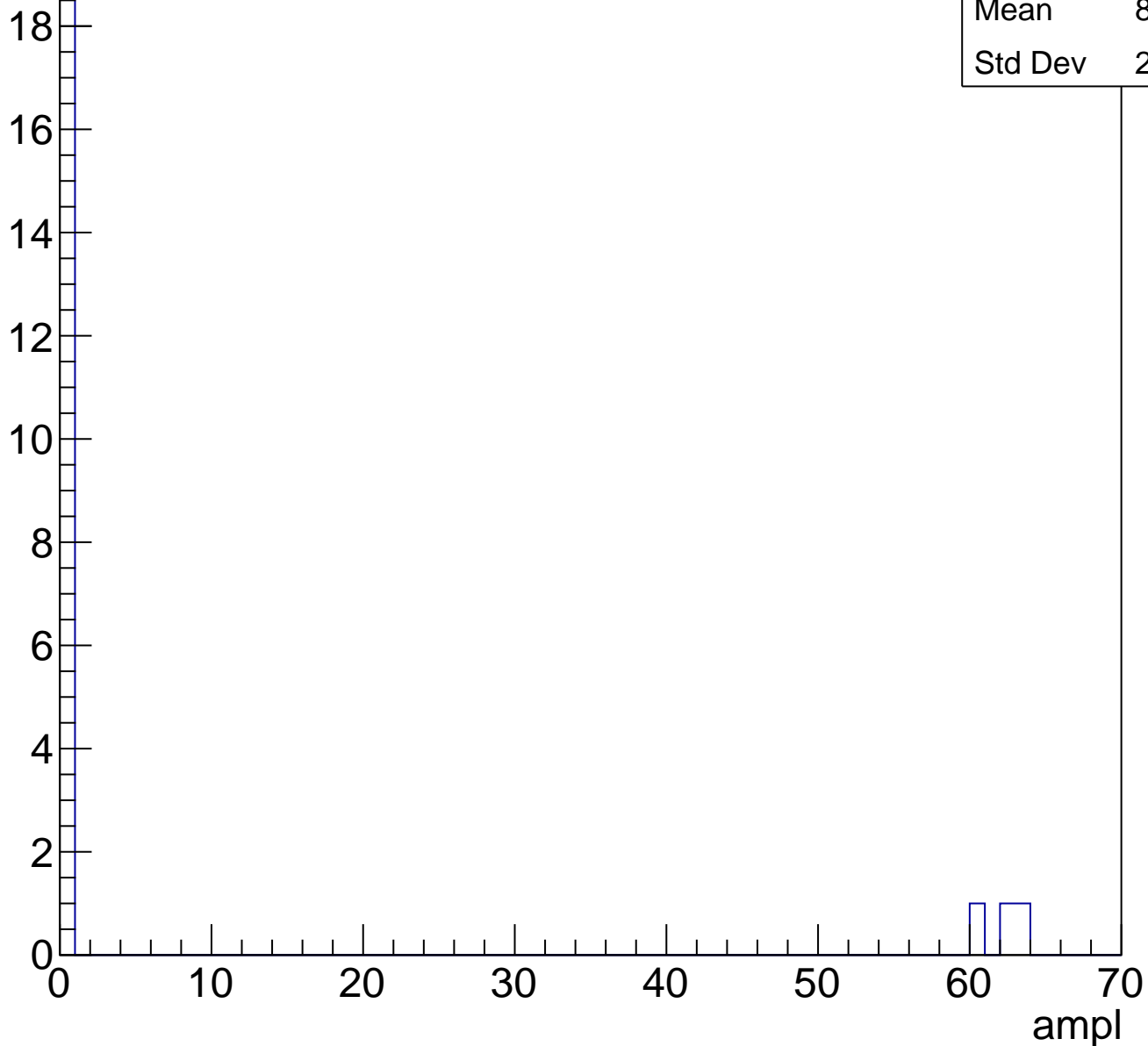


B1L103S, U1-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.409
Std Dev	21.17

Entry



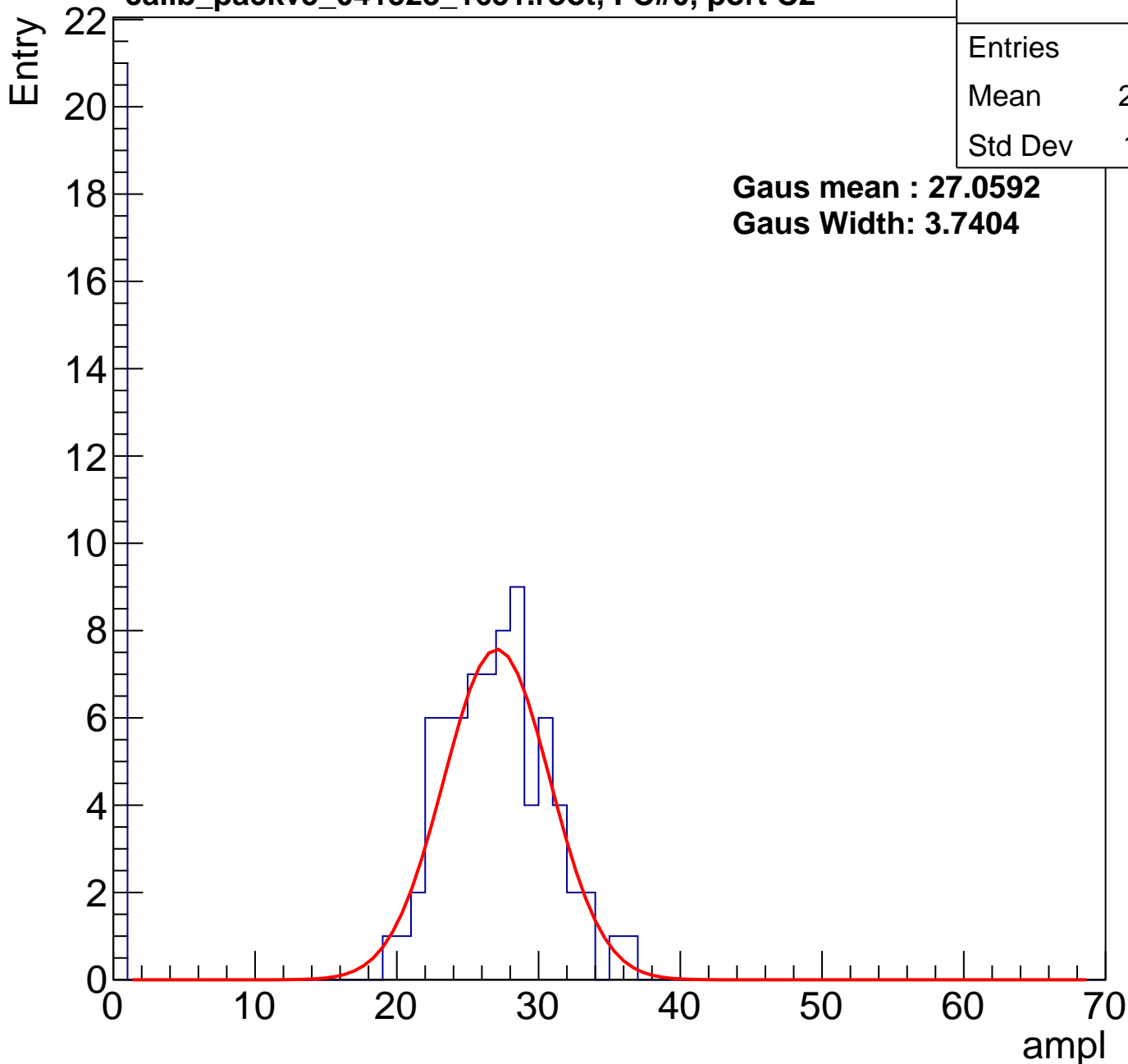
B1L103S, U1-ch109, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	20.65
Std Dev	11.51

Gaus mean : 27.0592

Gaus Width: 3.7404



B1L103S, U1-ch109, adc1

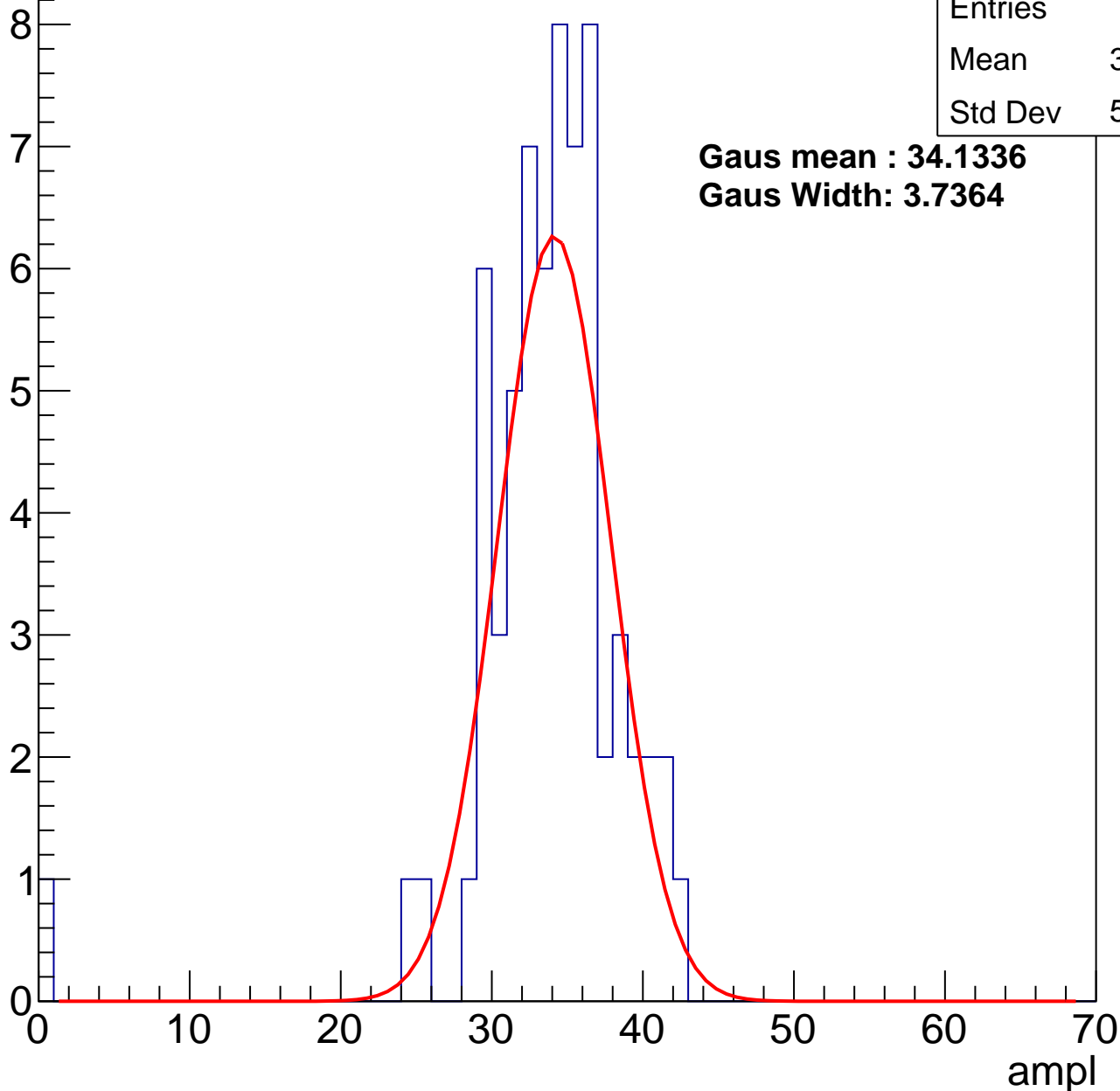
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.23
Std Dev	5.504

Gaus mean : 34.1336

Gaus Width: 3.7364



B1L103S, U1-ch109, adc2

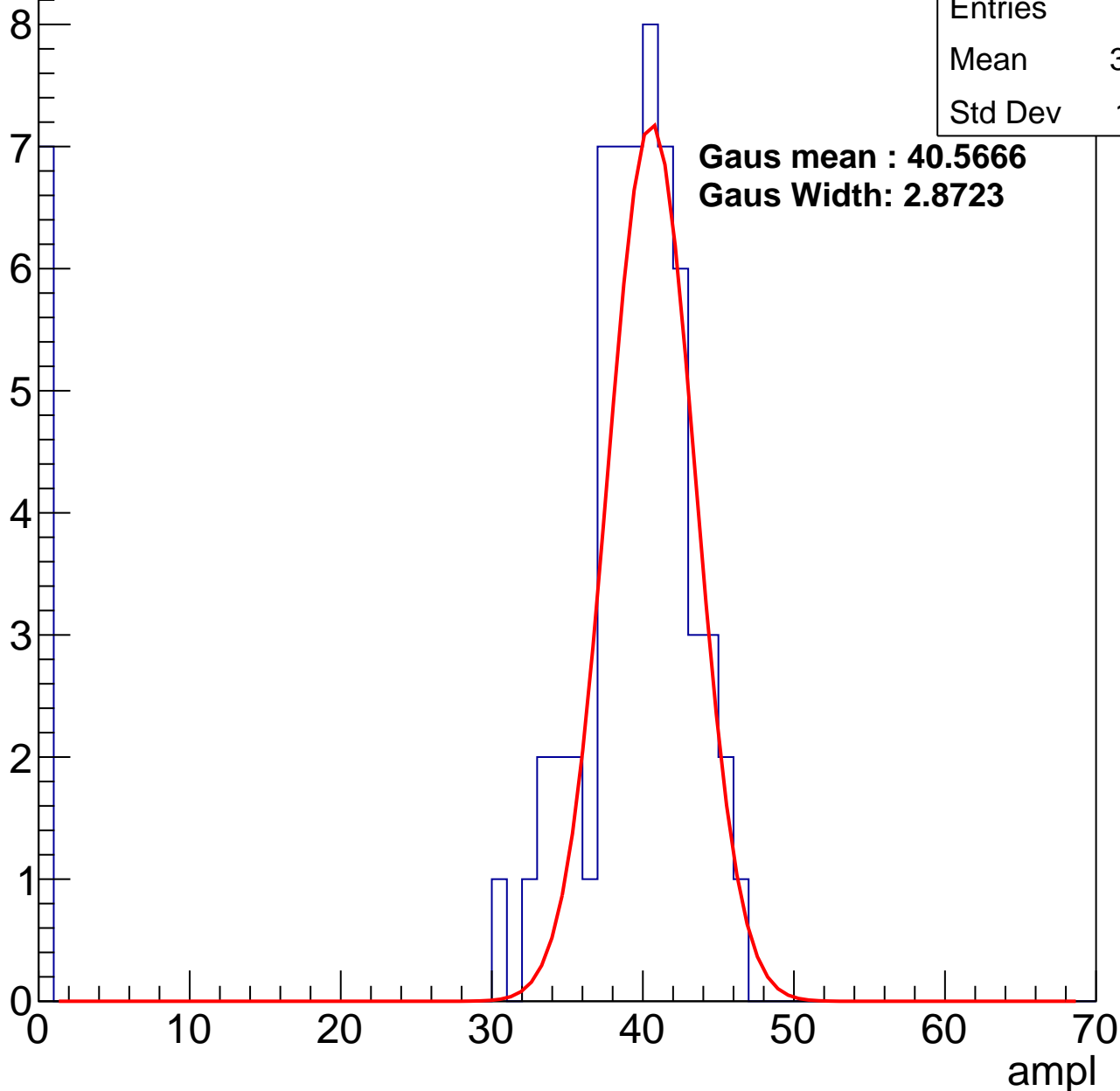
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.16
Std Dev	12.41

Gaus mean : 40.5666

Gaus Width: 2.8723

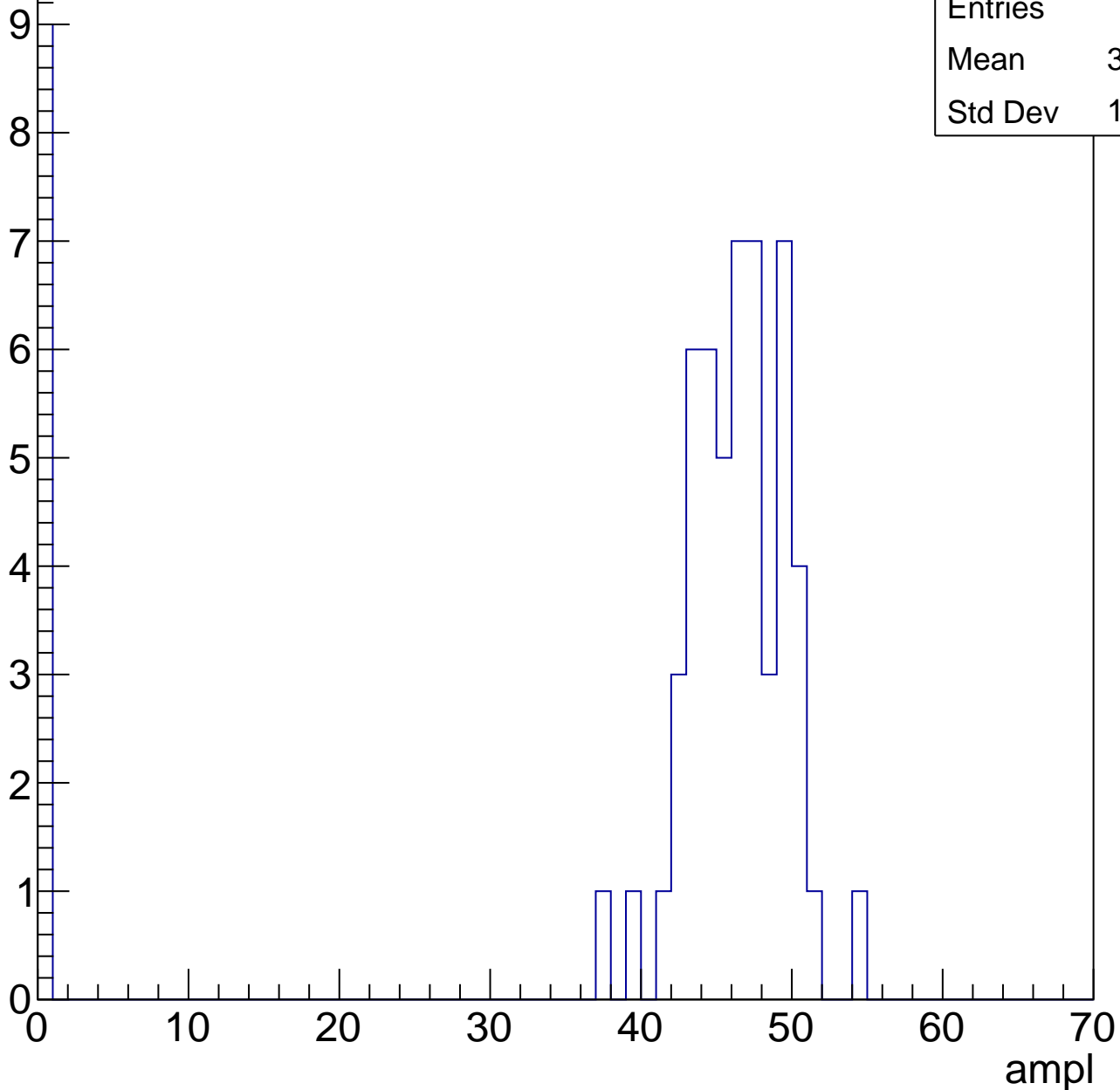


B1L103S, U1-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.24
Std Dev	16.43

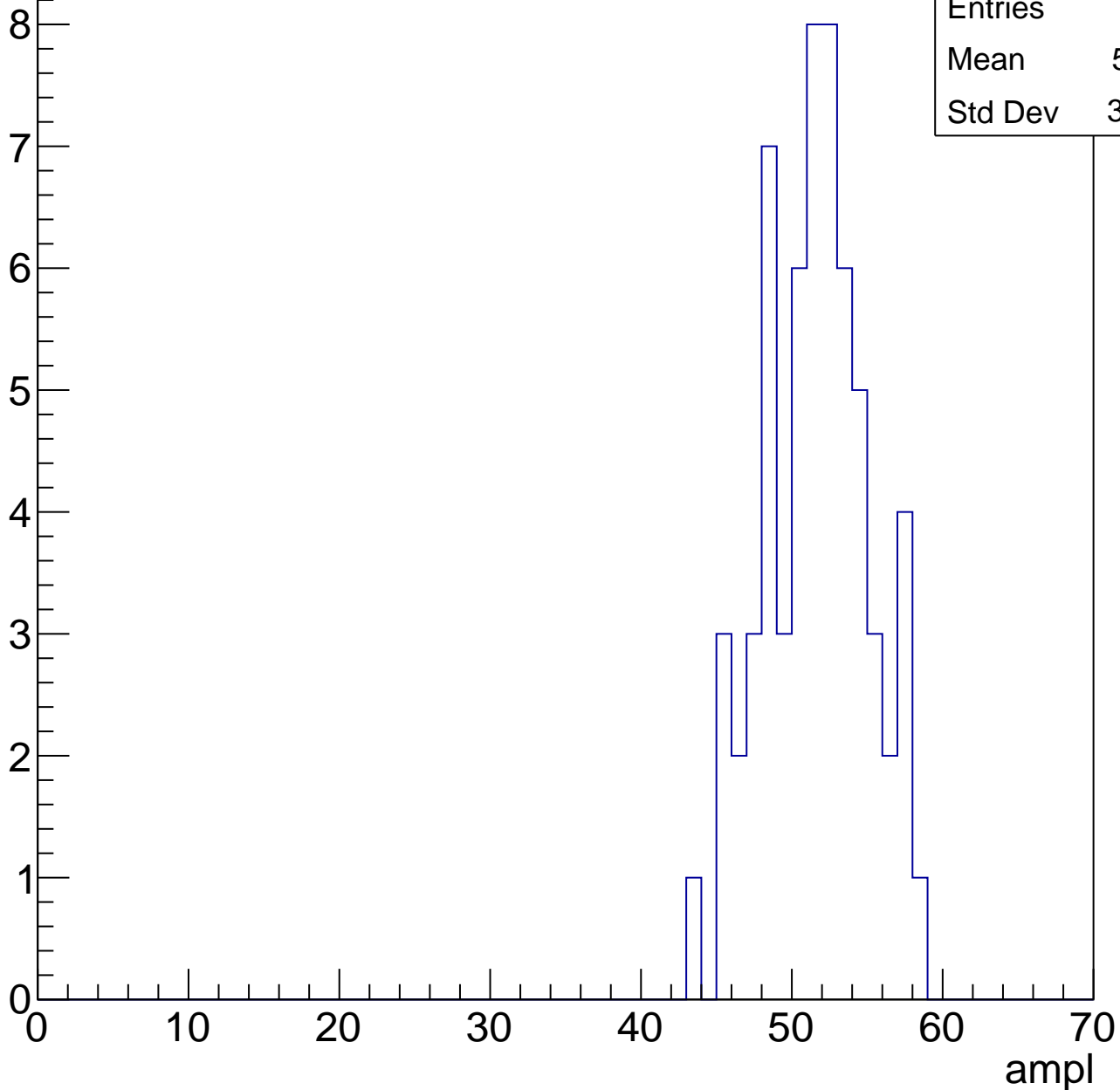


B1L103S, U1-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	51.11
Std Dev	3.404

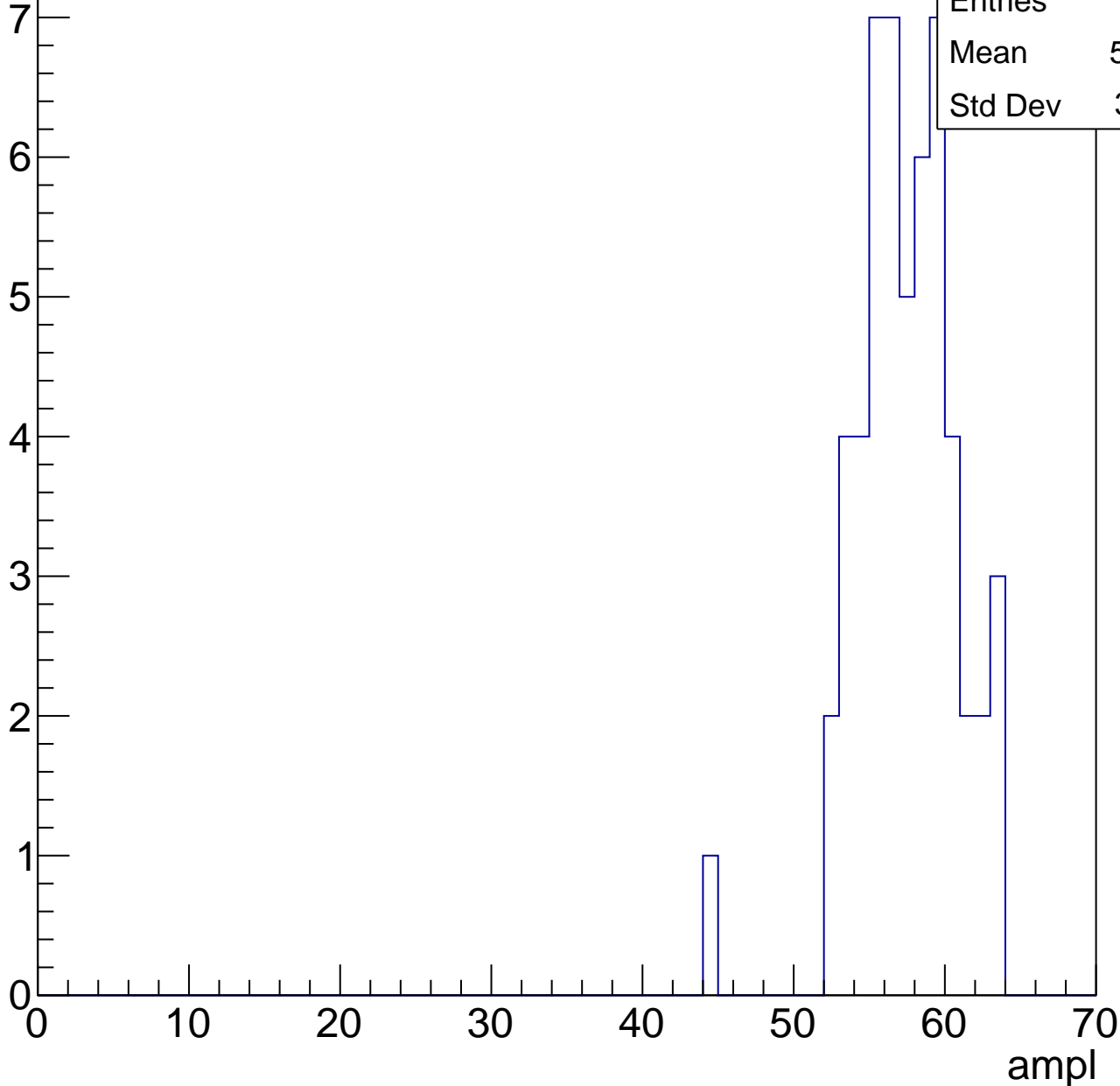


B1L103S, U1-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	56.93
Std Dev	3.371

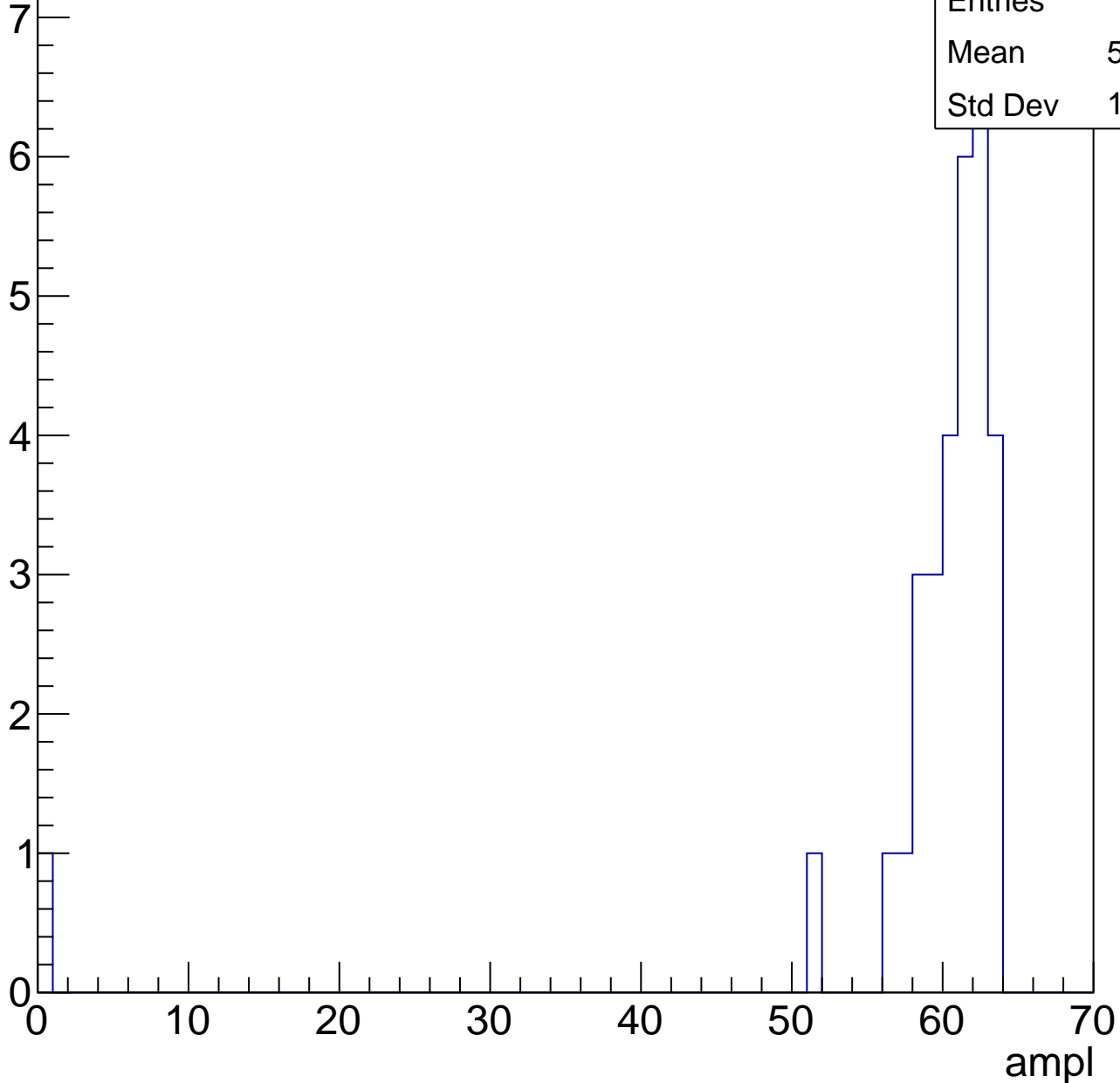


B1L103S, U1-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	58.29
Std Dev	10.93

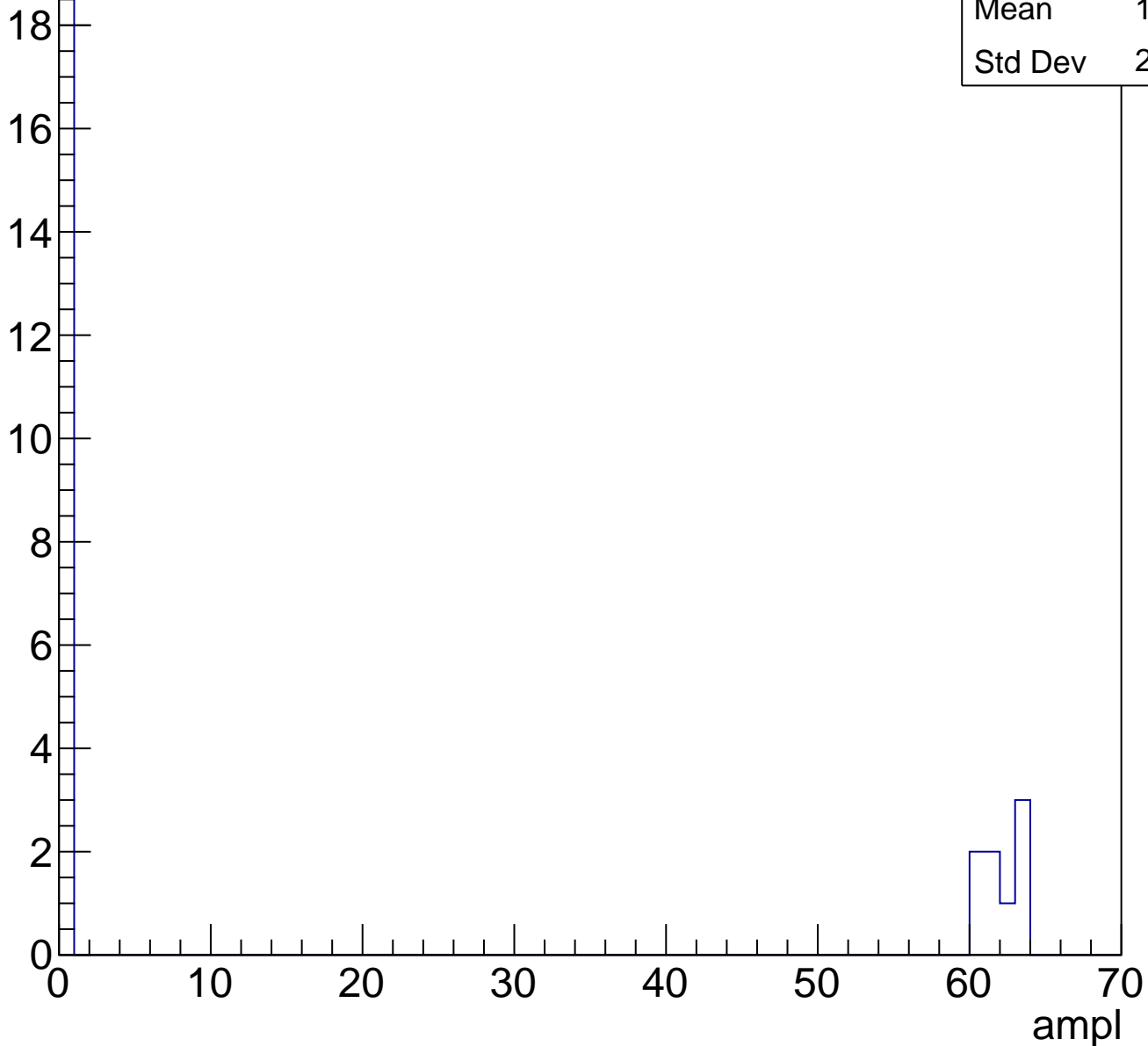


B1L103S, U1-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	27
Mean	18.26
Std Dev	28.15

Entry



B1L103S, U1-ch110, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	18.87
Std Dev	13.39

Gaus mean : 29.0492

Gaus Width: 3.4183

Entry

25

20

15

10

5

0

0

10

20

30

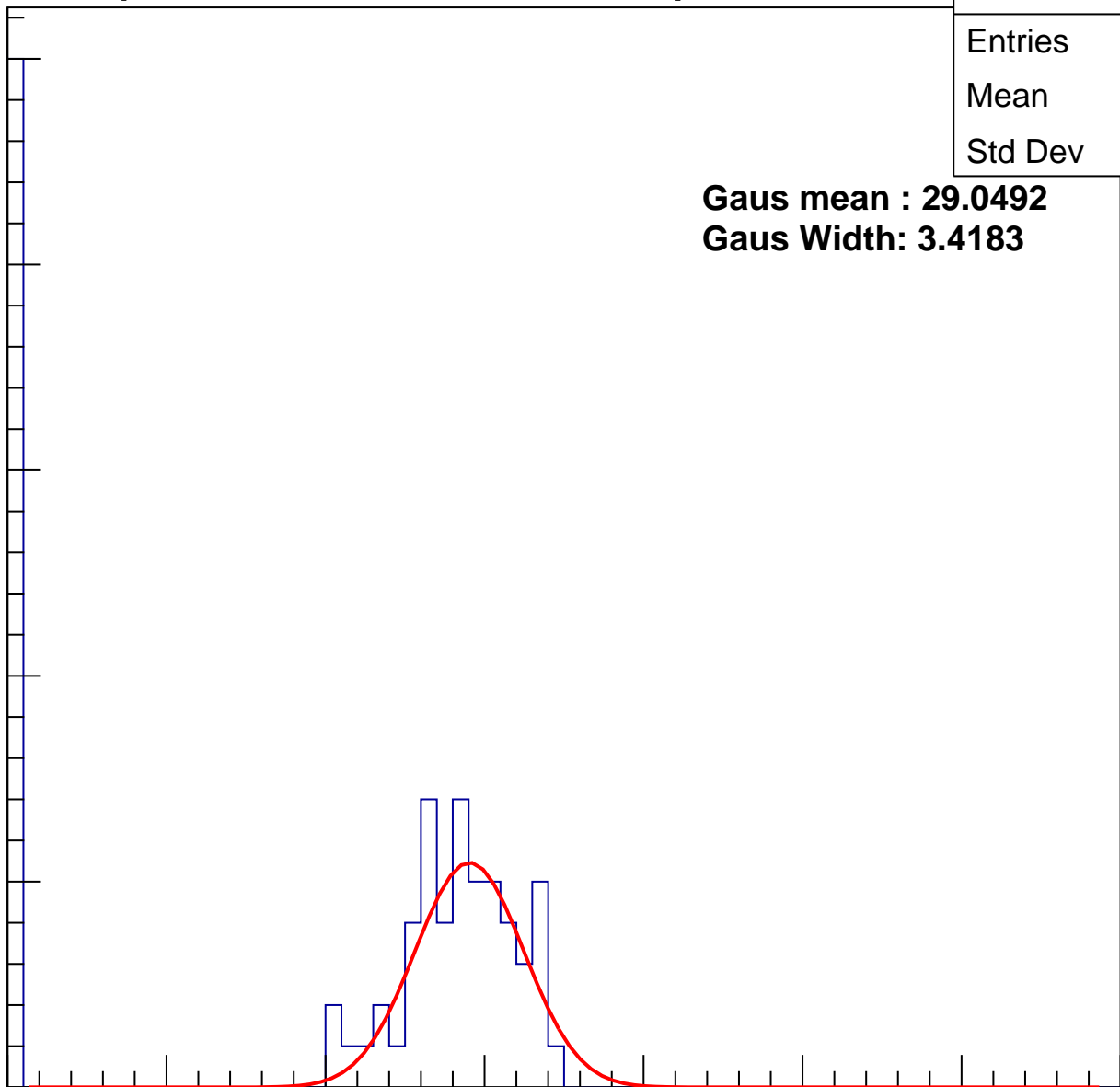
40

50

60

70

ampl



B1L103S, U1-ch110, adc1

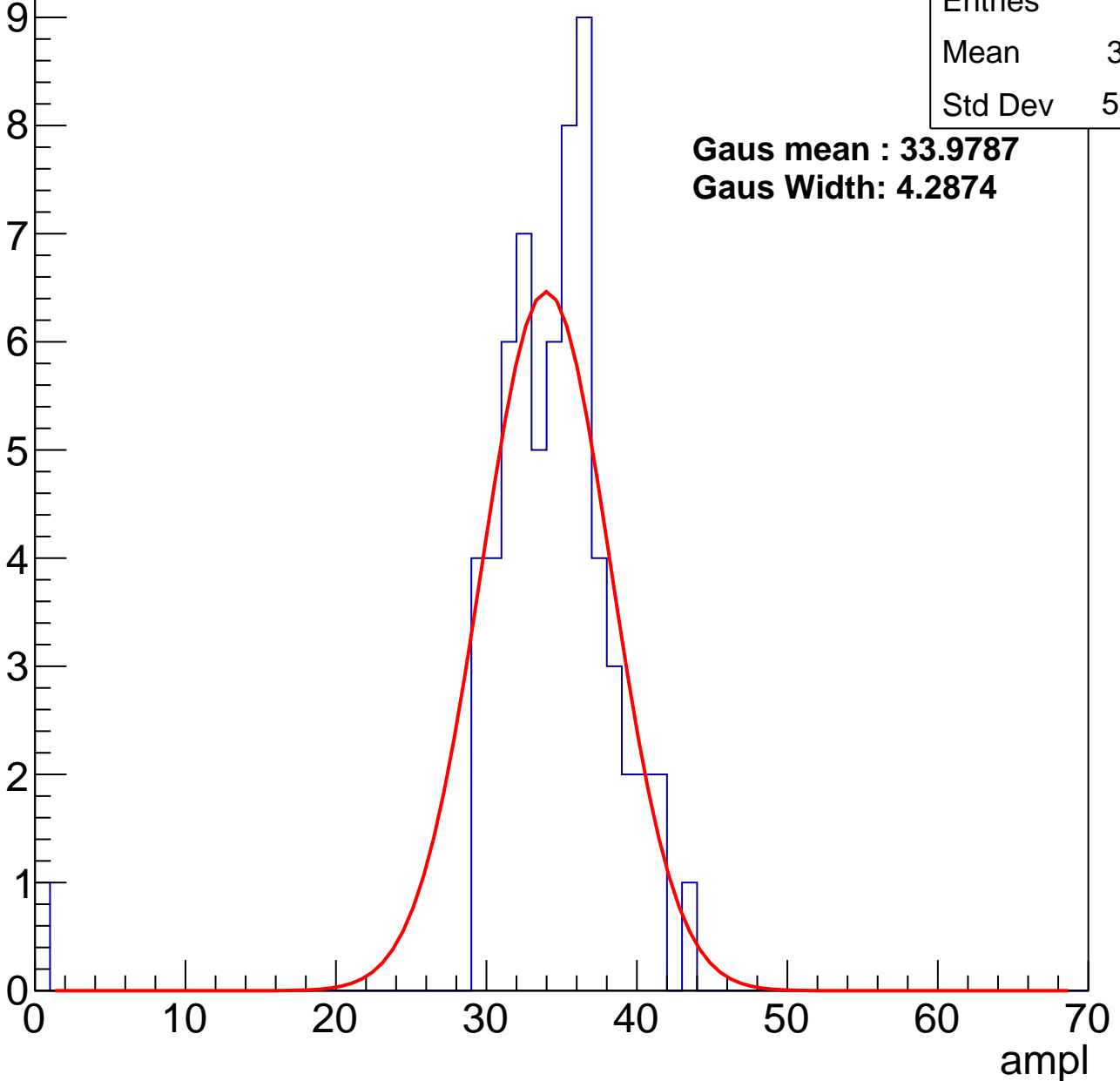
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	33.81
Std Dev	5.353

Gaus mean : 33.9787

Gaus Width: 4.2874



B1L103S, U1-ch110, adc2

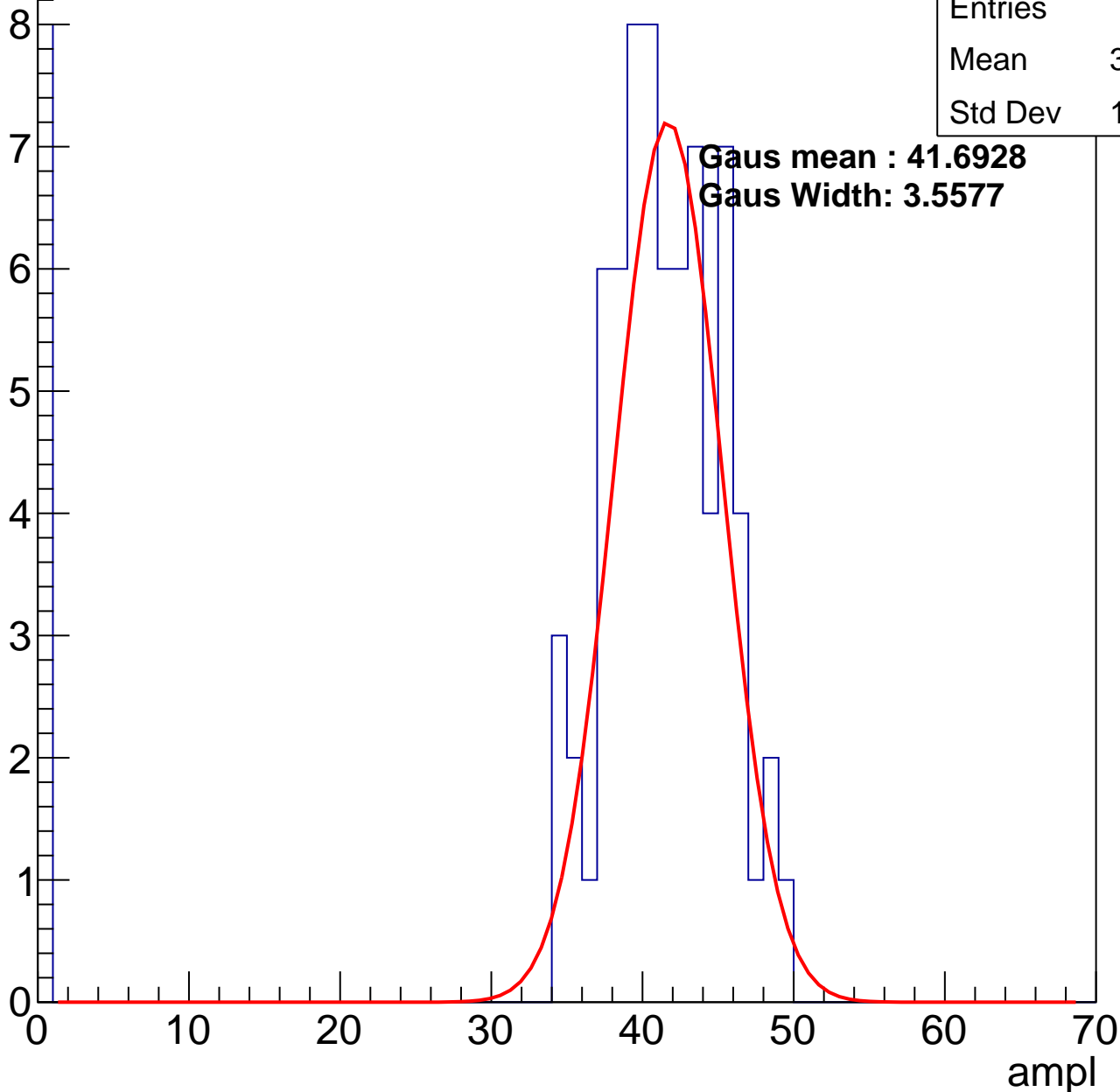
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	36.95
Std Dev	12.77

Gaus mean : 41.6928

Gaus Width: 3.5577

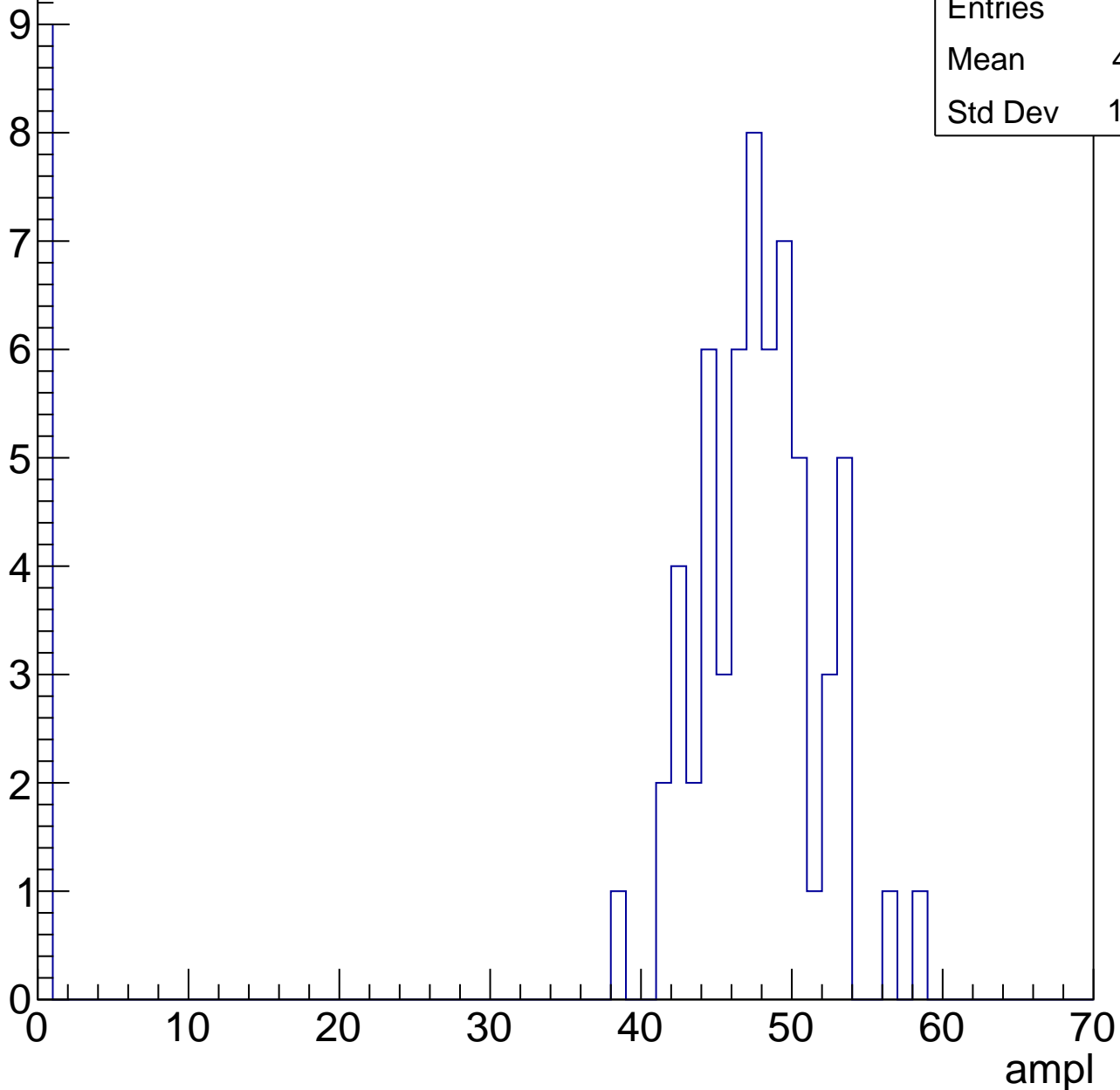


B1L103S, U1-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	41.31
Std Dev	16.27

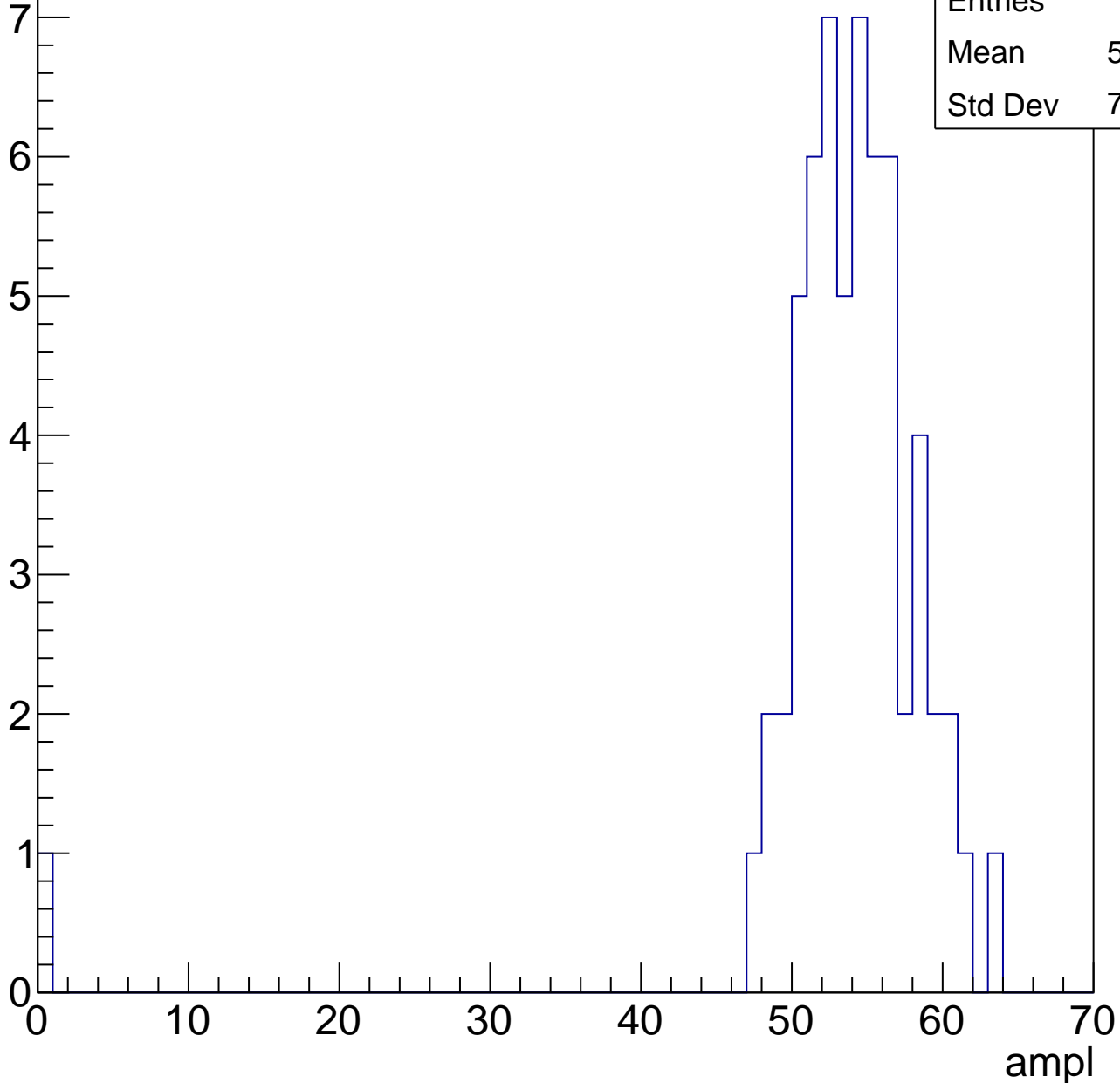


B1L103S, U1-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	52.97
Std Dev	7.696

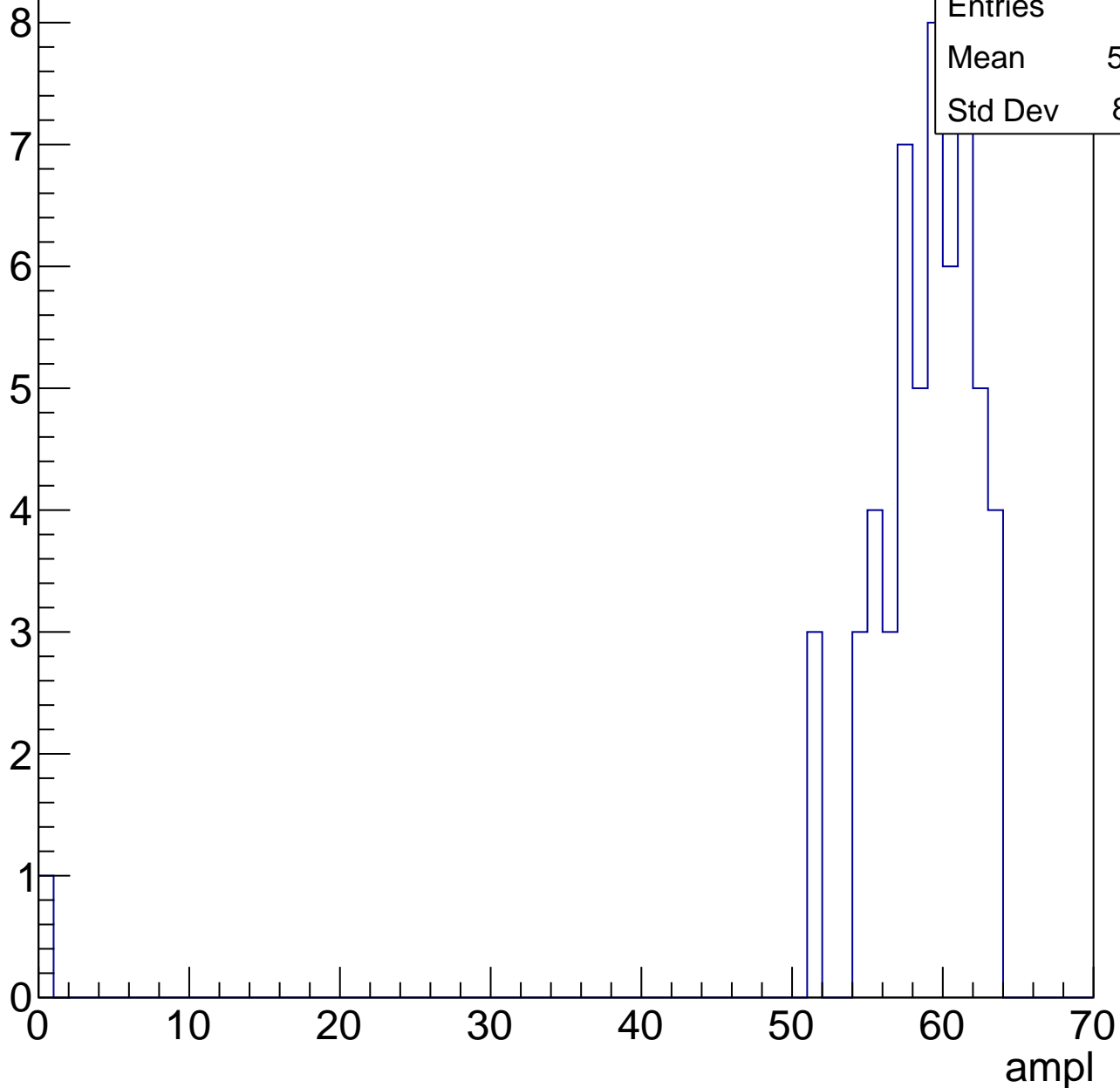


B1L103S, U1-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	57.44
Std Dev	8.251

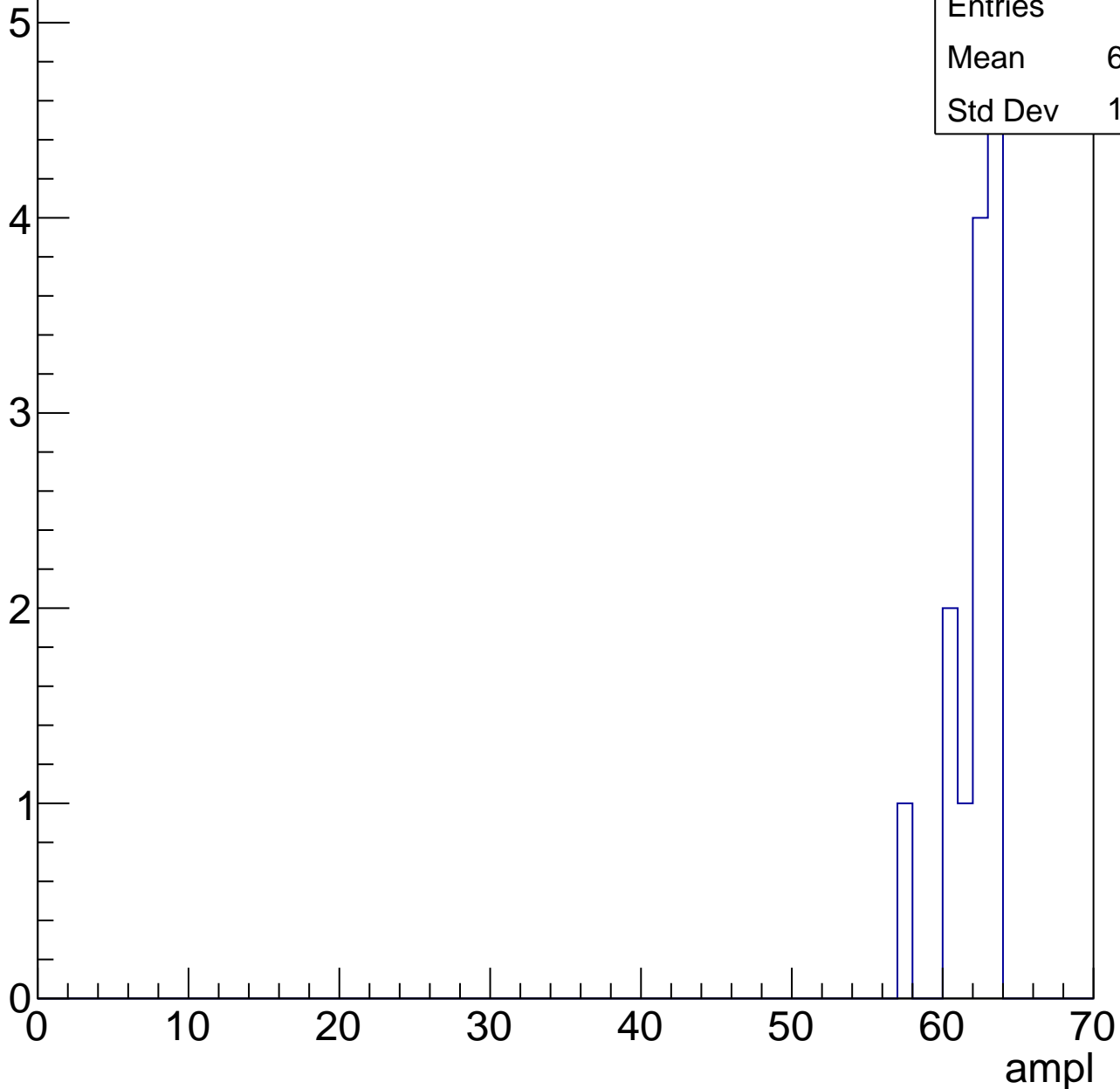


B1L103S, U1-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.62
Std Dev	1.689



B1L103S, U1-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U1-ch111, adc0

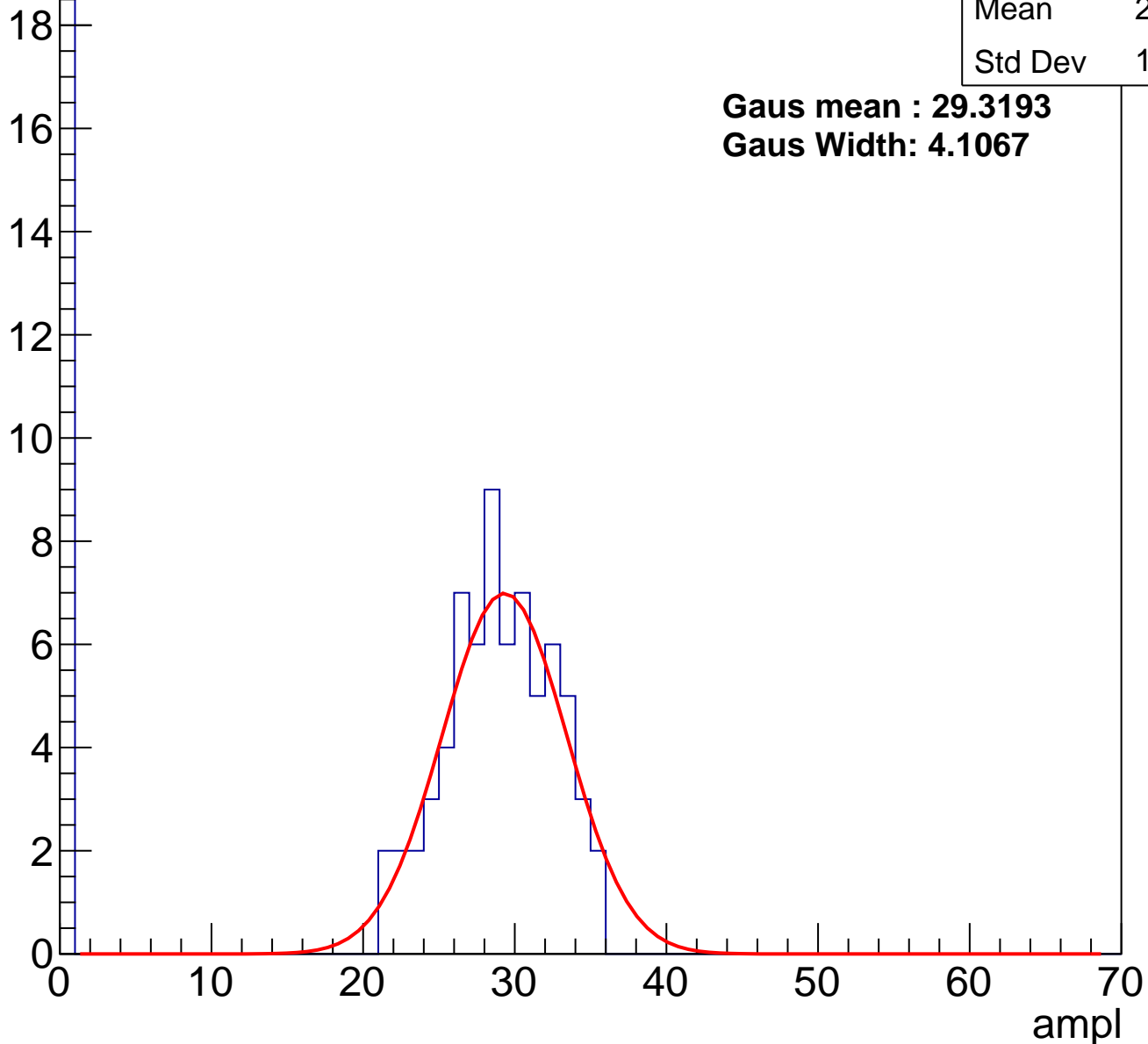
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	22.36
Std Dev	12.13

Gaus mean : 29.3193

Gaus Width: 4.1067

Entry



B1L103S, U1-ch111, adc1

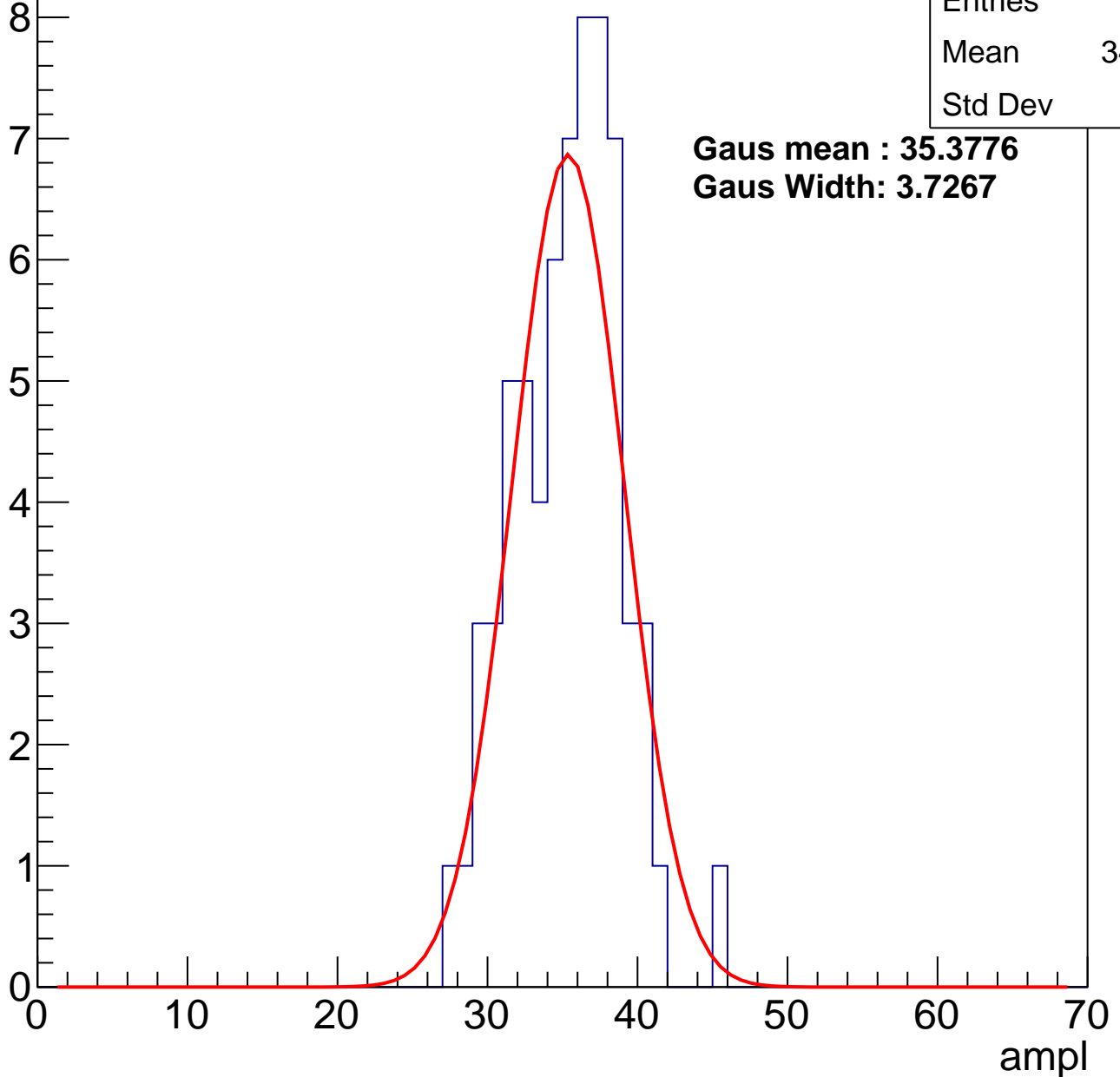
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.86
Std Dev	3.49

Gaus mean : 35.3776

Gaus Width: 3.7267



B1L103S, U1-ch111, adc2

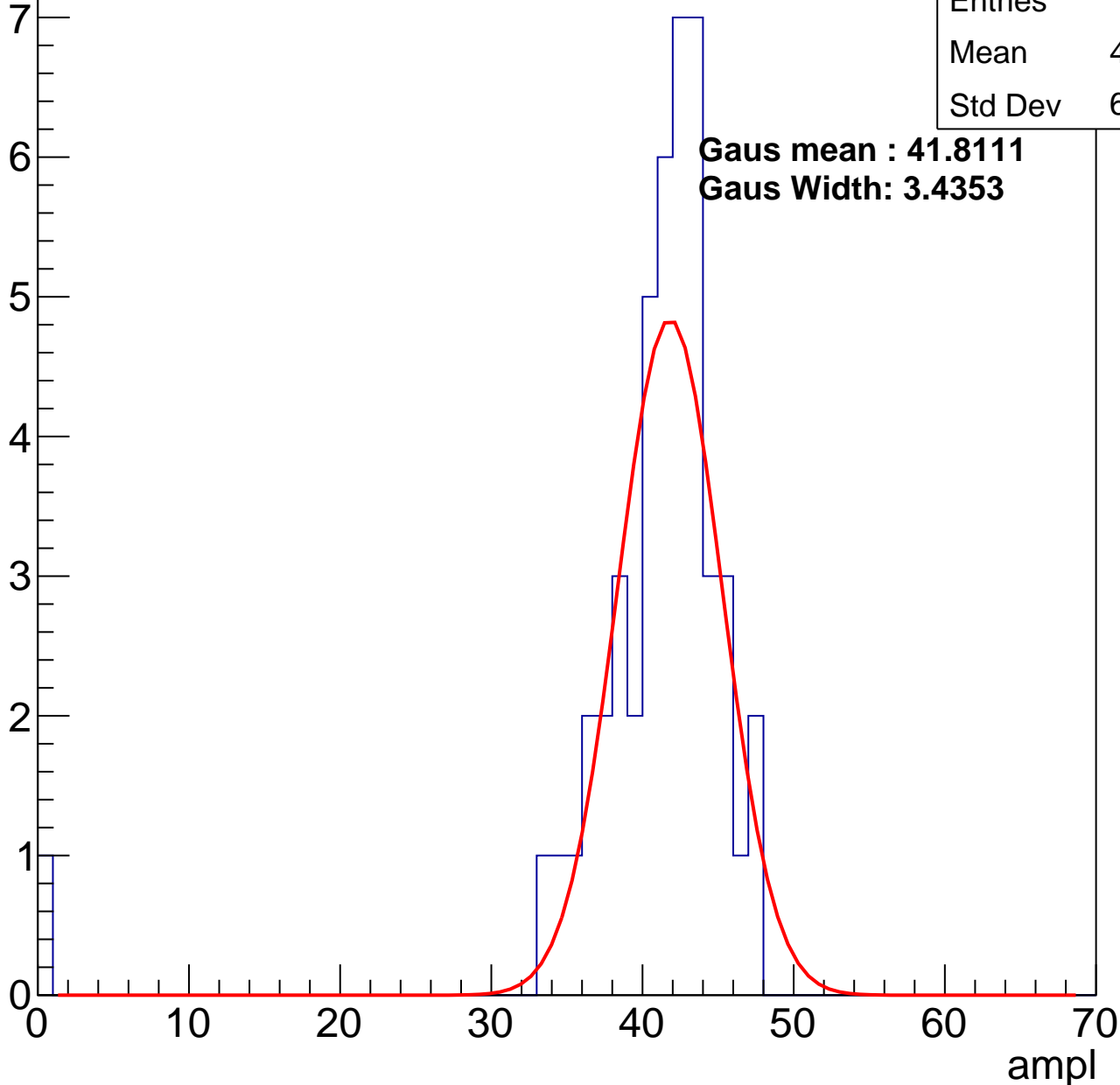
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	40.17
Std Dev	6.727

Gaus mean : 41.8111

Gaus Width: 3.4353



B1L103S, U1-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

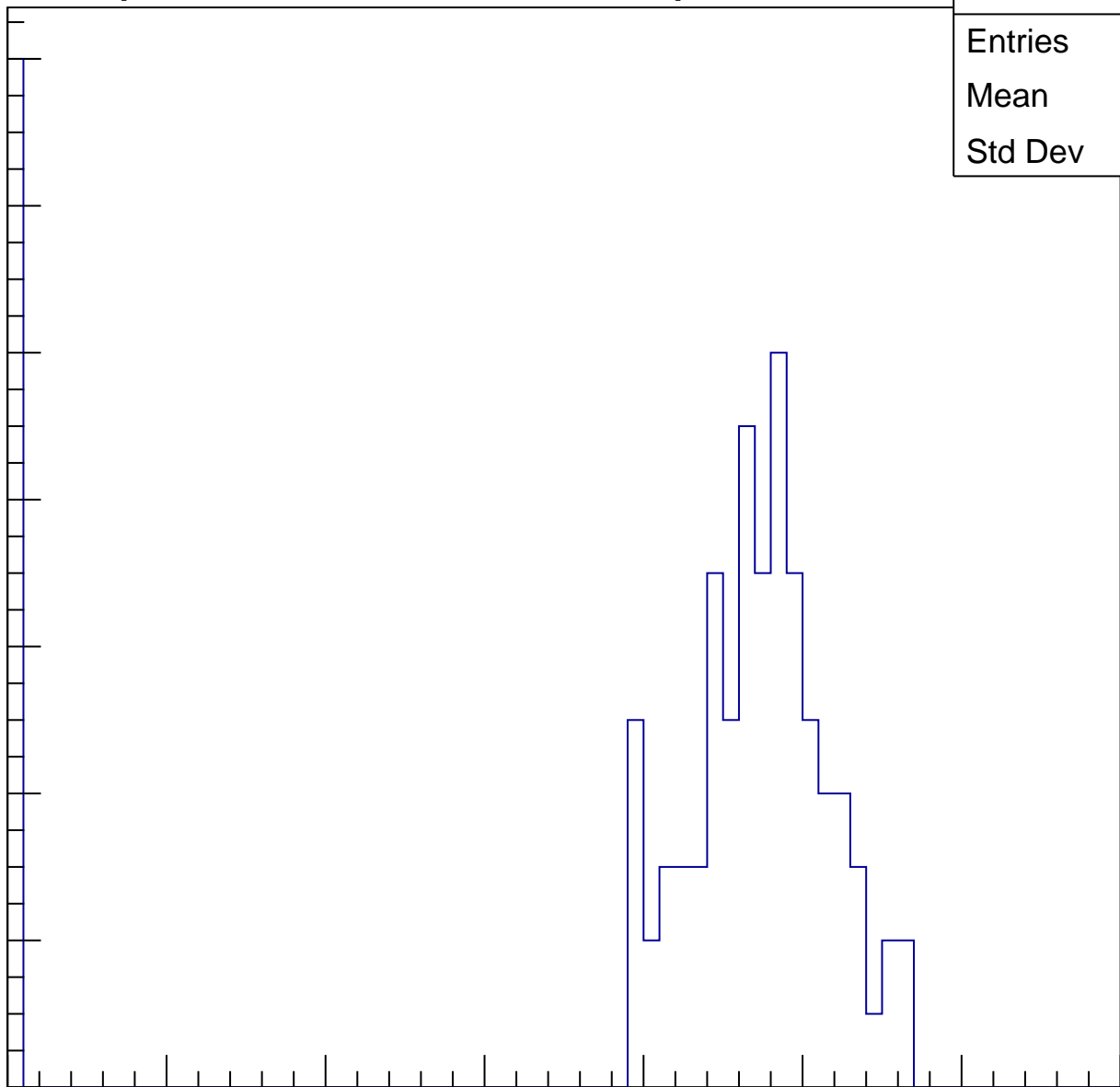
Entries	96
Mean	40.09
Std Dev	17.01

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

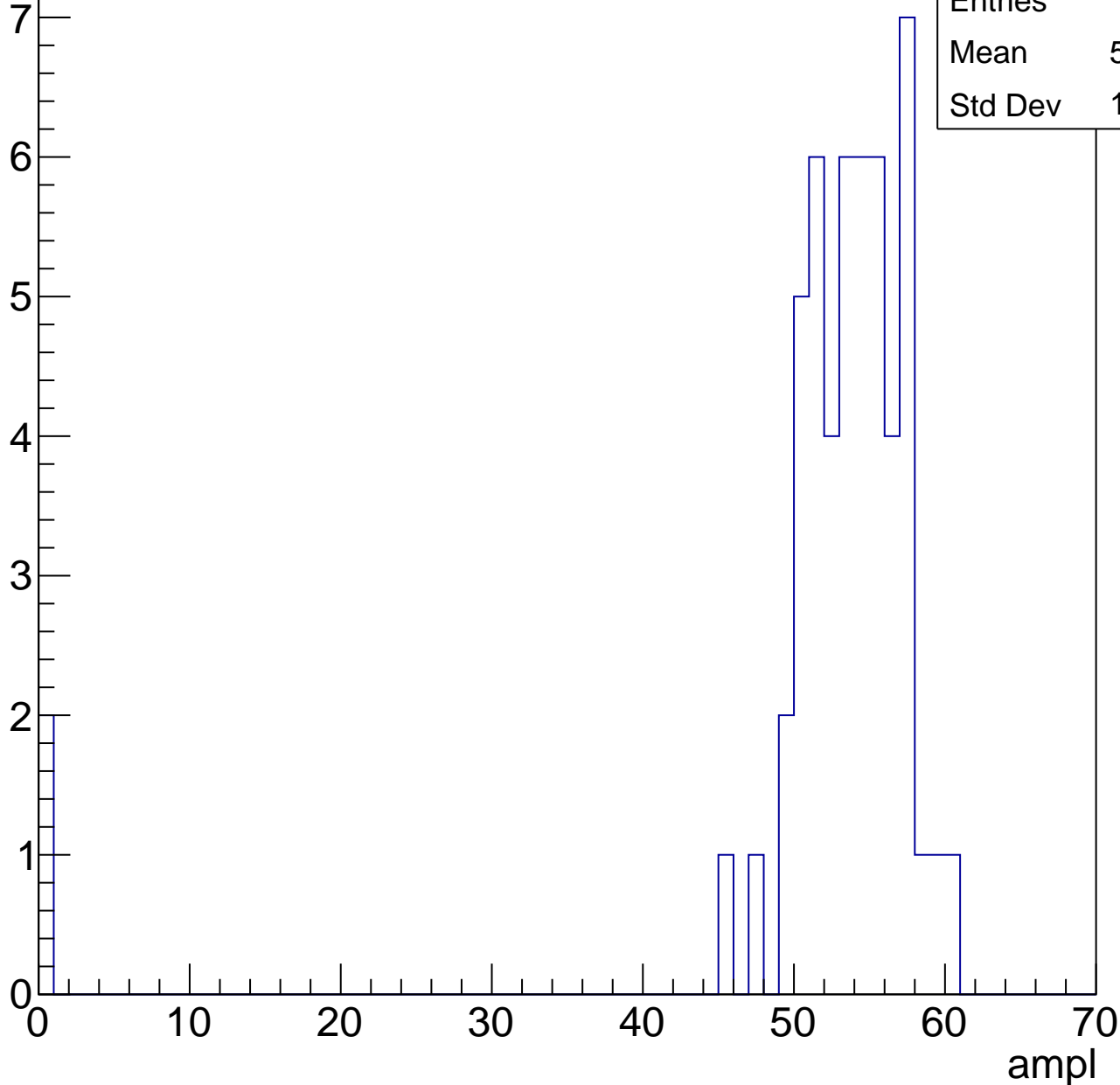


B1L103S, U1-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	51.43
Std Dev	10.62

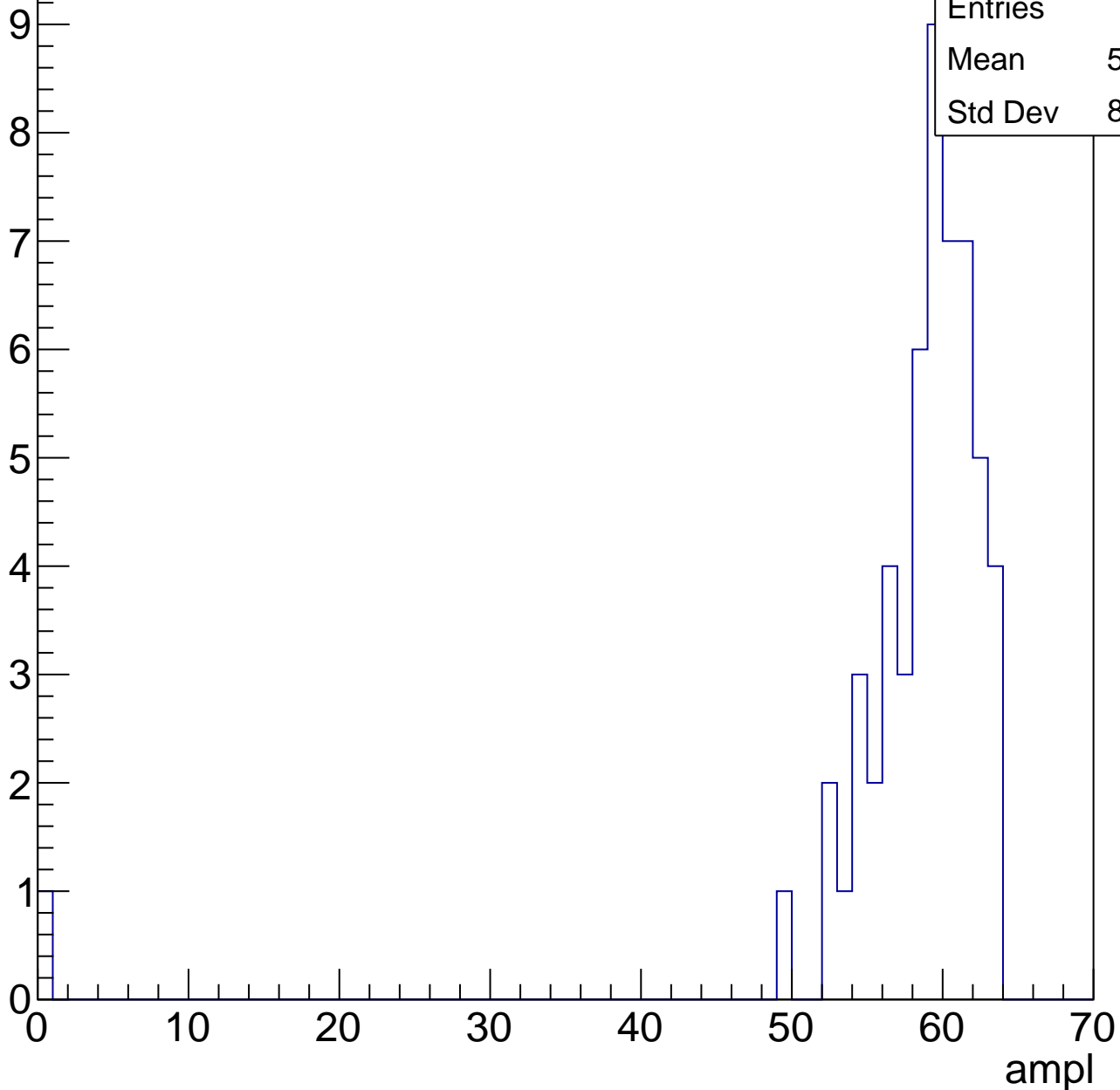


B1L103S, U1-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.47
Std Dev	8.412

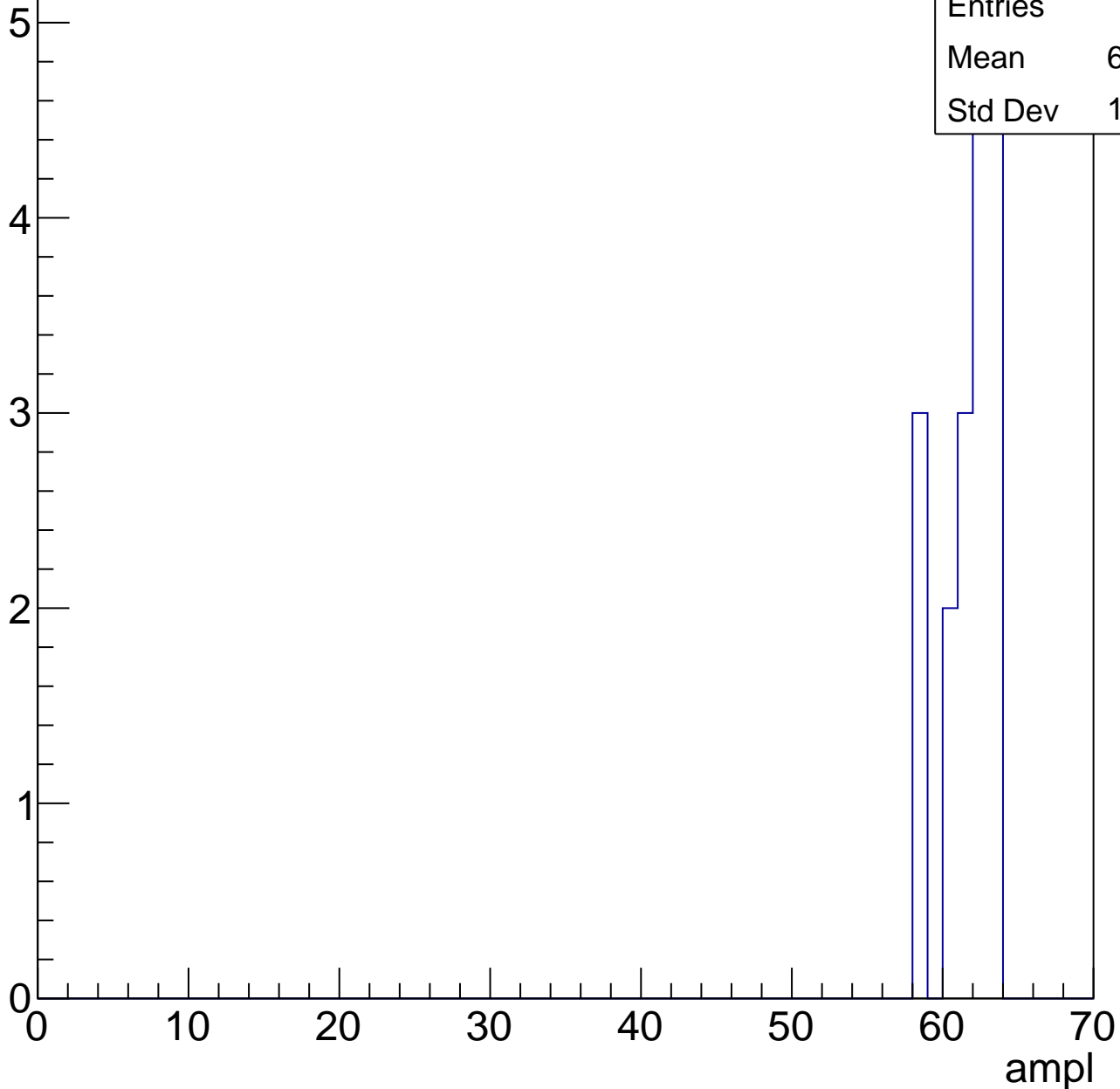


B1L103S, U1-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.22
Std Dev	1.718



B1L103S, U1-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U1-ch112, adc0

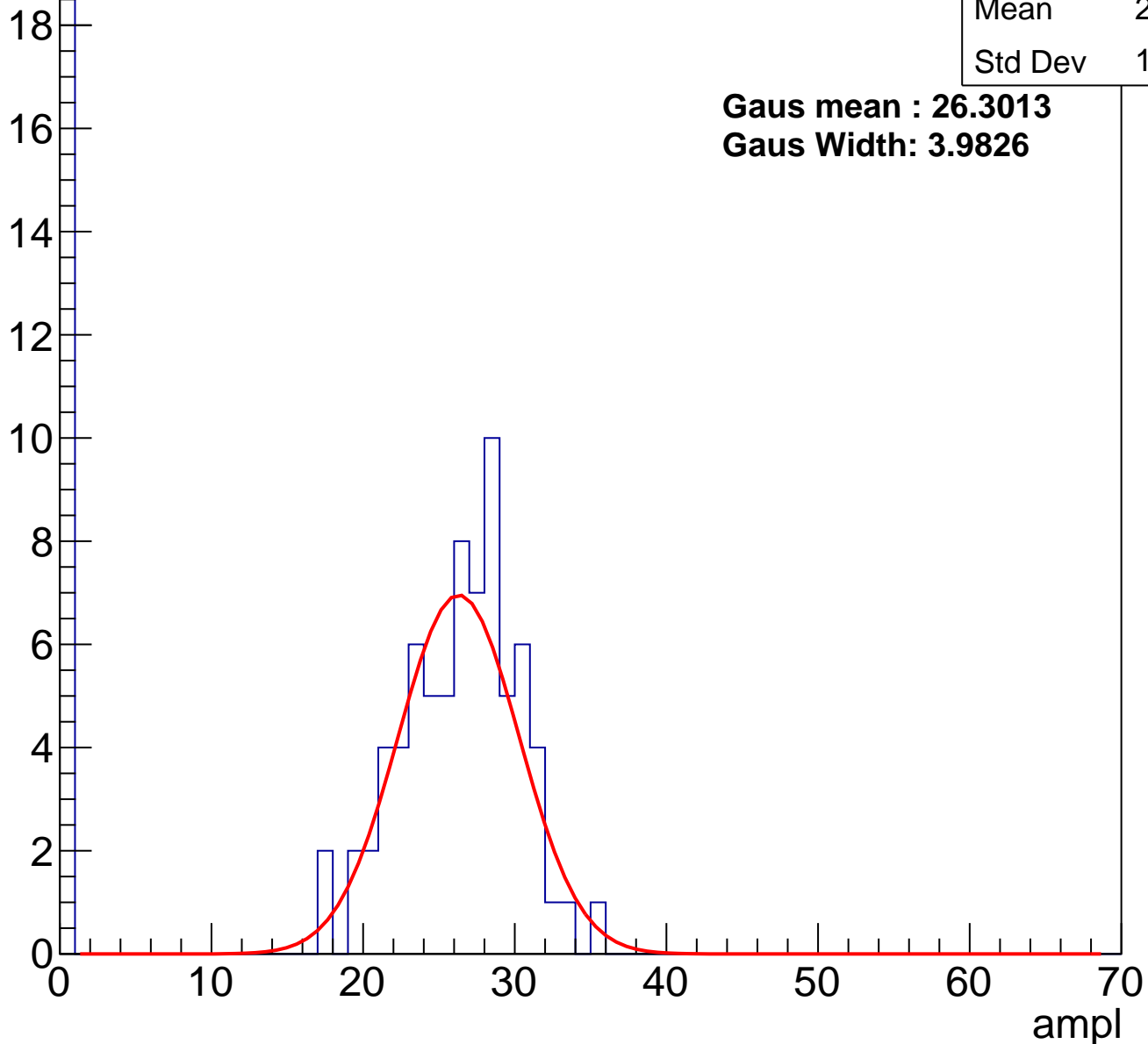
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	20.58
Std Dev	11.02

Gaus mean : 26.3013

Gaus Width: 3.9826

Entry



B1L103S, U1-ch112, adc1

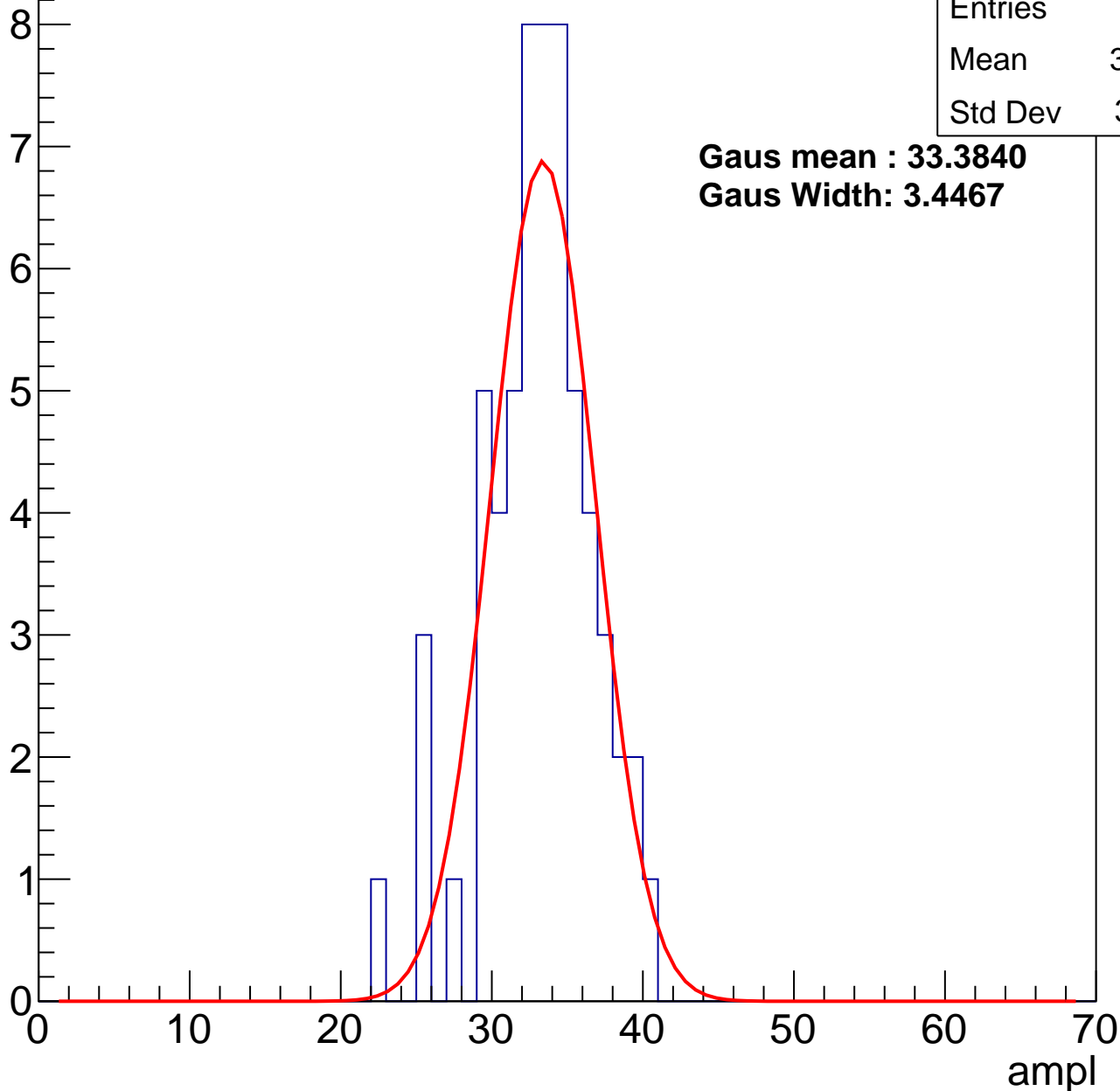
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	32.67
Std Dev	3.581

Gaus mean : 33.3840

Gaus Width: 3.4467



B1L103S, U1-ch112, adc2

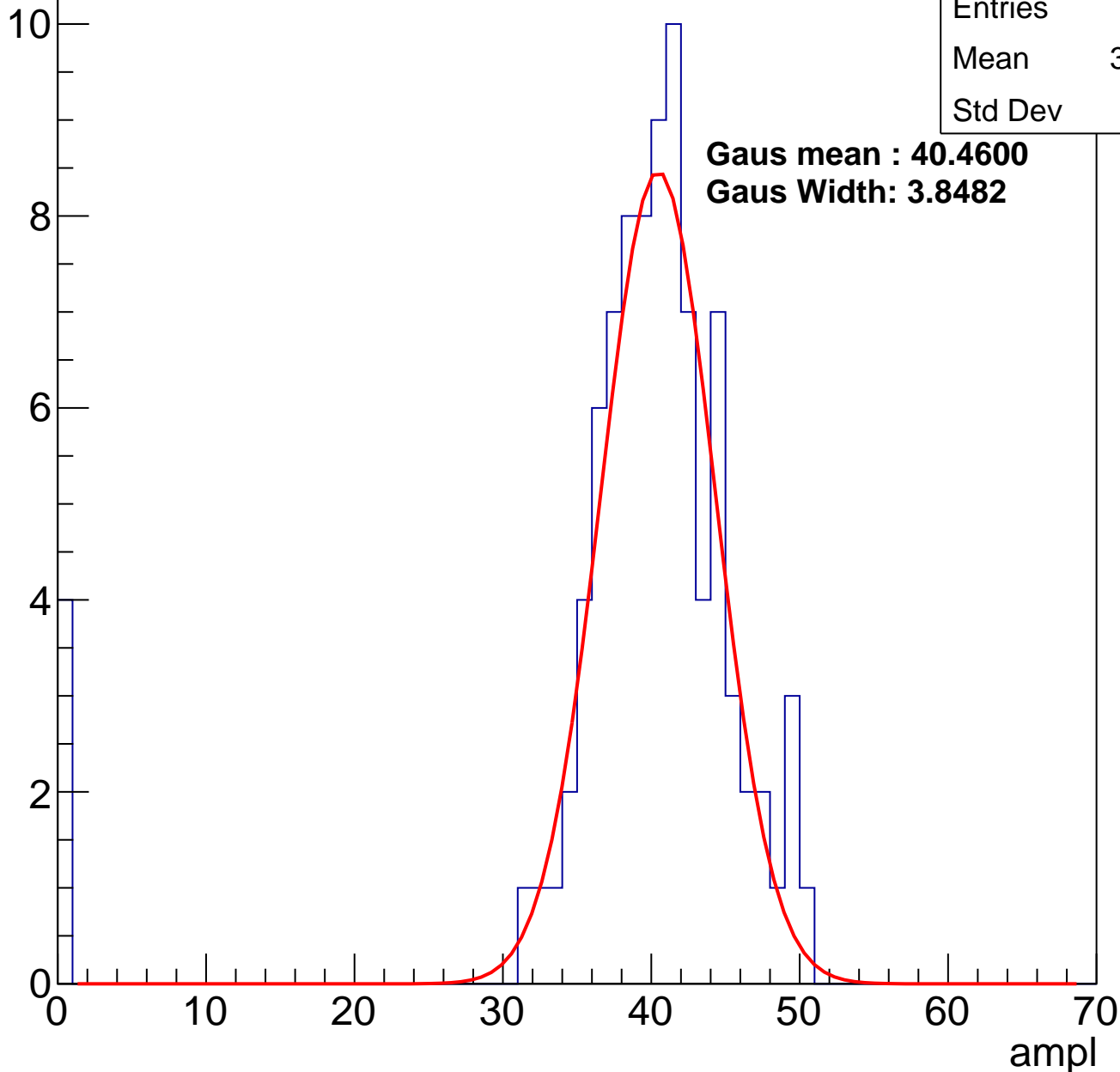
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	38.52
Std Dev	9.14

Gaus mean : 40.4600

Gaus Width: 3.8482

Entry



B1L103S, U1-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	62
Mean	38.6
Std Dev	18.16

Entry

10

8

6

4

2

0

0

10

20

30

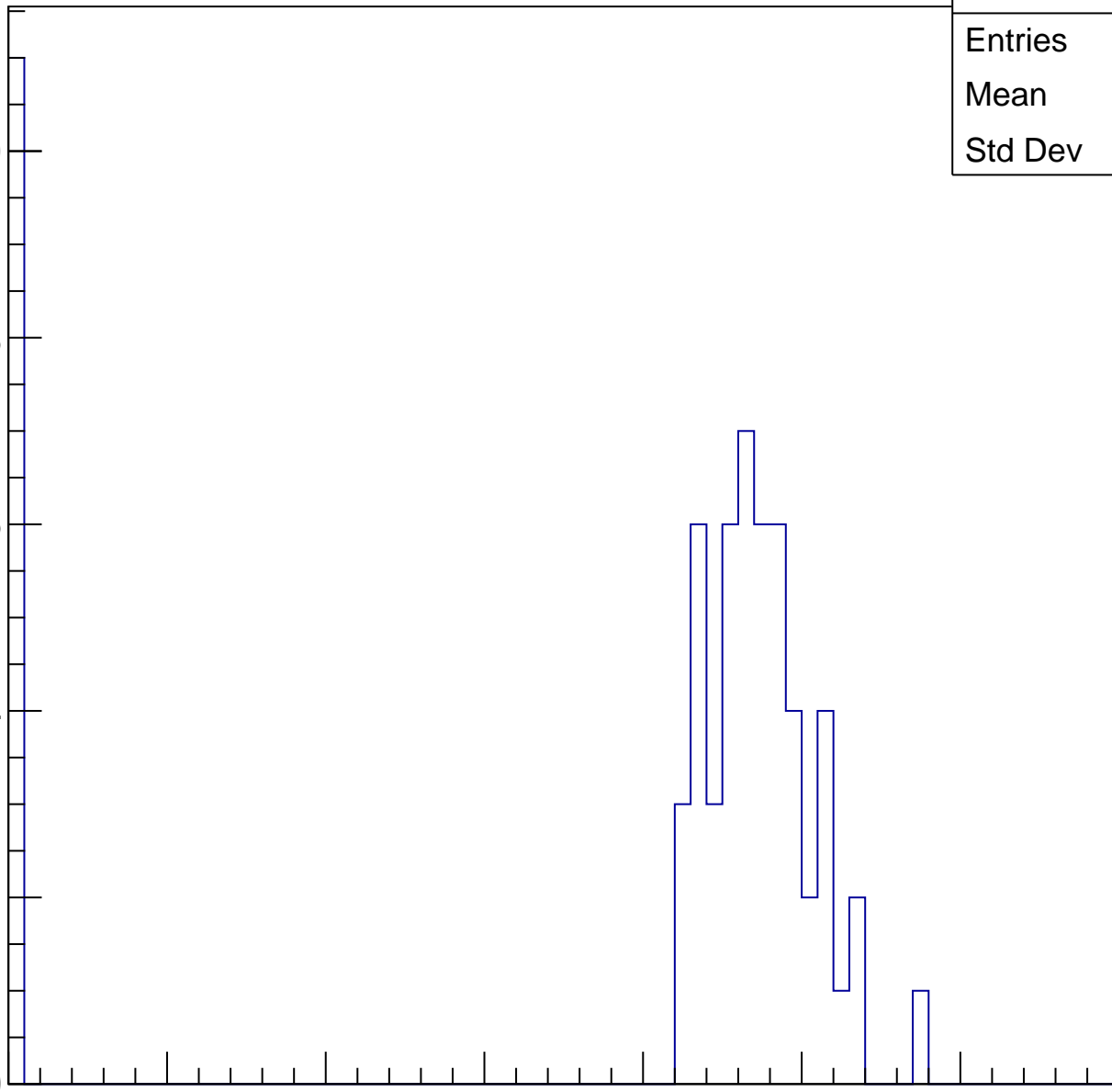
40

50

60

70

ampl

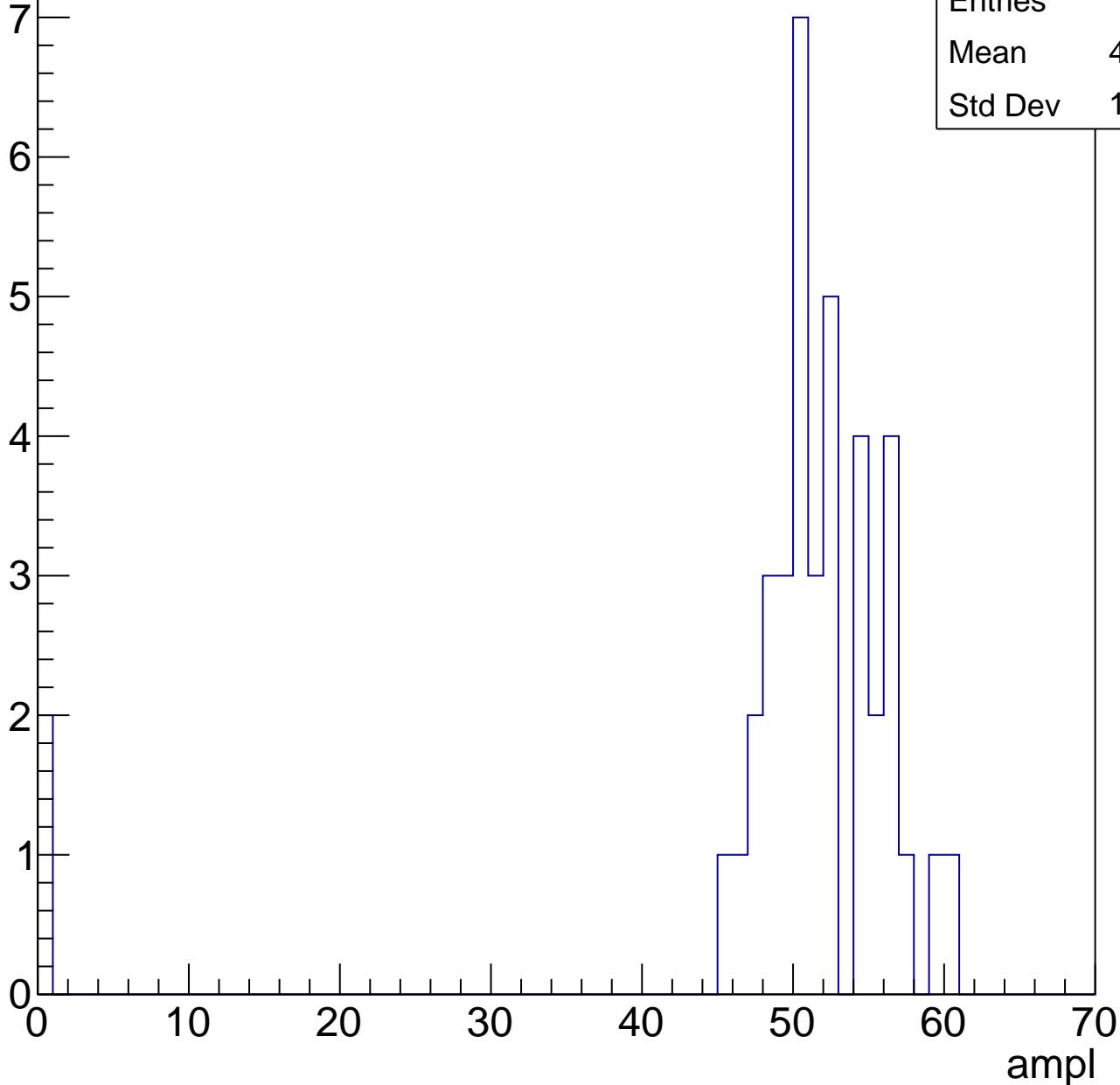


B1L103S, U1-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	49.12
Std Dev	11.78

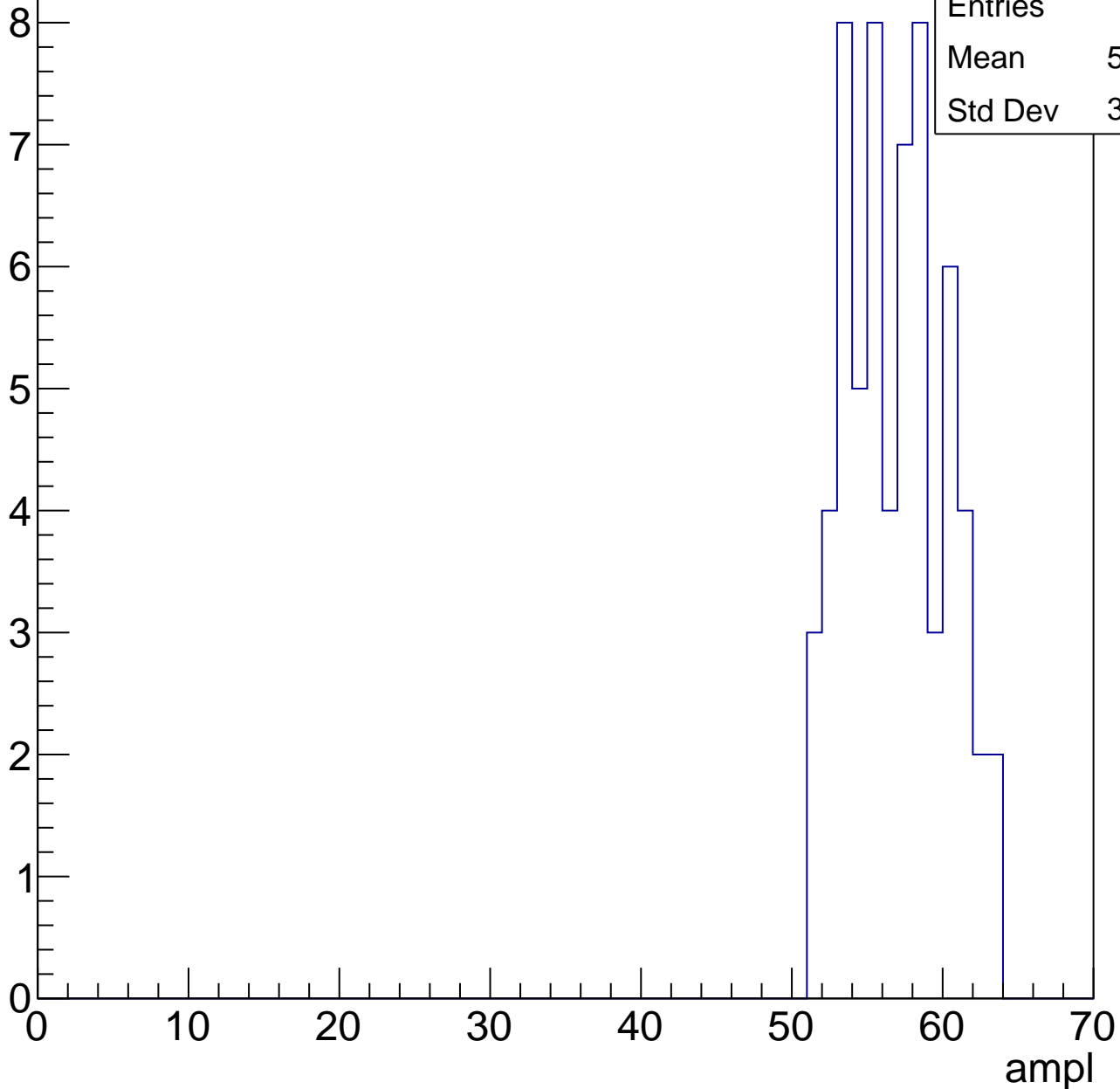


B1L103S, U1-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

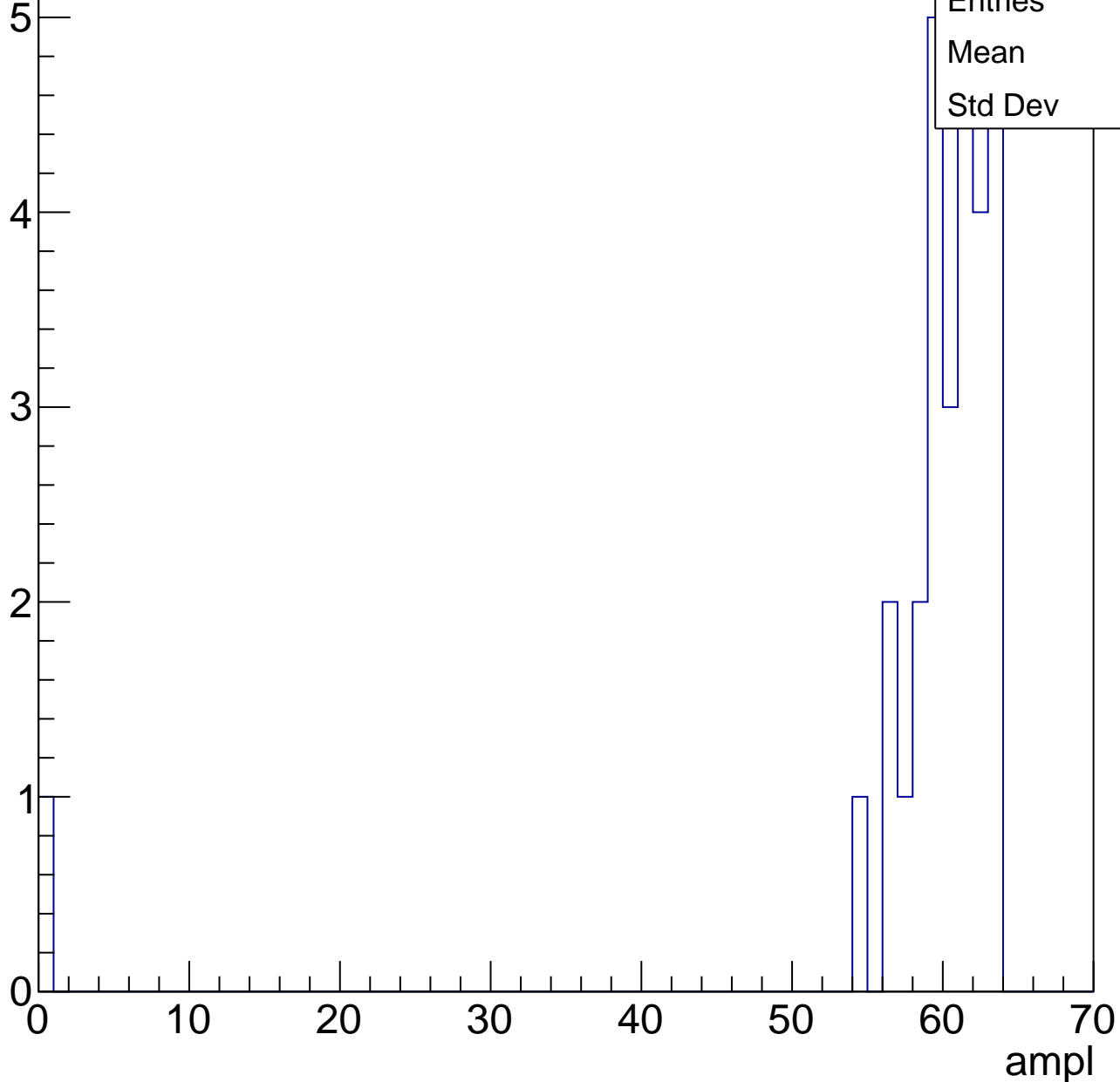
Entries	64
Mean	56.45
Std Dev	3.206



B1L103S, U1-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

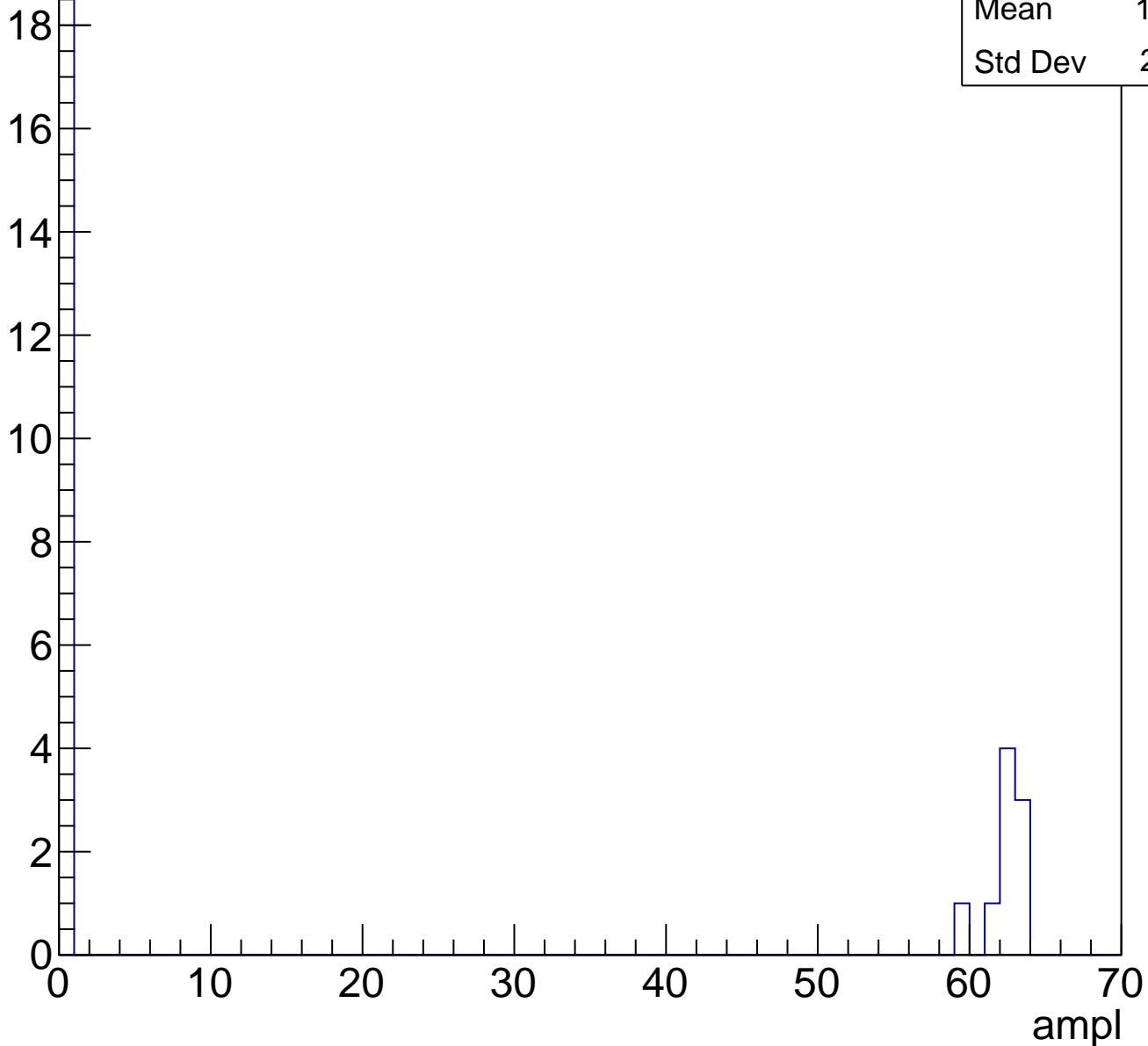


B1L103S, U1-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	28
Mean	19.89
Std Dev	28.91

Entry



B1L103S, U1-ch113, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	22.9
Std Dev	9.825

Gaus mean : 27.1480

Gaus Width: 4.0760

Entry

10

8

6

4

2

0

0

10

20

30

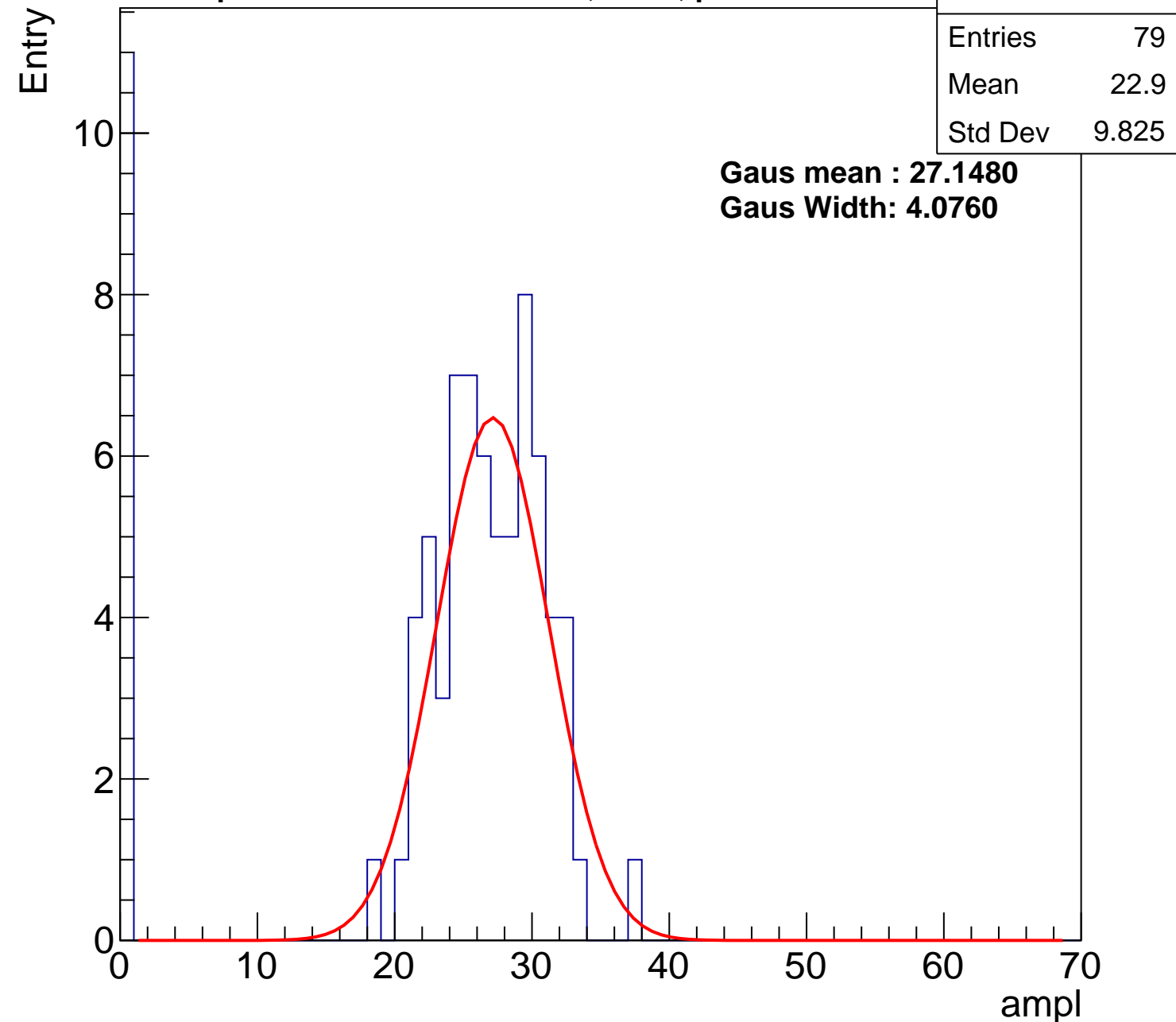
40

50

60

70

ampl

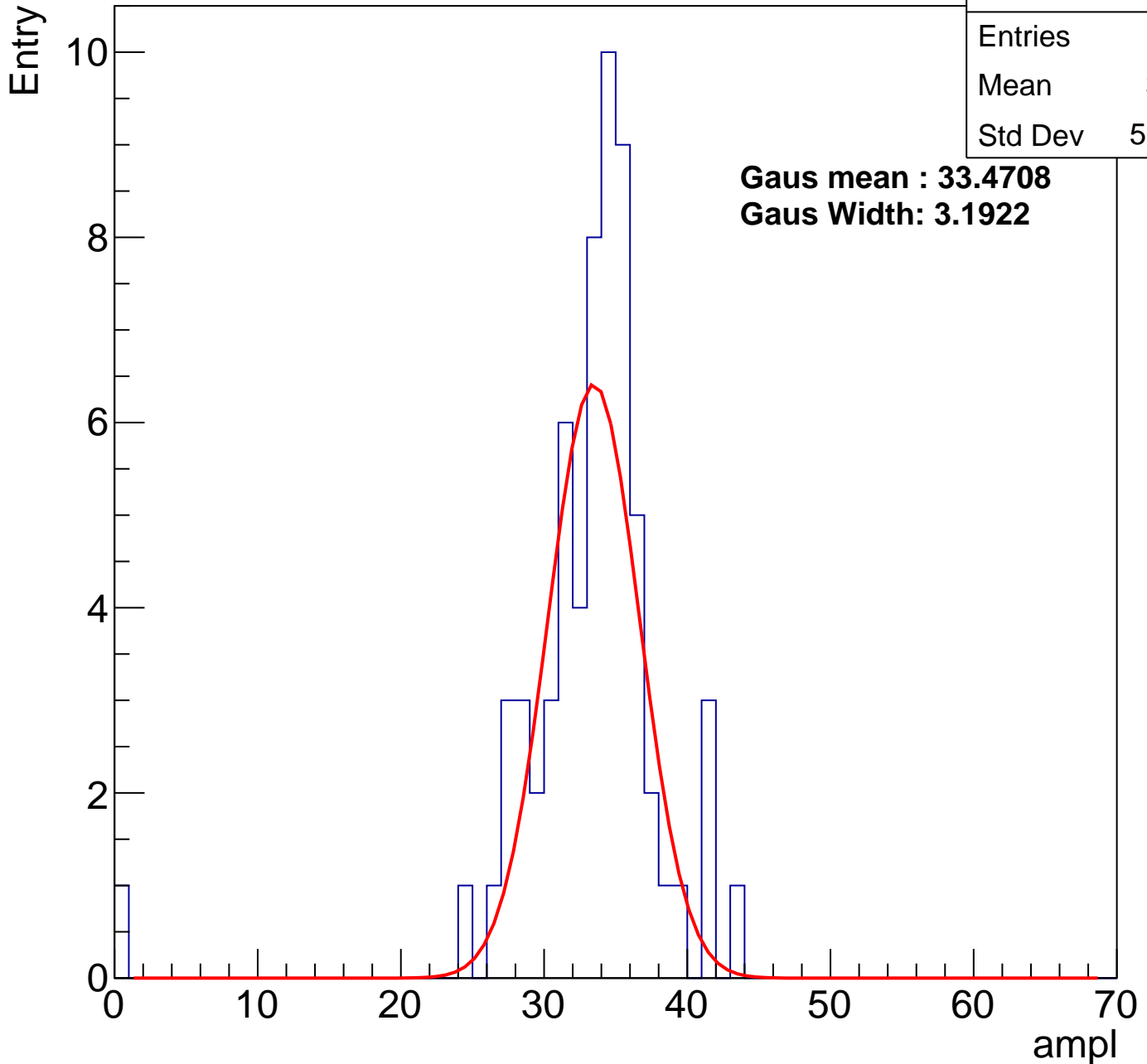


B1L103S, U1-ch113, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	32.7
Std Dev	5.533

Gaus mean : 33.4708
Gaus Width: 3.1922



B1L103S, U1-ch113, adc2

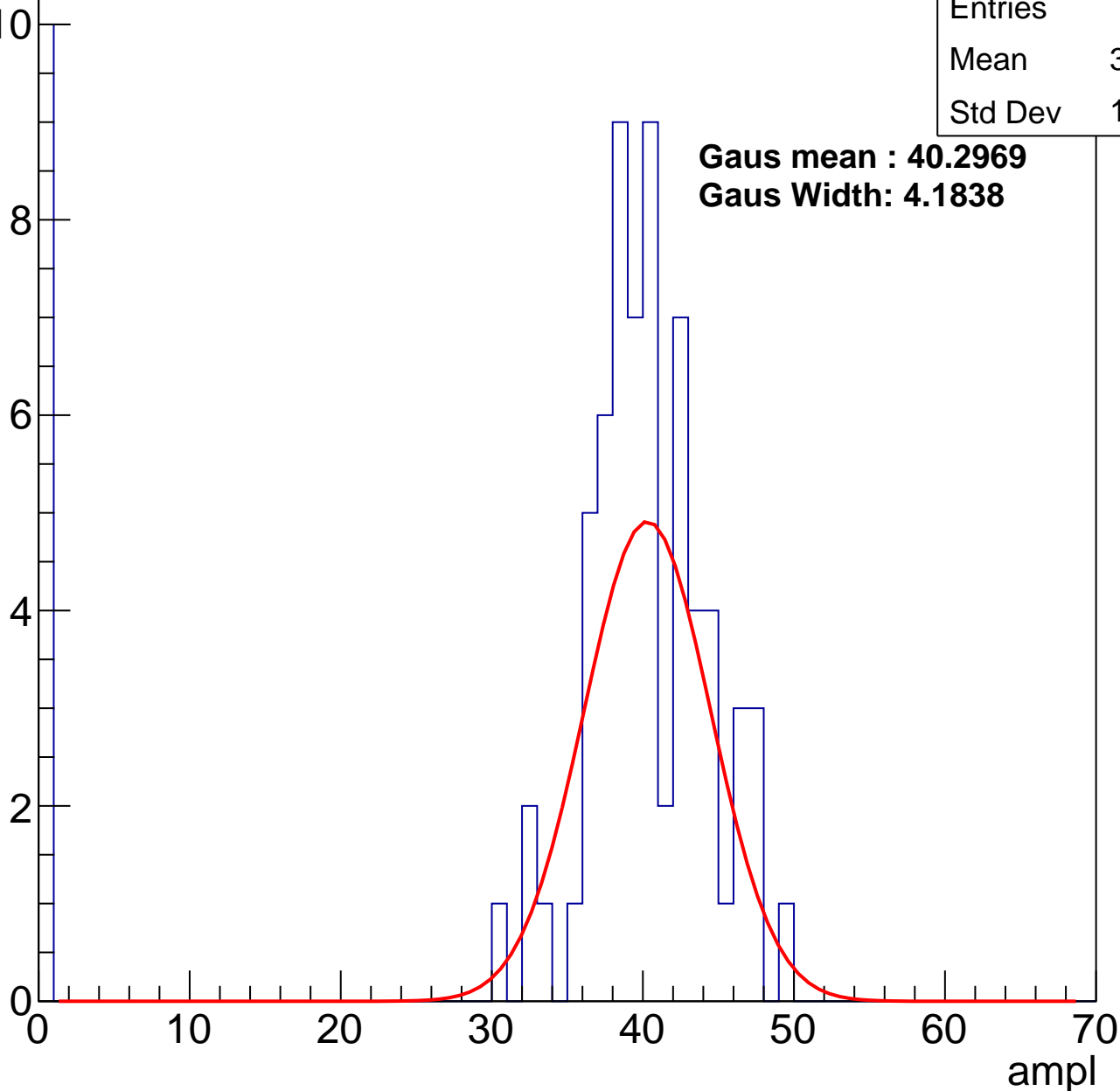
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.68
Std Dev	13.97

Gaus mean : 40.2969

Gaus Width: 4.1838

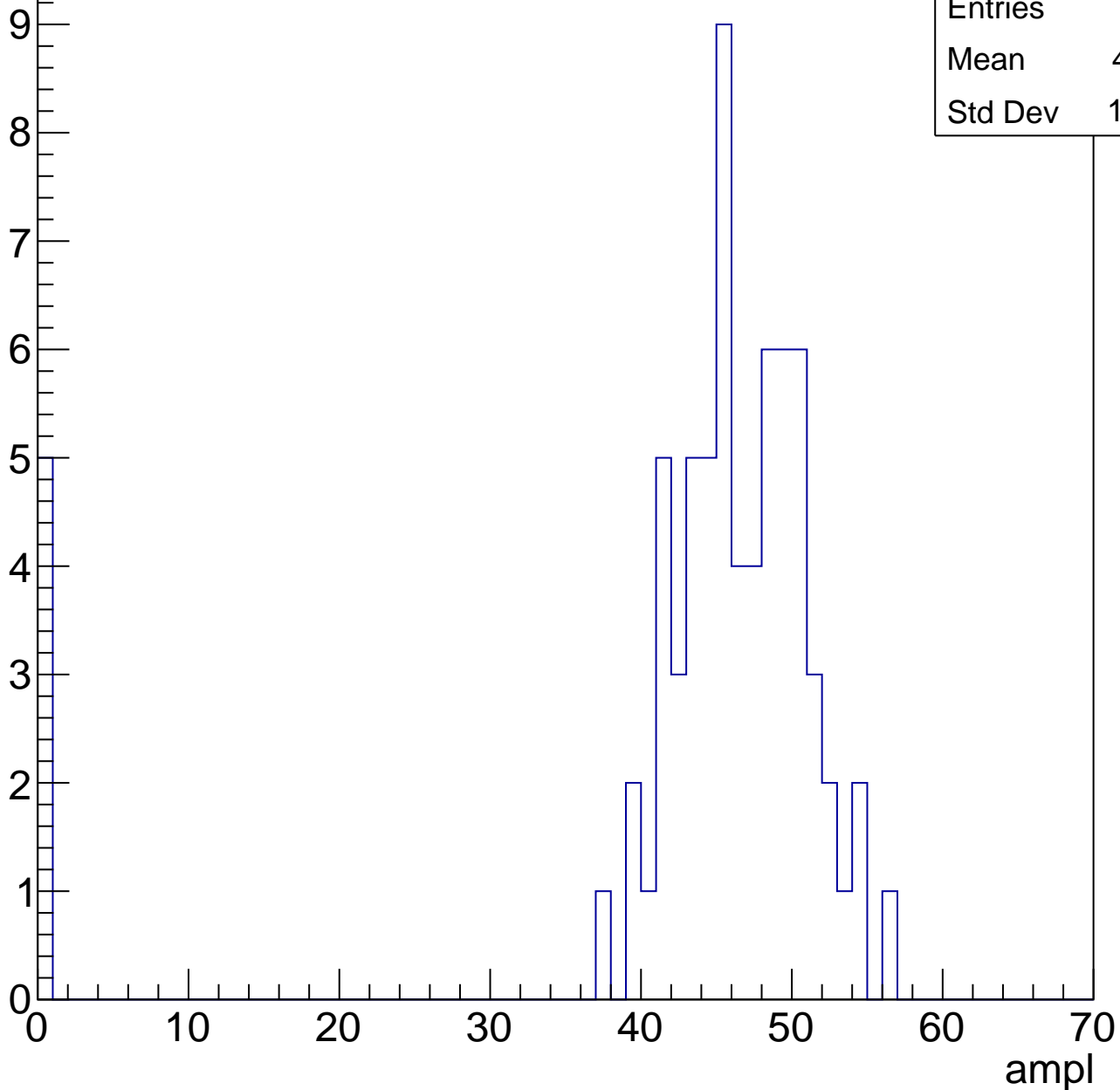


B1L103S, U1-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.01
Std Dev	12.45

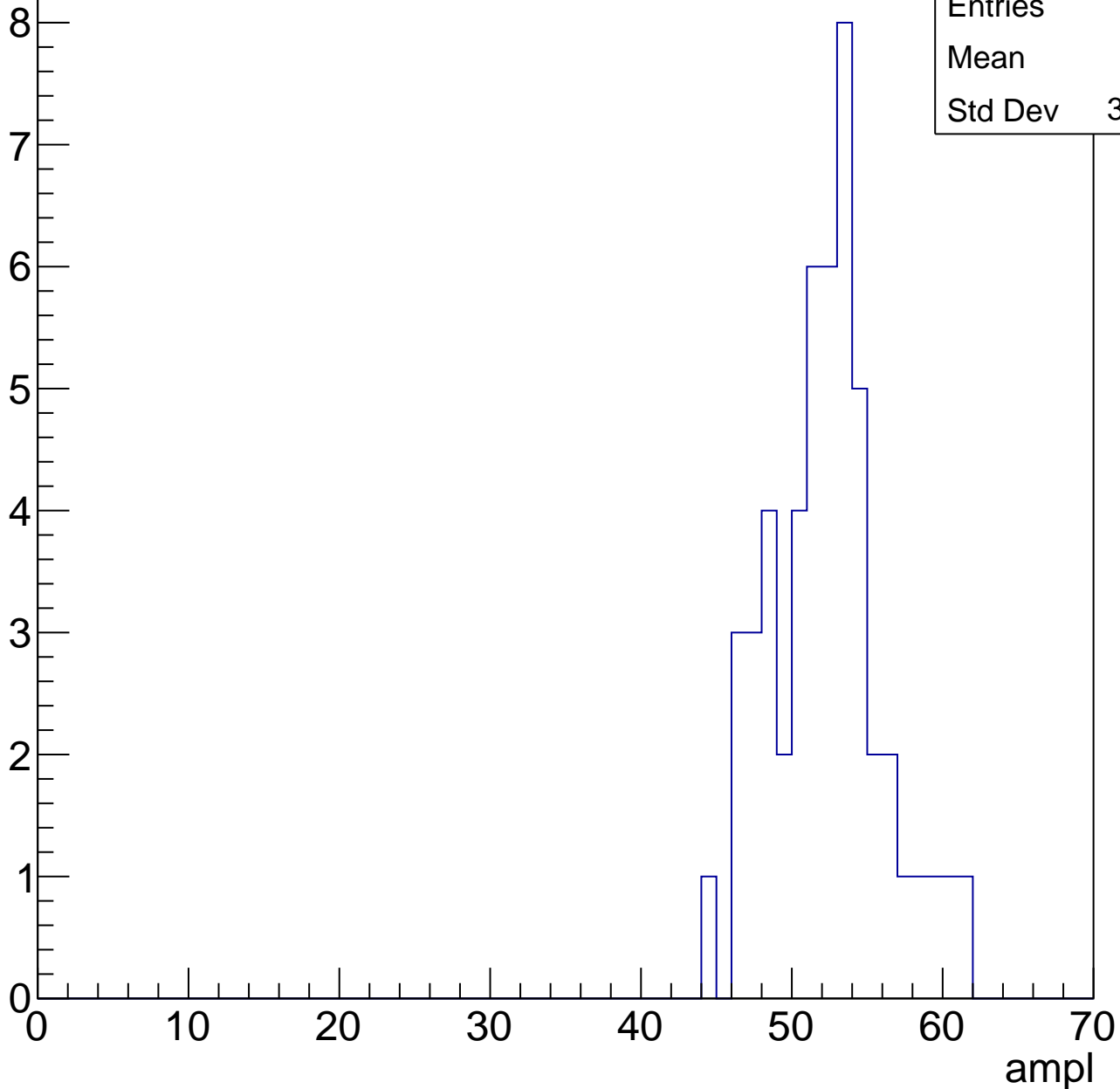


B1L103S, U1-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	51.8
Std Dev	3.657

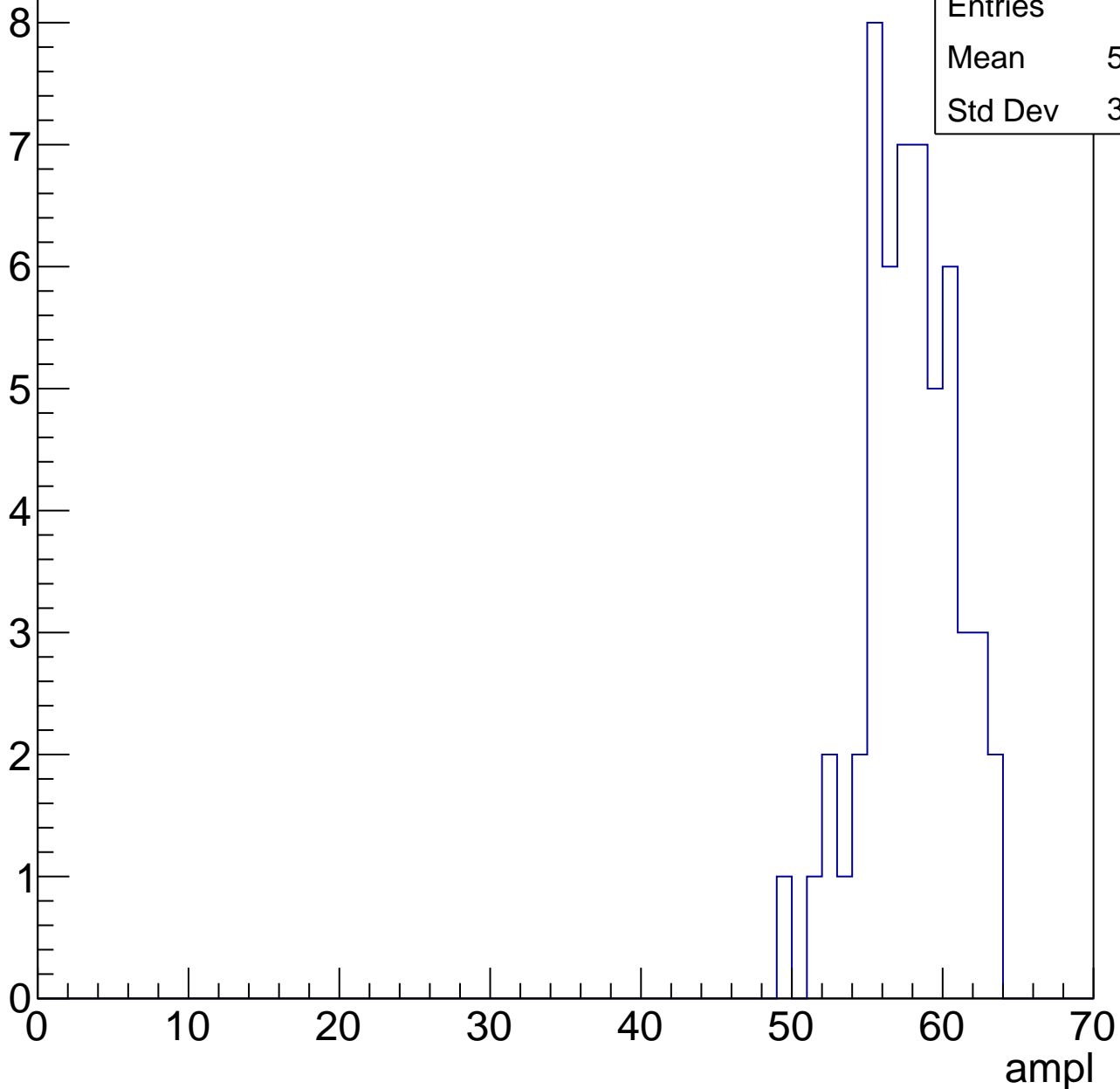


B1L103S, U1-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.33
Std Dev	3.018

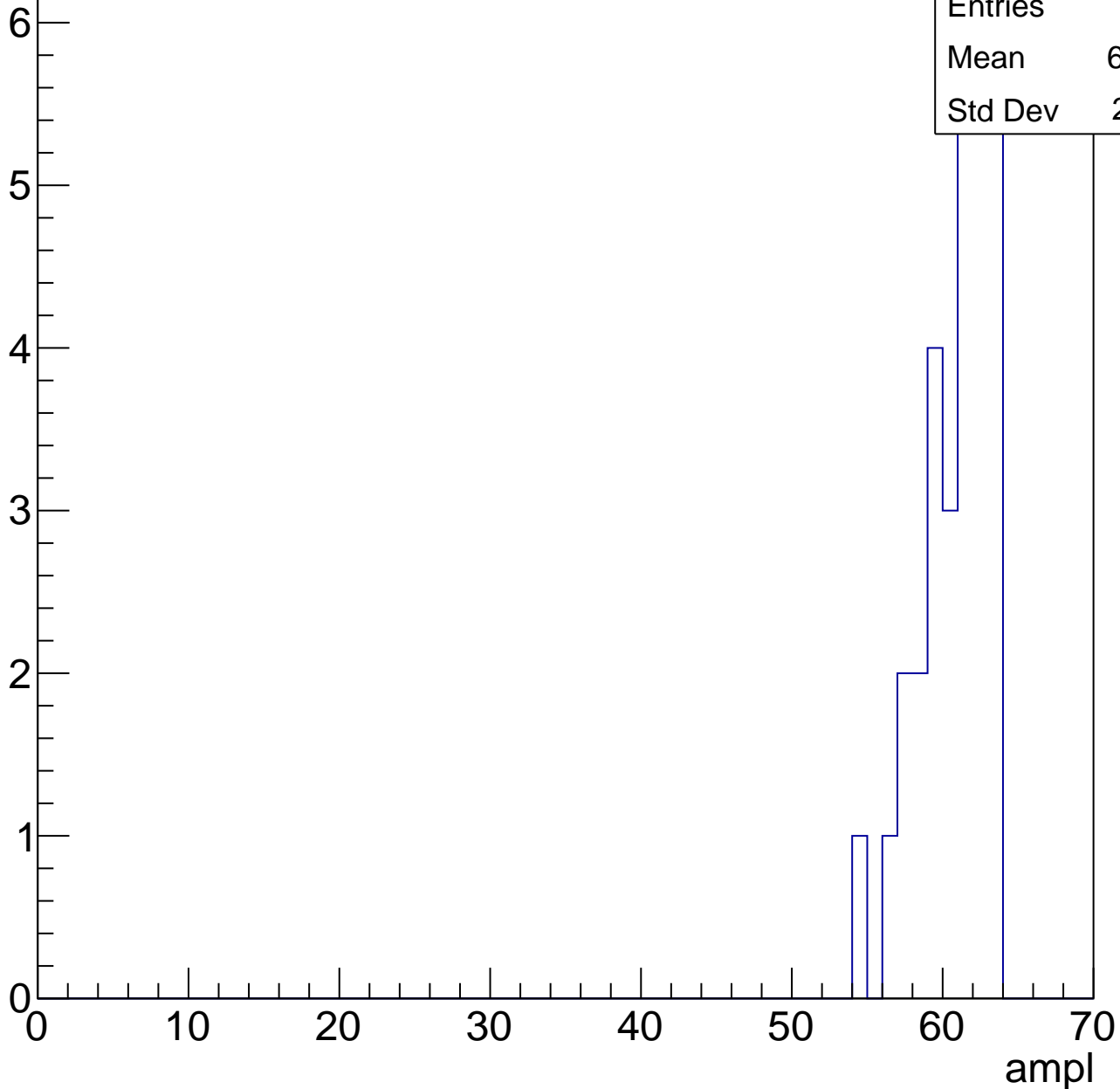


B1L103S, U1-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	60.39
Std Dev	2.281

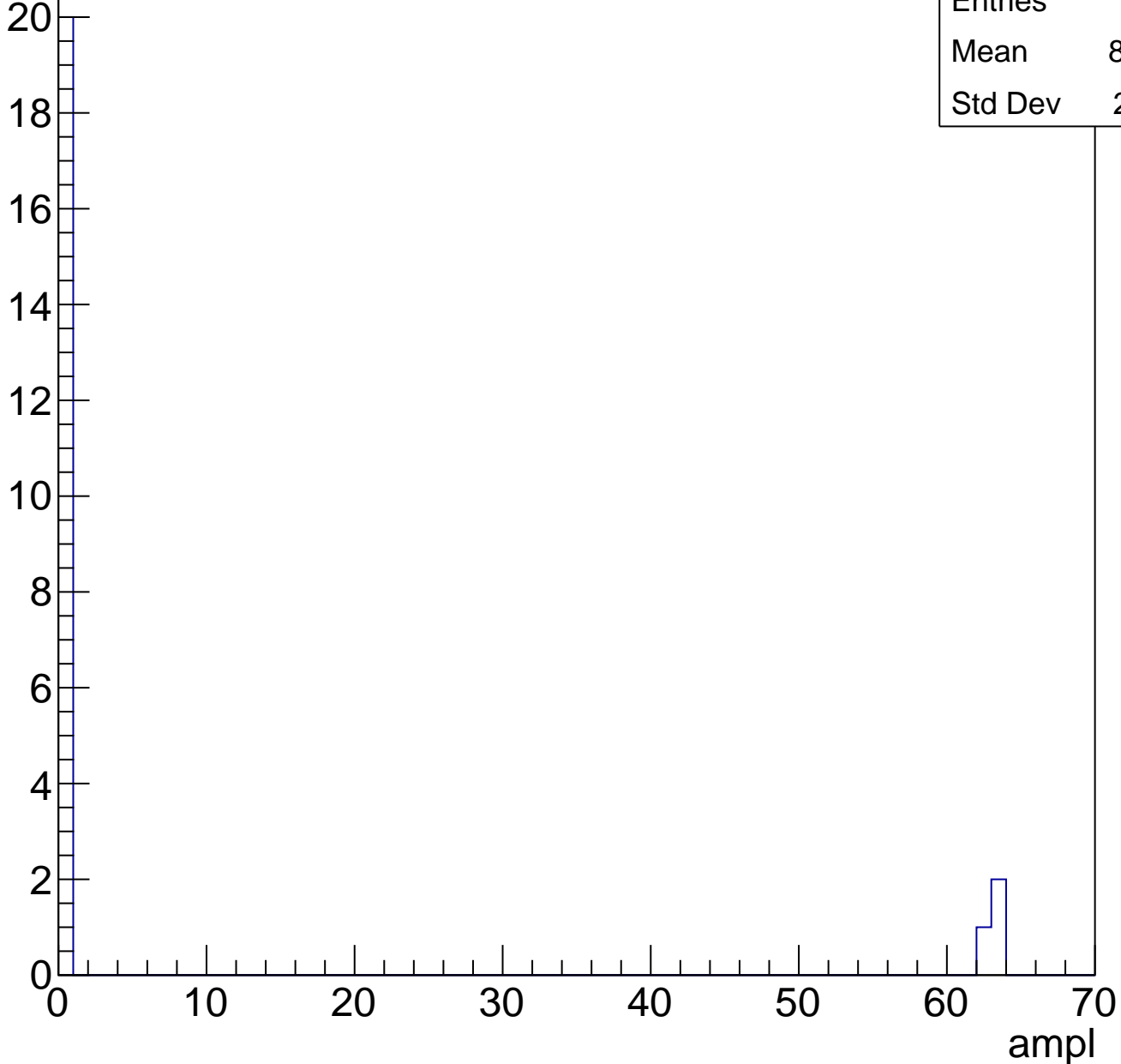


B1L103S, U1-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.174
Std Dev	21.11

Entry



B1L103S, U1-ch114, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	17.63
Std Dev	12.53

Gaus mean : 26.9238

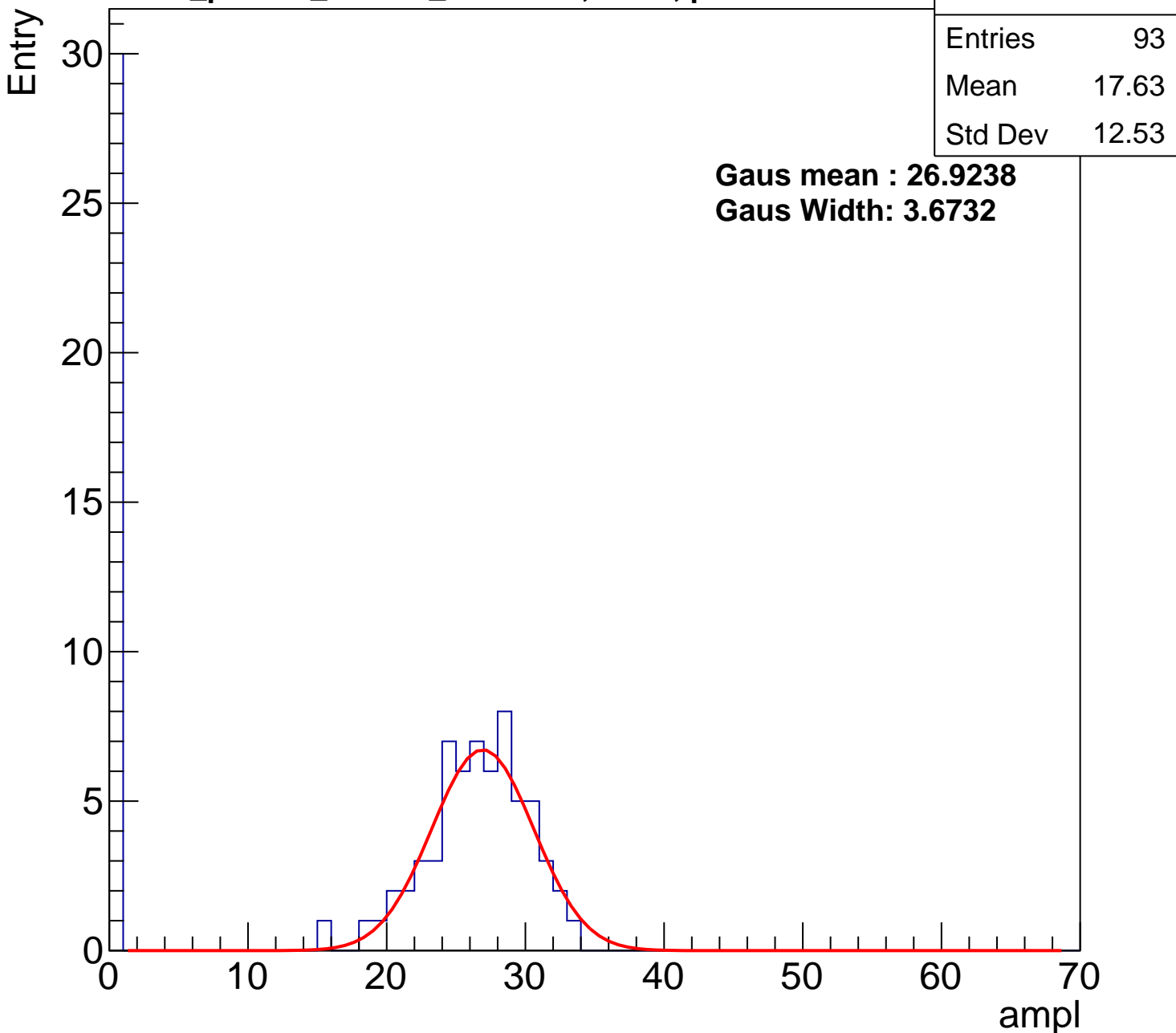
Gaus Width: 3.6732

Entry

30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch114, adc1

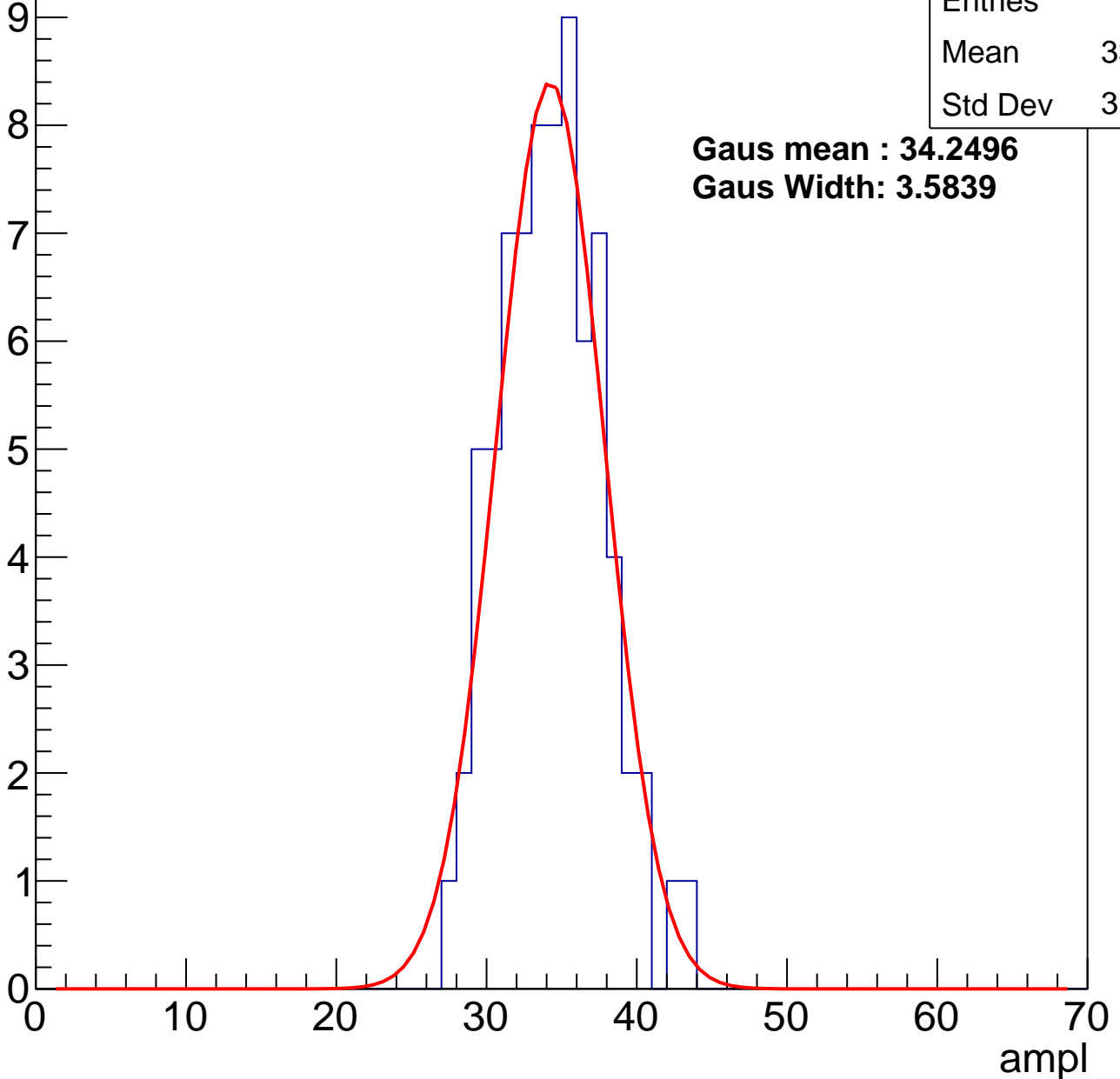
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.87
Std Dev	3.368

Gaus mean : 34.2496

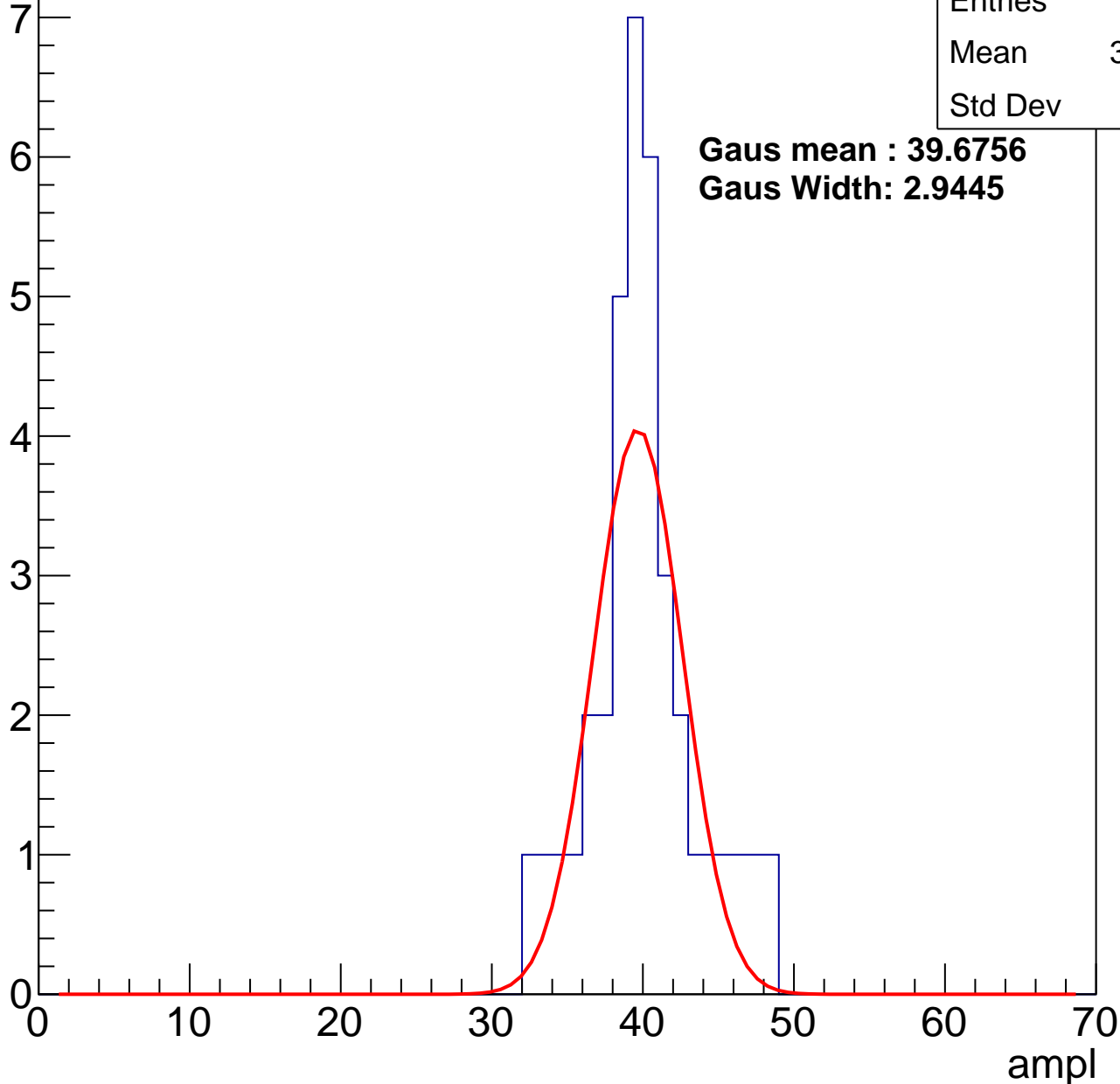
Gaus Width: 3.5839



B1L103S, U1-ch114, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

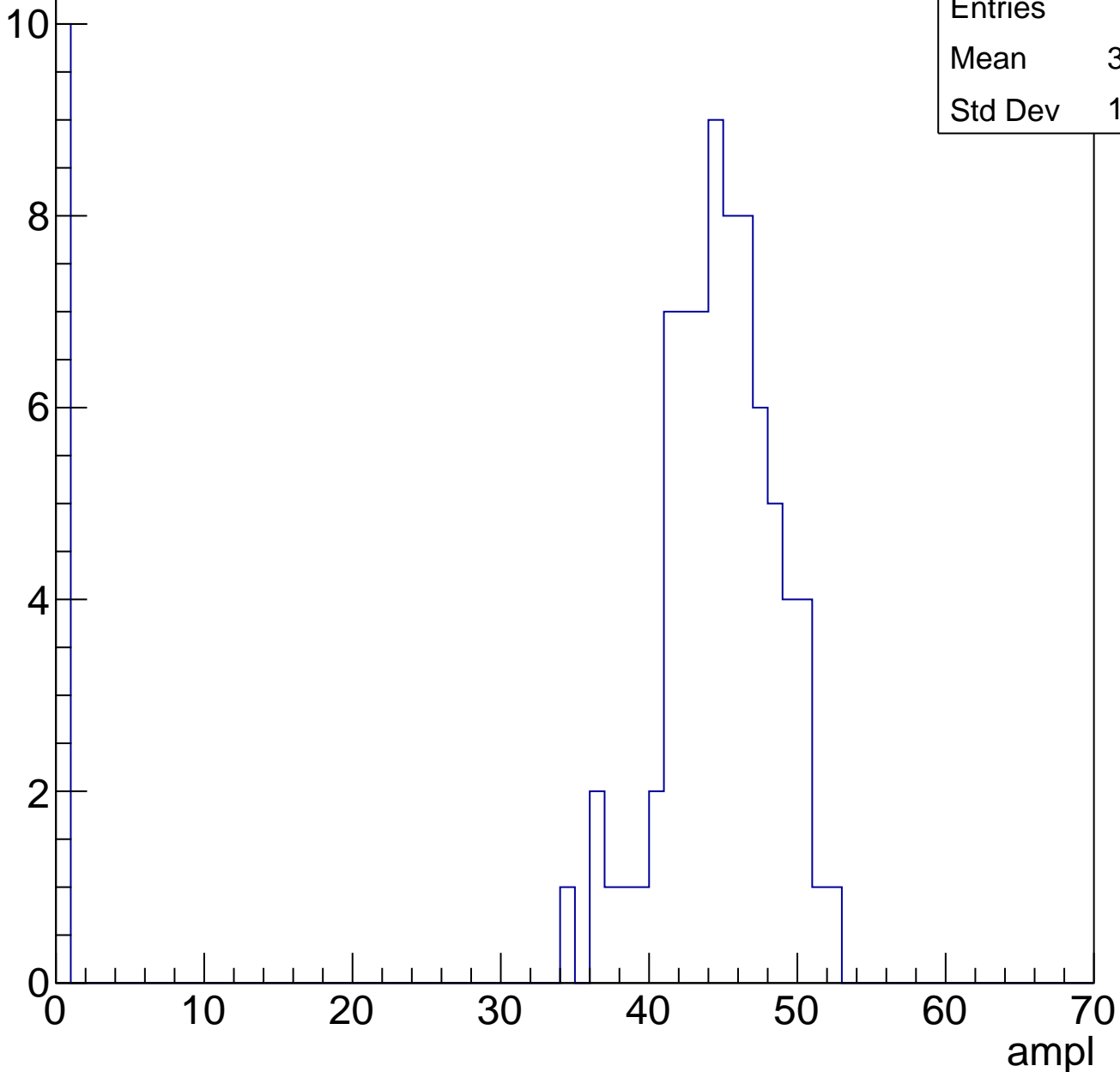


B1L103S, U1-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	39.14
Std Dev	14.69

Entry

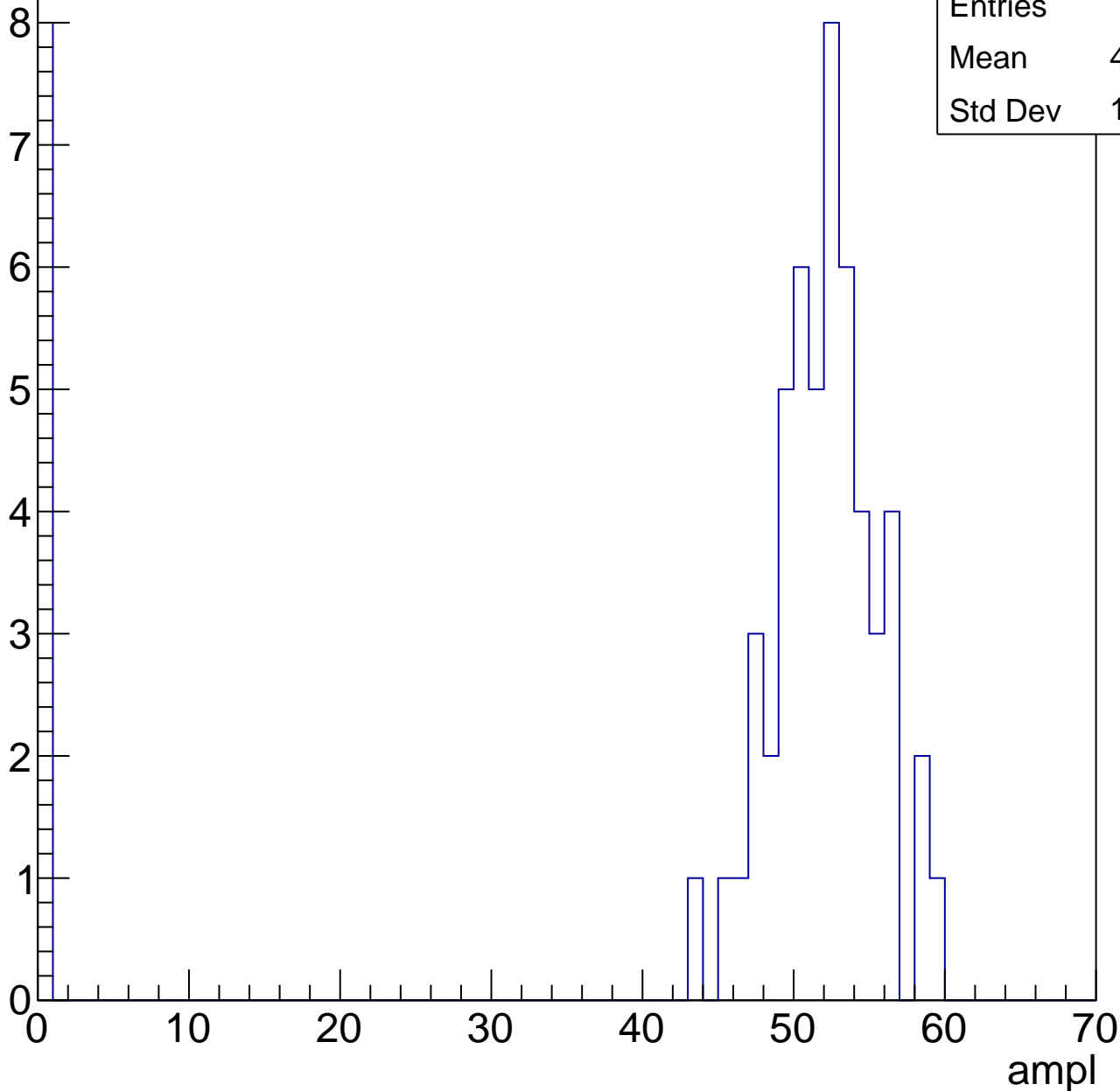


B1L103S, U1-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	44.75
Std Dev	17.82

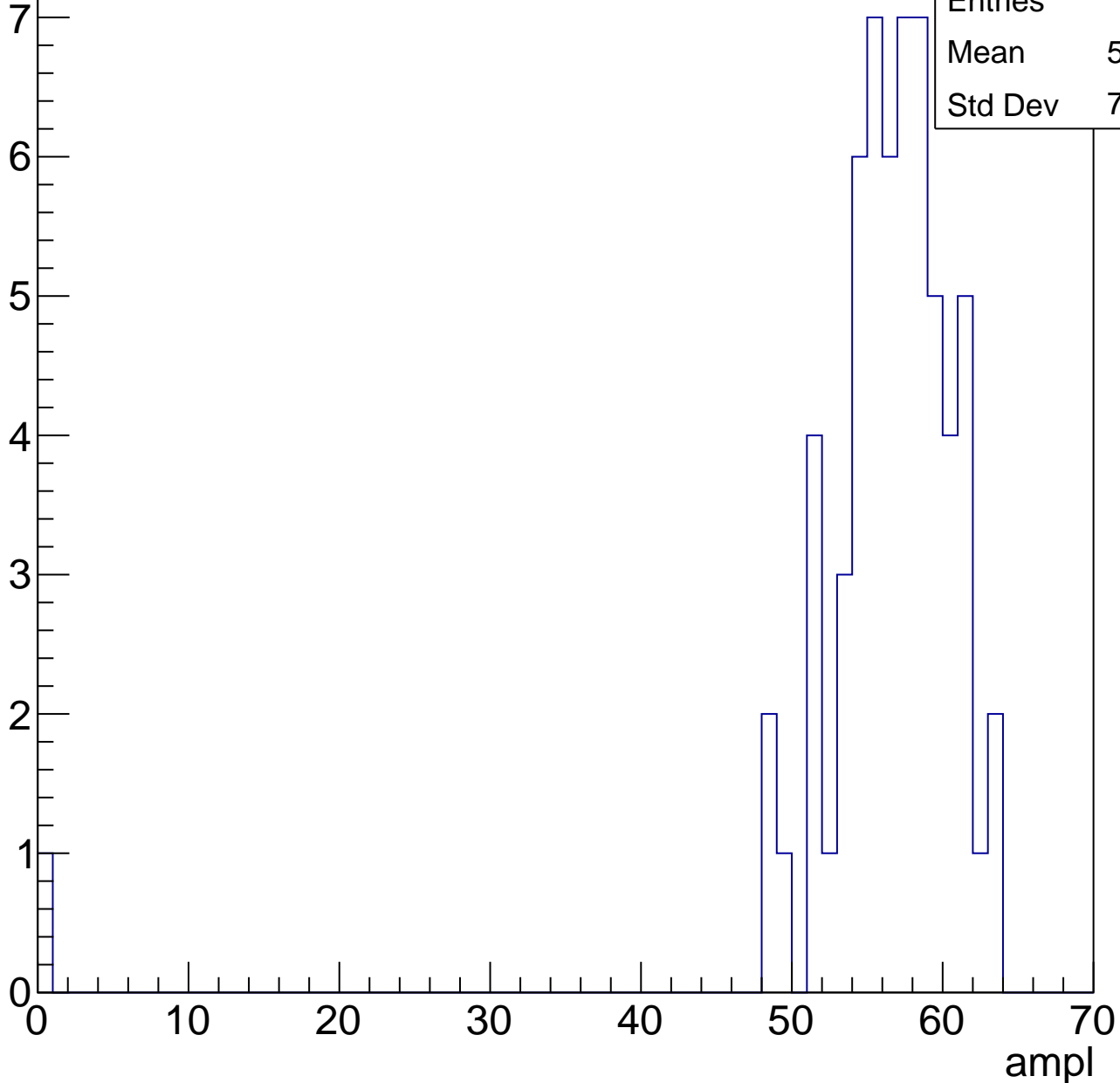


B1L103S, U1-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	55.45
Std Dev	7.902

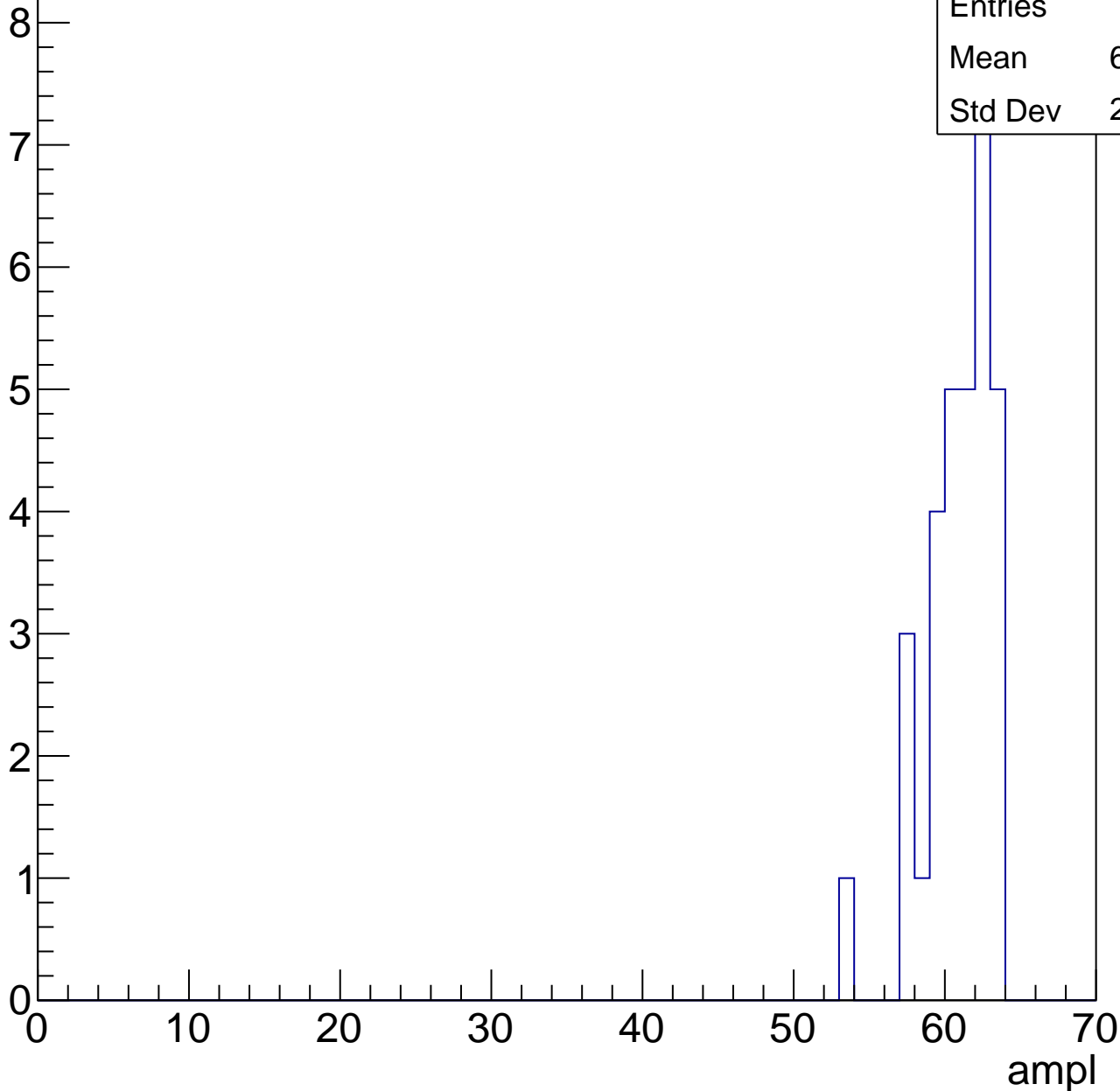


B1L103S, U1-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	60.44
Std Dev	2.235

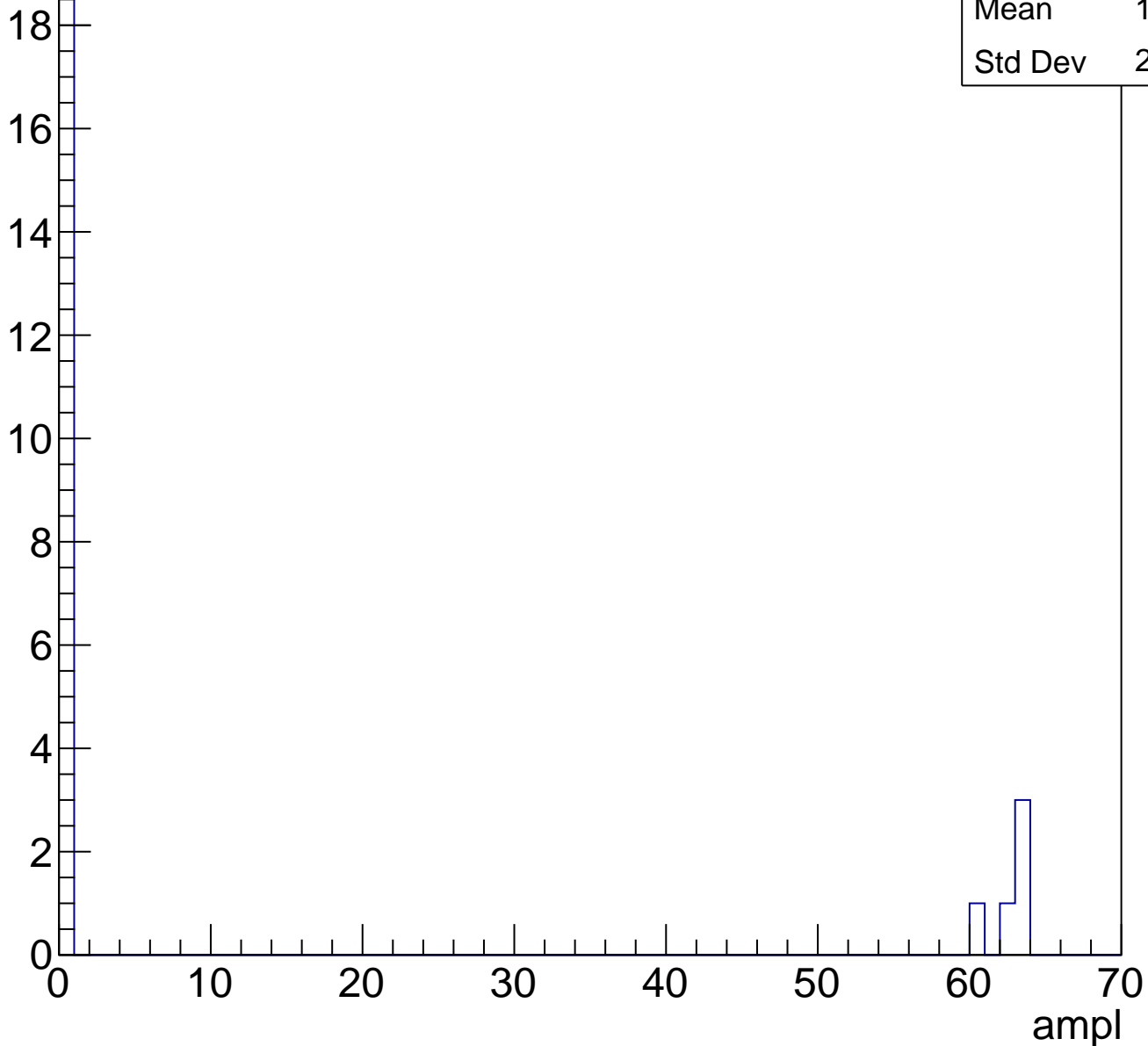


B1L103S, U1-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.96
Std Dev	25.27

Entry



B1L103S, U1-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	111
Mean	18.66
Std Dev	11.86

Gaus mean : 26.2850

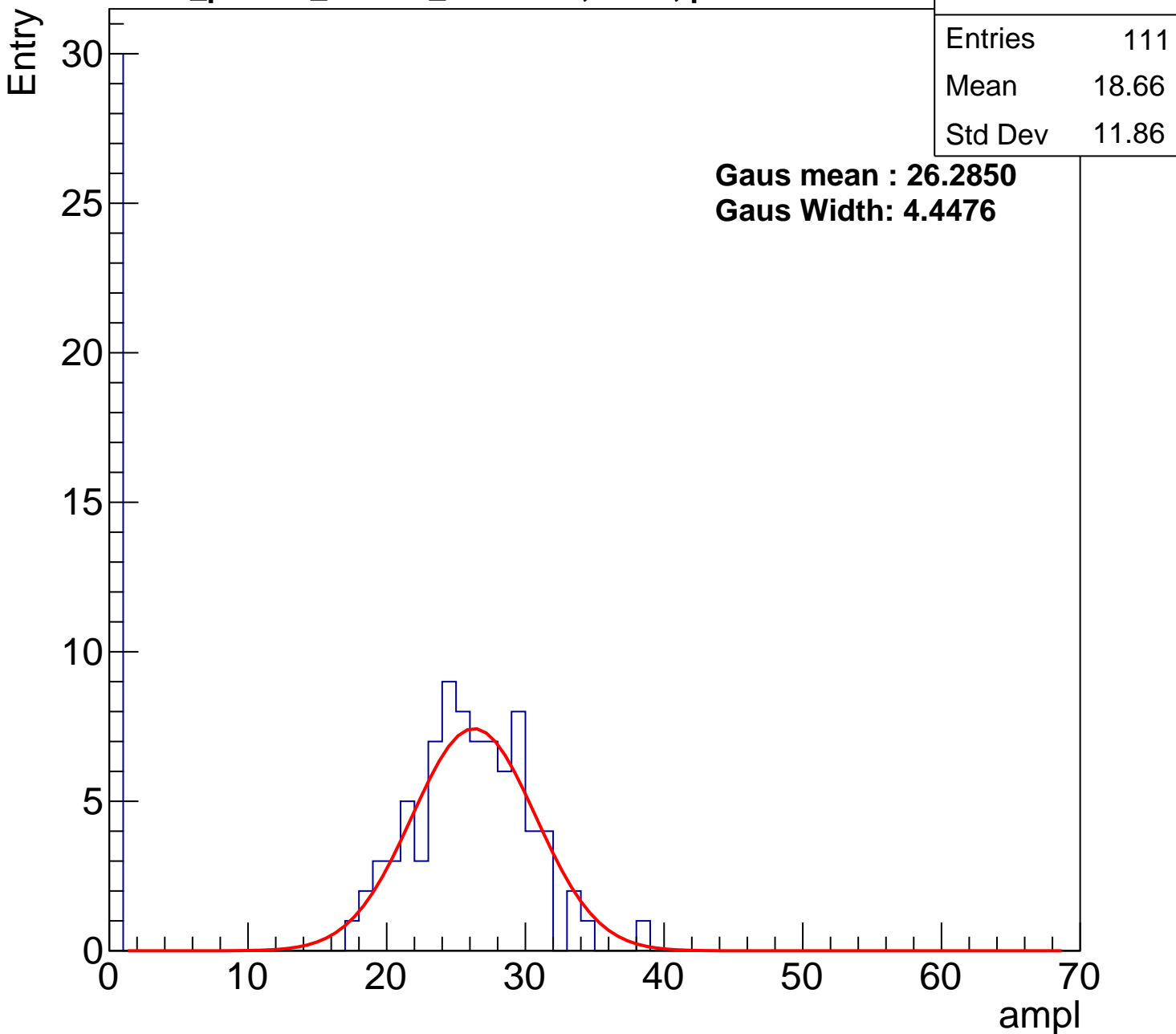
Gaus Width: 4.4476

Entry

30
25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch115, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	32.78
Std Dev	3.526

Gaus mean : 33.5007

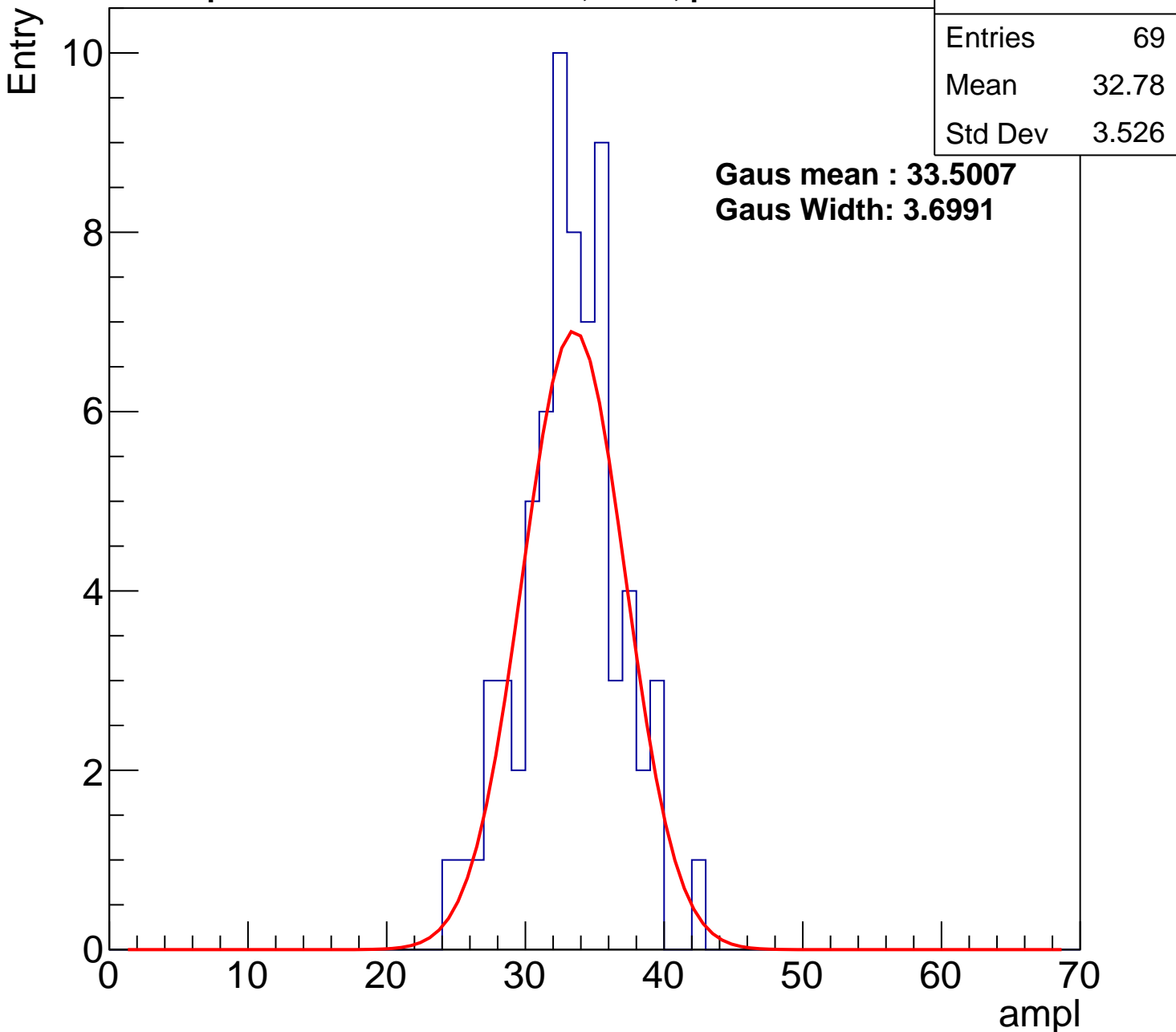
Gaus Width: 3.6991

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U1-ch115, adc2

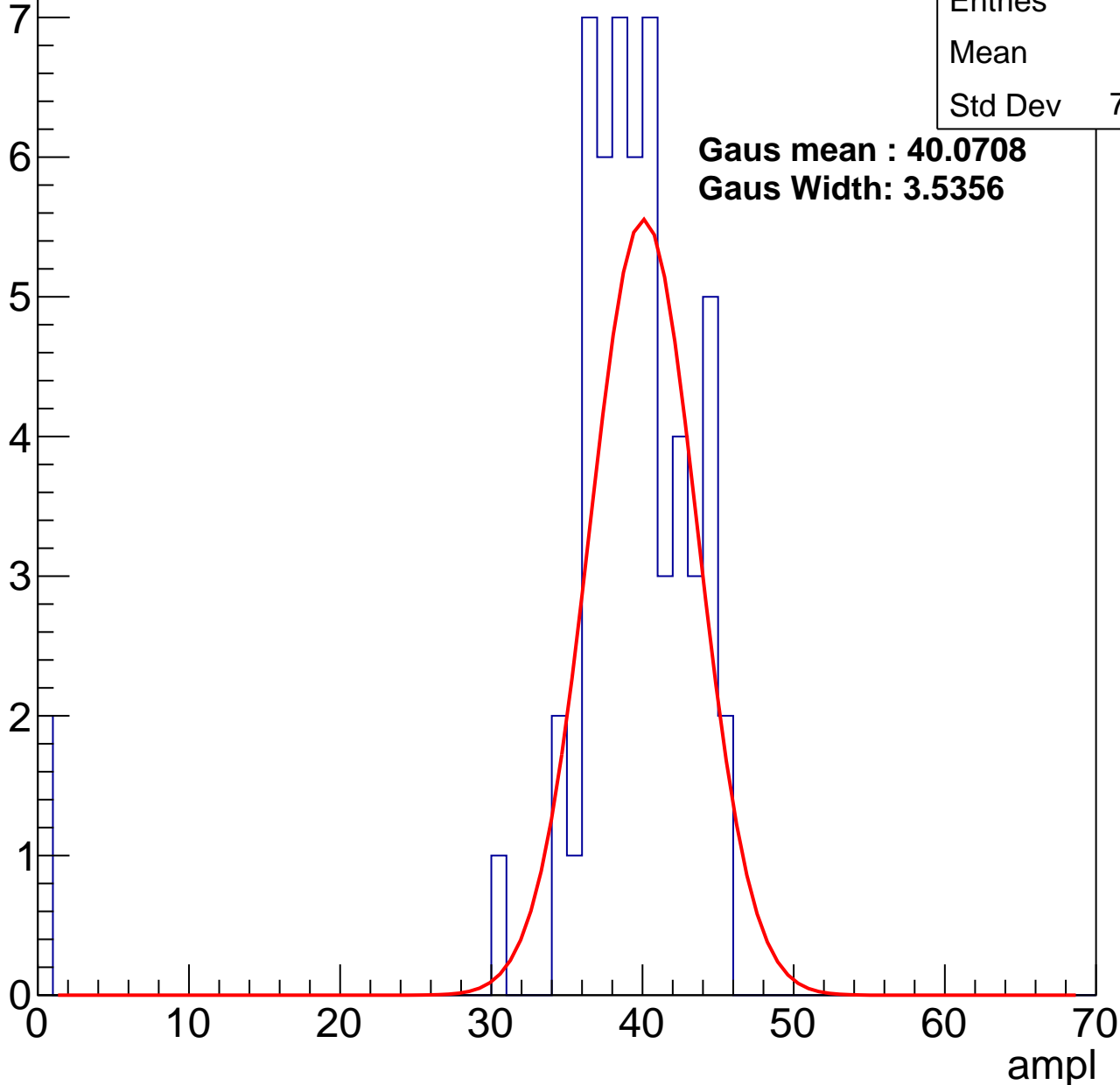
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	37.8
Std Dev	7.909

Gaus mean : 40.0708

Gaus Width: 3.5356

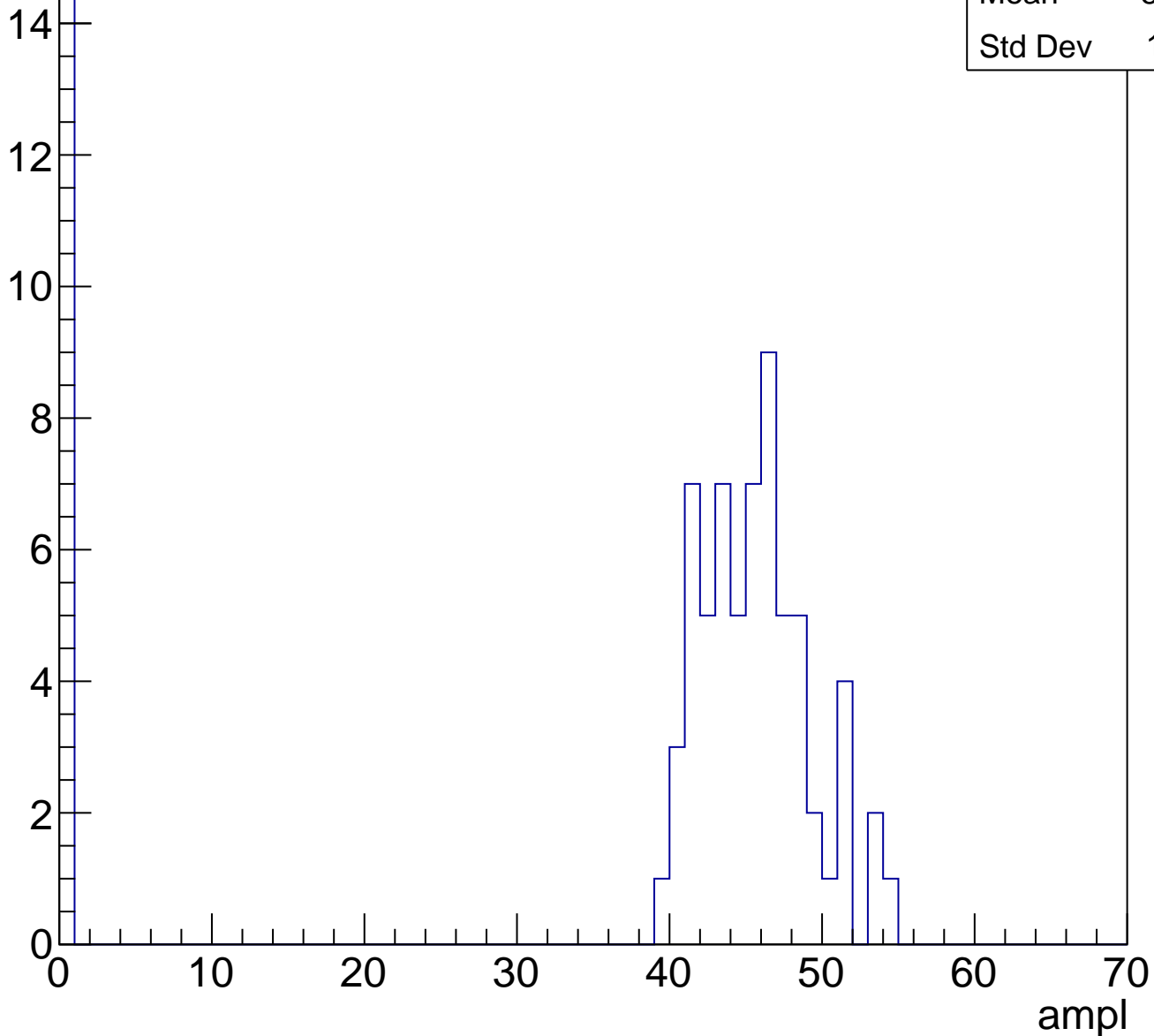


B1L103S, U1-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	36.62
Std Dev	18.01

Entry

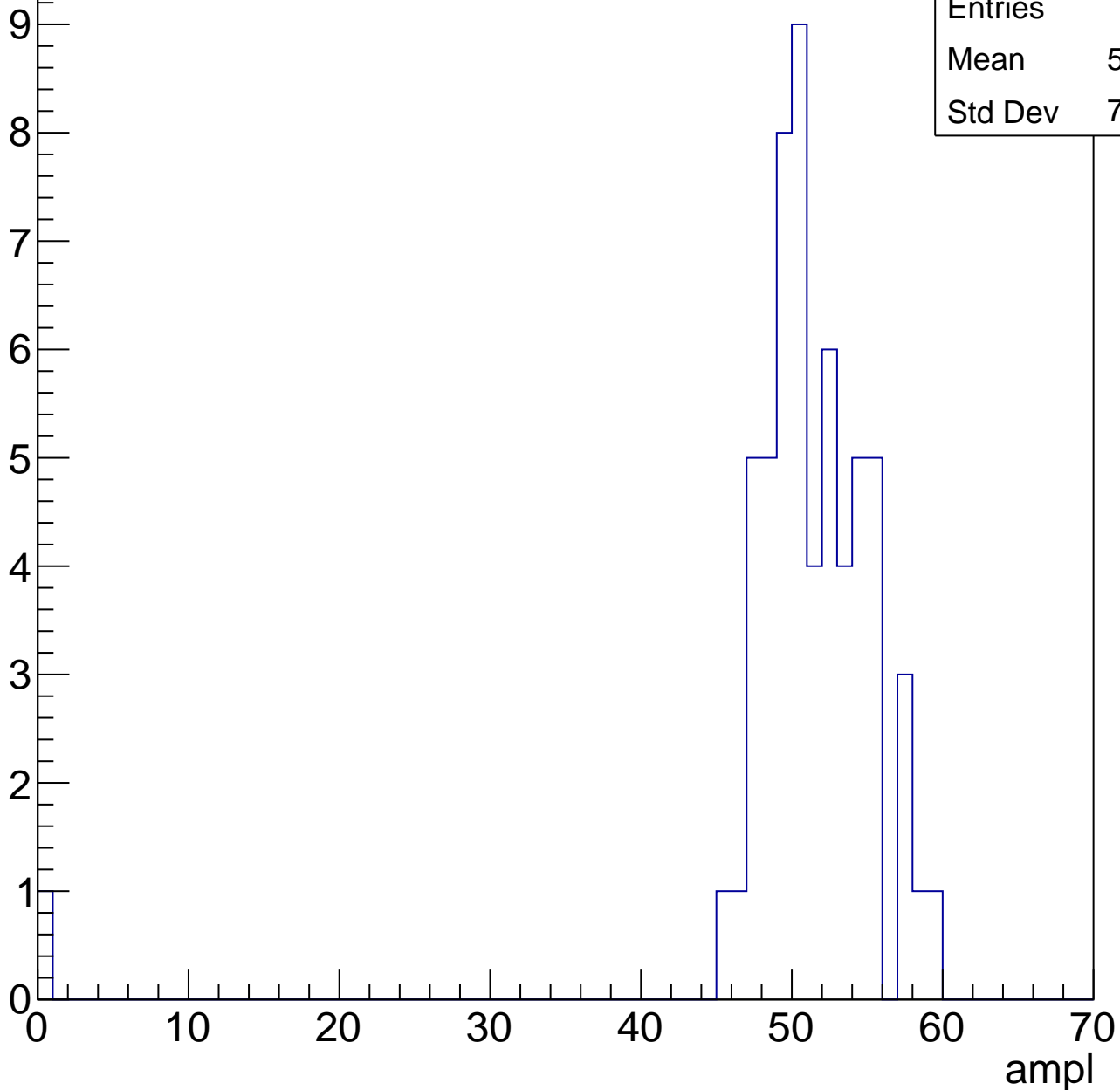


B1L103S, U1-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	50.32
Std Dev	7.329

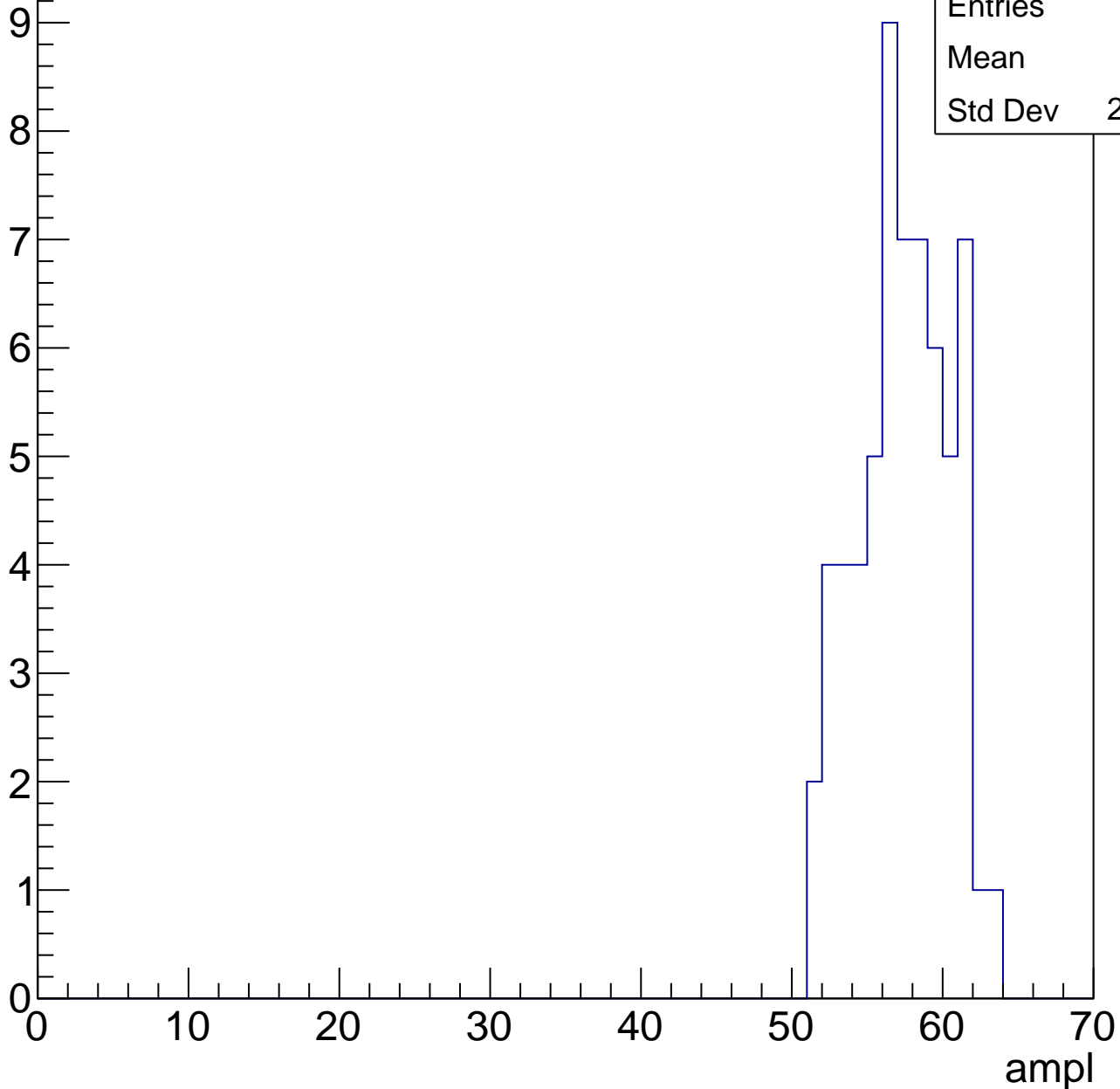


B1L103S, U1-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	56.9
Std Dev	2.977

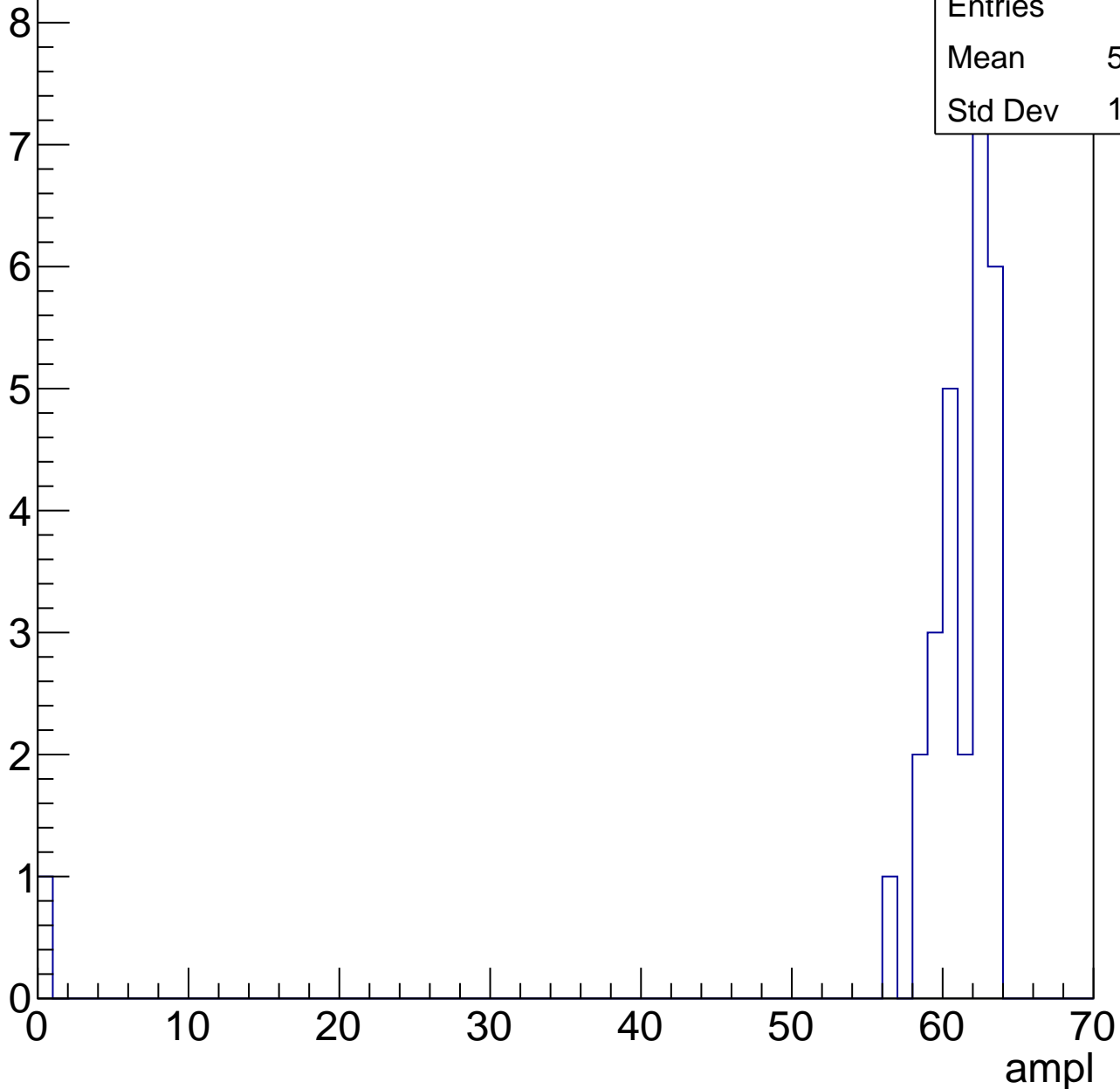


B1L103S, U1-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.75
Std Dev	11.45

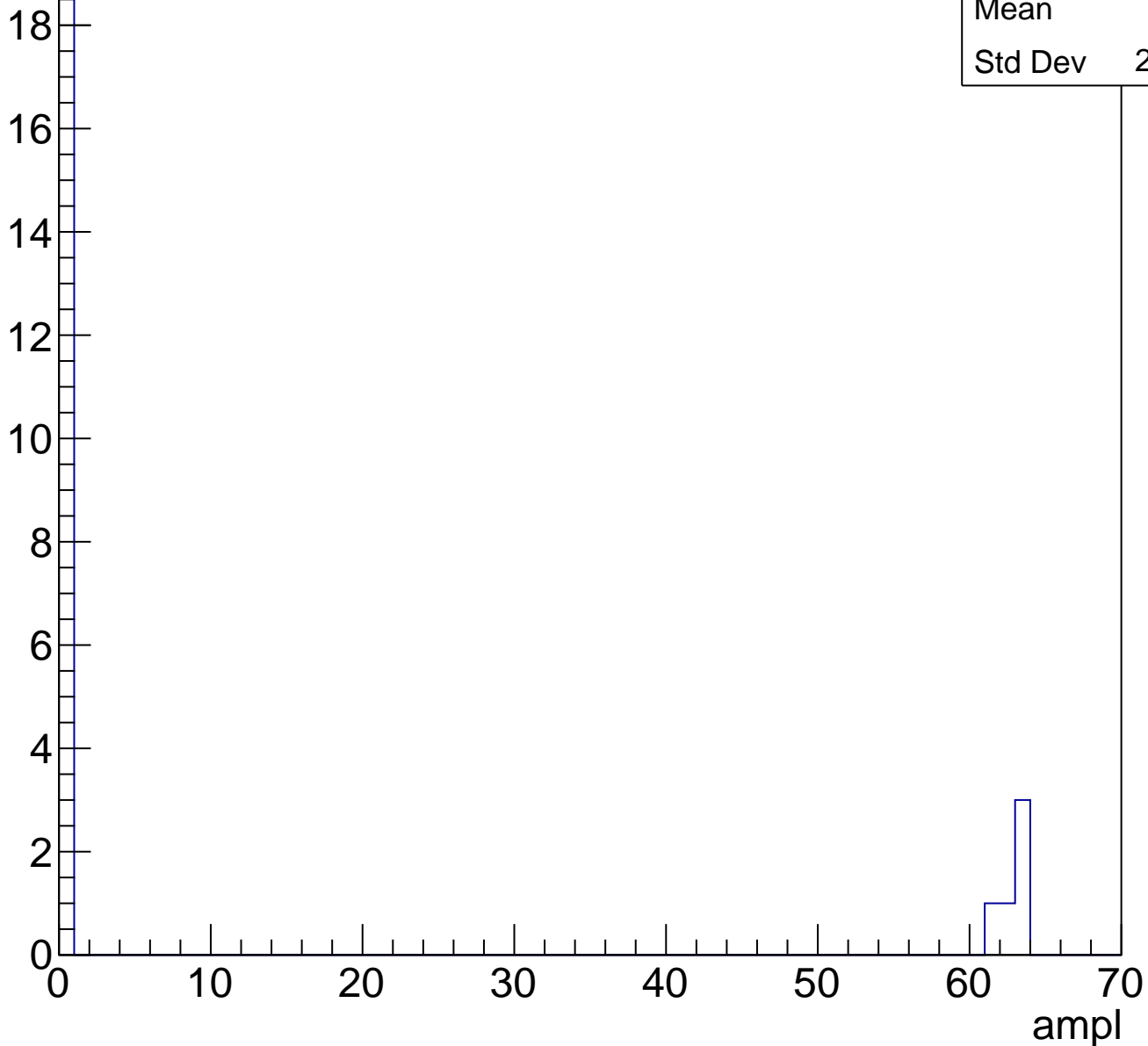


B1L103S, U1-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	13
Std Dev	25.34

Entry



B1L103S, U1-ch116, adc0

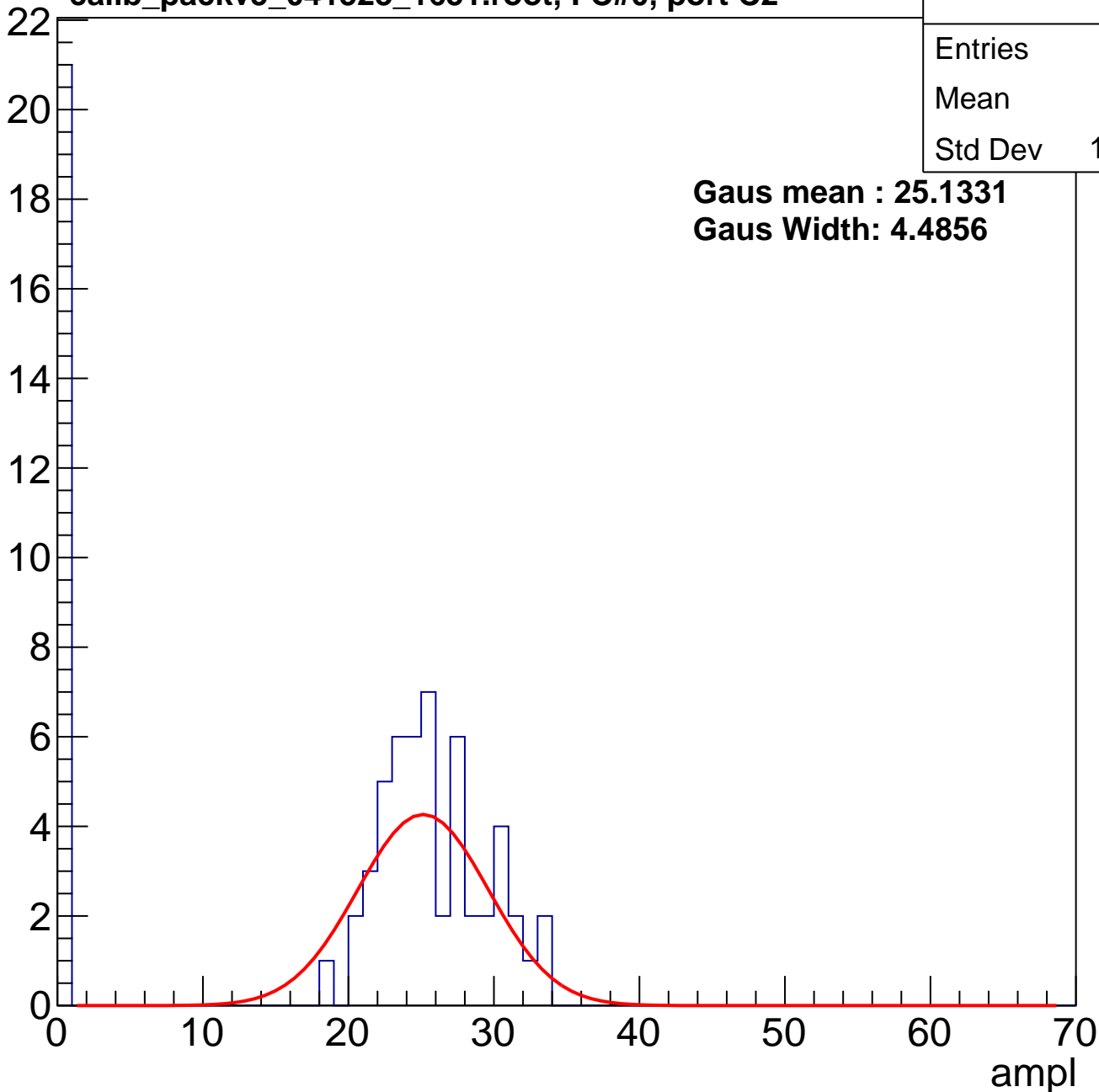
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	18
Std Dev	11.93

Gaus mean : 25.1331

Gaus Width: 4.4856

Entry



B1L103S, U1-ch116, adc1

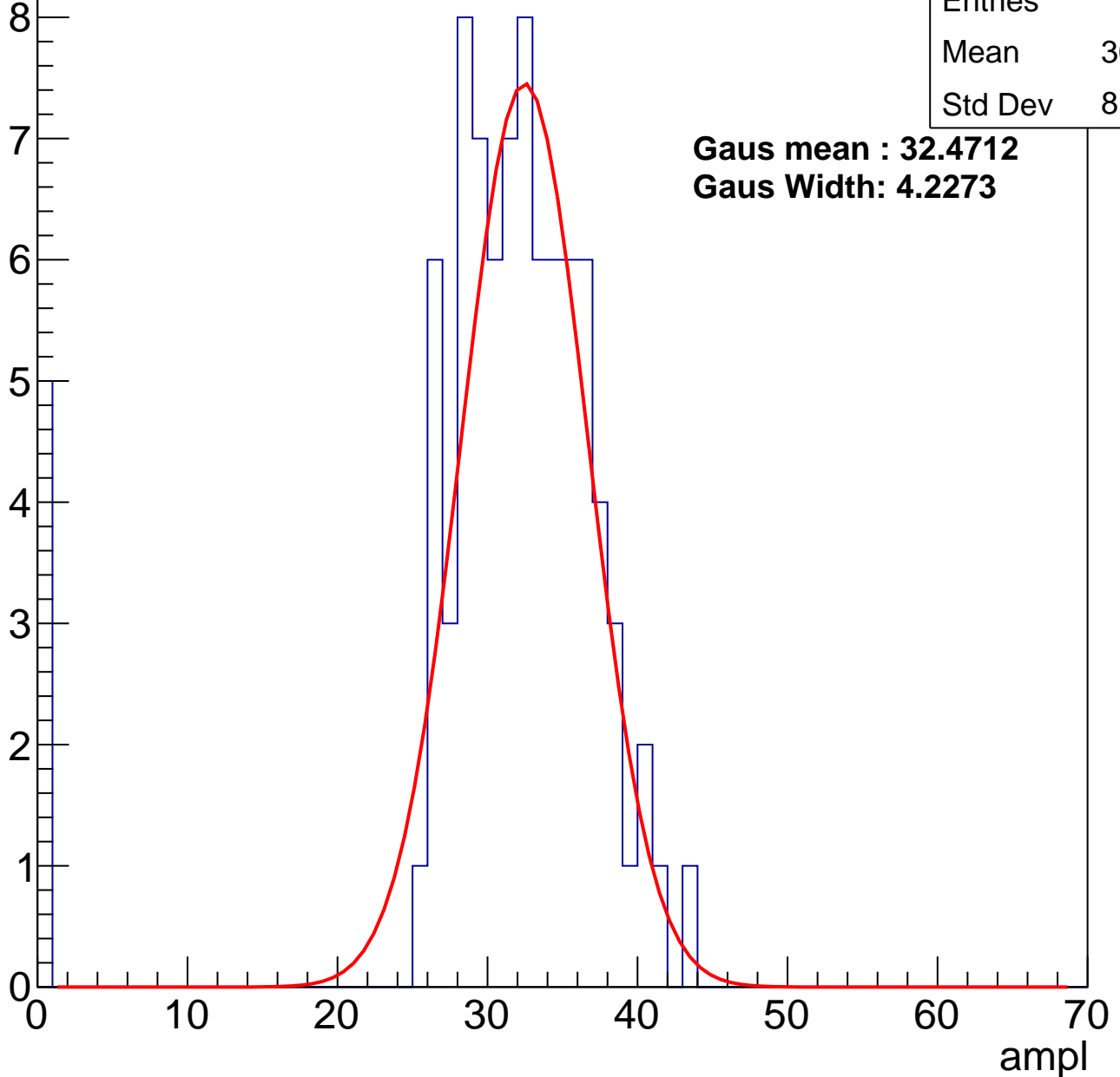
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	30.29
Std Dev	8.438

Gaus mean : 32.4712

Gaus Width: 4.2273



B1L103S, U1-ch116, adc2

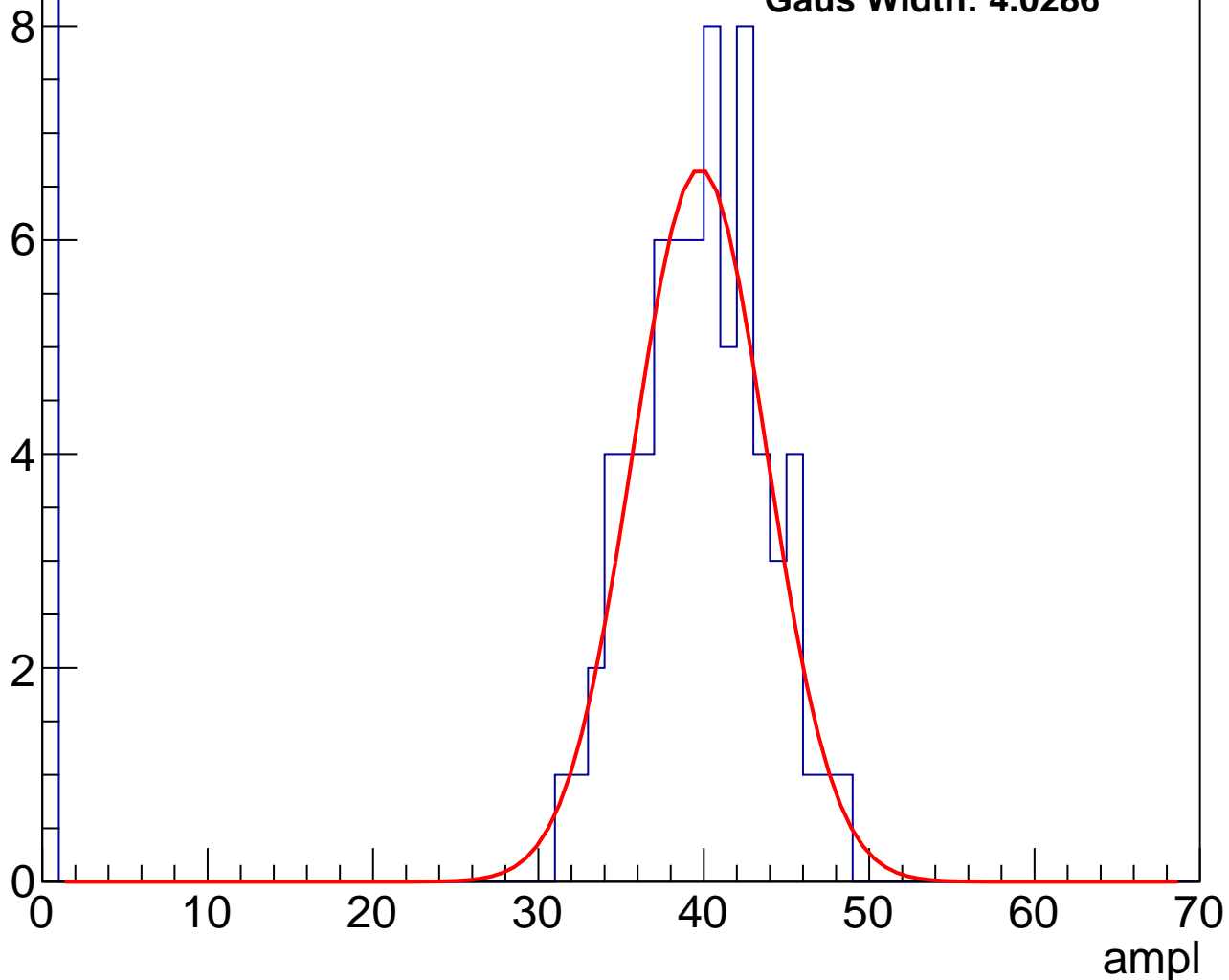
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	34.42
Std Dev	13.57

Gaus mean : 39.7621

Gaus Width: 4.0286

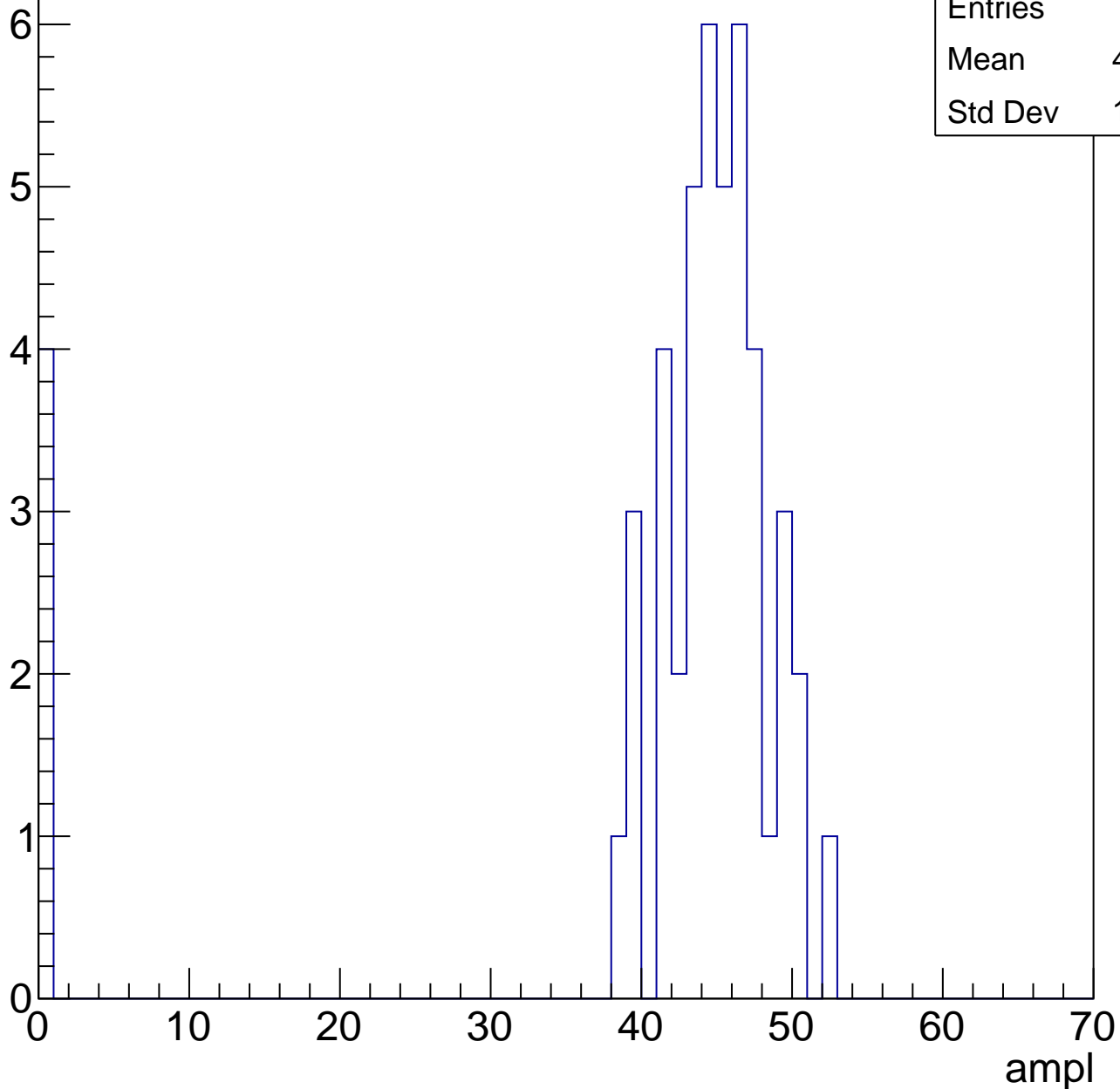


B1L103S, U1-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	40.81
Std Dev	12.81

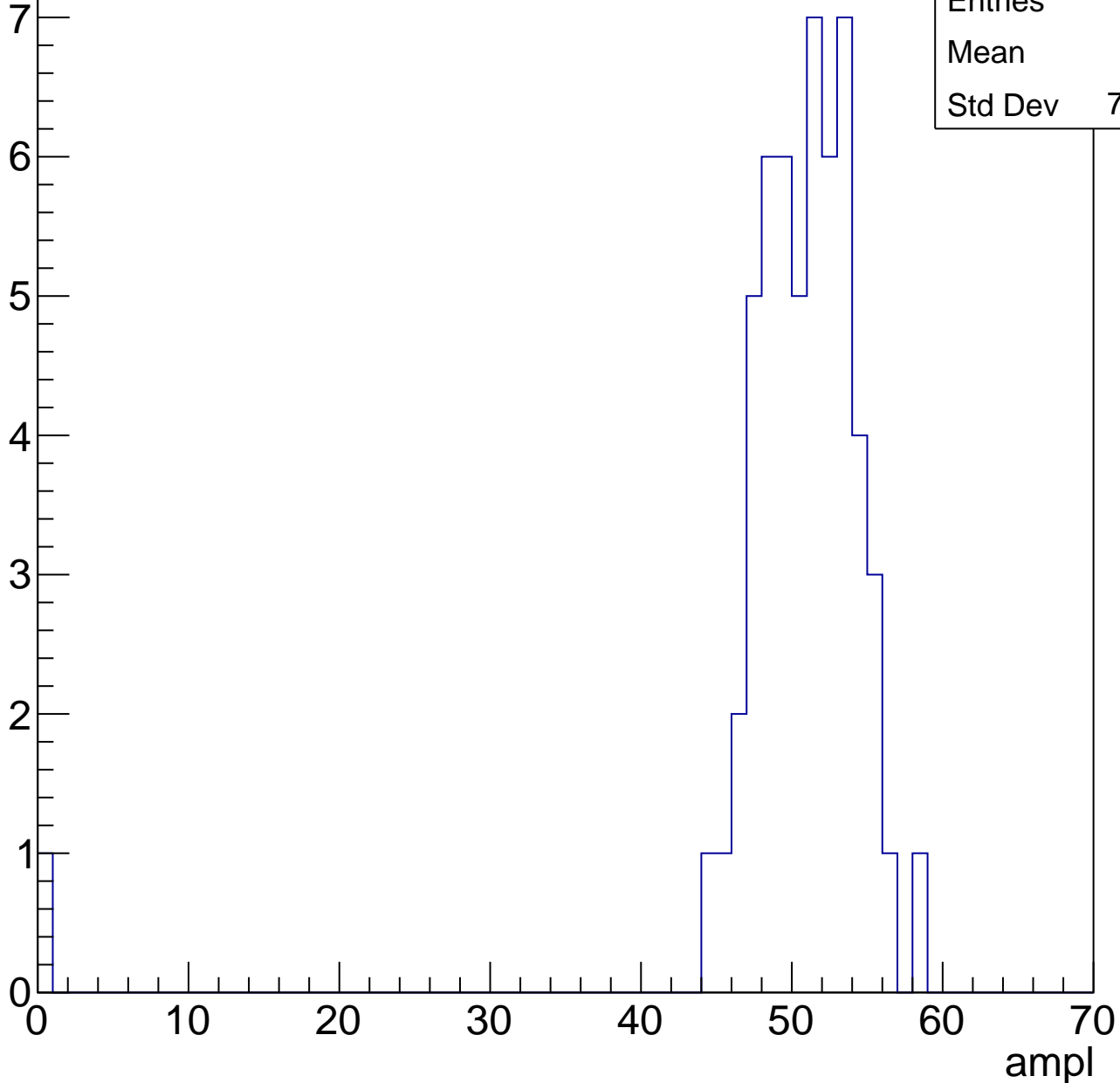


B1L103S, U1-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

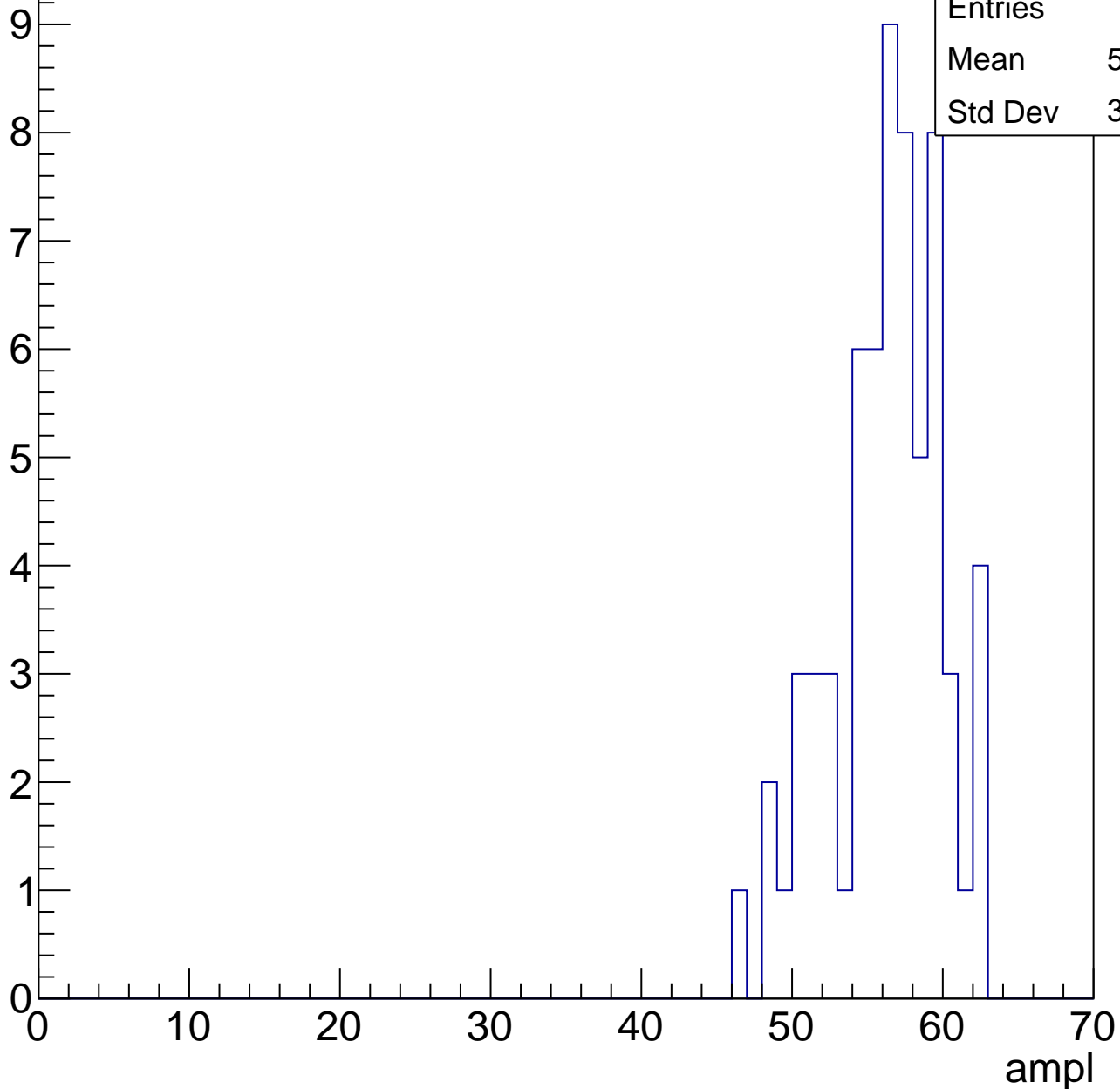
Entries	56
Mean	49.7
Std Dev	7.314



B1L103S, U1-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

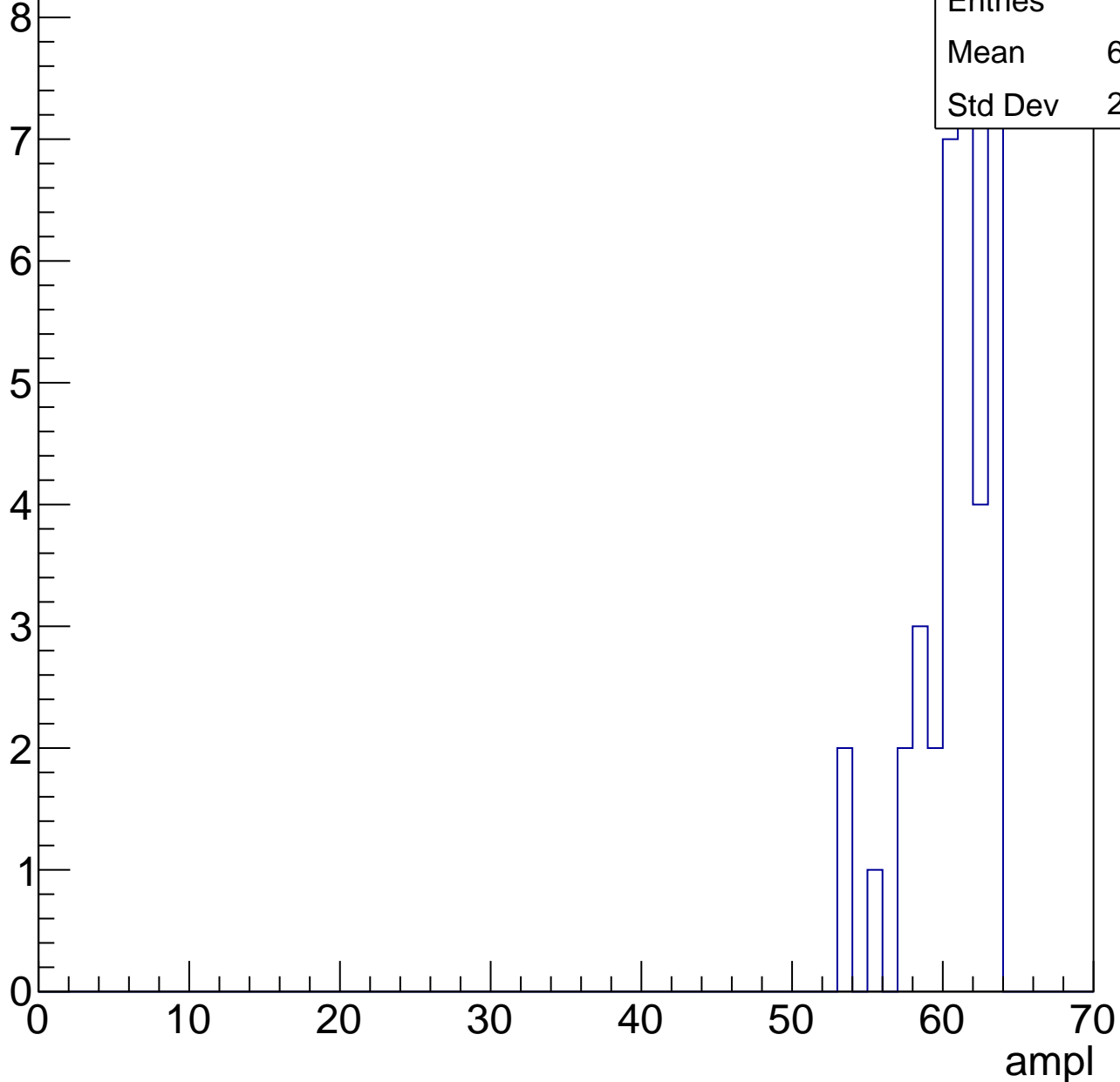


Entries	64
Mean	55.75
Std Dev	3.666

B1L103S, U1-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

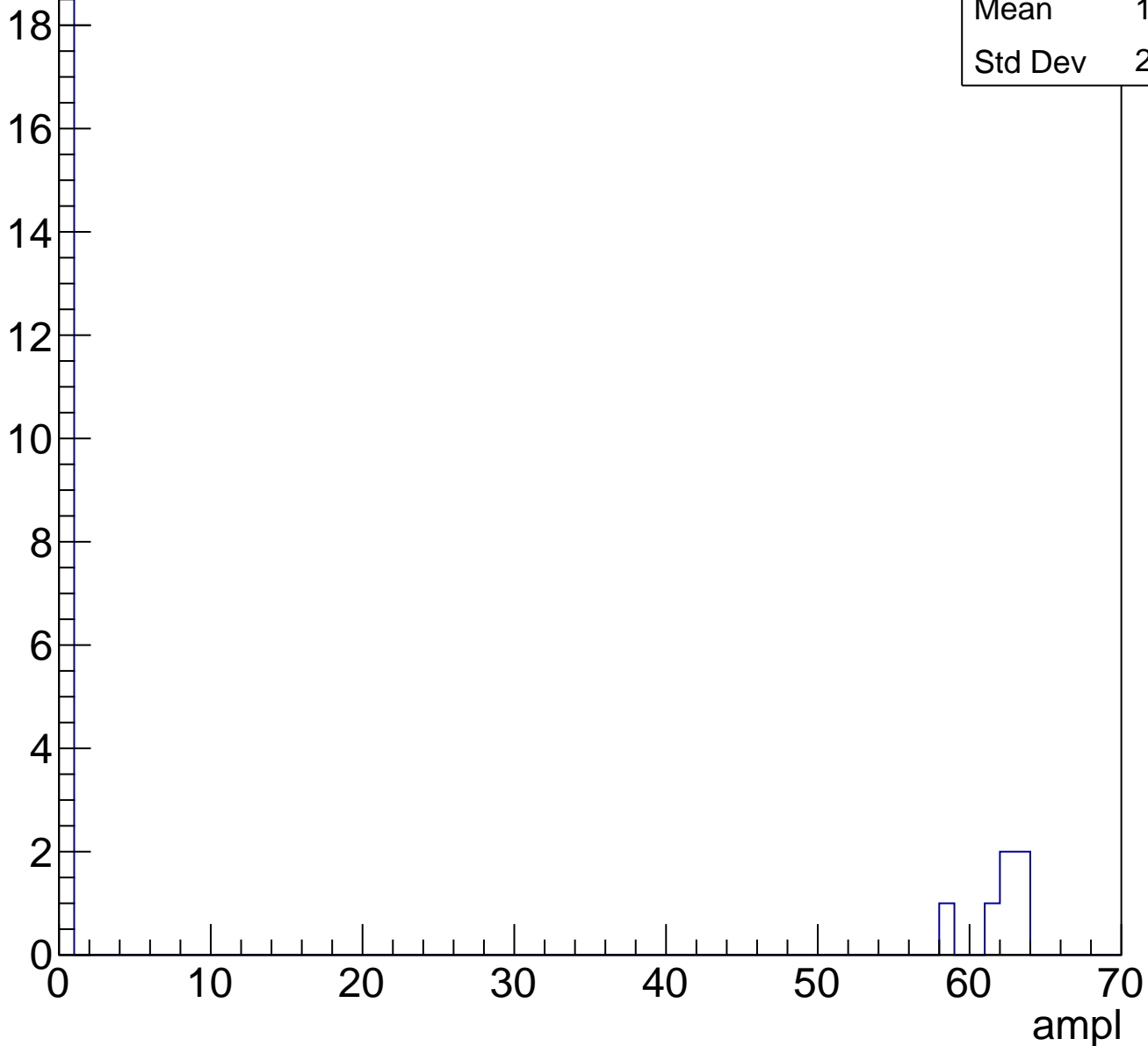


B1L103S, U1-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	25
Mean	14.76
Std Dev	26.28

Entry



B1L103S, U1-ch117, adc0

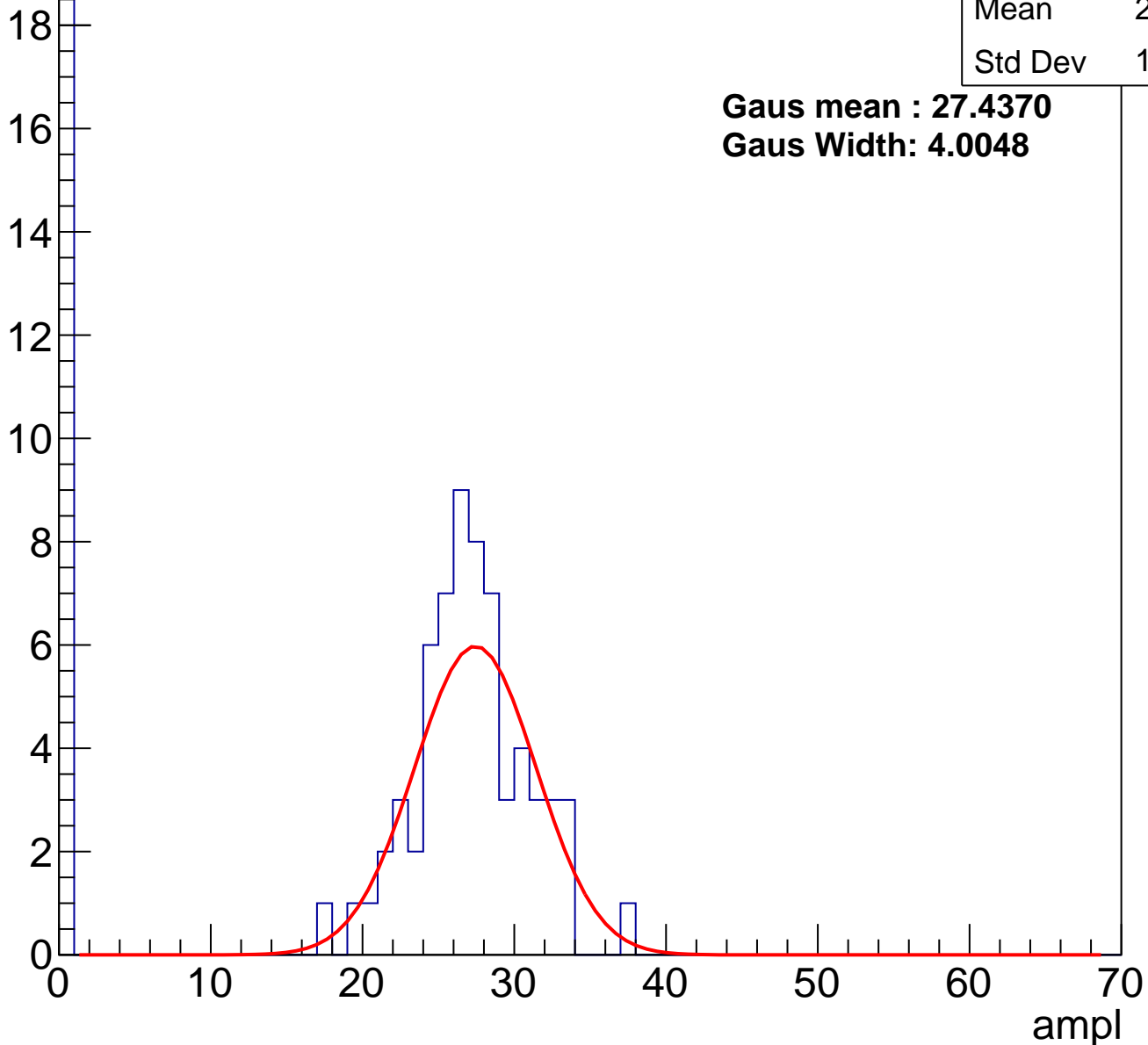
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	20.57
Std Dev	11.66

Gaus mean : 27.4370

Gaus Width: 4.0048

Entry



B1L103S, U1-ch117, adc1

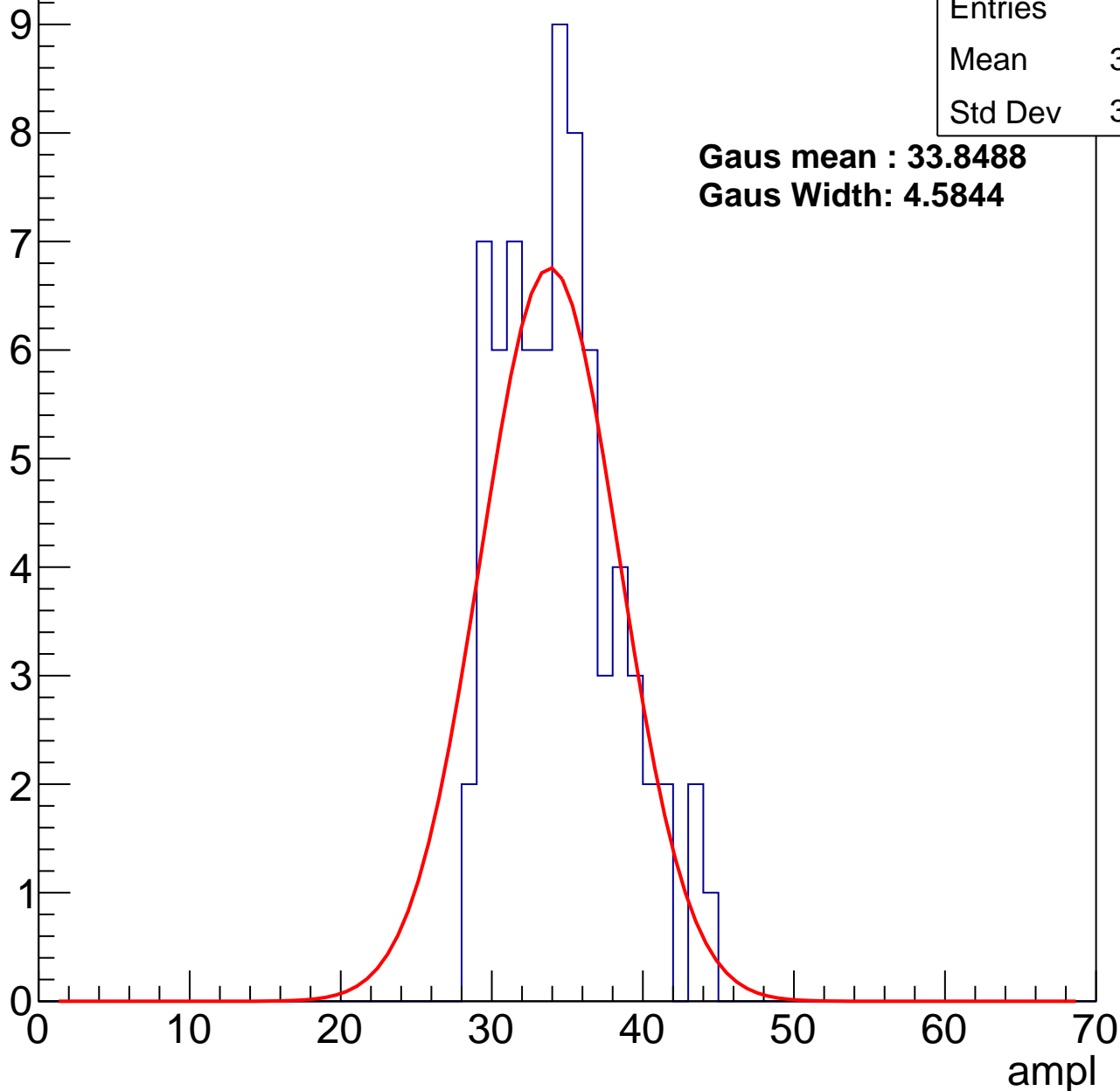
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	34.05
Std Dev	3.795

Gaus mean : 33.8488

Gaus Width: 4.5844



B1L103S, U1-ch117, adc2

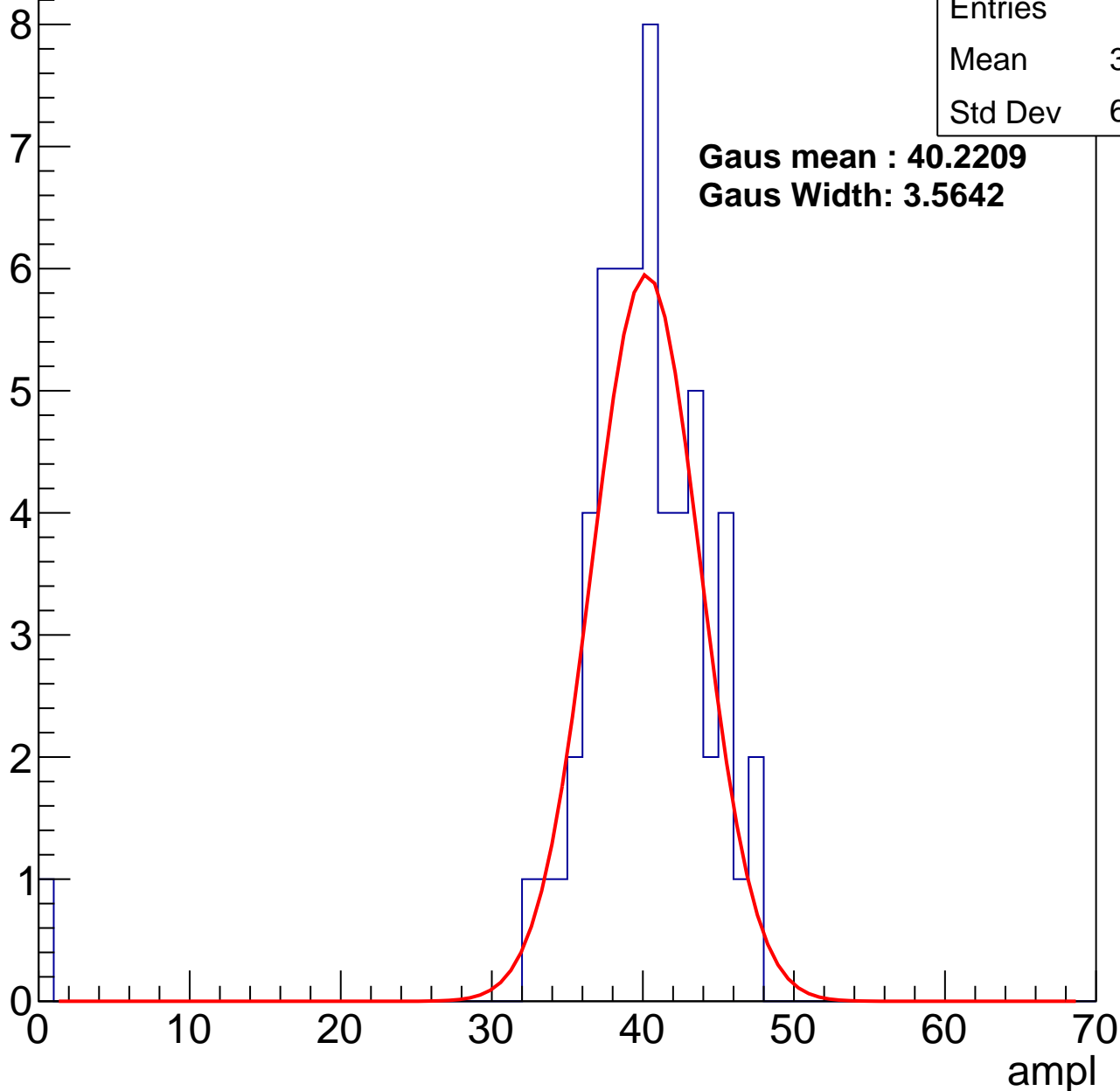
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	39.17
Std Dev	6.212

Gaus mean : 40.2209

Gaus Width: 3.5642

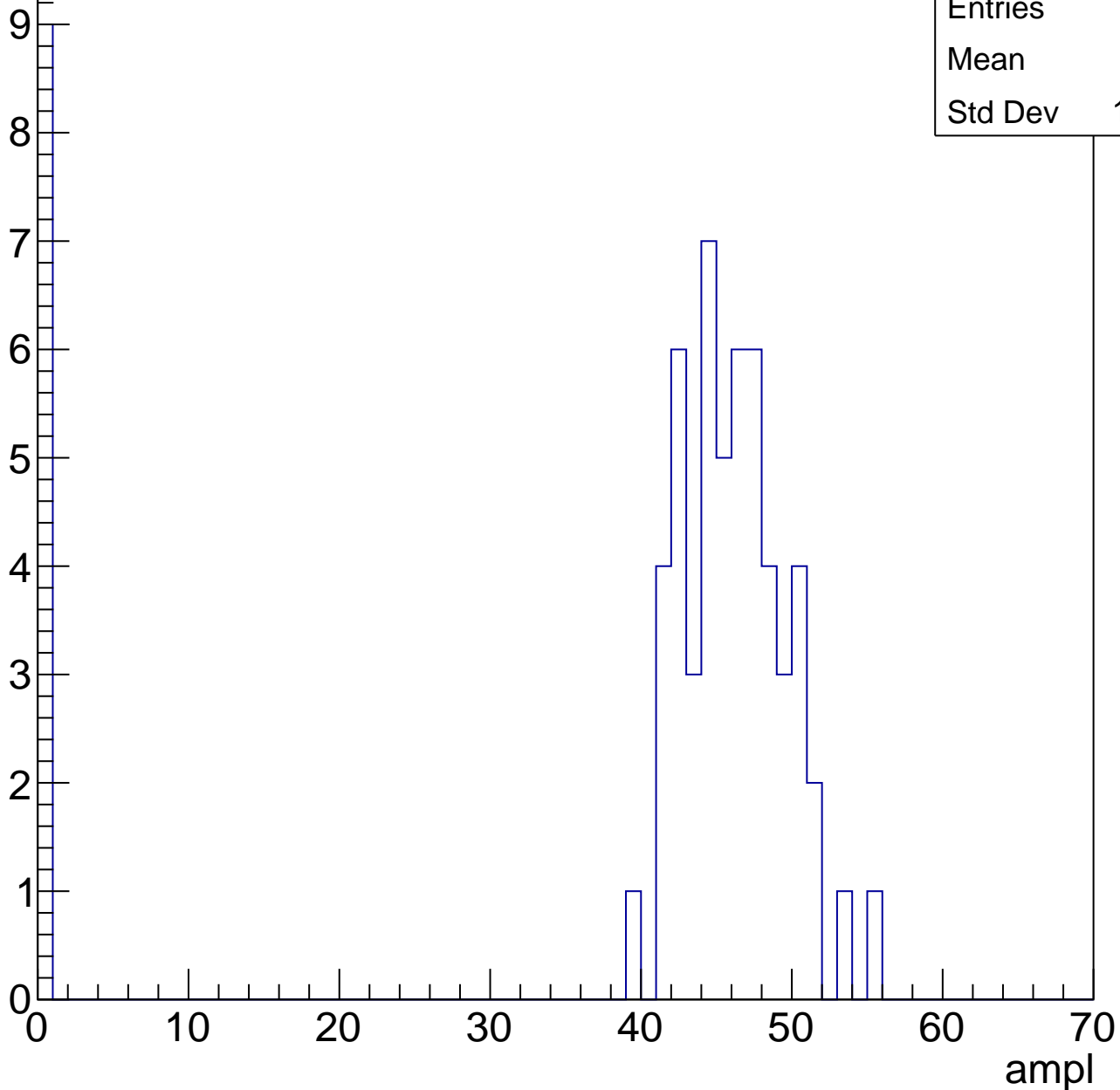


B1L103S, U1-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.1
Std Dev	16.41

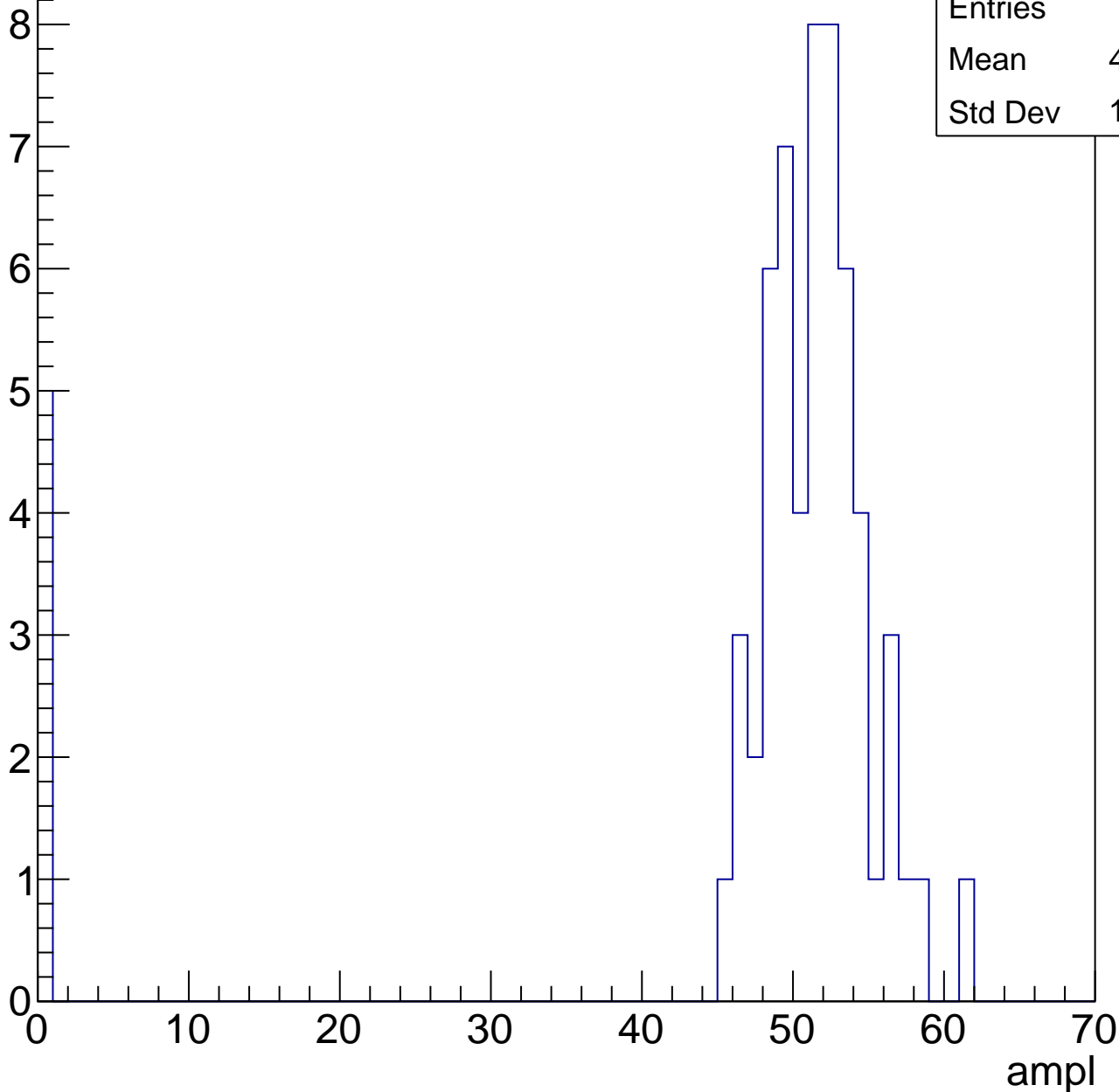


B1L103S, U1-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

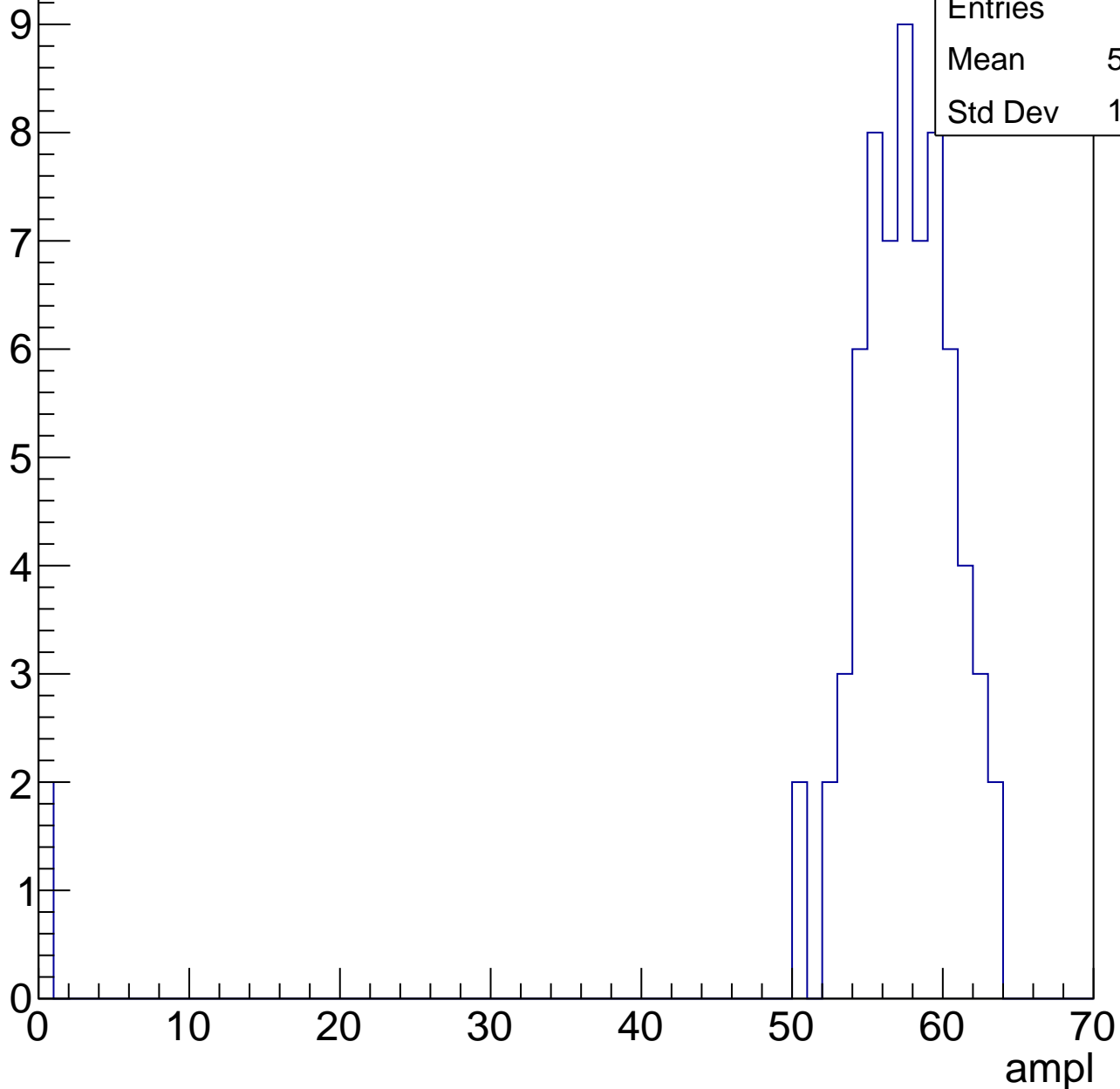
Entries	61
Mean	46.97
Std Dev	14.36



B1L103S, U1-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

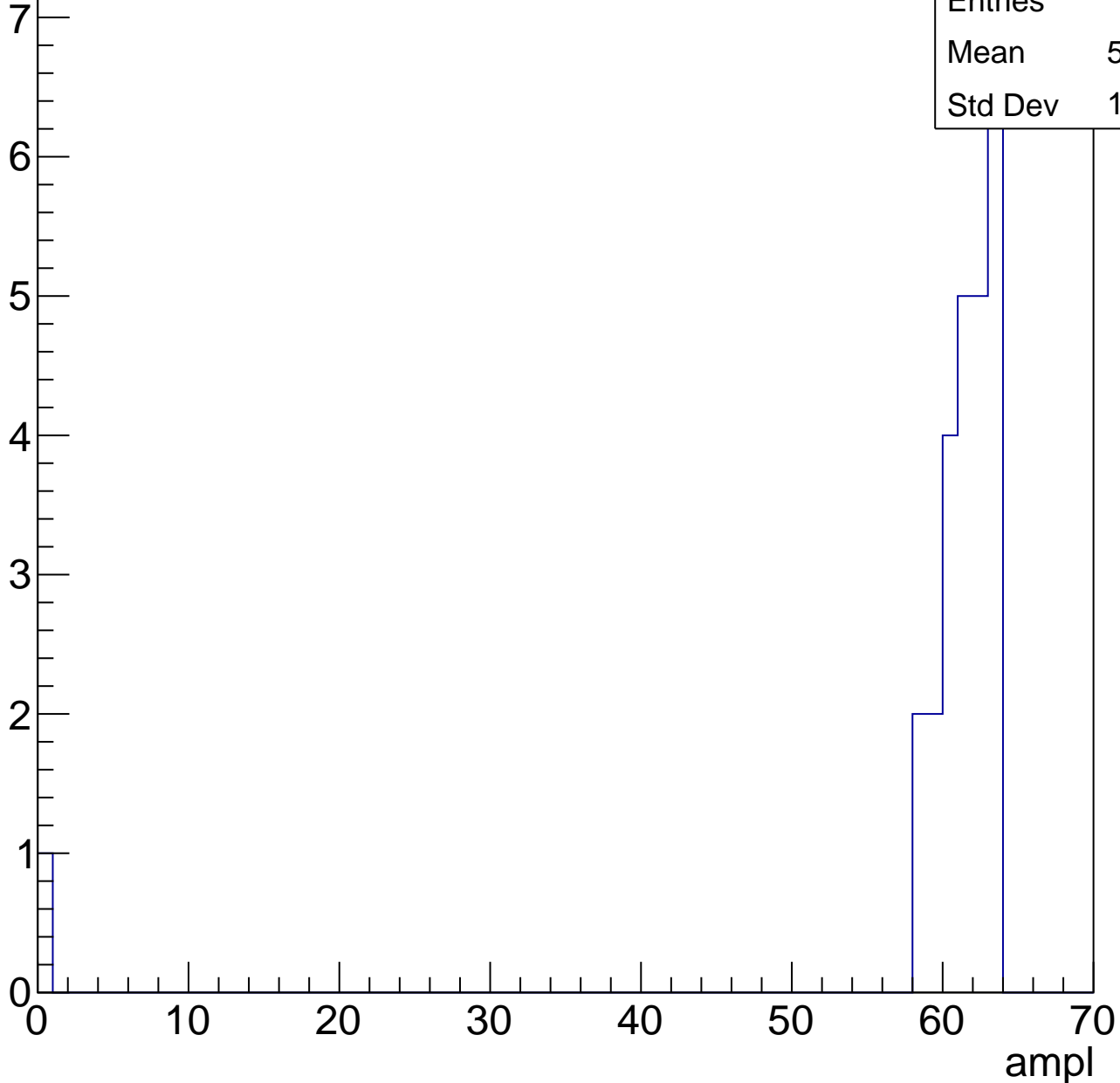


B1L103S, U1-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.85
Std Dev	11.87

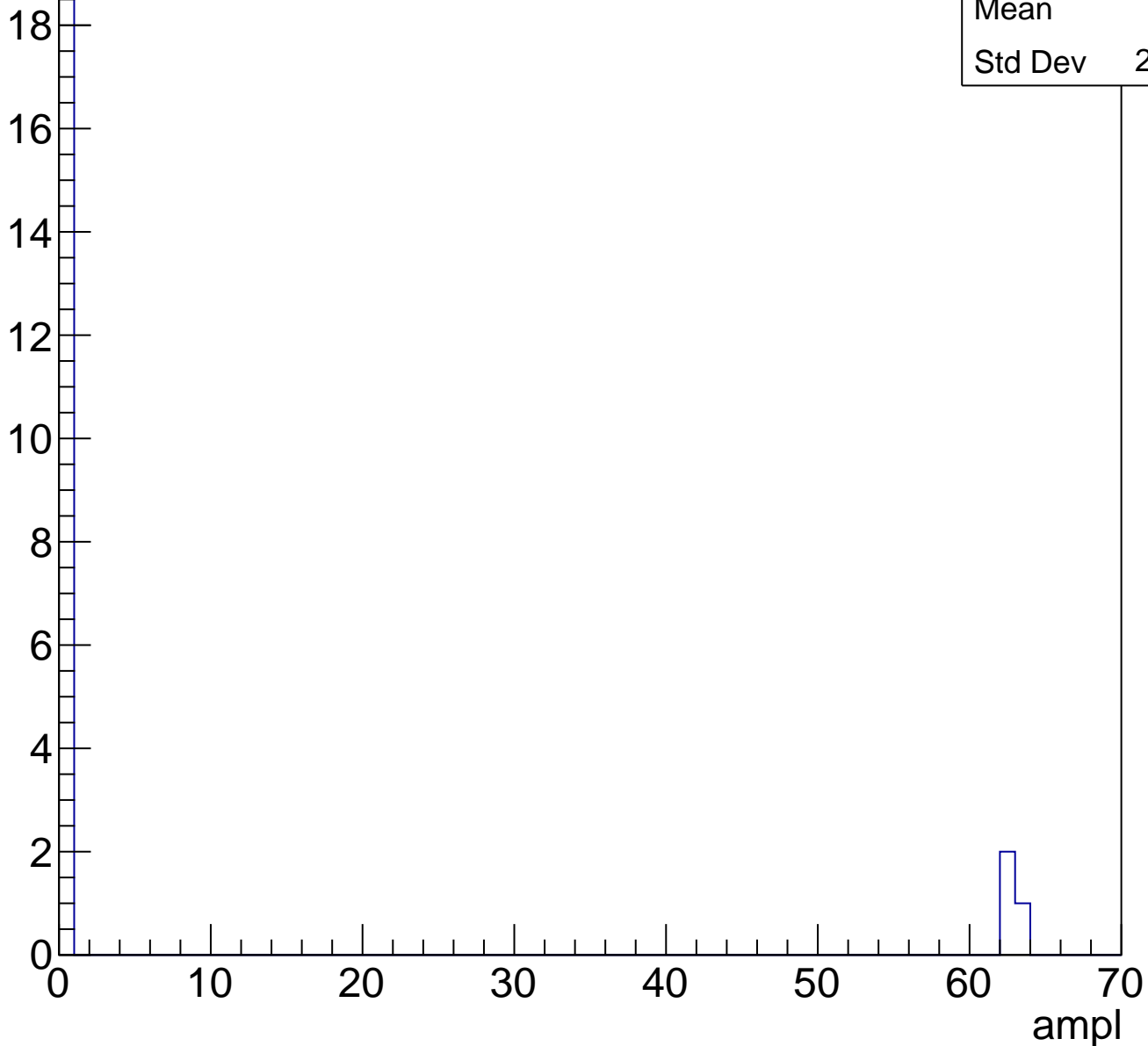


B1L103S, U1-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U1-ch118, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	17.94
Std Dev	12.2

Gaus mean : 26.0705

Gaus Width: 4.0618

Entry

30

25

20

15

10

5

0

0

10

20

30

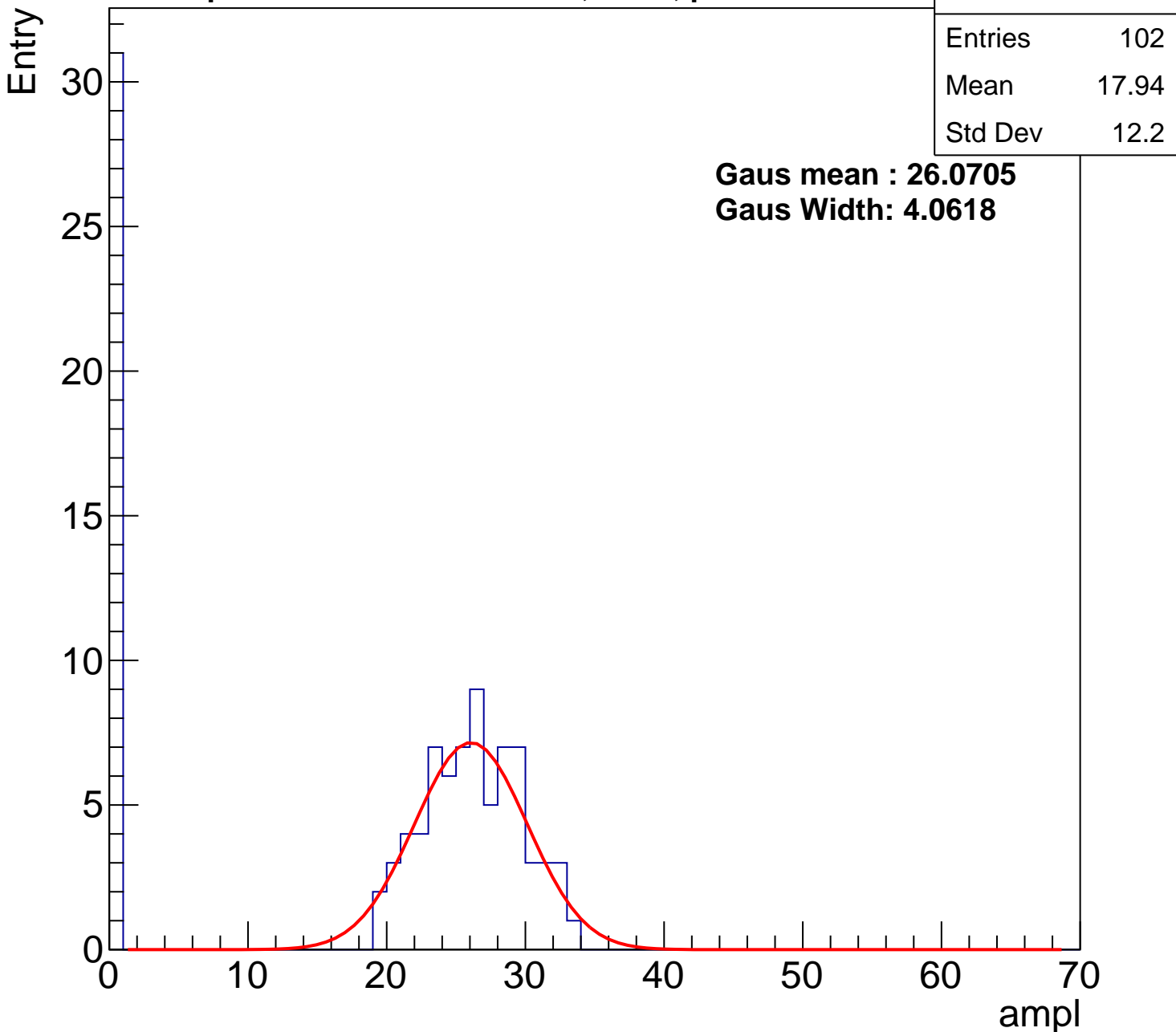
40

50

60

70

ampl



B1L103S, U1-ch118, adc1

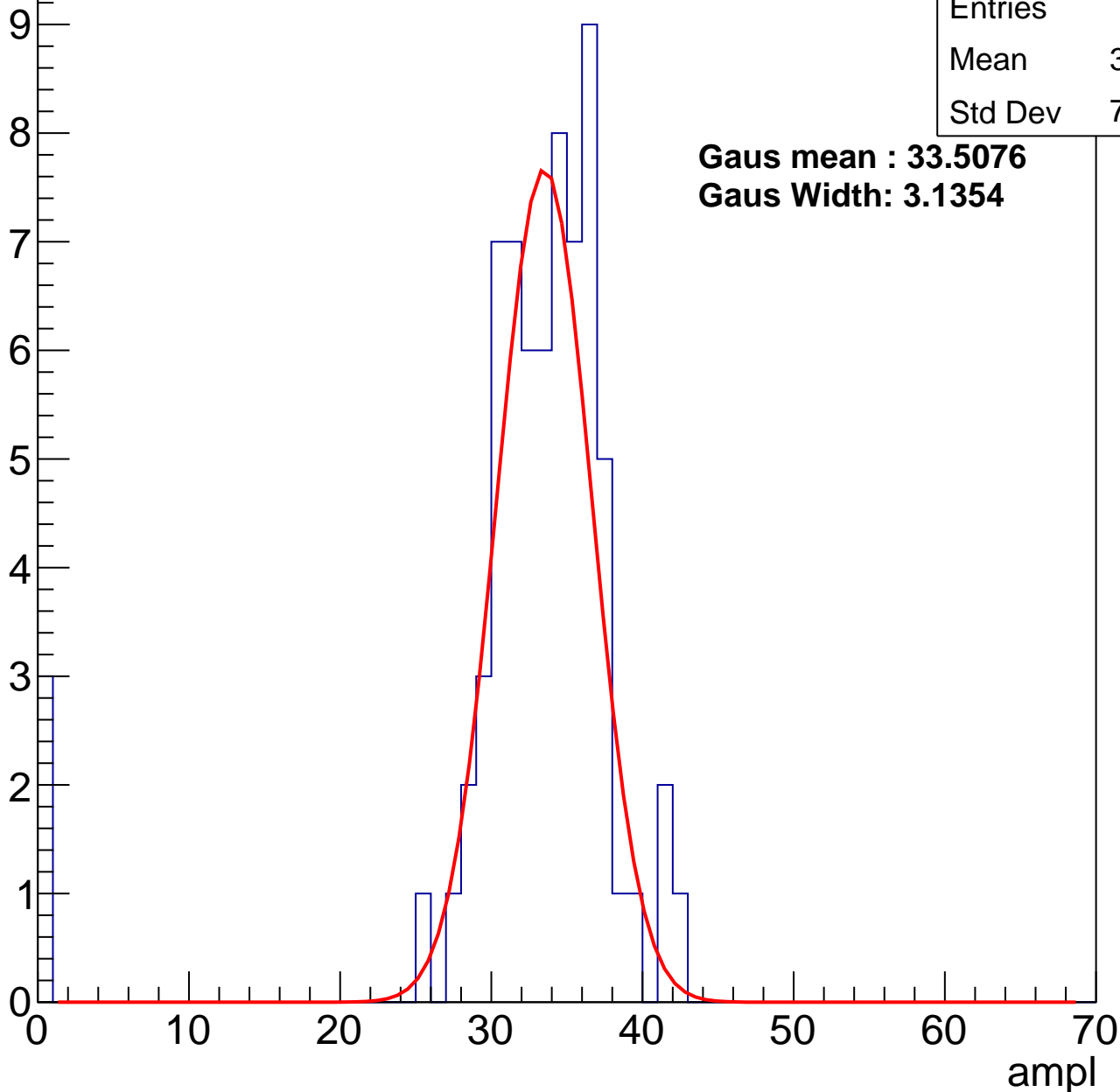
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	31.99
Std Dev	7.519

Gaus mean : 33.5076

Gaus Width: 3.1354



B1L103S, U1-ch118, adc2

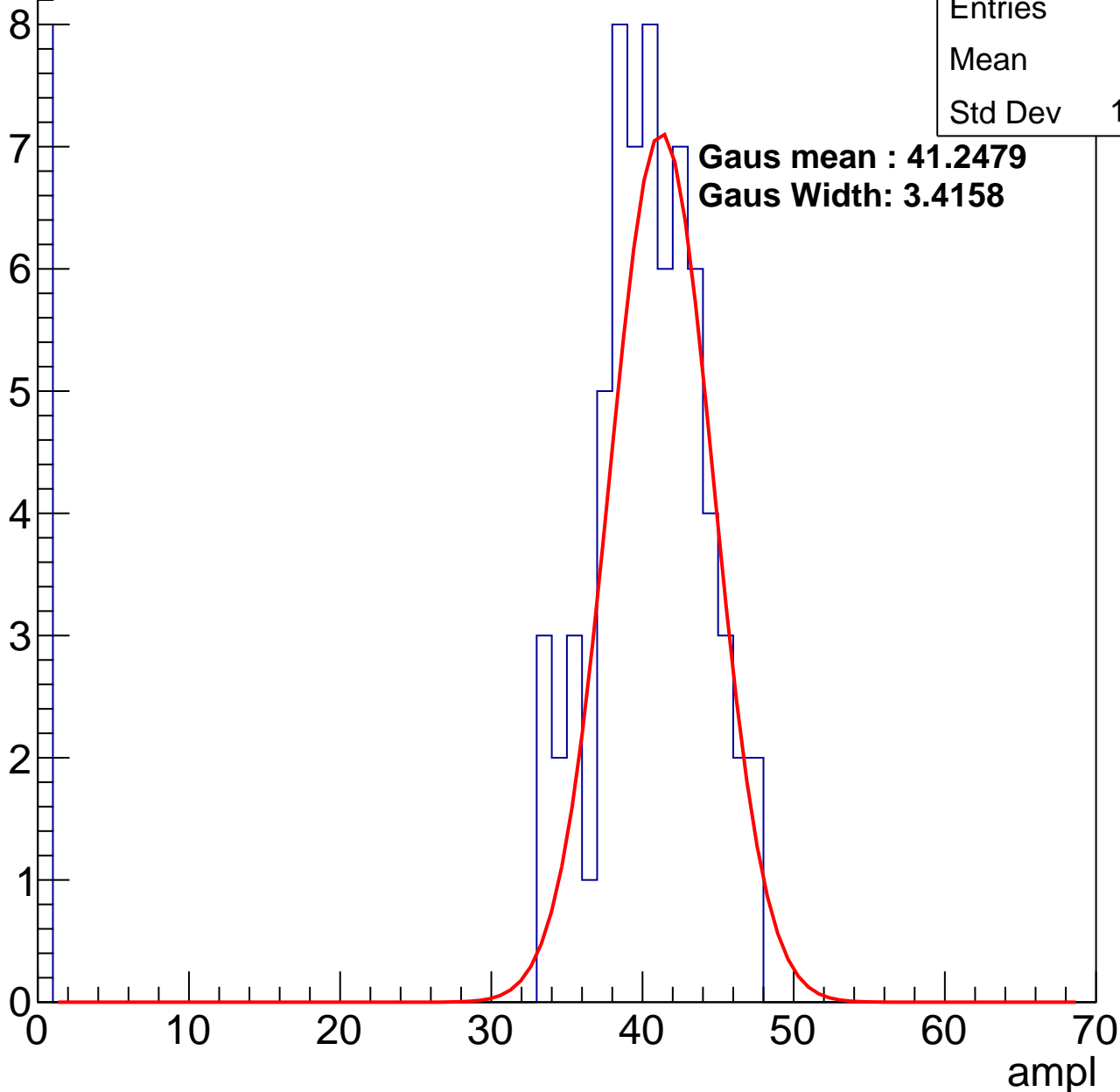
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.8
Std Dev	12.79

Gaus mean : 41.2479

Gaus Width: 3.4158

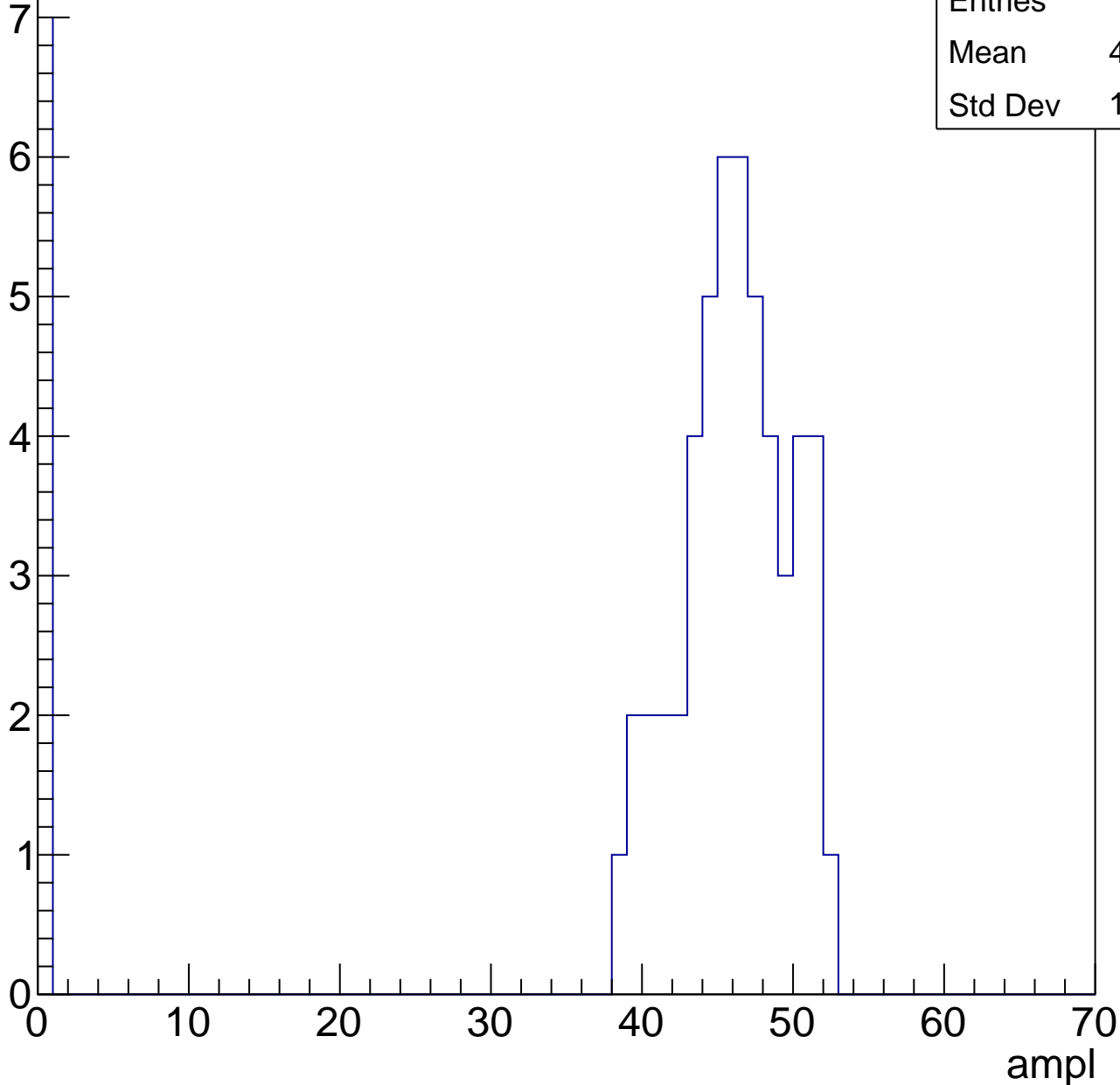


B1L103S, U1-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	40.17
Std Dev	15.24

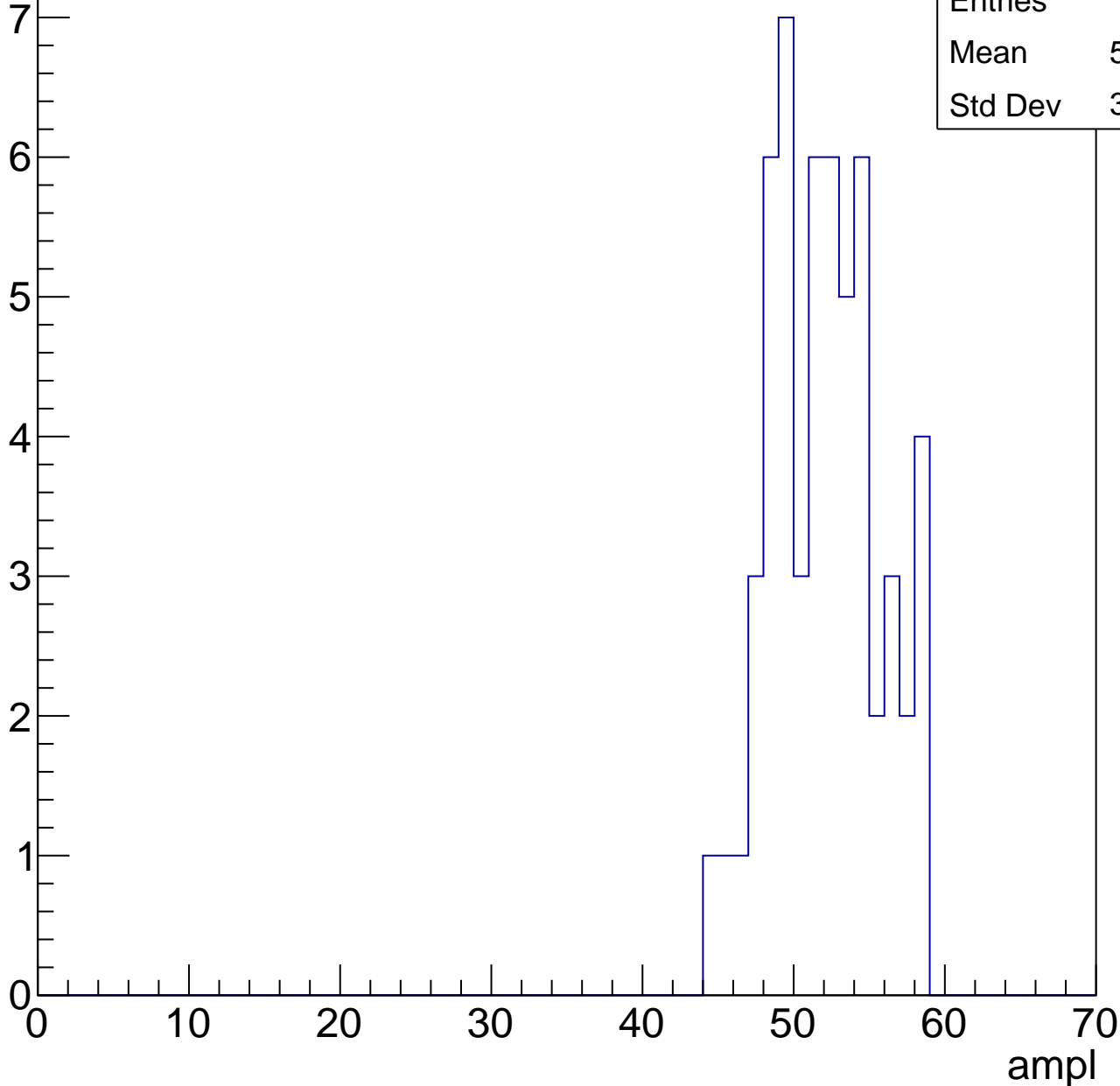


B1L103S, U1-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	51.57
Std Dev	3.484

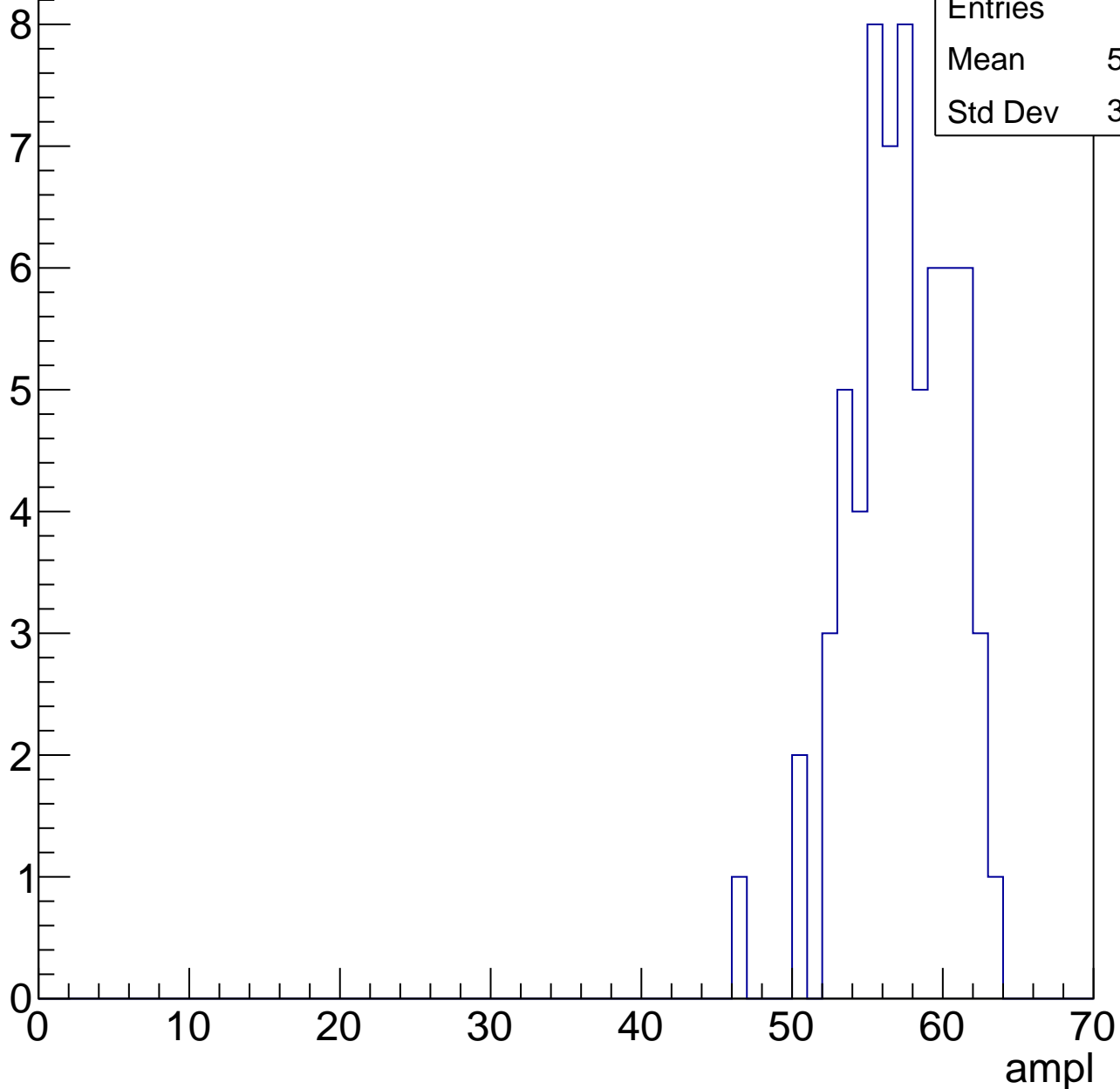


B1L103S, U1-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	56.77
Std Dev	3.364

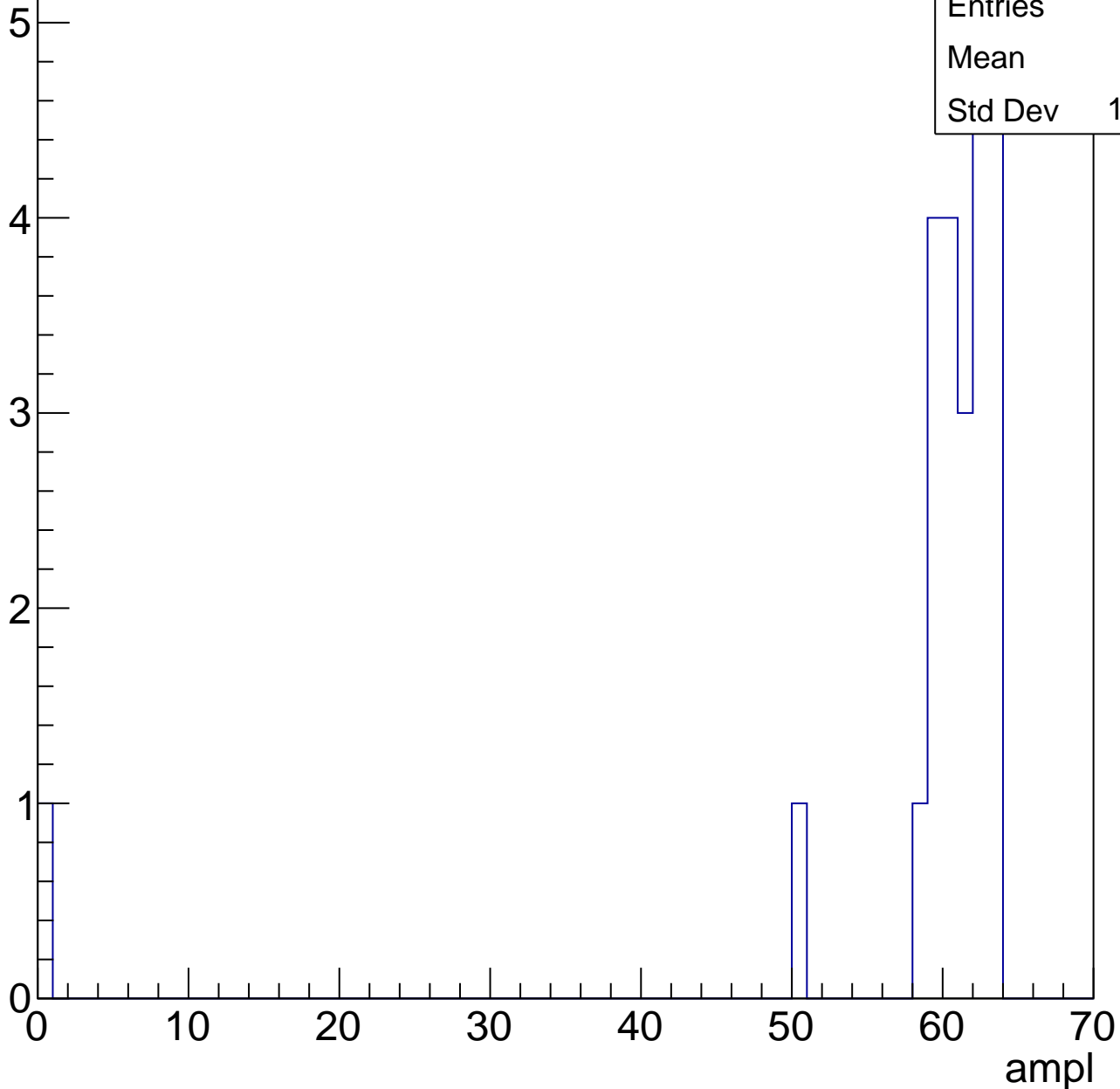


B1L103S, U1-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58
Std Dev	12.38

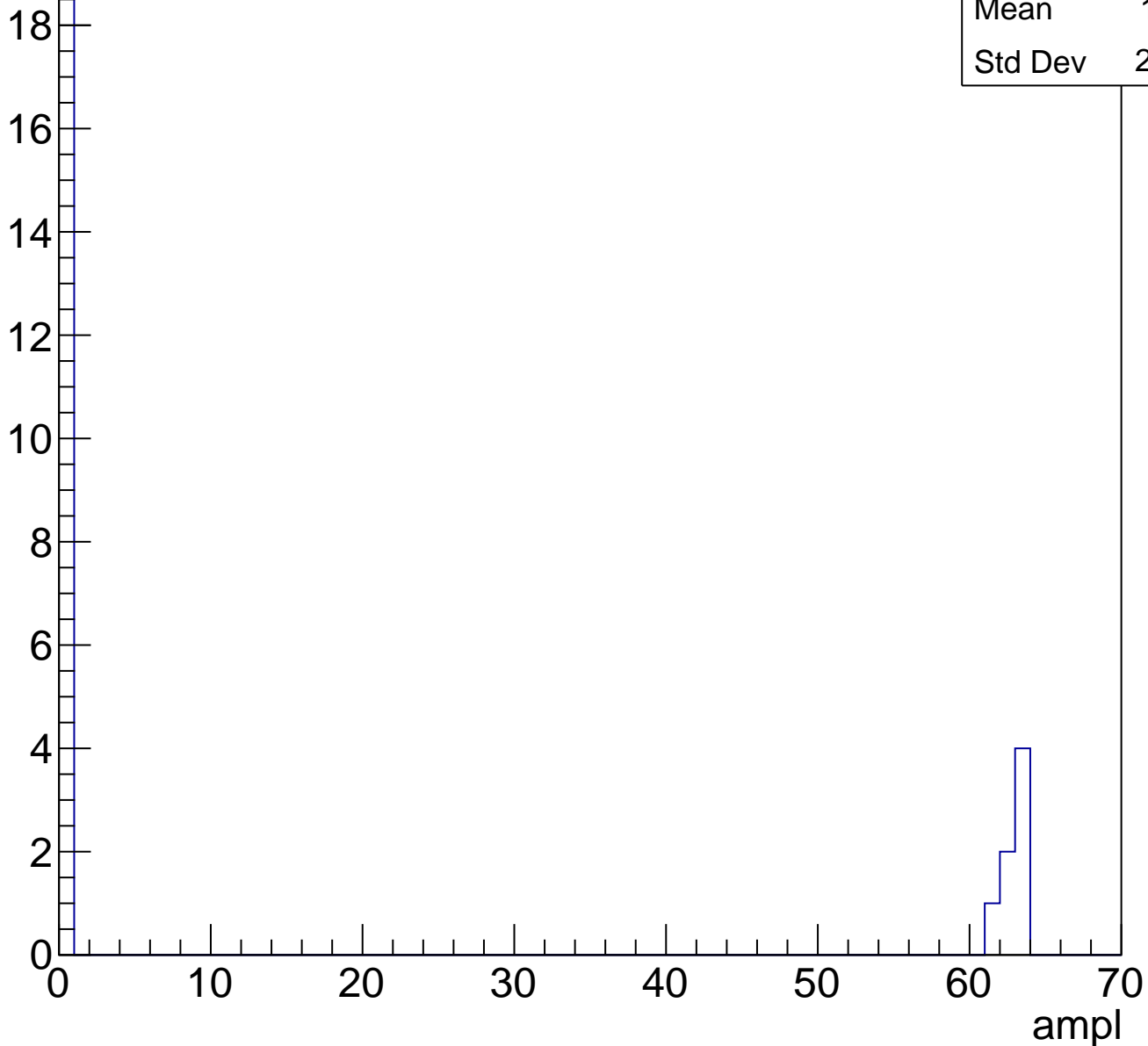


B1L103S, U1-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	16.81
Std Dev	27.69

Entry



B1L103S, U1-ch119, adc0

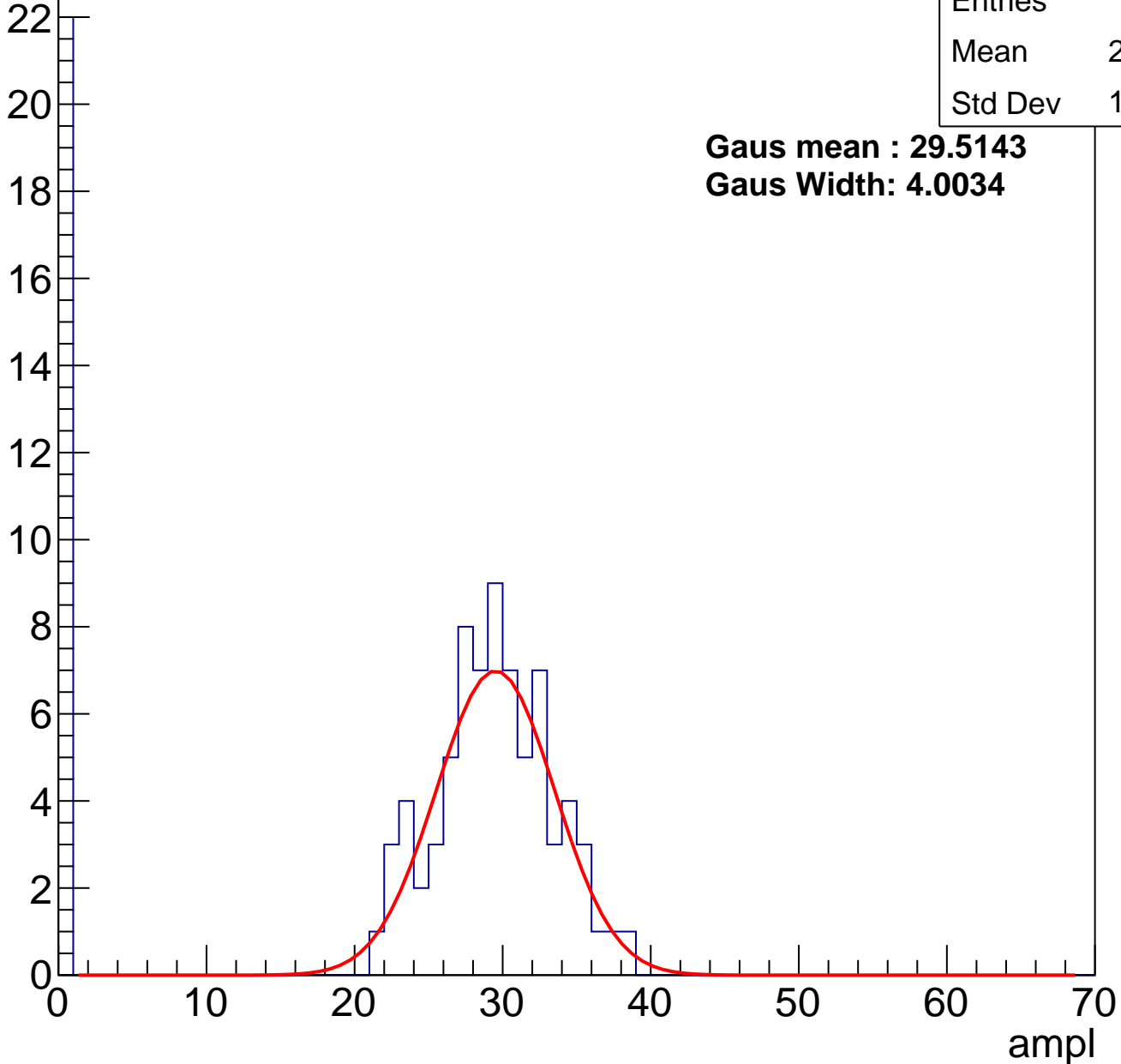
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	22.34
Std Dev	12.63

Gaus mean : 29.5143

Gaus Width: 4.0034

Entry



B1L103S, U1-ch119, adc1

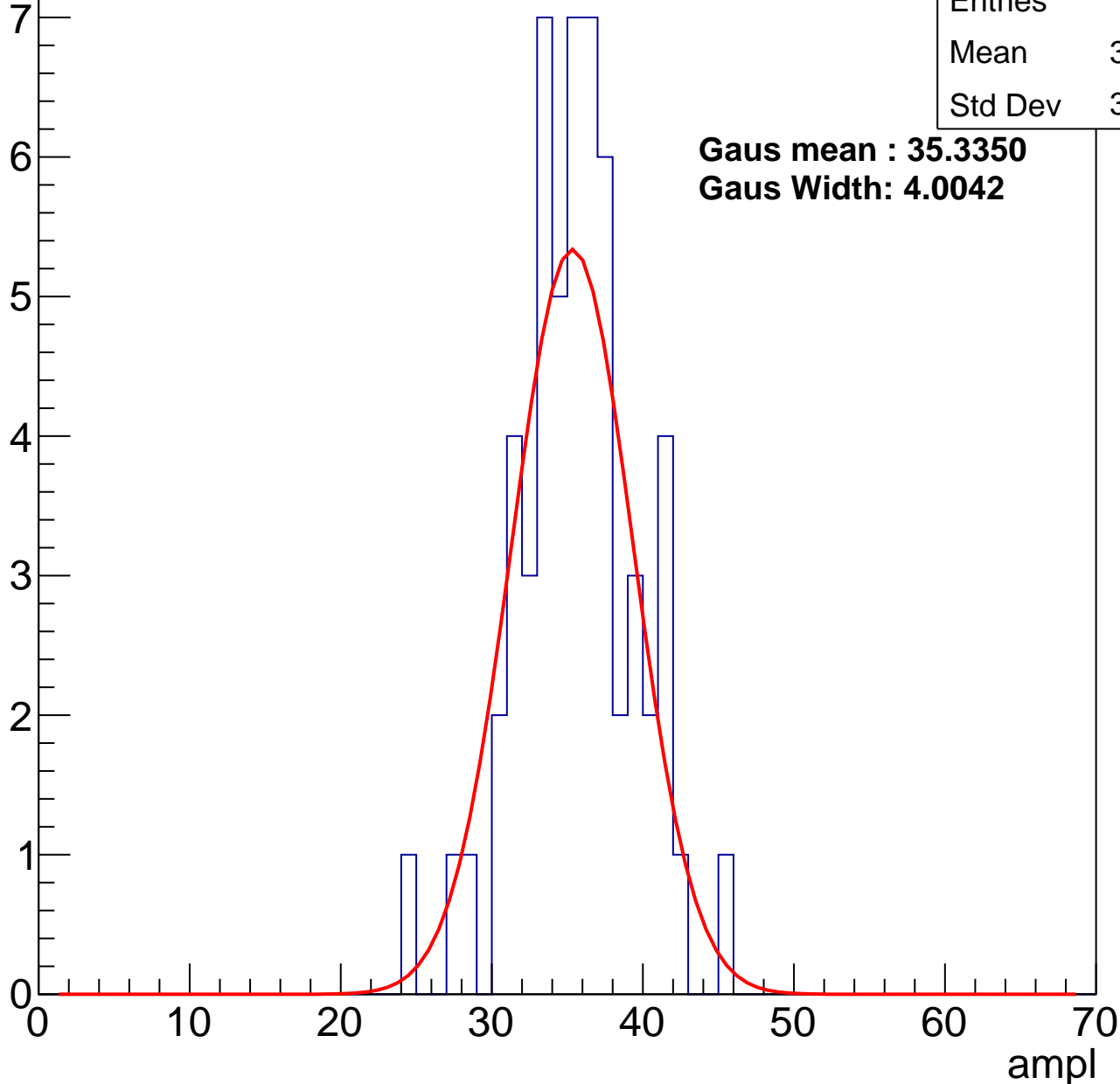
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	35.14
Std Dev	3.855

Gaus mean : 35.3350

Gaus Width: 4.0042



B1L103S, U1-ch119, adc2

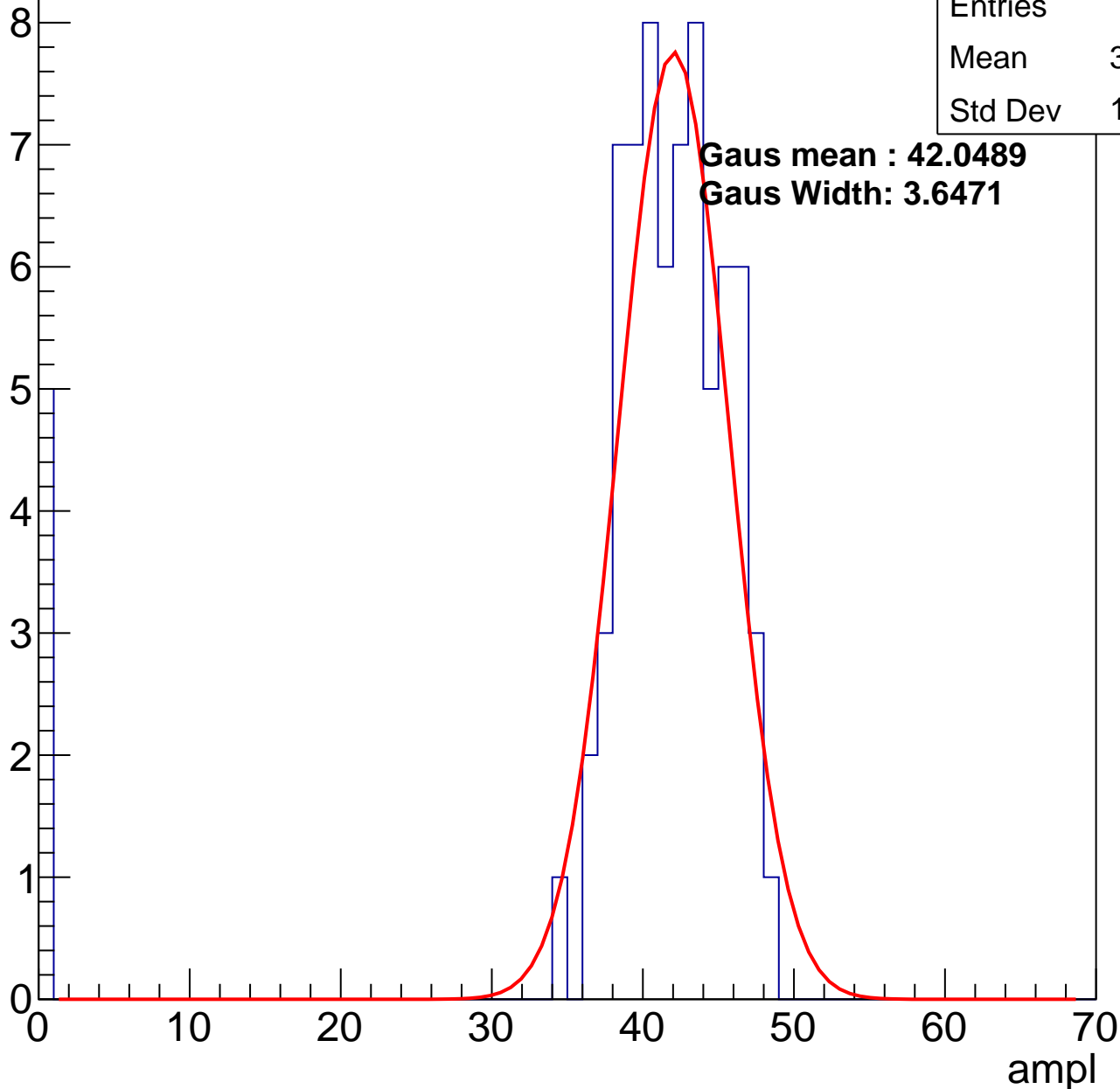
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	38.87
Std Dev	10.83

Gaus mean : 42.0489

Gaus Width: 3.6471

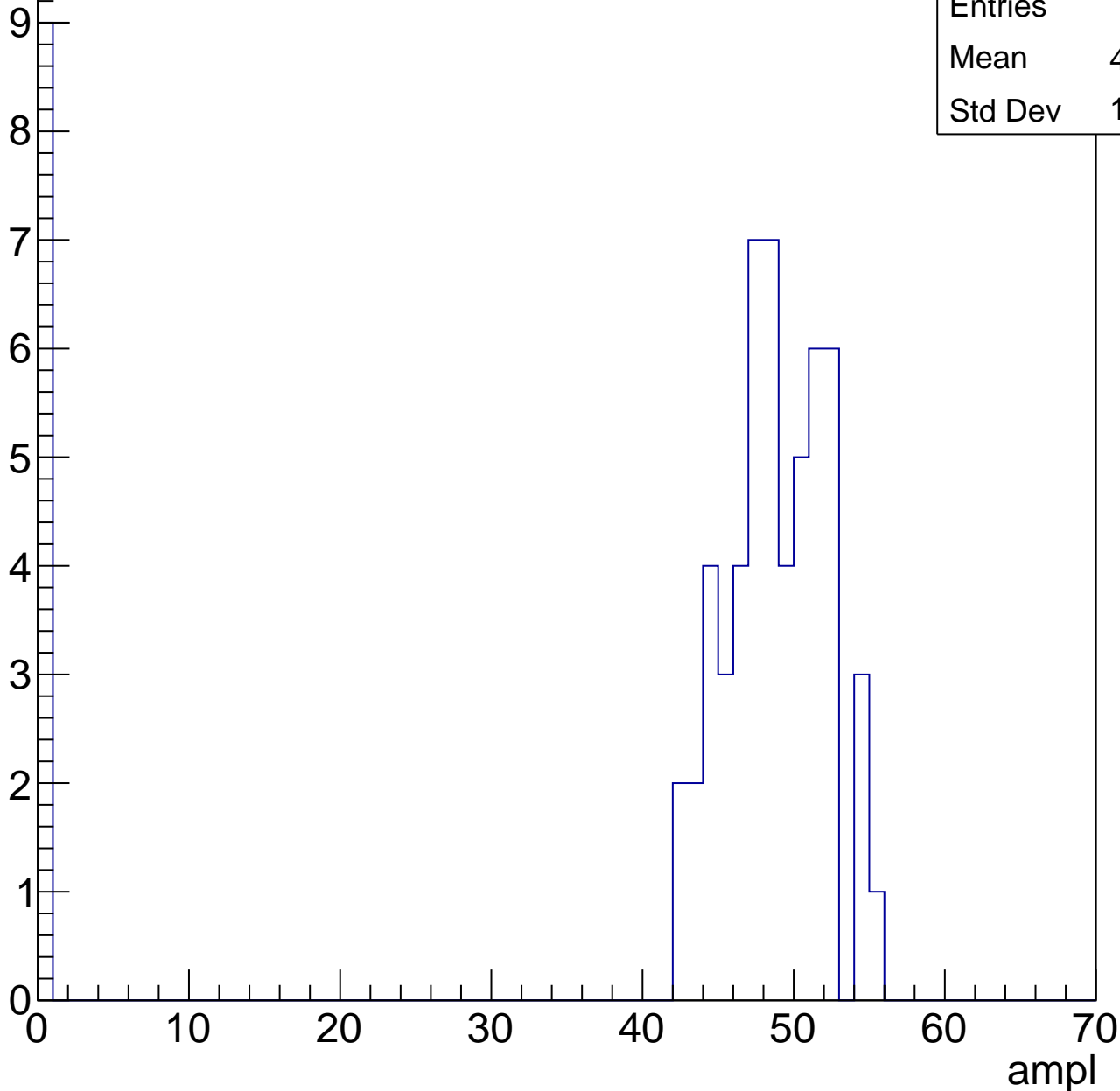


B1L103S, U1-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.44
Std Dev	17.18

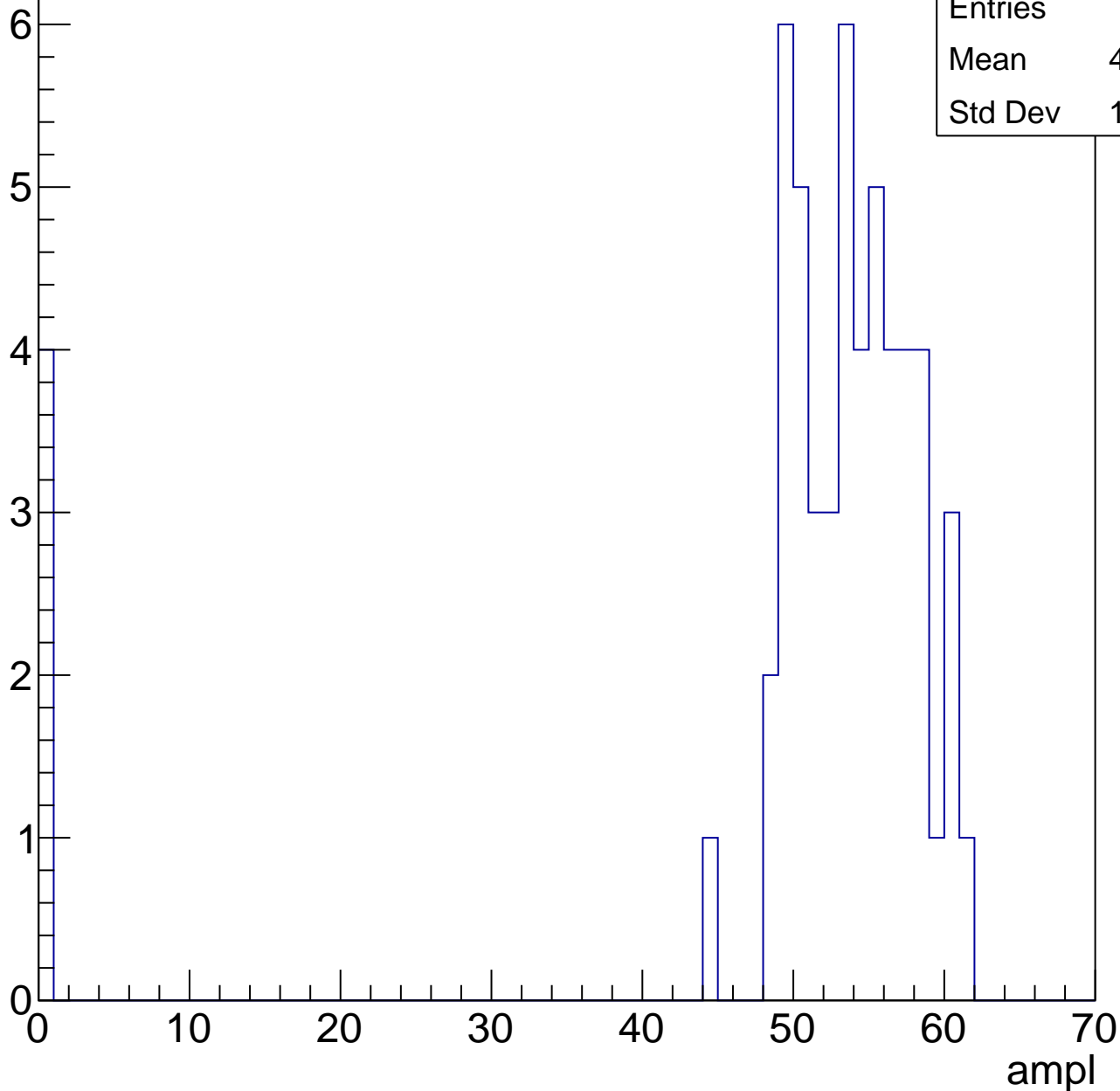


B1L103S, U1-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.75
Std Dev	14.27

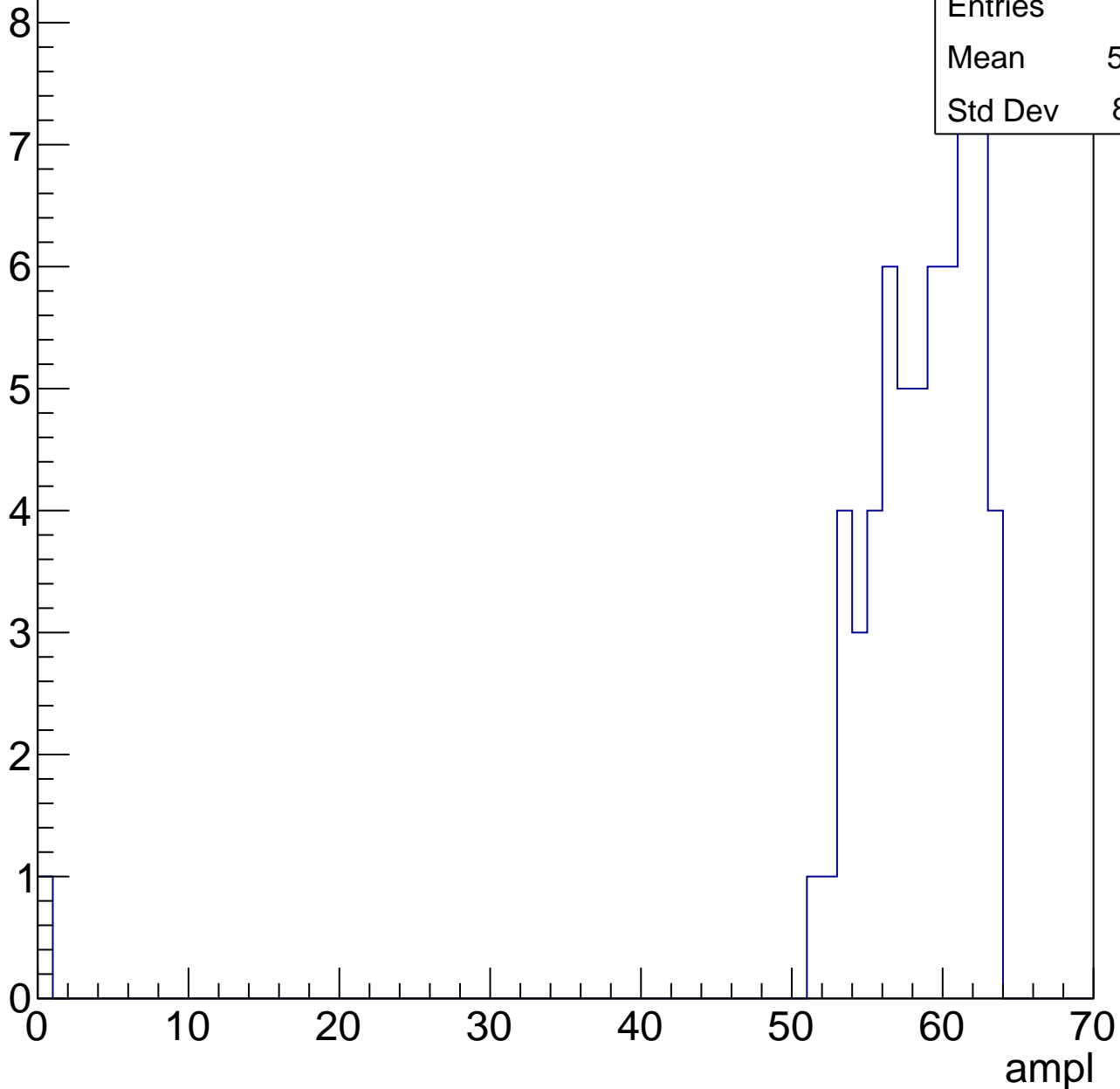


B1L103S, U1-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	57.39
Std Dev	8.001

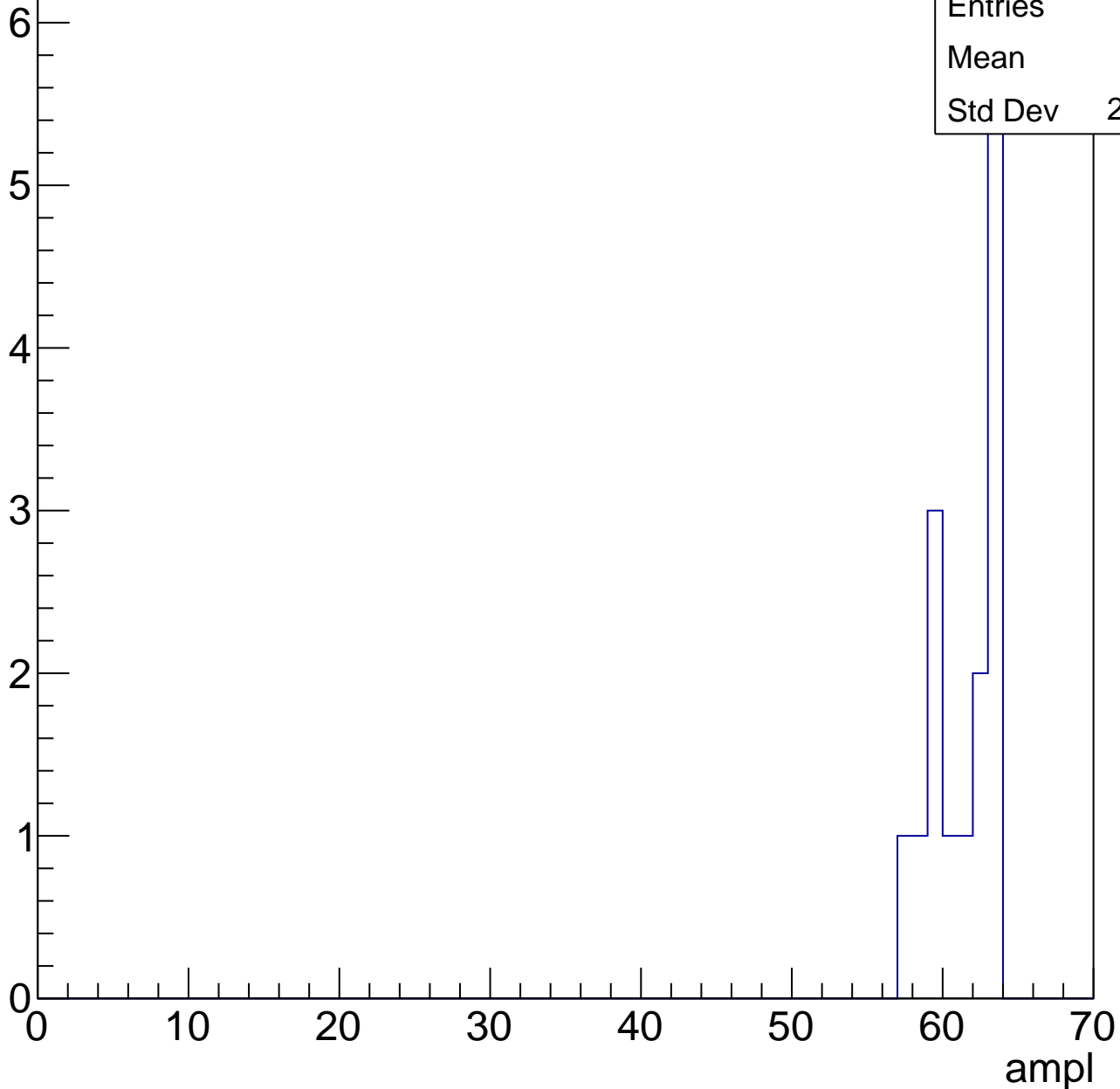


B1L103S, U1-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61
Std Dev	2.066



B1L103S, U1-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

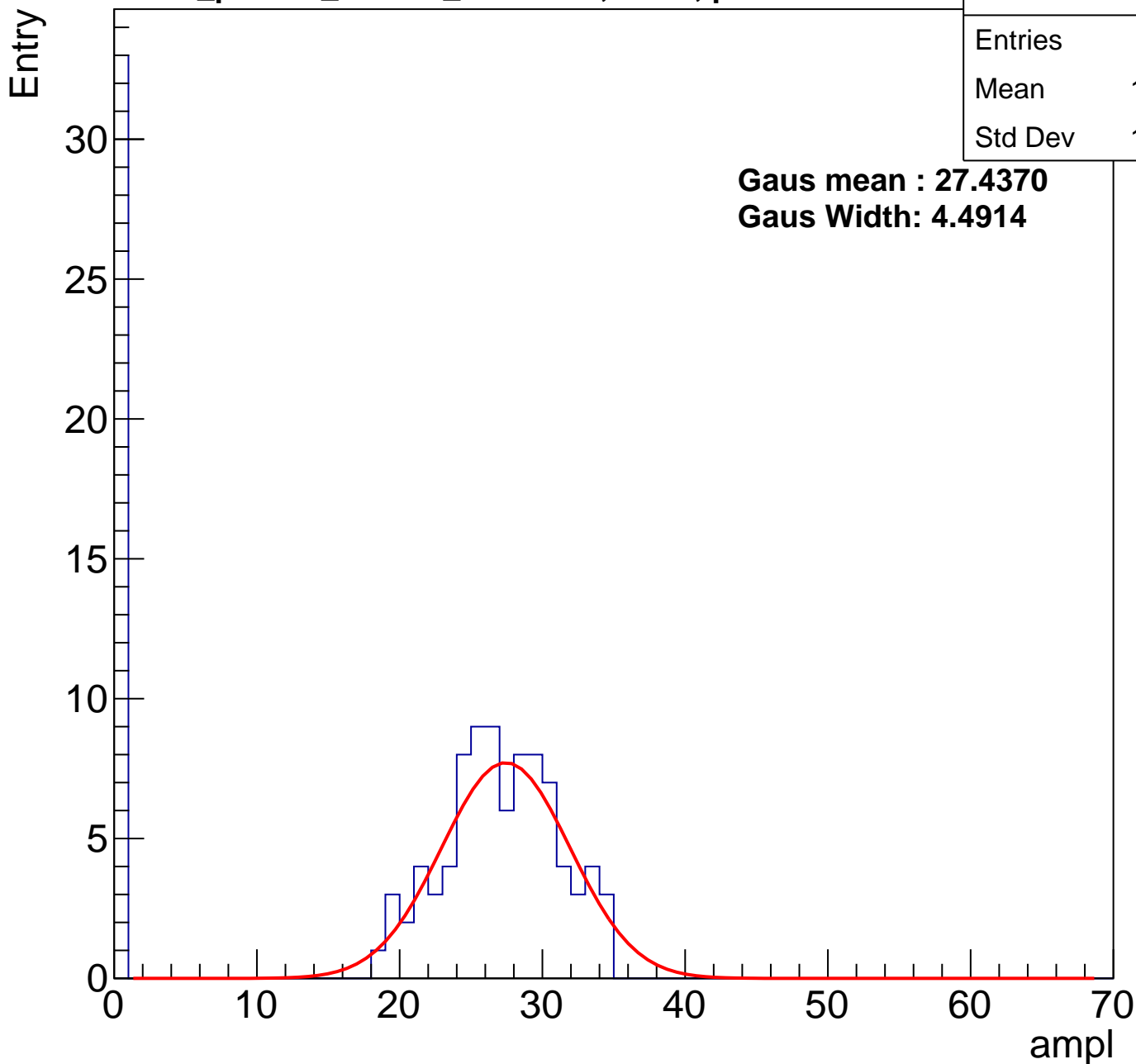
B1L103S, U1-ch120, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	119
Mean	19.24
Std Dev	12.37

Gaus mean : 27.4370

Gaus Width: 4.4914



B1L103S, U1-ch120, adc1

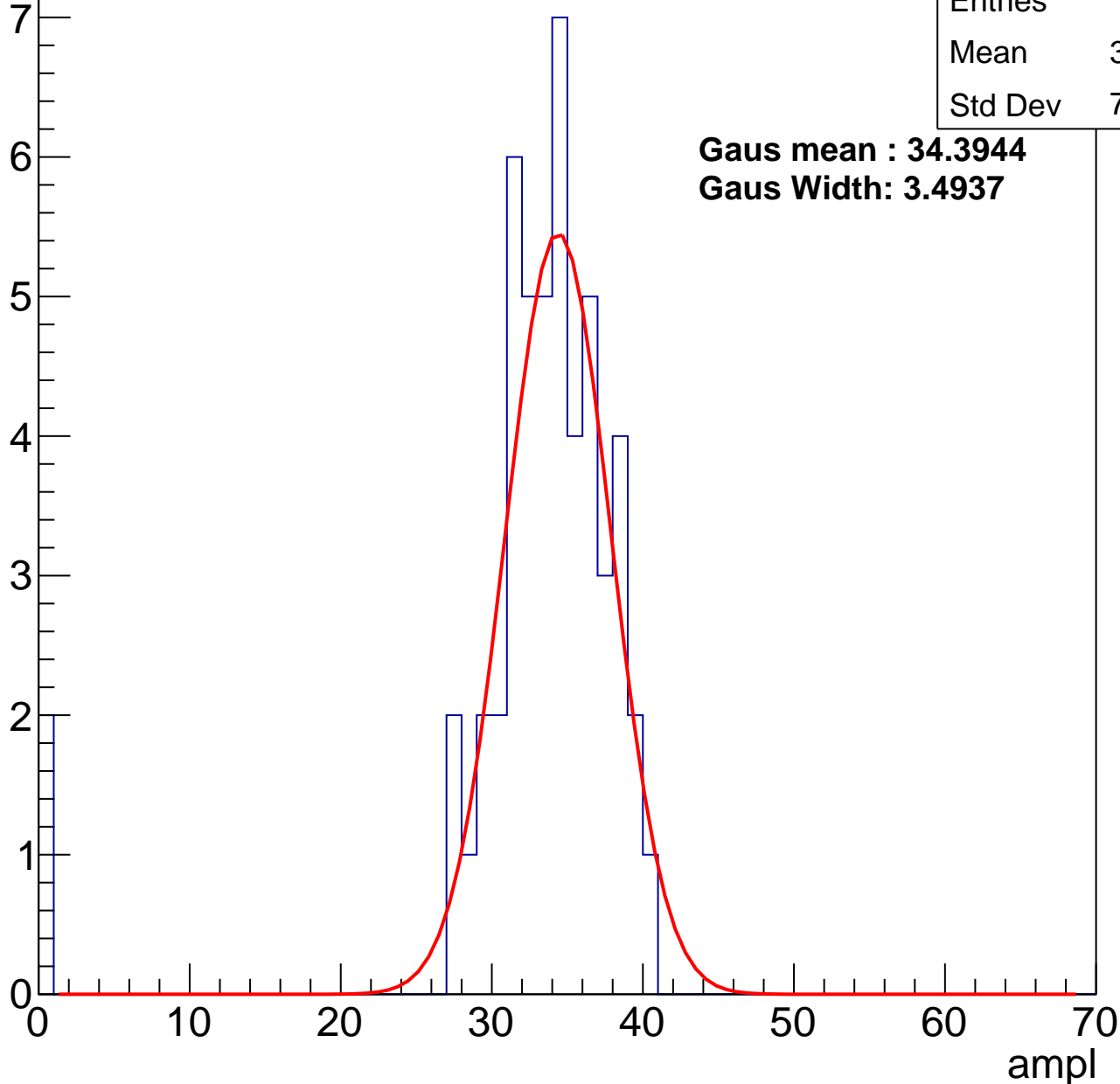
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	32.35
Std Dev	7.235

Gaus mean : 34.3944

Gaus Width: 3.4937



B1L103S, U1-ch120, adc2

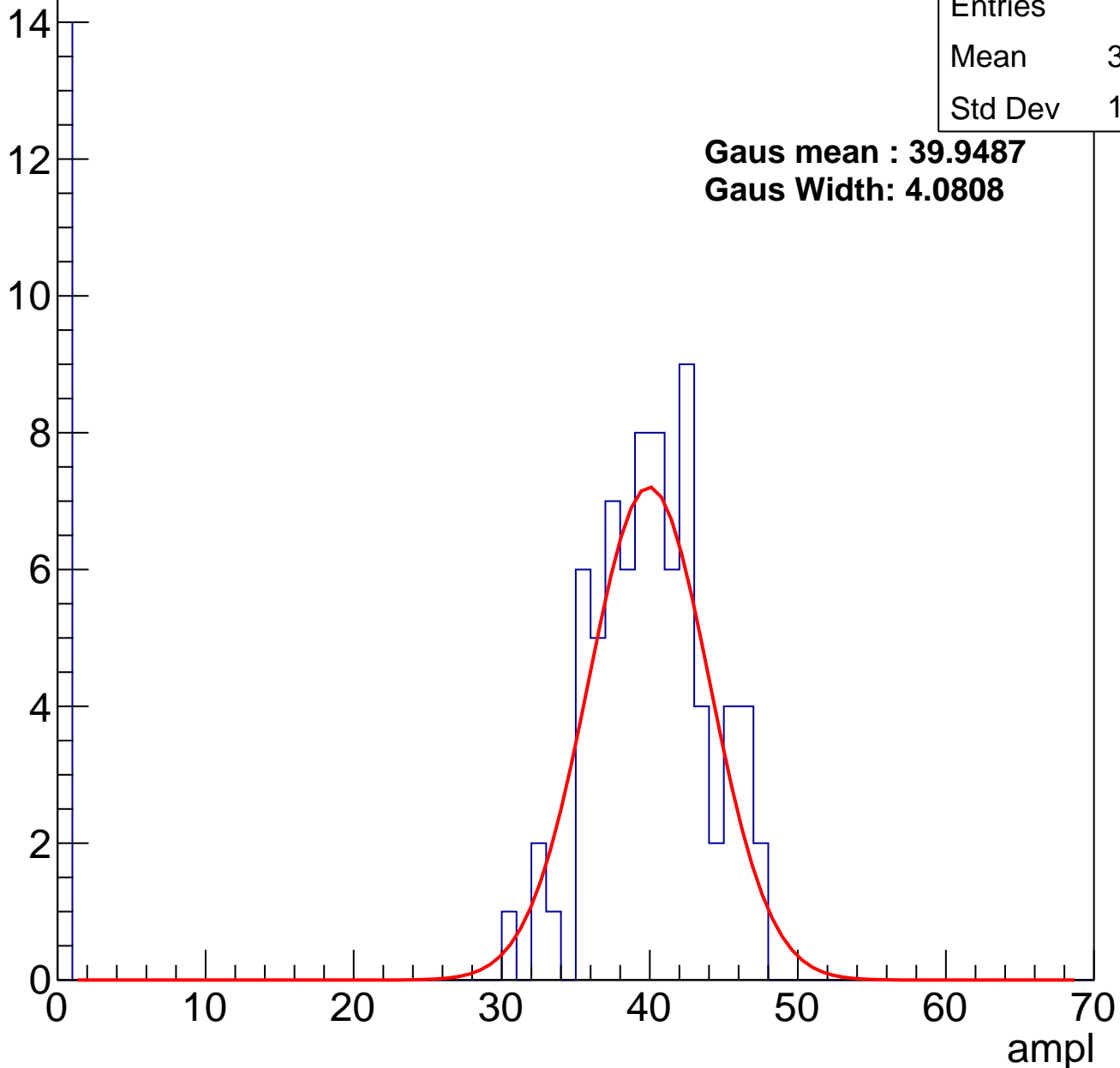
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	33.46
Std Dev	14.86

Gaus mean : 39.9487

Gaus Width: 4.0808

Entry

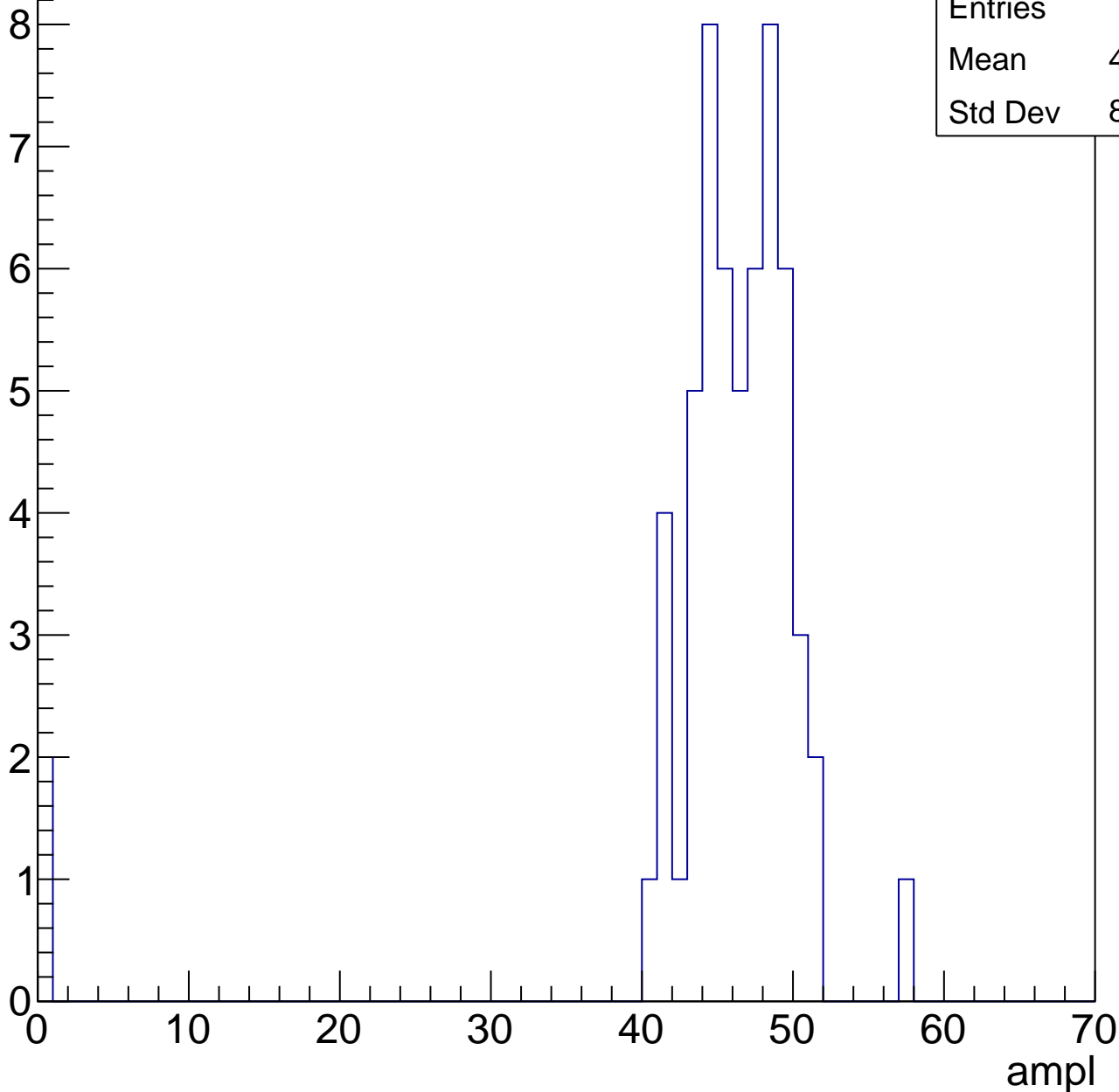


B1L103S, U1-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	44.52
Std Dev	8.954

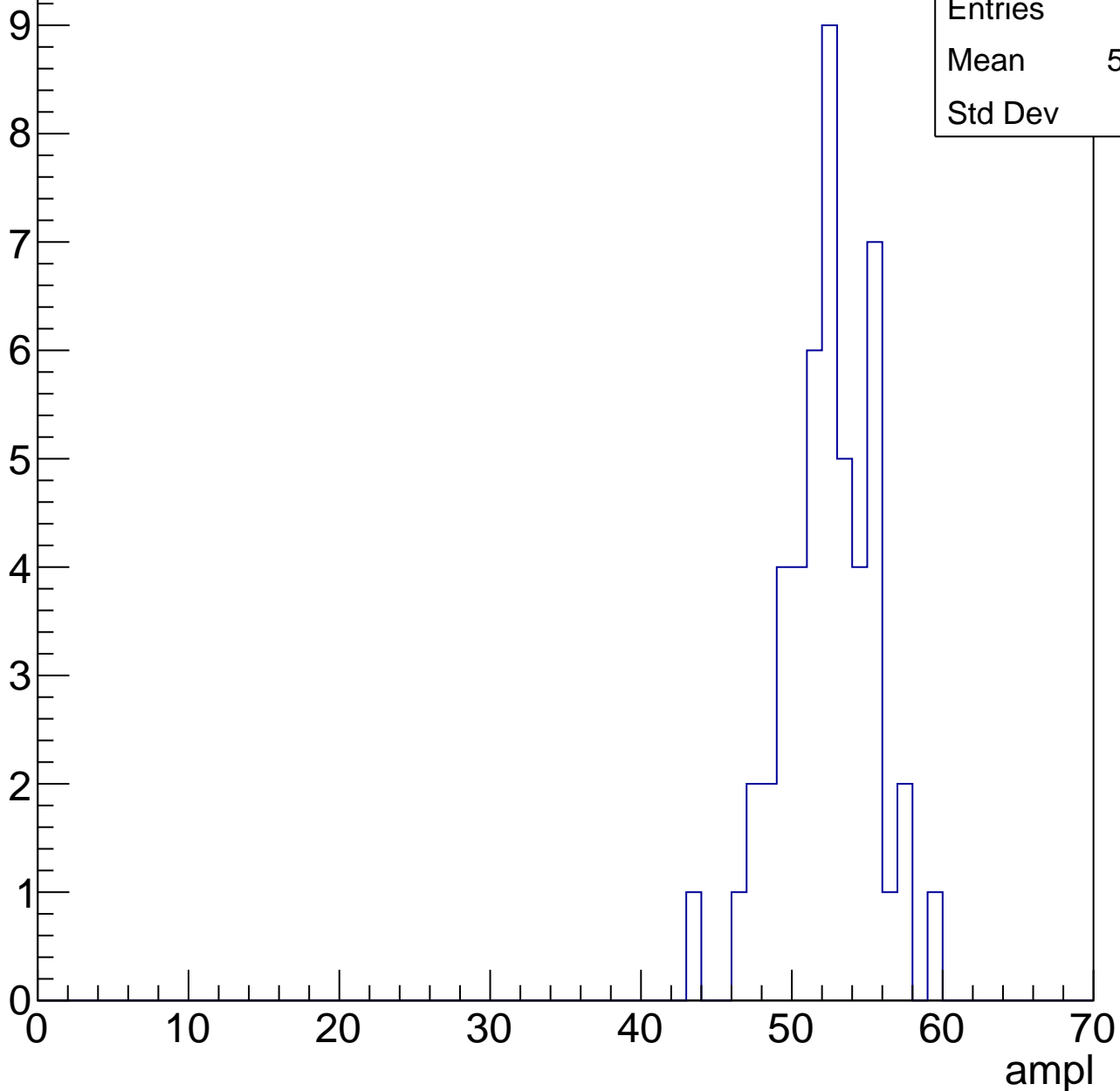


B1L103S, U1-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	51.92
Std Dev	3.05

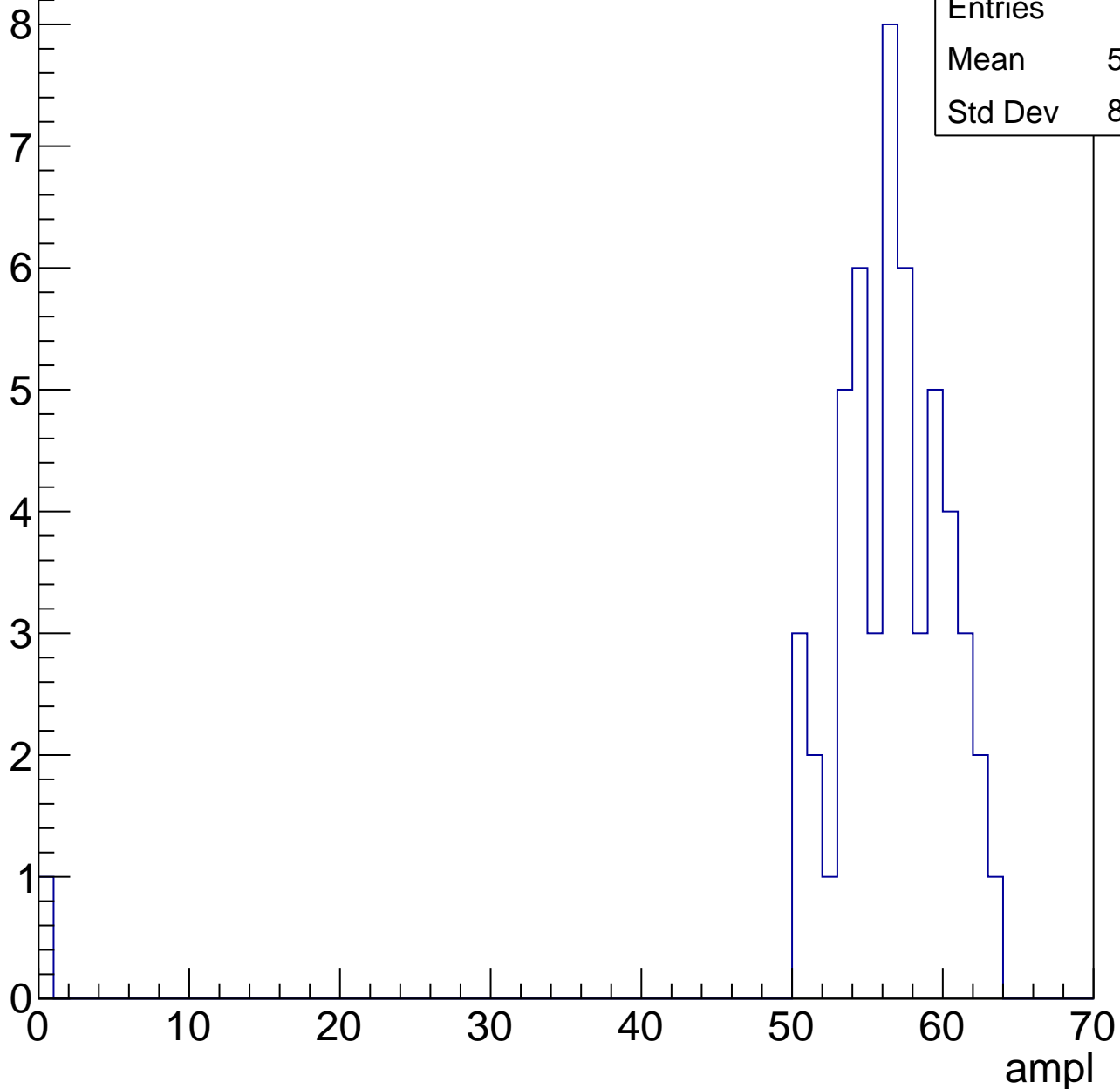


B1L103S, U1-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

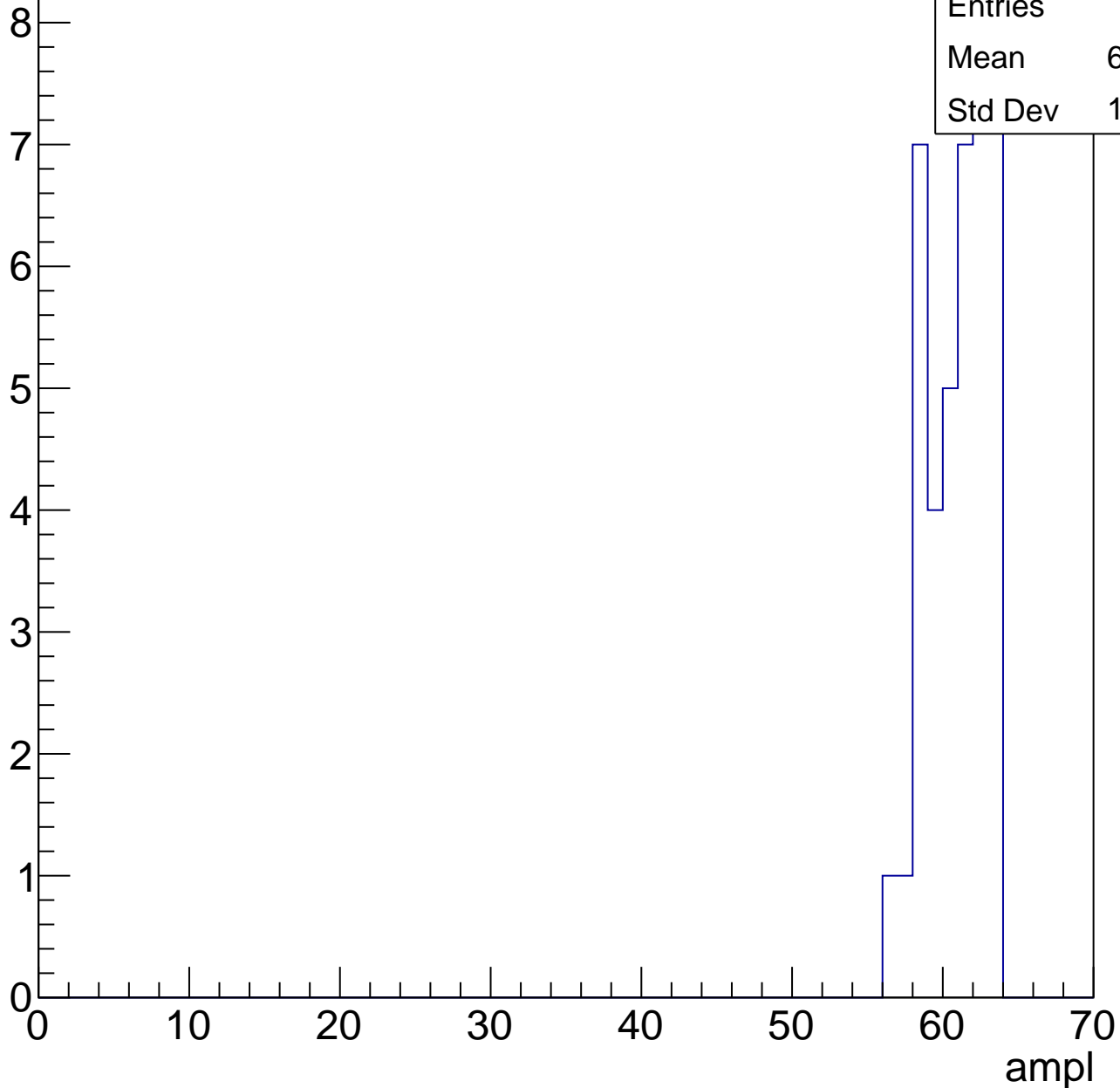
Entries	53
Mean	55.23
Std Dev	8.325



B1L103S, U1-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

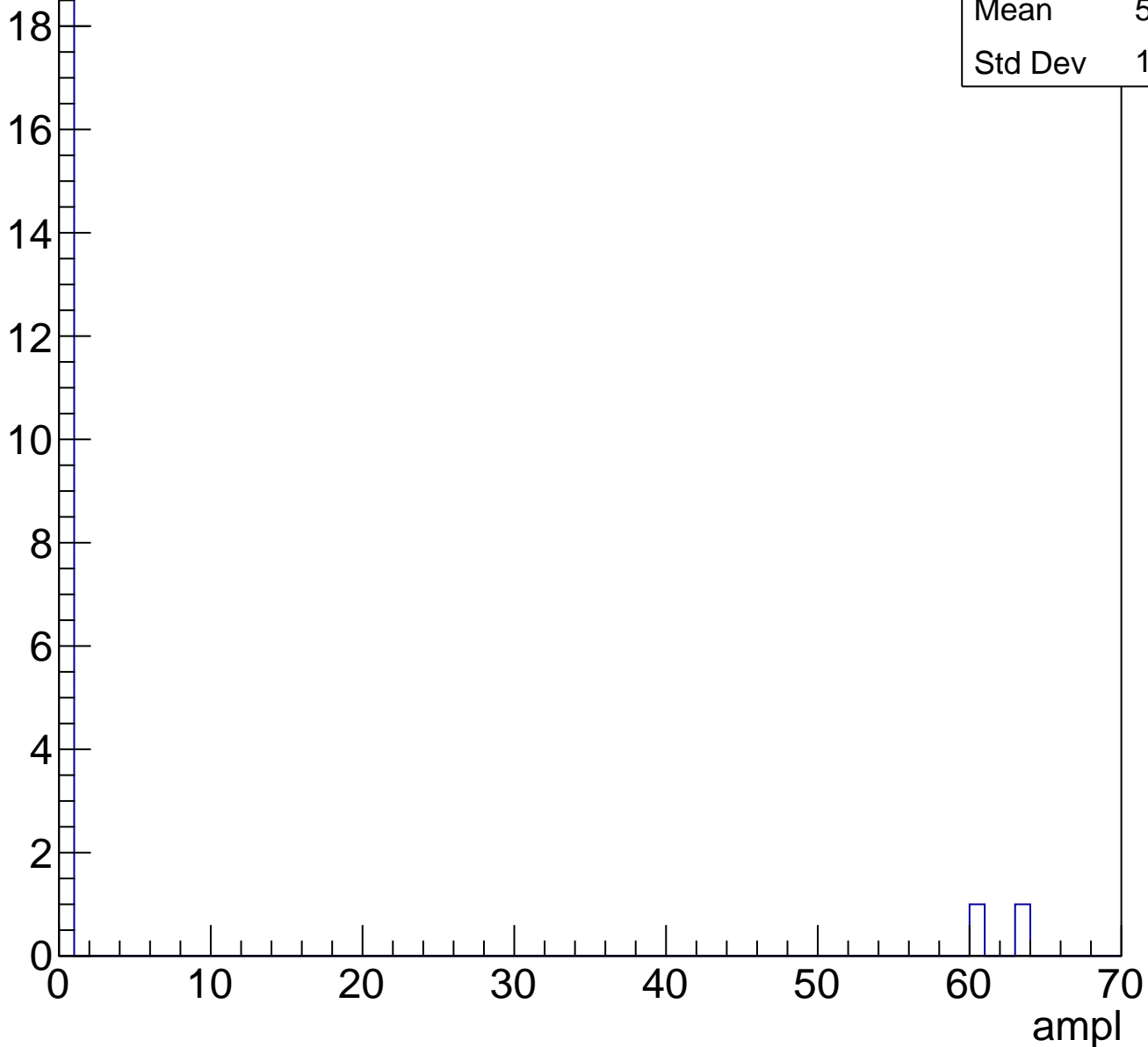


B1L103S, U1-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.857
Std Dev	18.06

Entry



B1L103S, U1-ch121, adc0

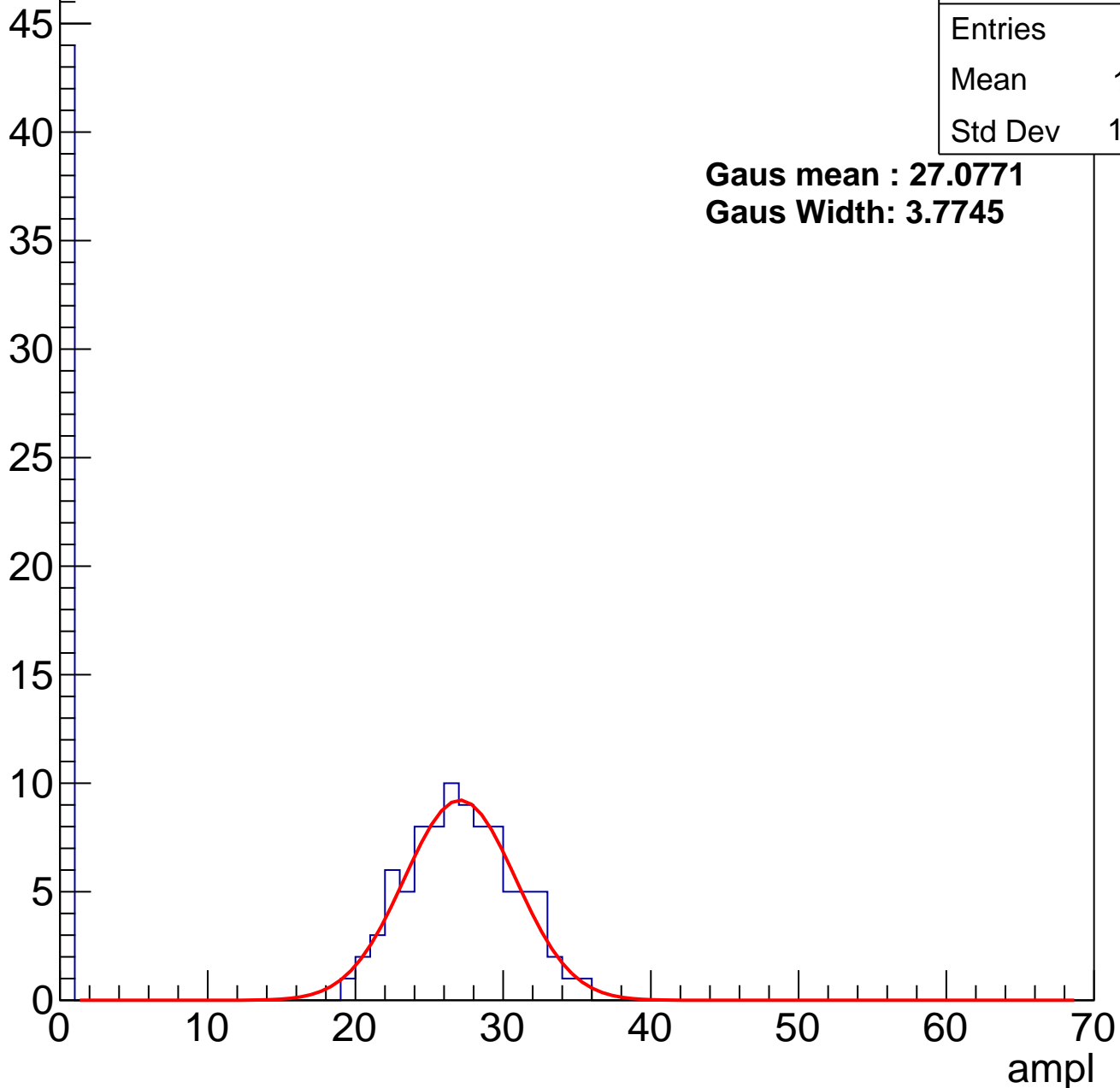
calib_packv5_041523_1651.root, FC#0, port C2

Entries	131
Mean	17.71
Std Dev	12.92

Gaus mean : 27.0771

Gaus Width: 3.7745

Entry



B1L103S, U1-ch121, adc1

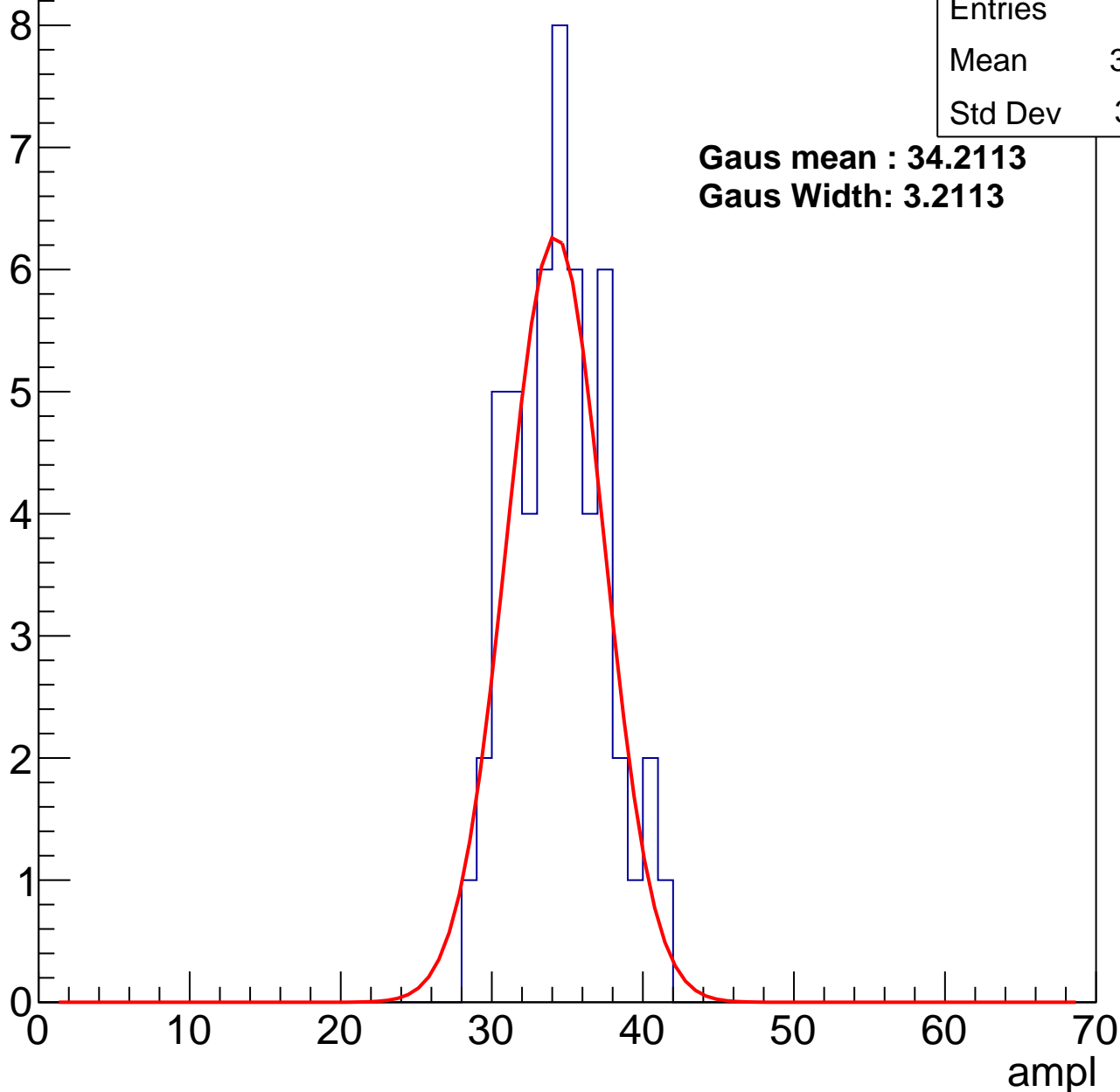
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	33.98
Std Dev	3.031

Gaus mean : 34.2113

Gaus Width: 3.2113



B1L103S, U1-ch121, adc2

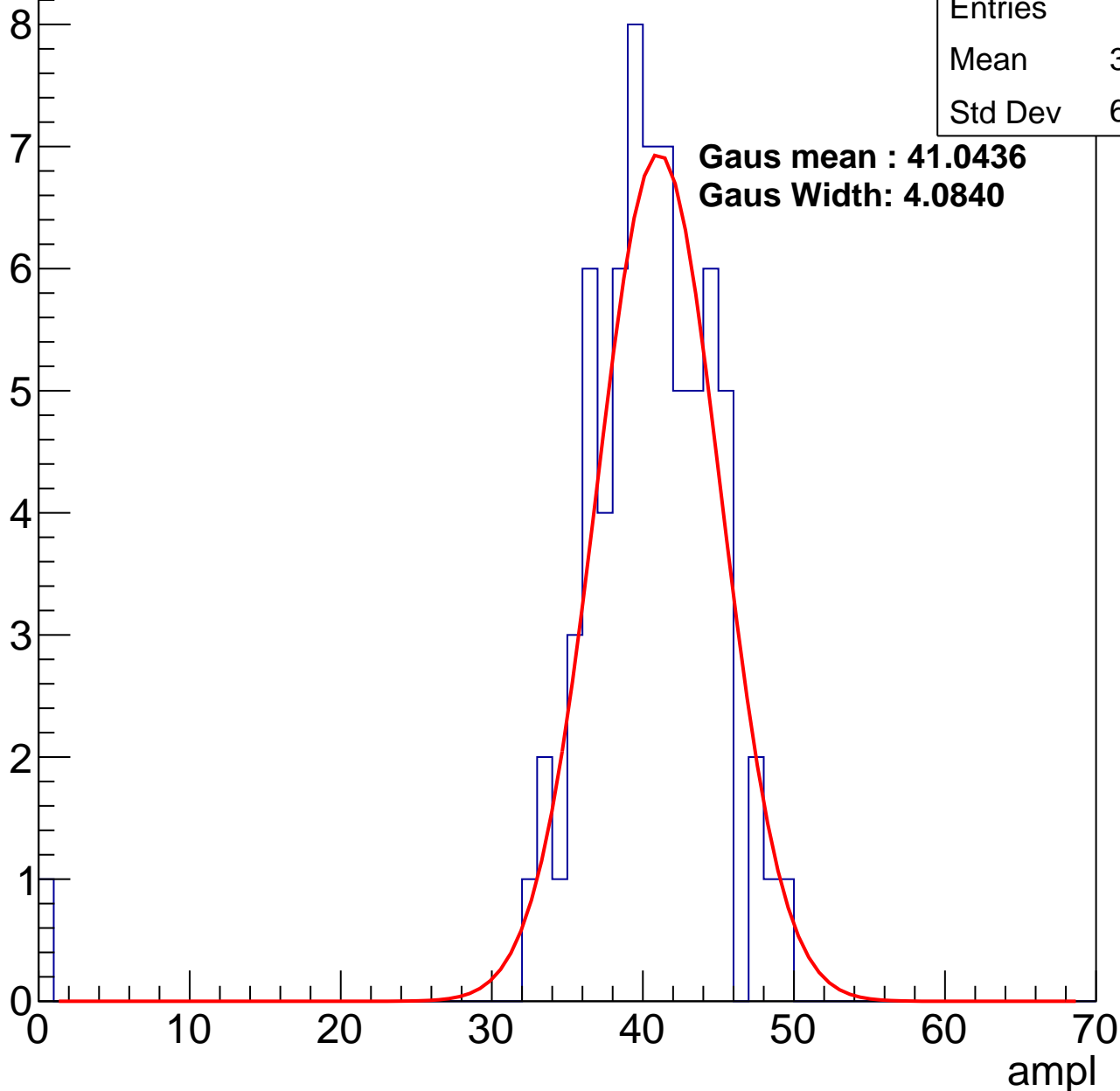
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	39.62
Std Dev	6.008

Gaus mean : 41.0436

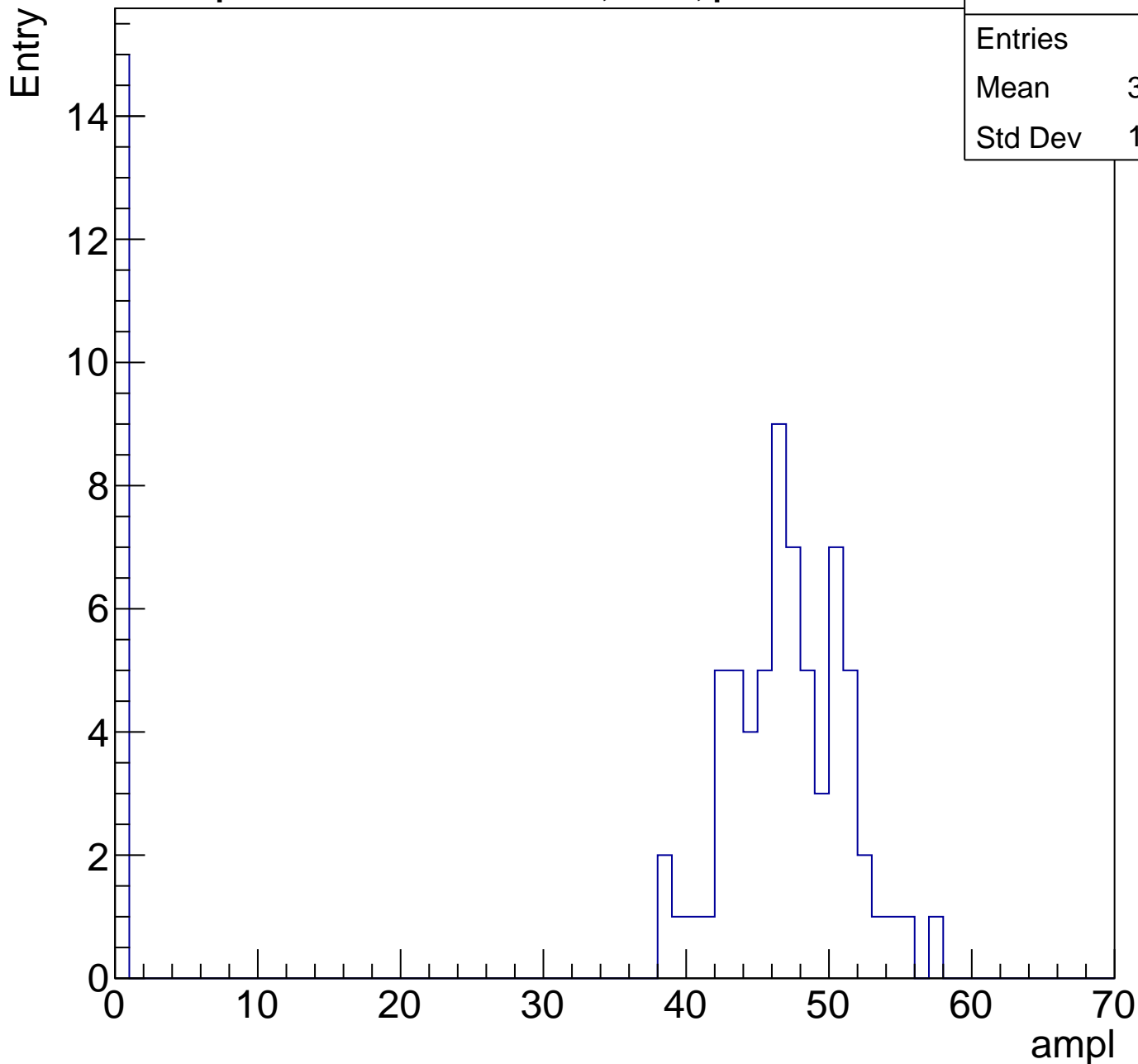
Gaus Width: 4.0840



B1L103S, U1-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	38.02
Std Dev	18.48

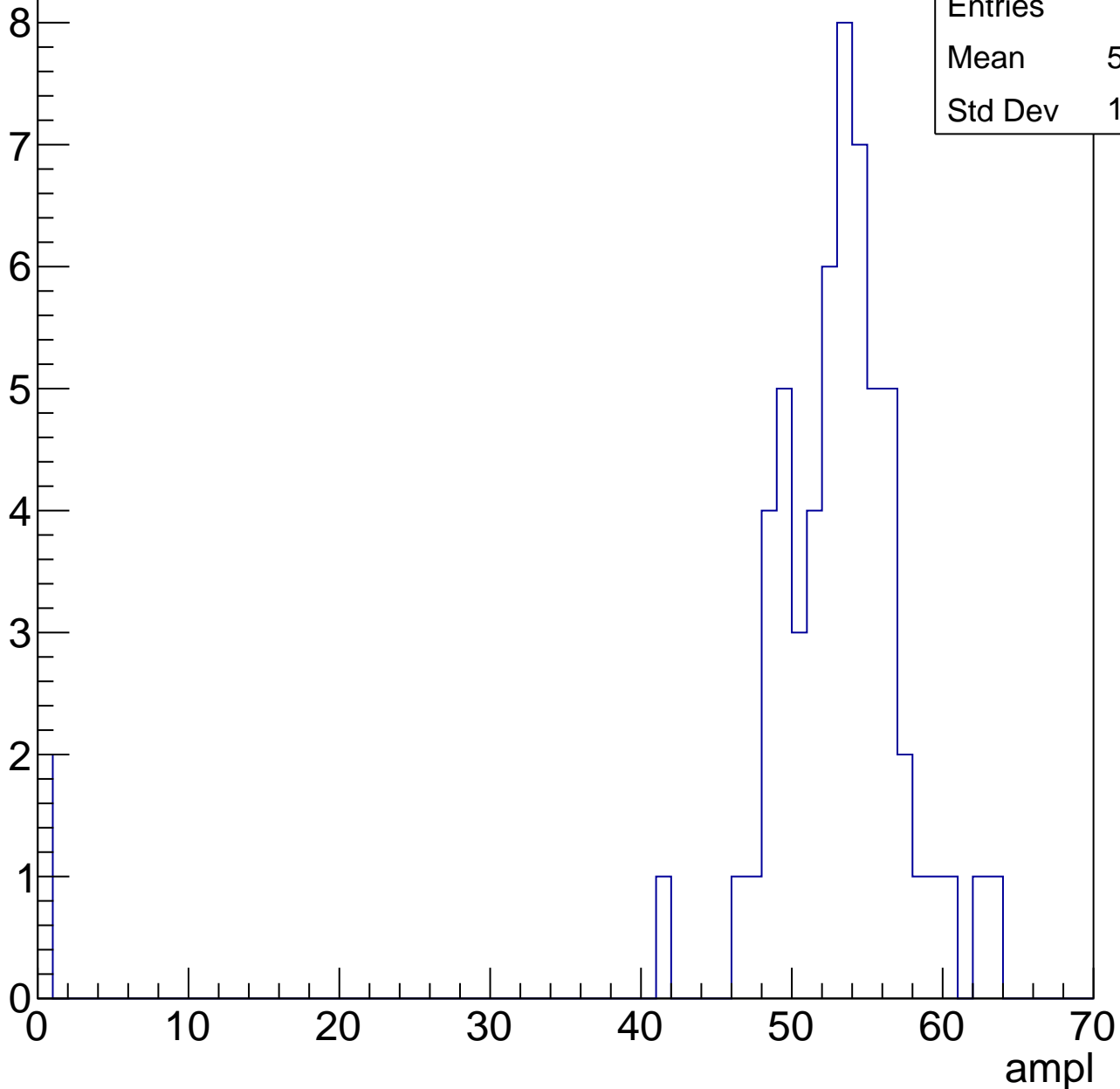


B1L103S, U1-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.02
Std Dev	10.28

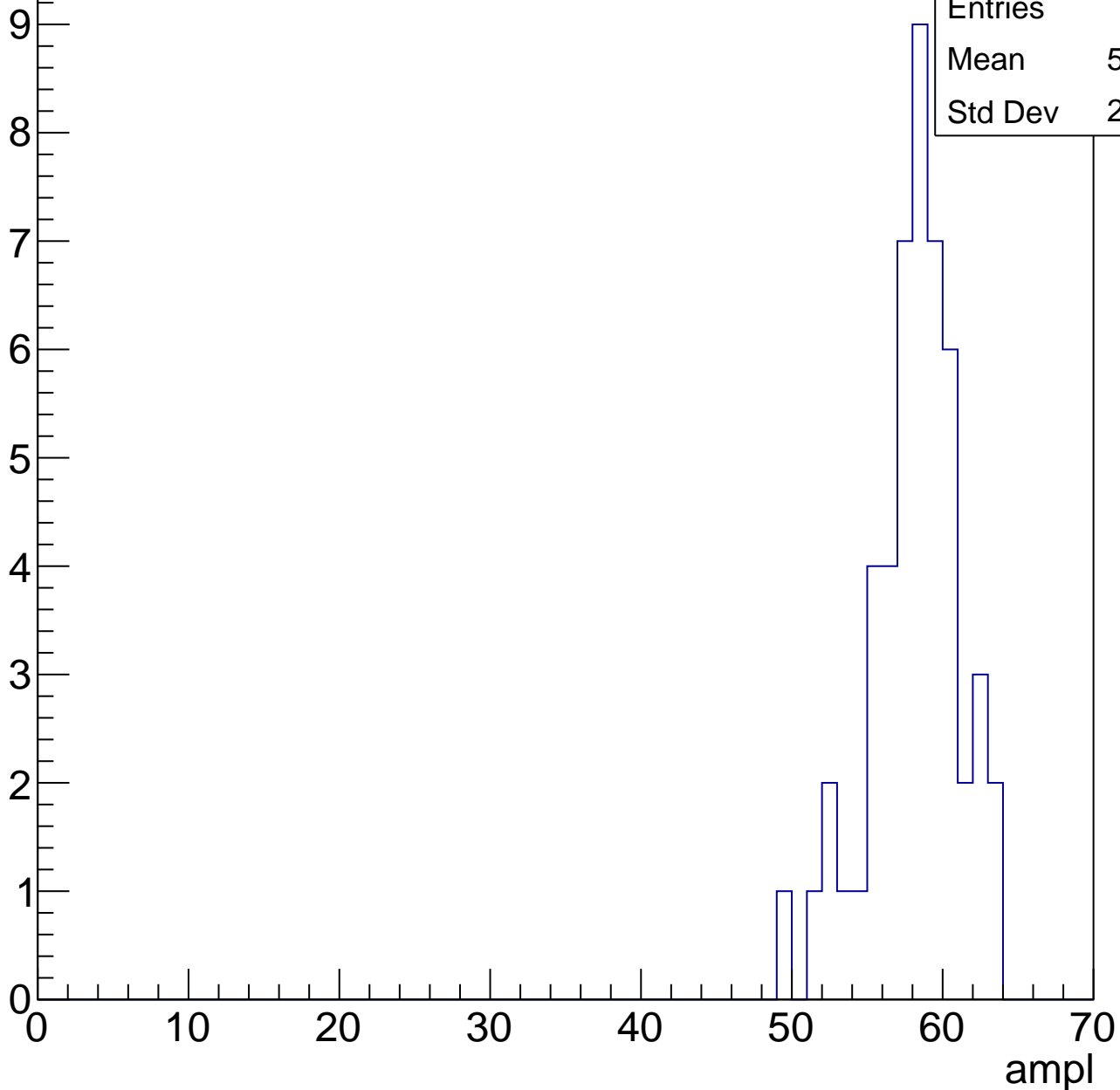


B1L103S, U1-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.66
Std Dev	2.977

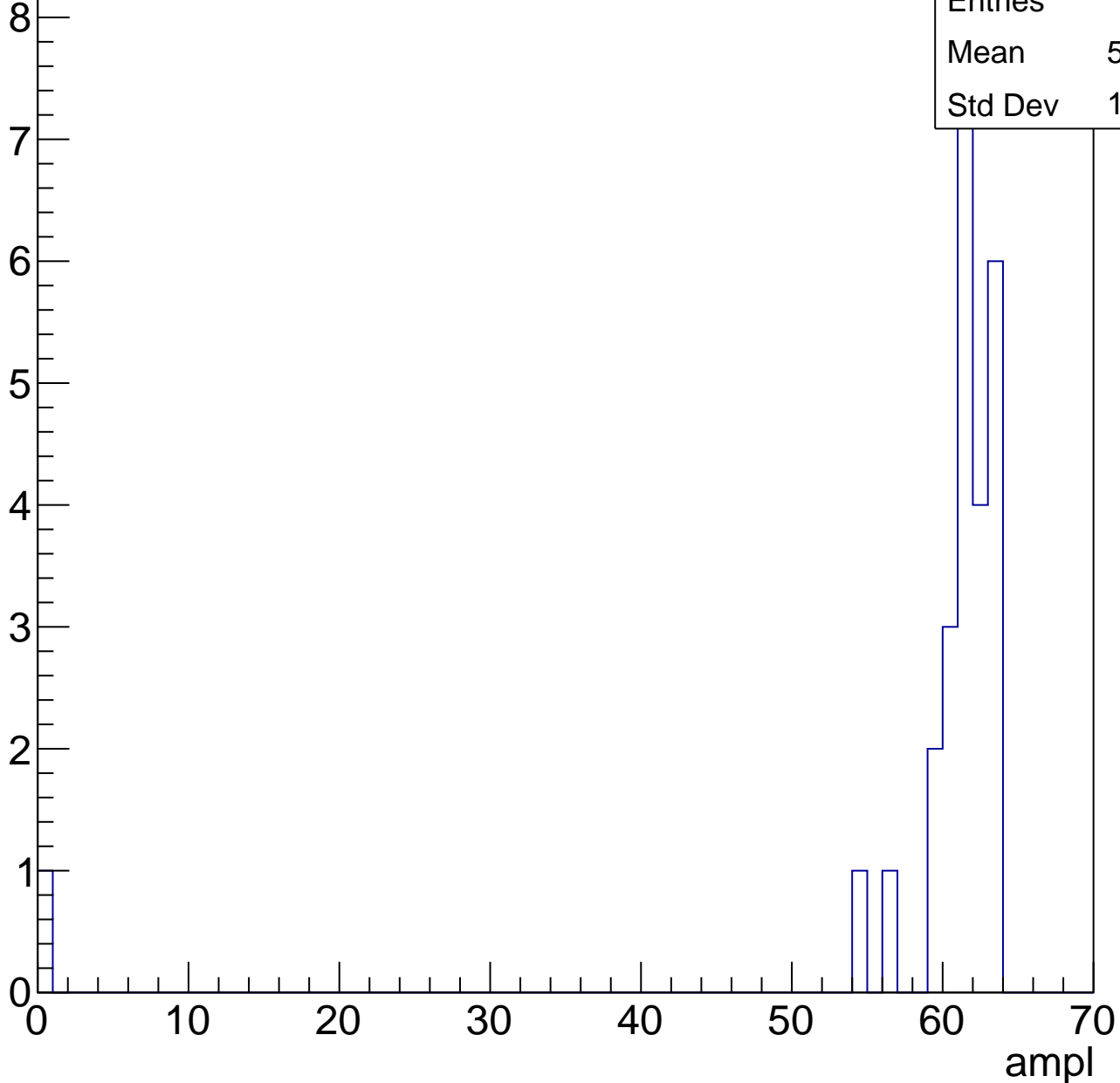


B1L103S, U1-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.54
Std Dev	11.89

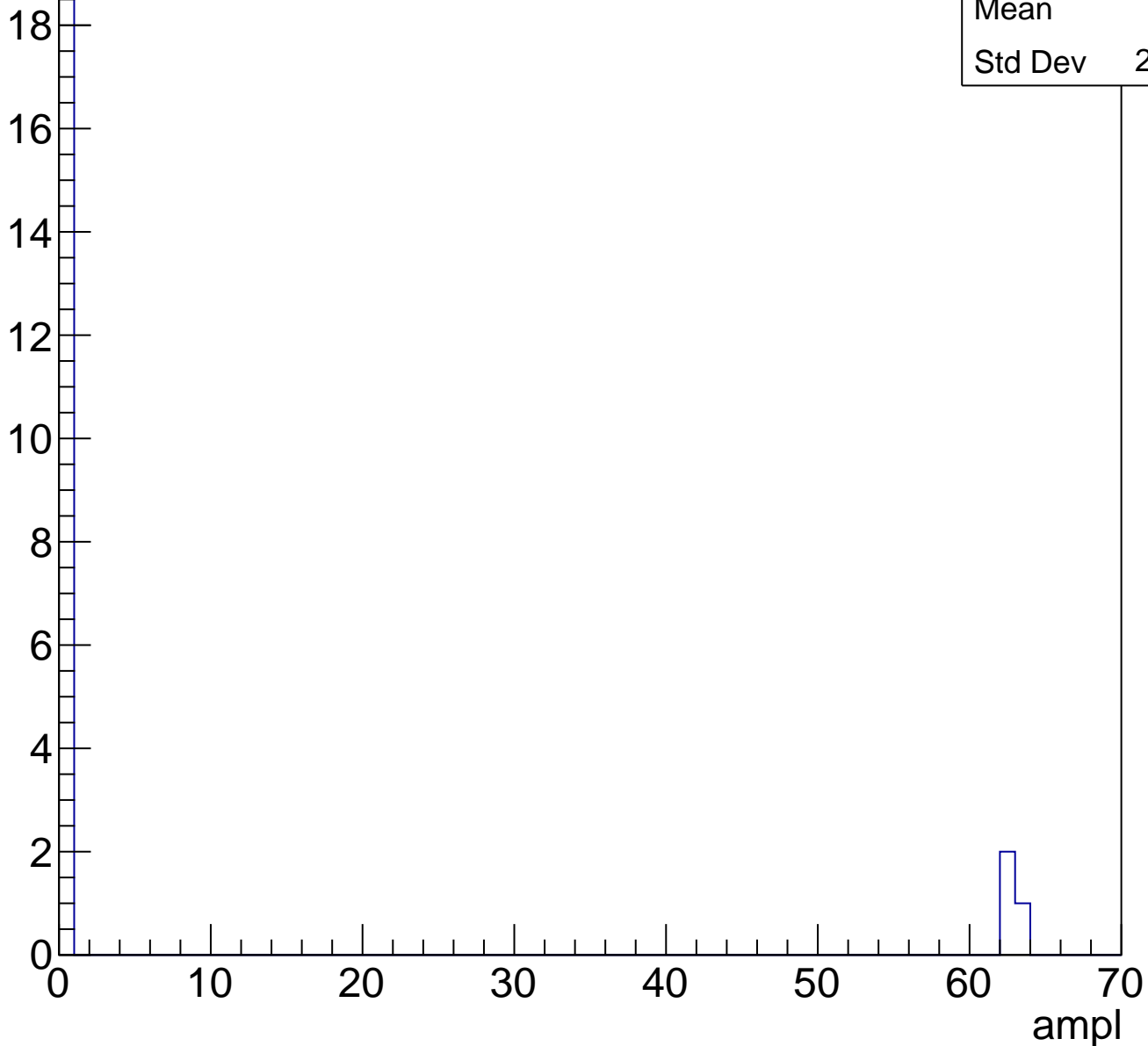


B1L103S, U1-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U1-ch122, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	16.87
Std Dev	13.3

Gaus mean : 27.3419

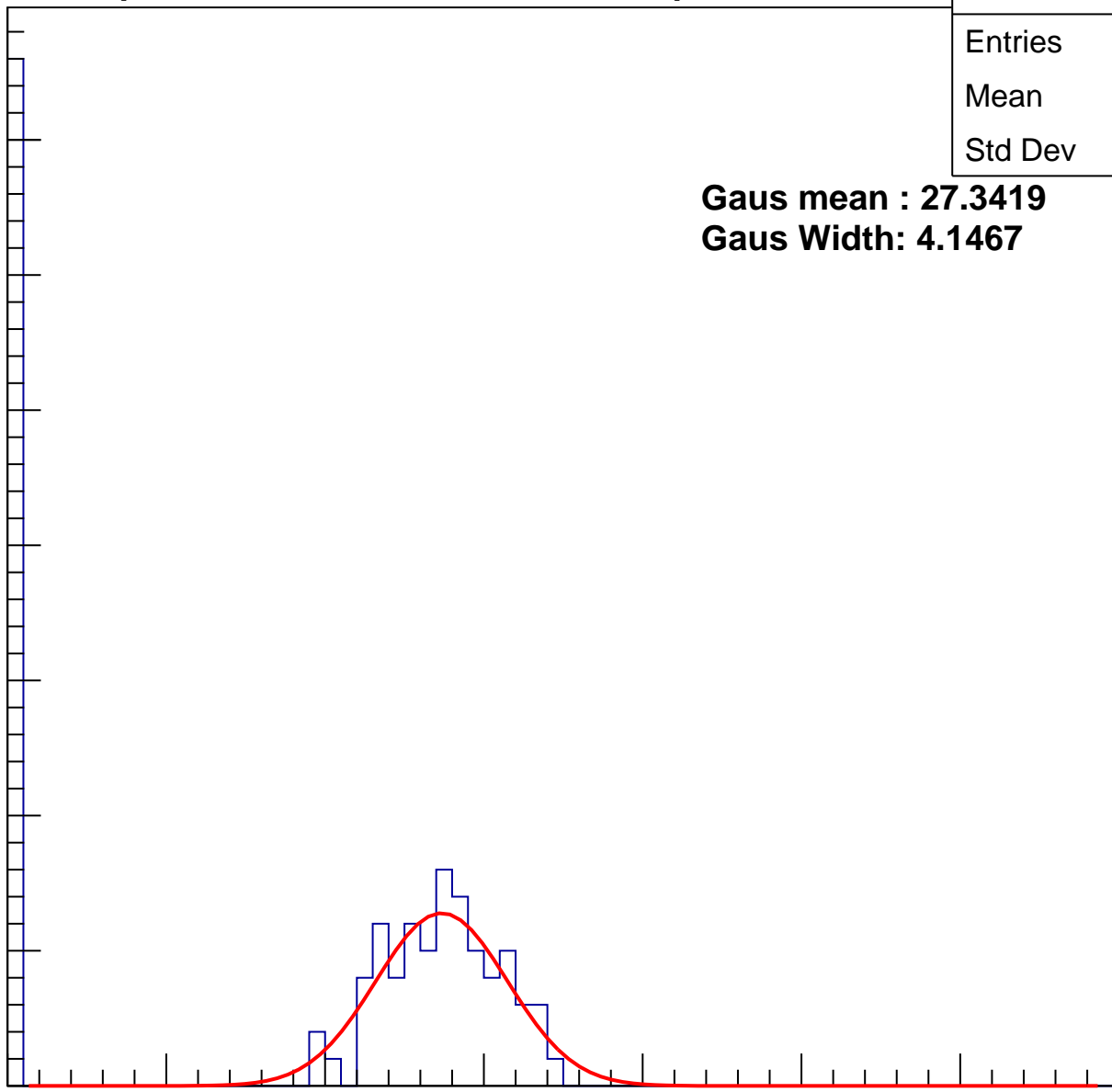
Gaus Width: 4.1467

Entry

35
30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch122, adc1

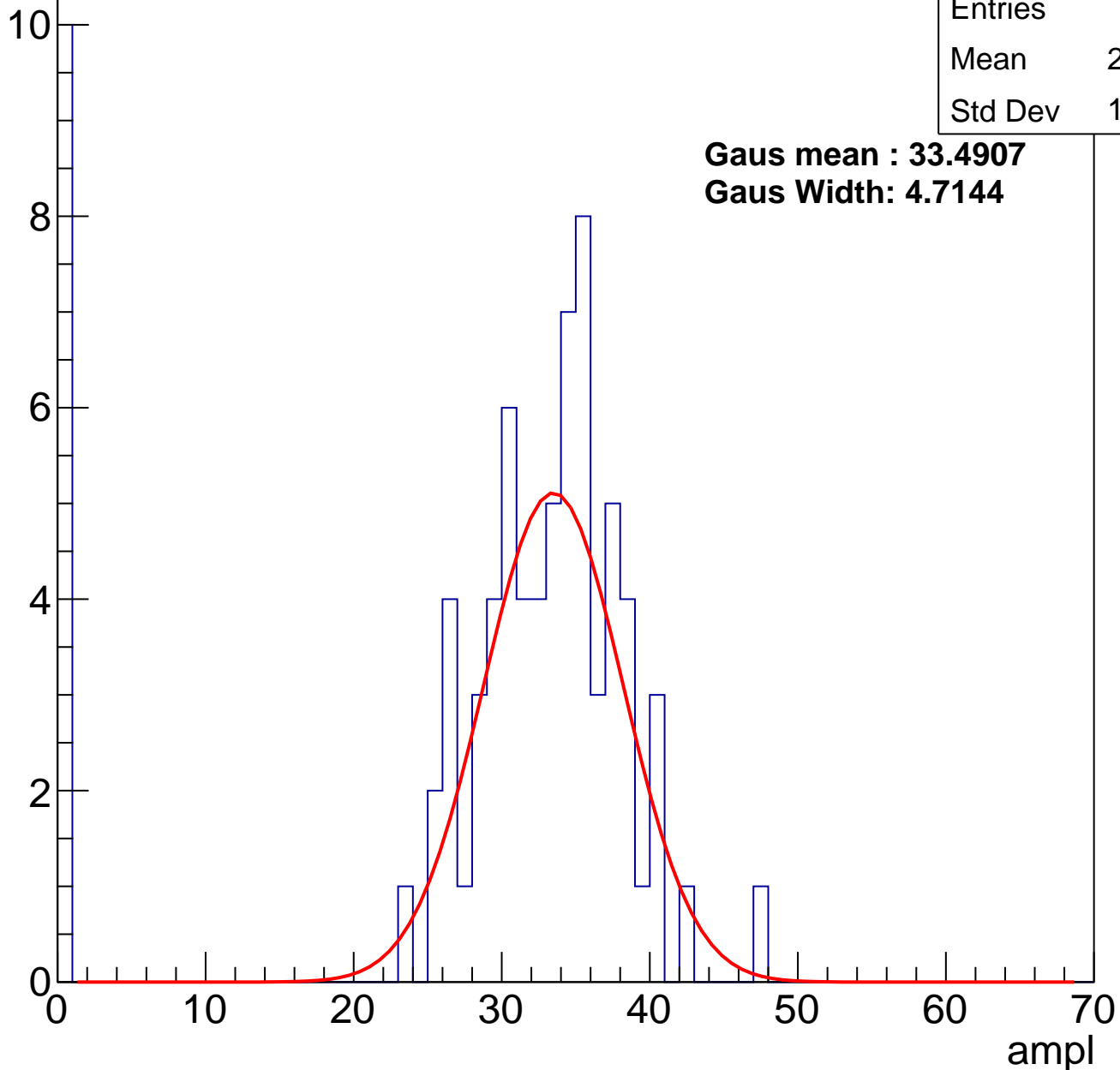
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	28.73
Std Dev	11.88

Gaus mean : 33.4907

Gaus Width: 4.7144

Entry



B1L103S, U1-ch122, adc2

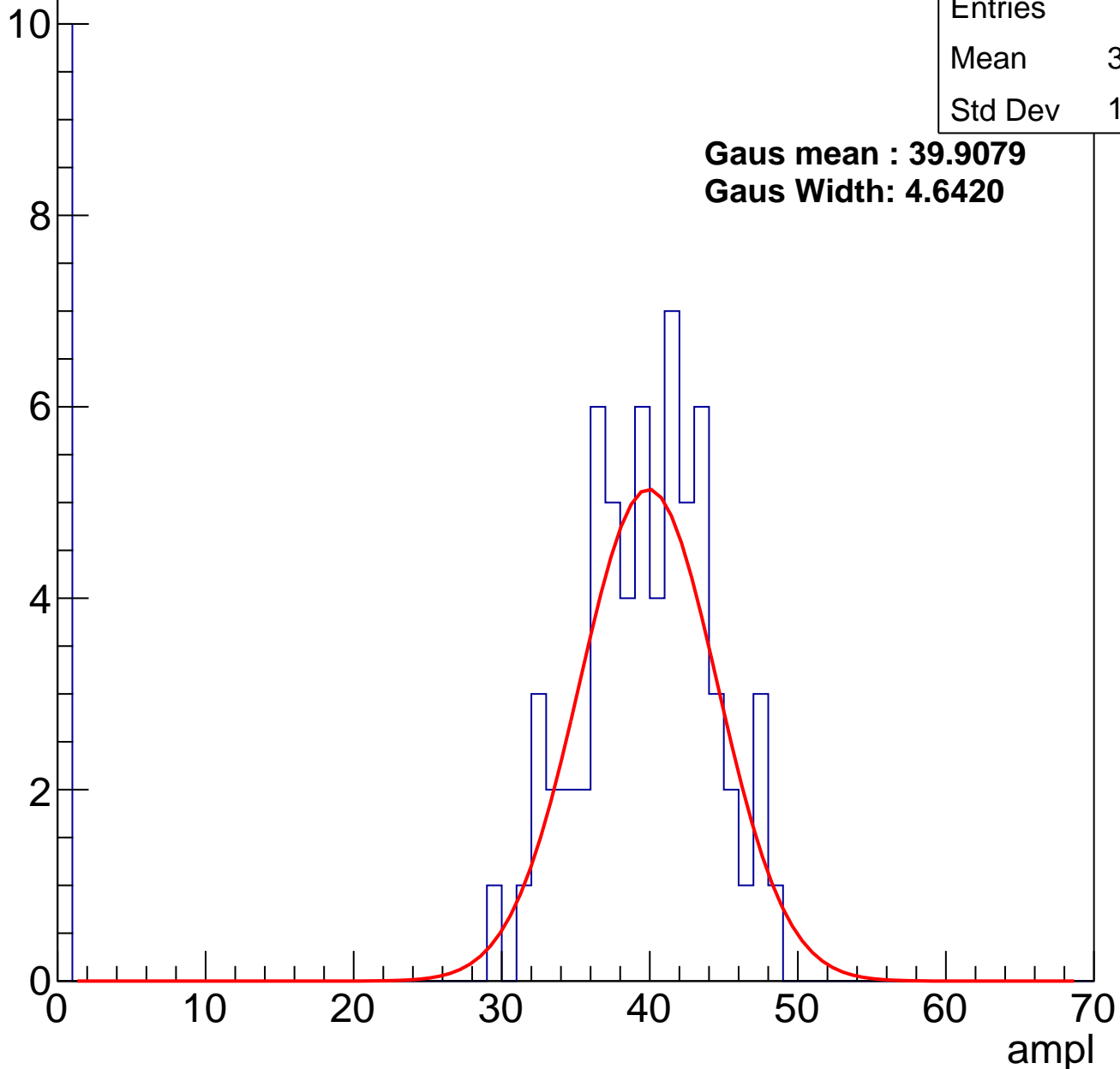
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	34.04
Std Dev	14.03

Gaus mean : 39.9079

Gaus Width: 4.6420

Entry

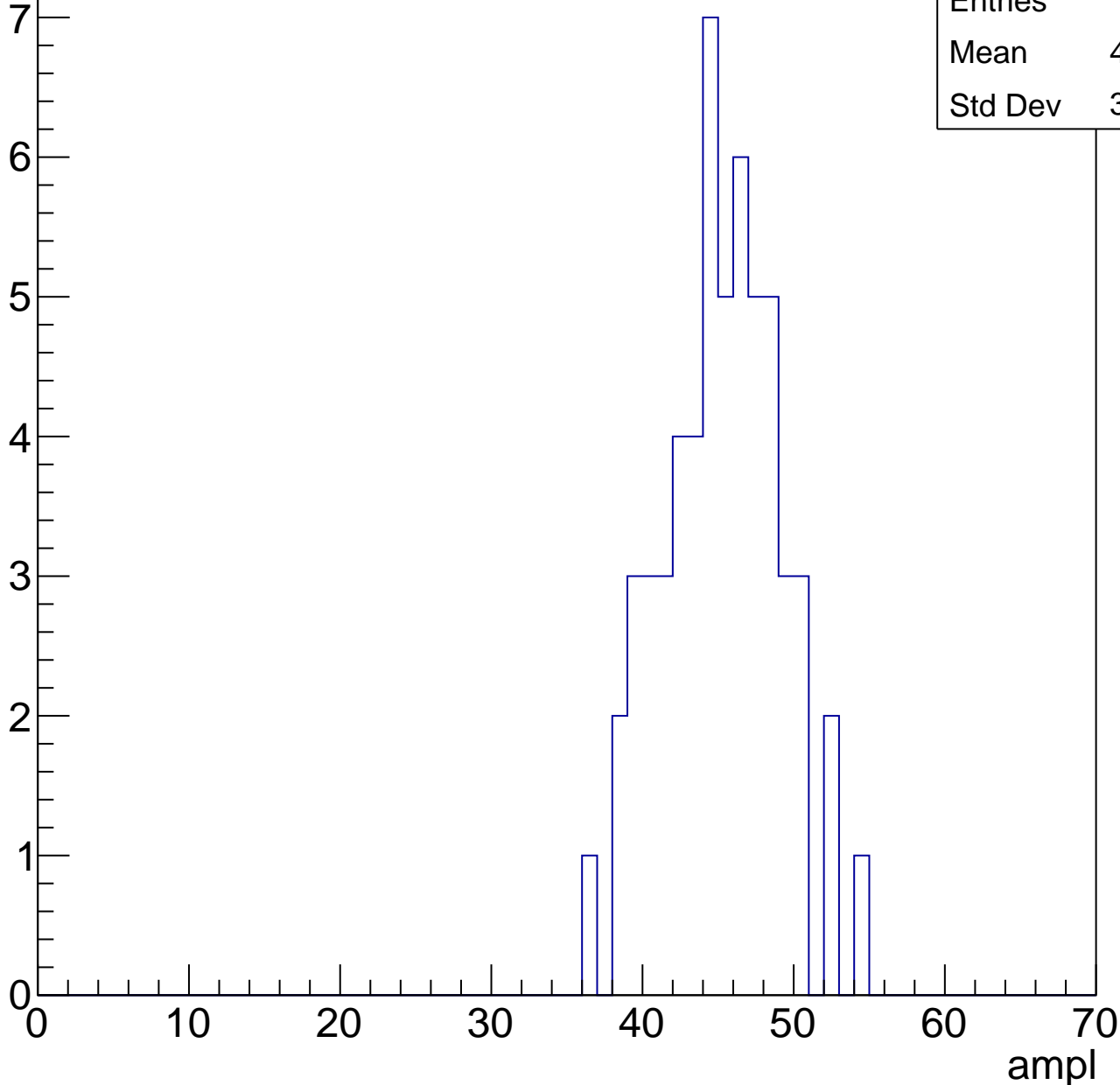


B1L103S, U1-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	44.75
Std Dev	3.836

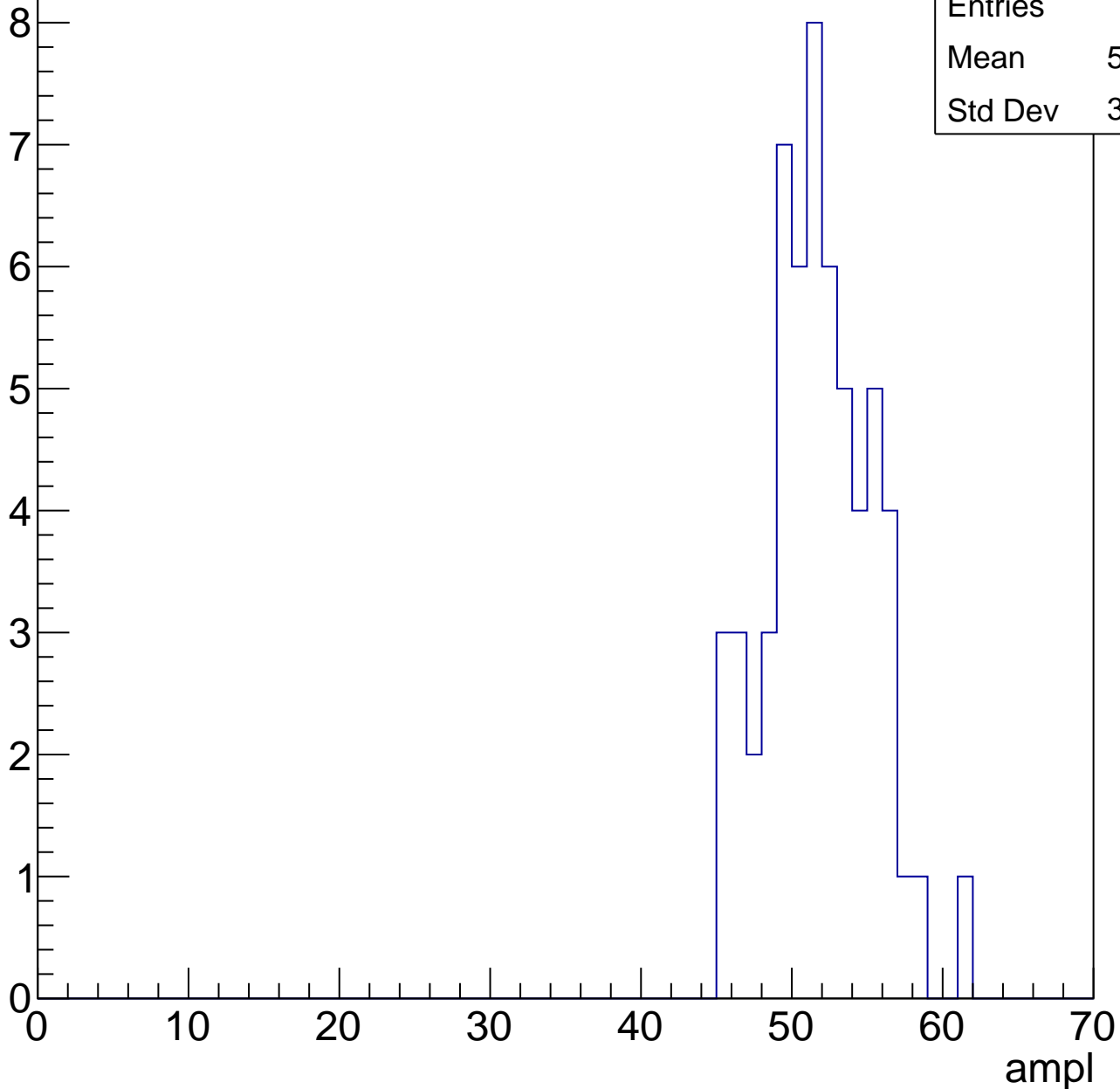


B1L103S, U1-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.36
Std Dev	3.433

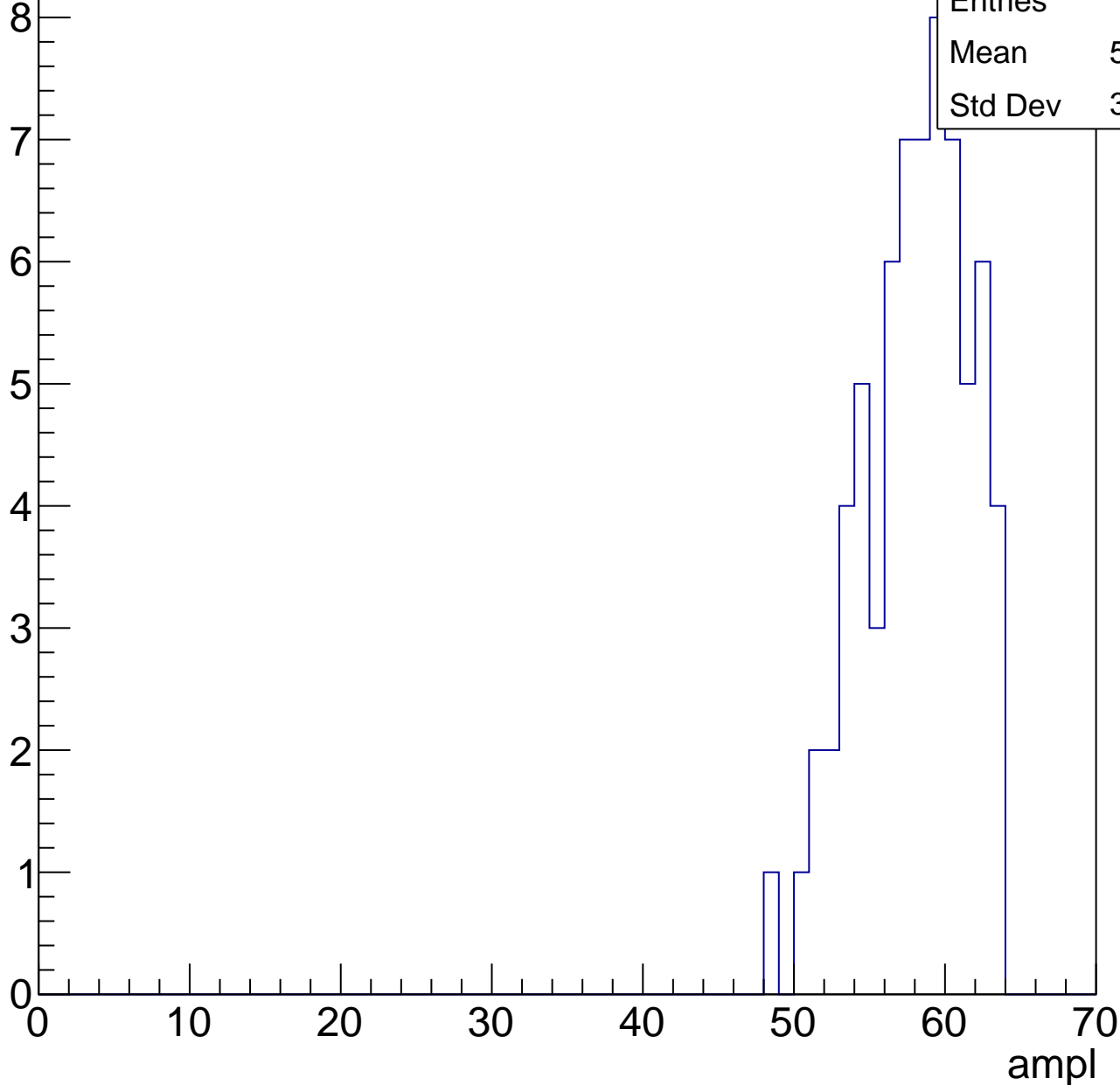


B1L103S, U1-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

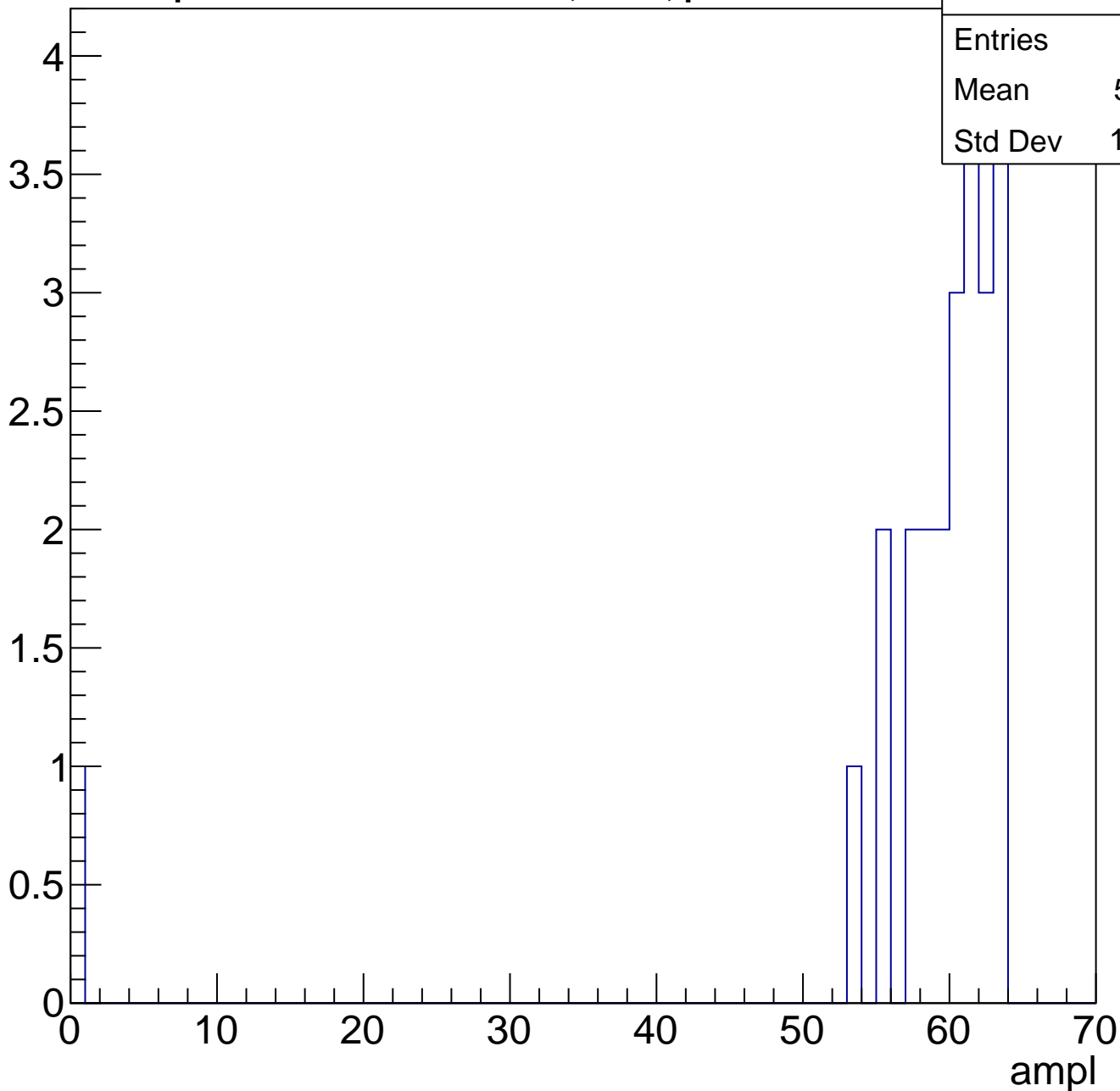
Entries	68
Mean	57.54
Std Dev	3.504



B1L103S, U1-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

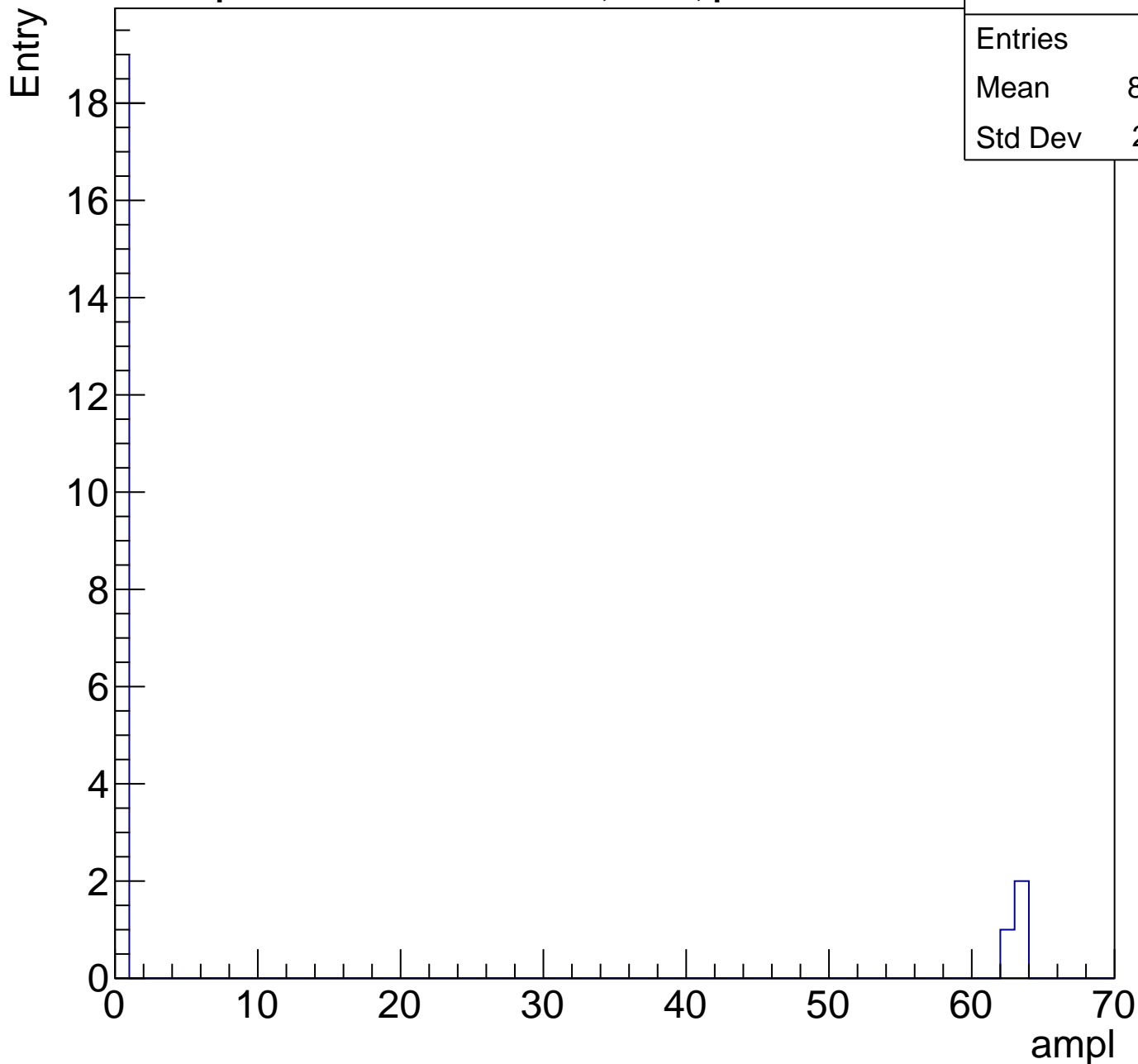


Entries	24
Mean	57.21
Std Dev	12.23

B1L103S, U1-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51



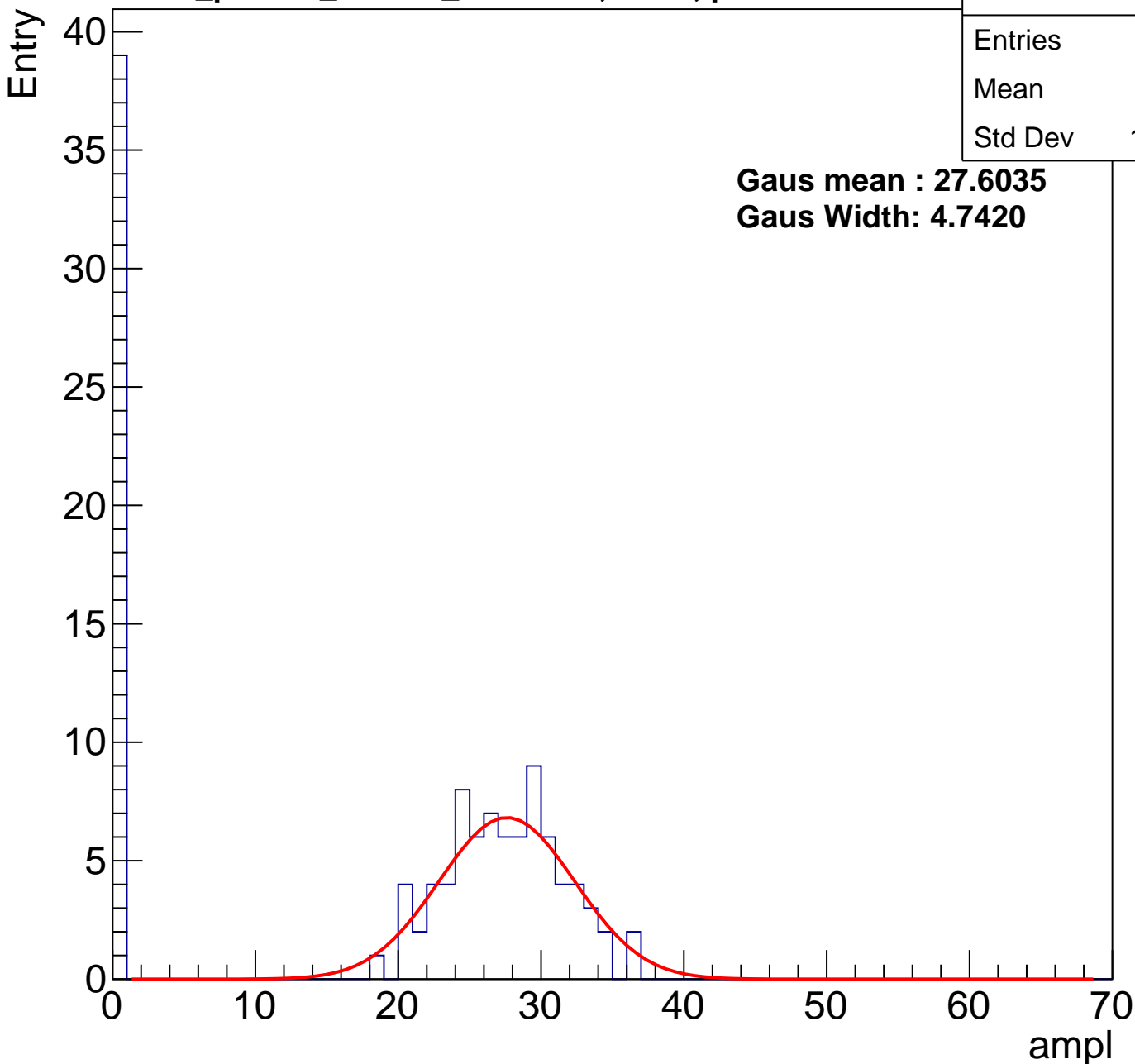
B1L103S, U1-ch123, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	117
Mean	18
Std Dev	13.14

Gaus mean : 27.6035

Gaus Width: 4.7420



B1L103S, U1-ch123, adc1

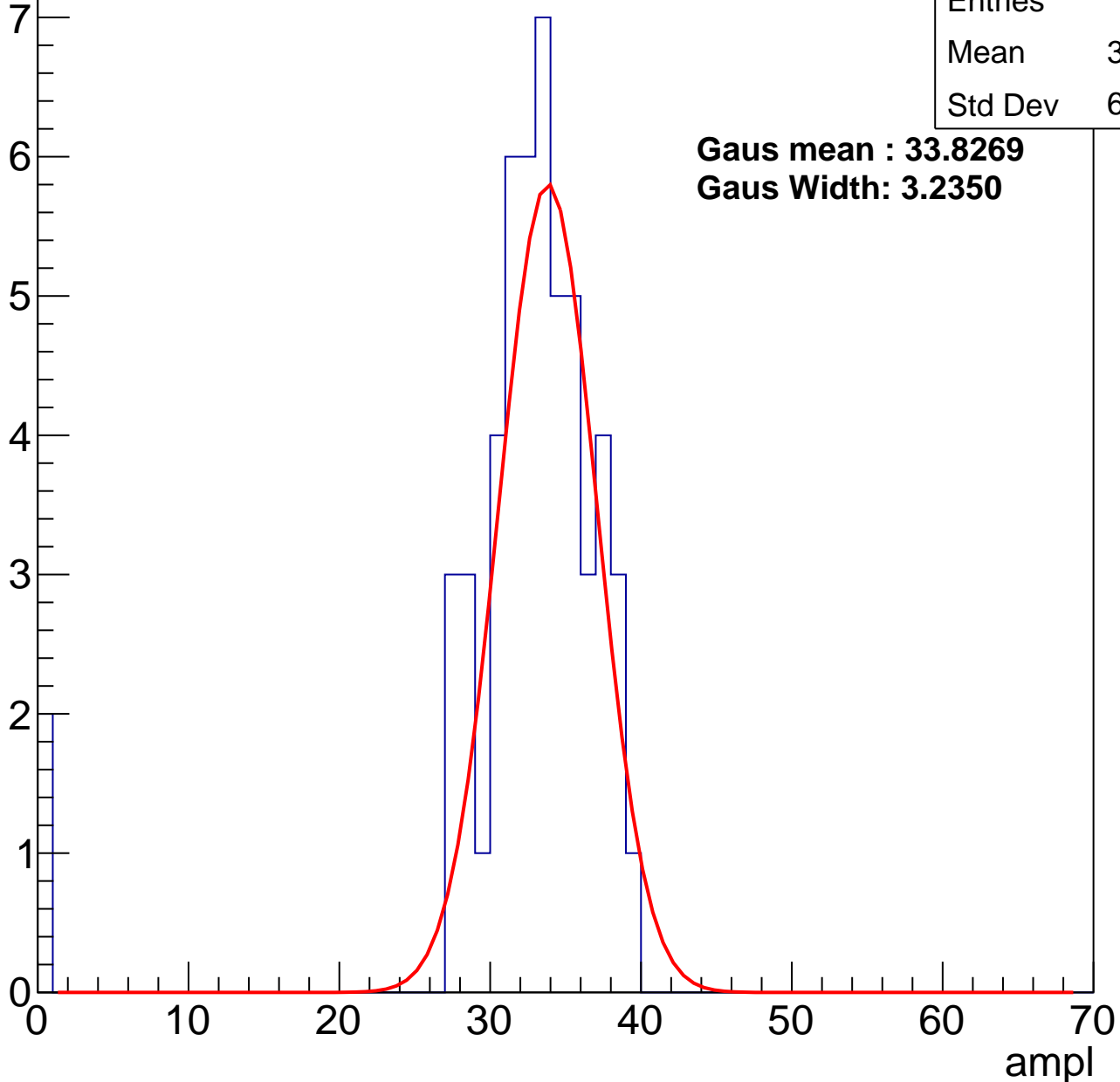
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	31.64
Std Dev	6.966

Gaus mean : 33.8269

Gaus Width: 3.2350



B1L103S, U1-ch123, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	35.33
Std Dev	13.68

Gaus mean : 40.2516

Gaus Width: 4.1735

Entry

10

8

6

4

2

0

0

10

20

30

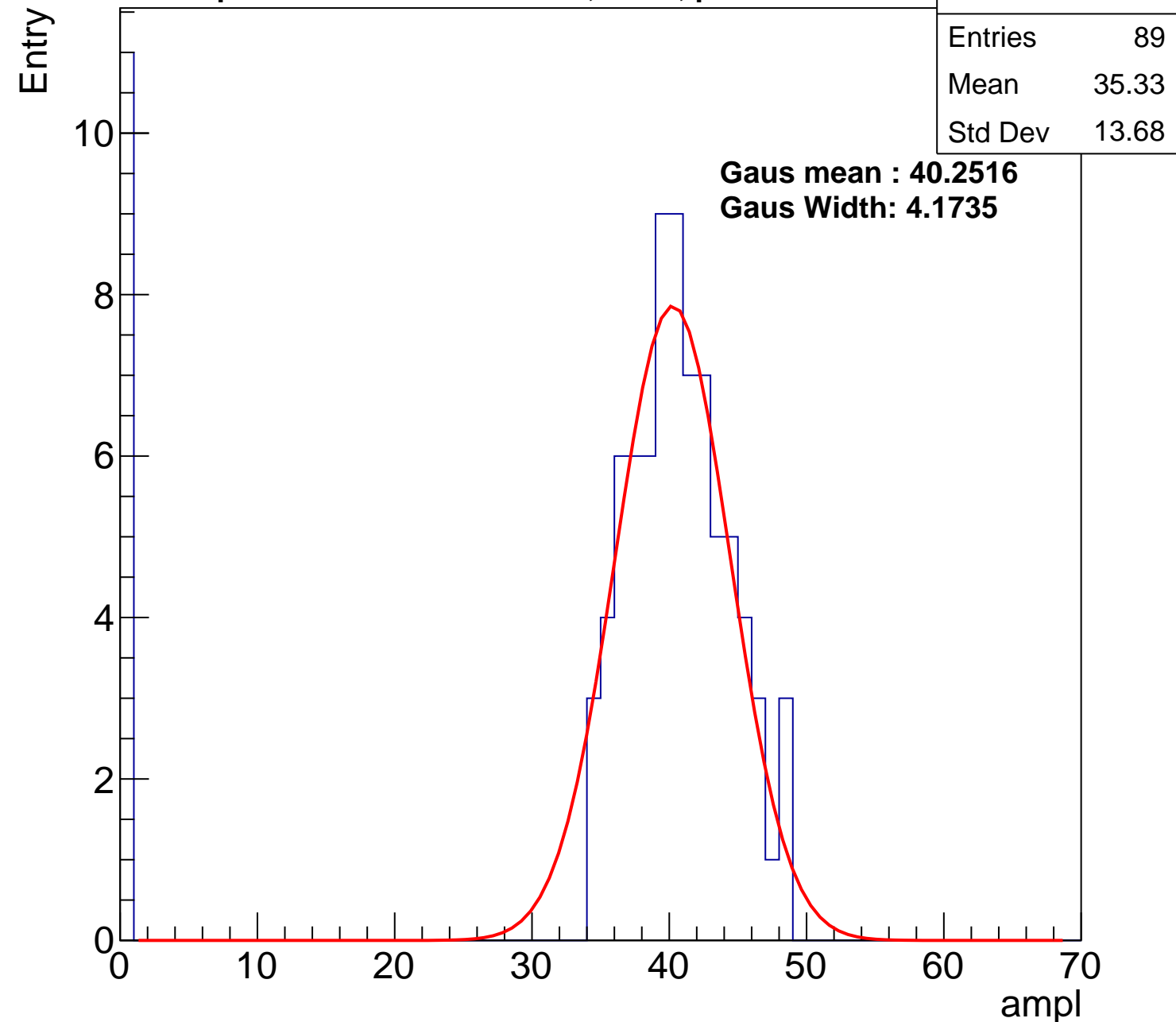
40

50

60

70

ampl

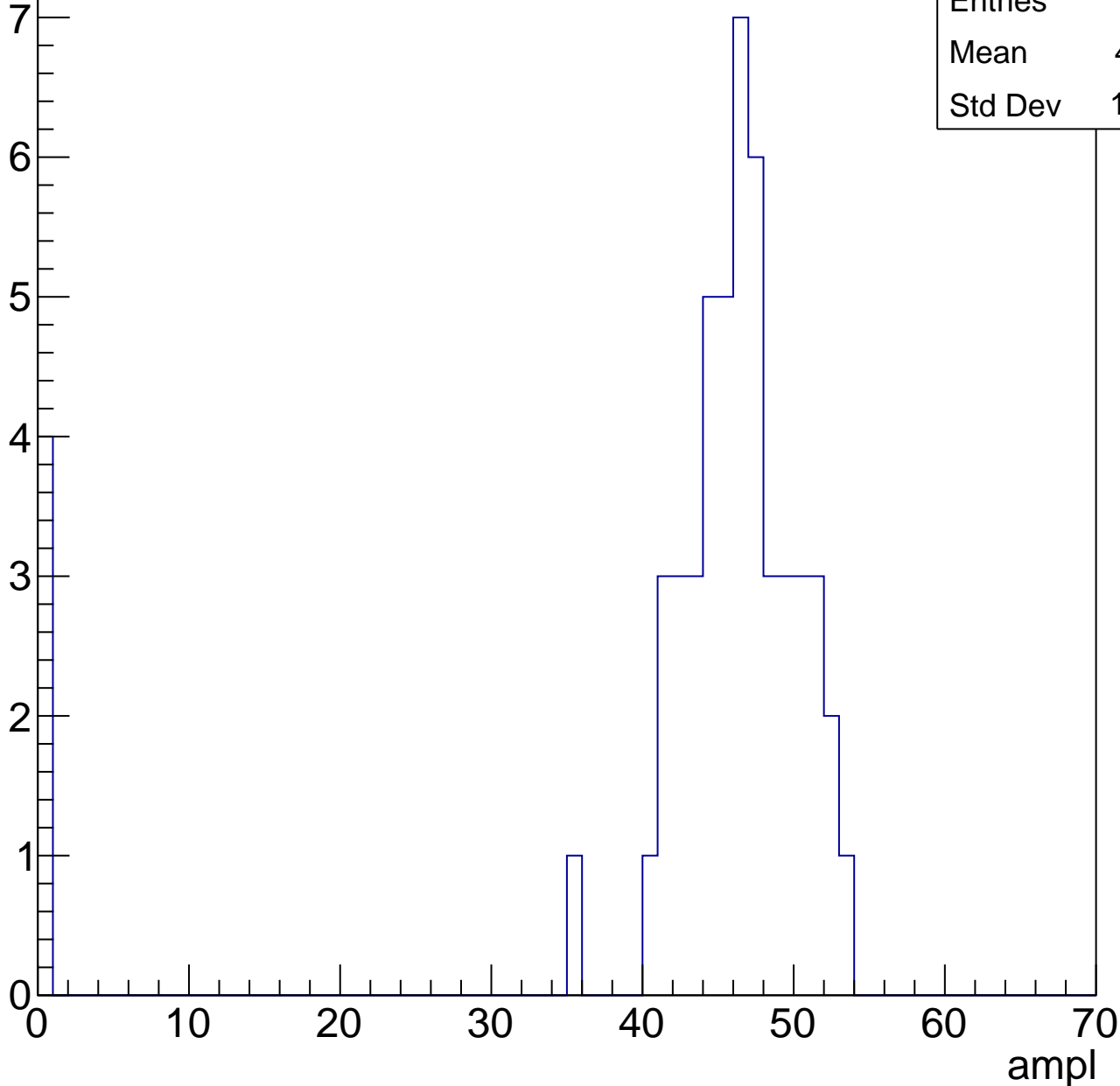


B1L103S, U1-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	42.51
Std Dev	12.62

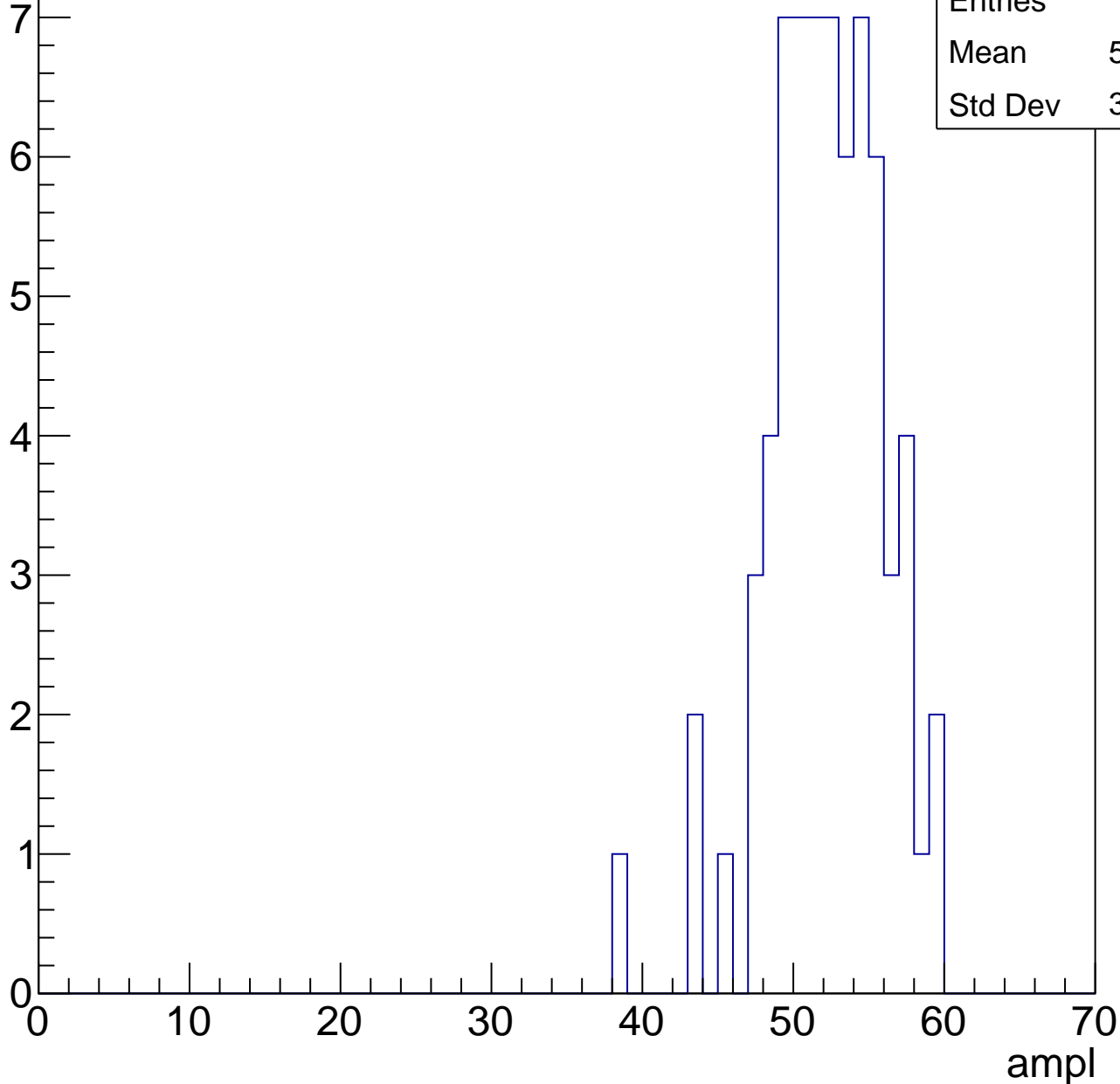


B1L103S, U1-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

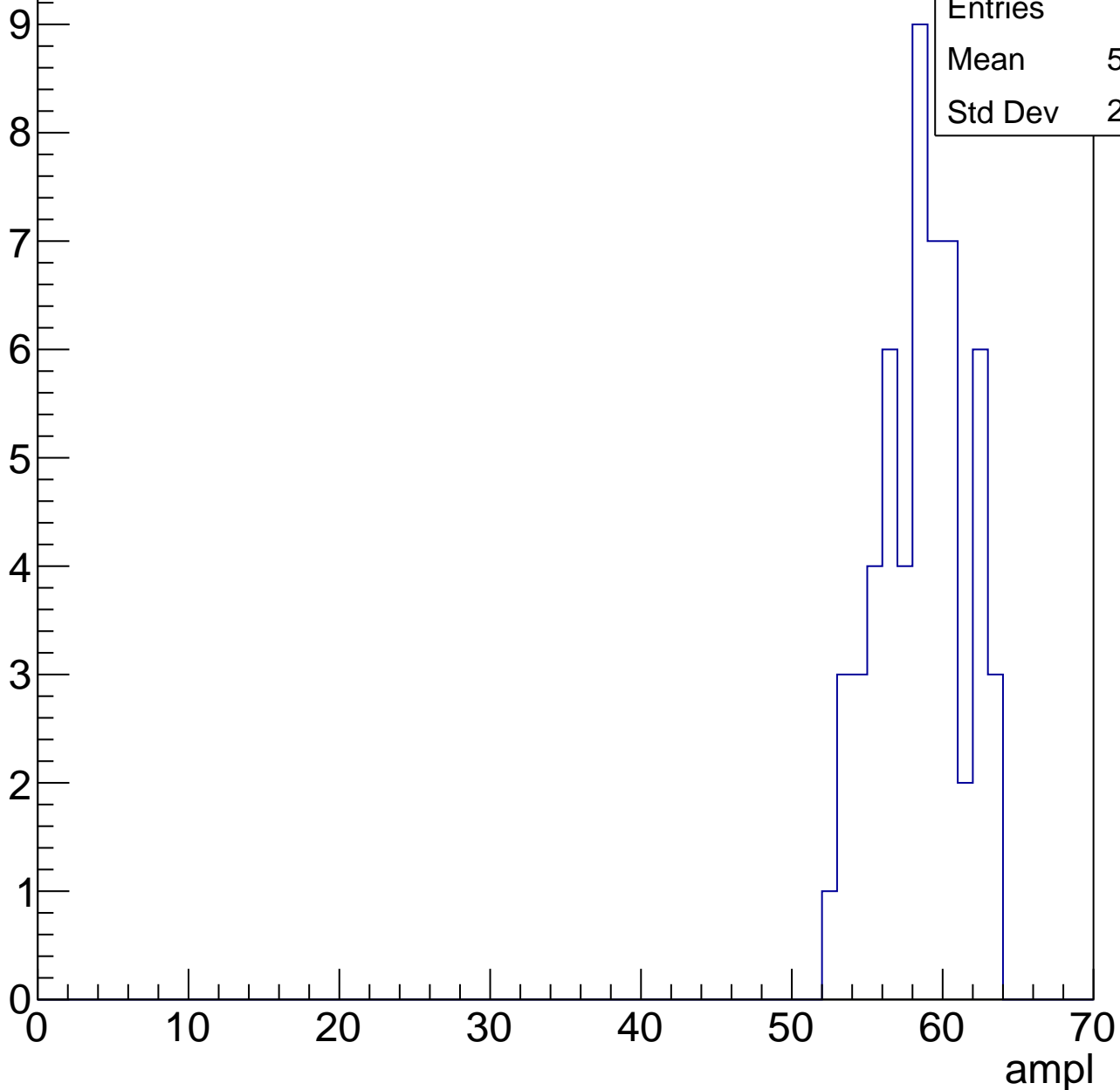
Entries	68
Mean	51.68
Std Dev	3.848



B1L103S, U1-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



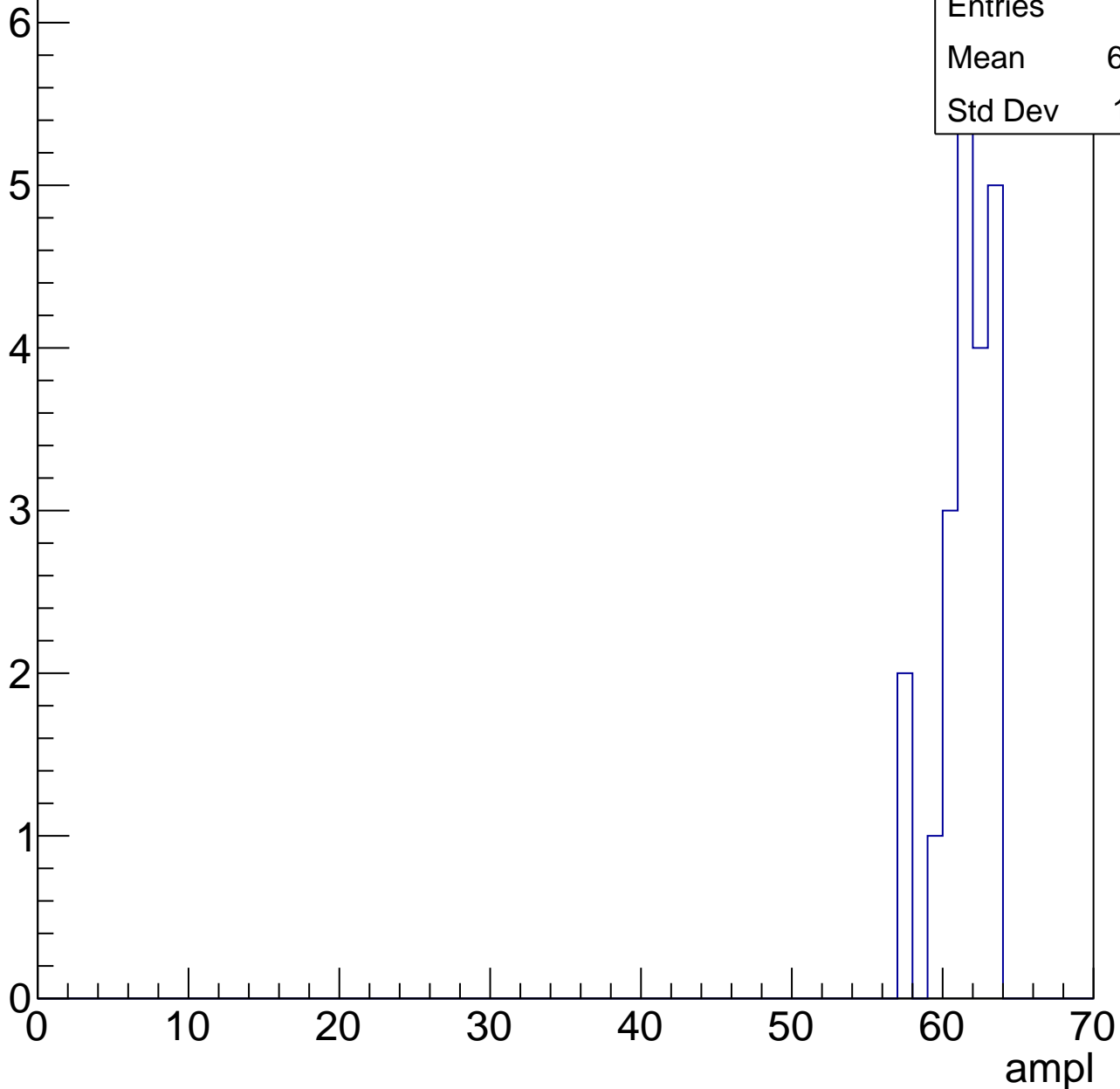
Entries	55
Mean	58.09
Std Dev	2.849

B1L103S, U1-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.05
Std Dev	1.731



B1L103S, U1-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

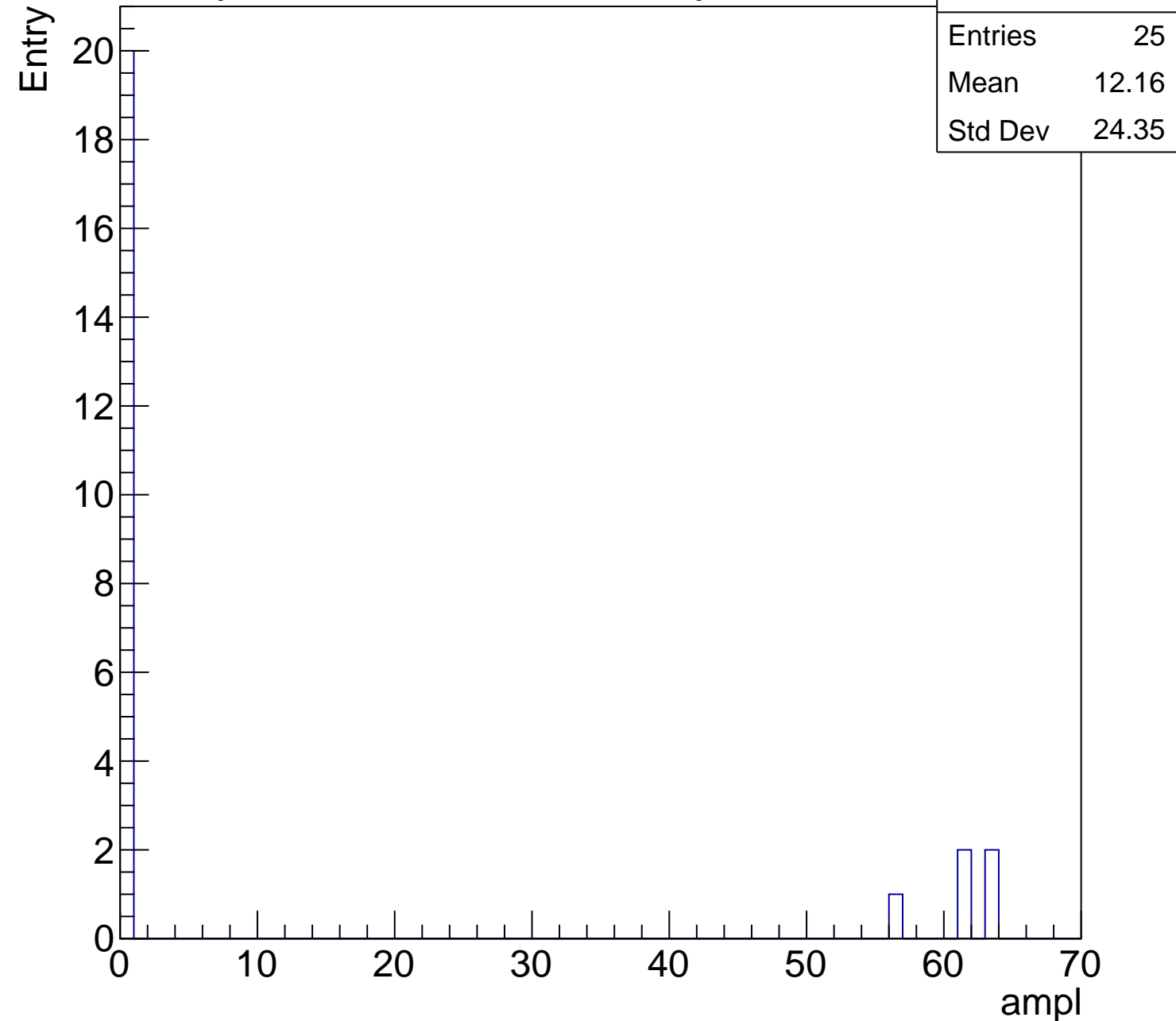
Entries	25
Mean	12.16
Std Dev	24.35

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



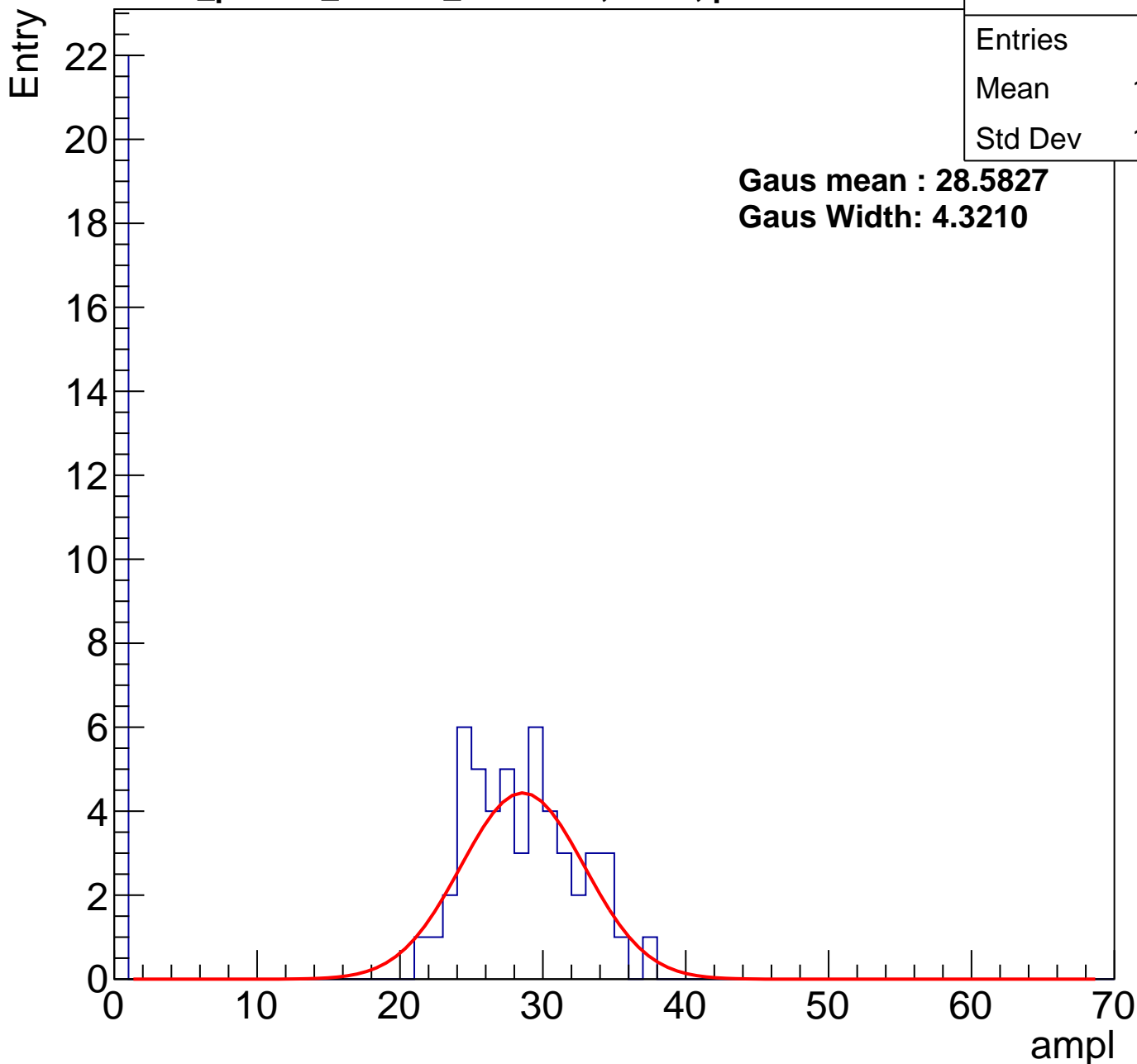
B1L103S, U1-ch124, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	19.51
Std Dev	13.31

Gaus mean : 28.5827

Gaus Width: 4.3210



B1L103S, U1-ch124, adc1

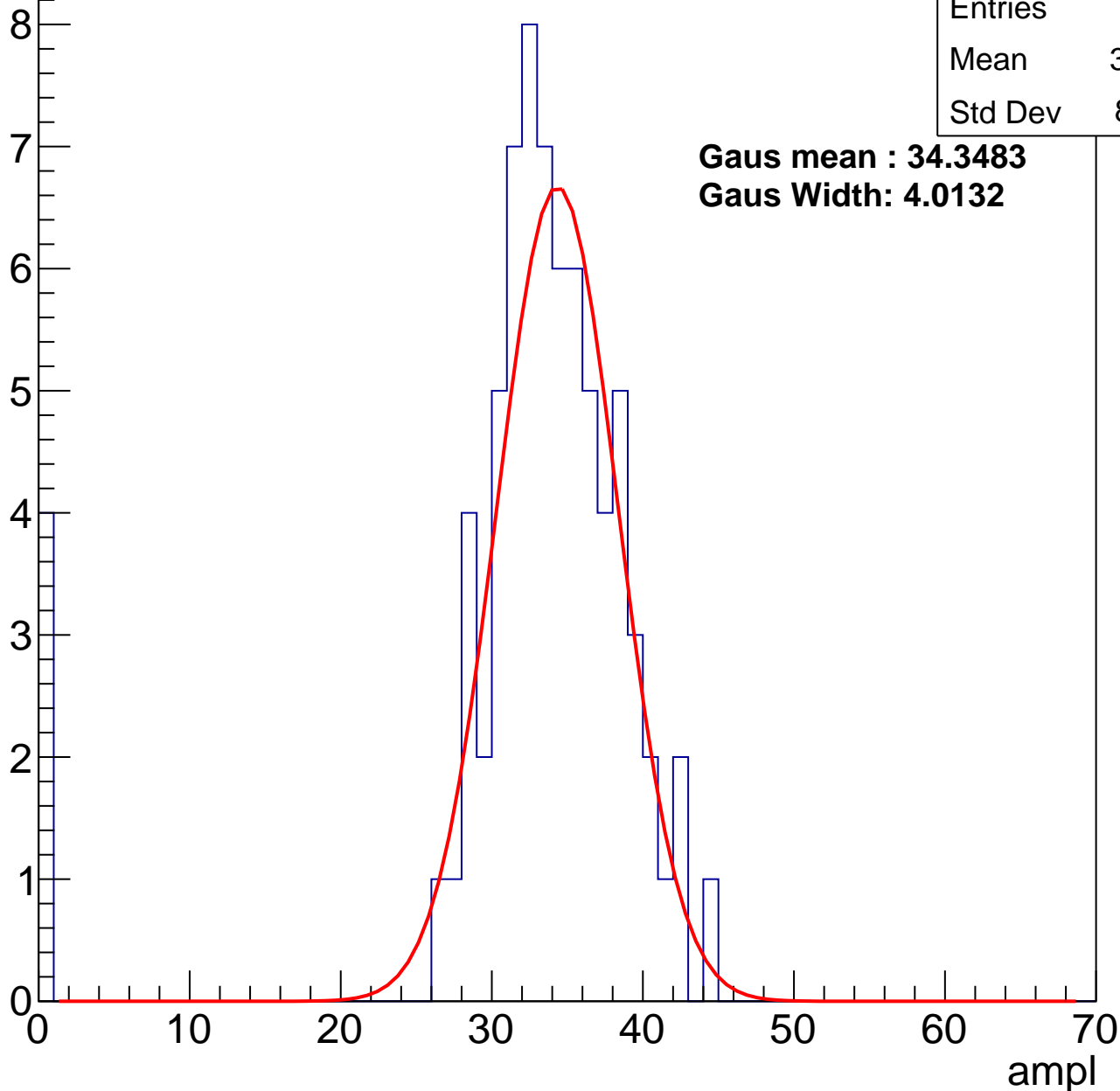
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	32.09
Std Dev	8.551

Gaus mean : 34.3483

Gaus Width: 4.0132



B1L103S, U1-ch124, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	34.88
Std Dev	15.09

Gaus mean : 41.4394

Gaus Width: 3.9785

Entry

12

10

8

6

4

2

0

0

10

20

30

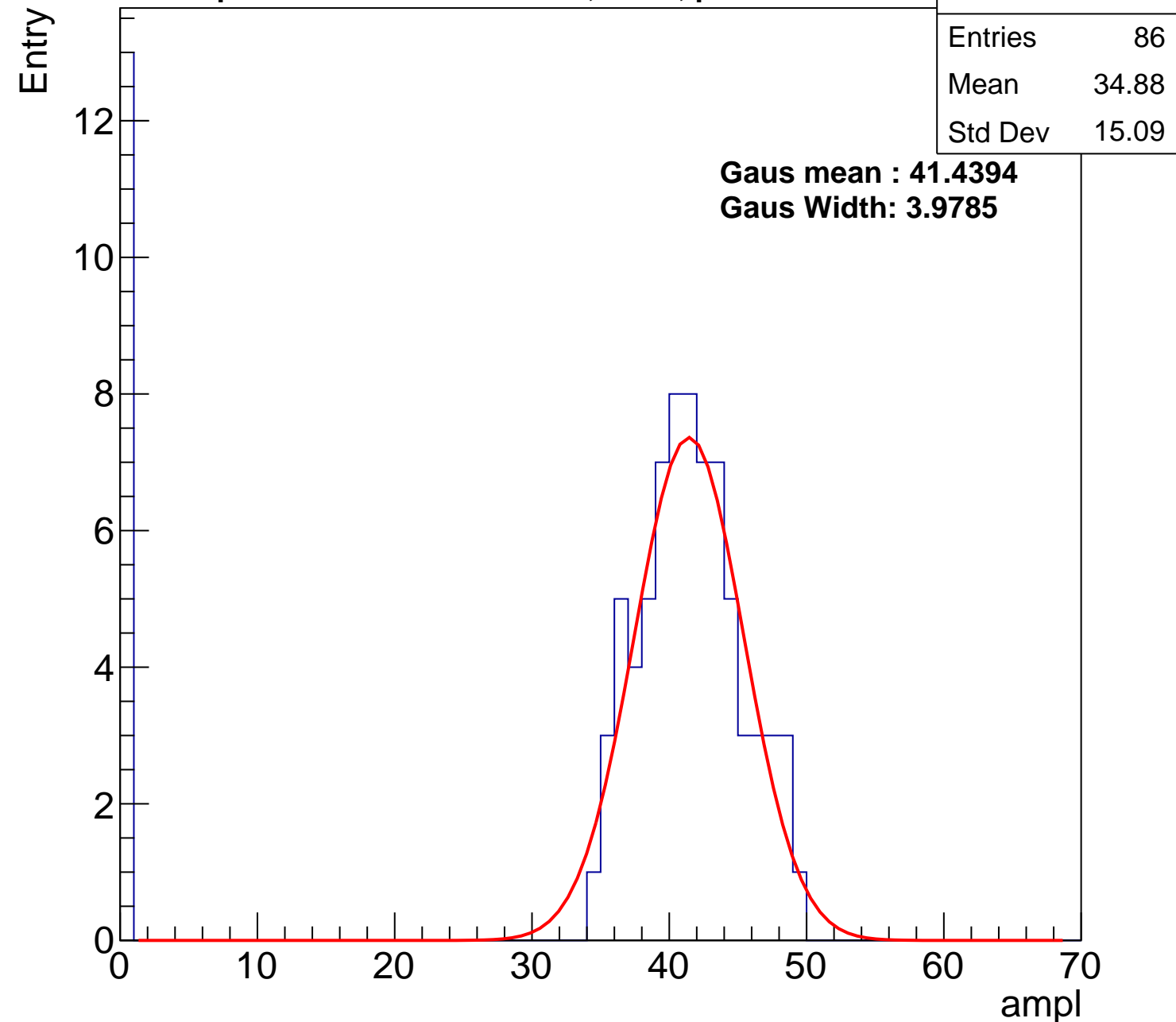
40

50

60

70

ampl

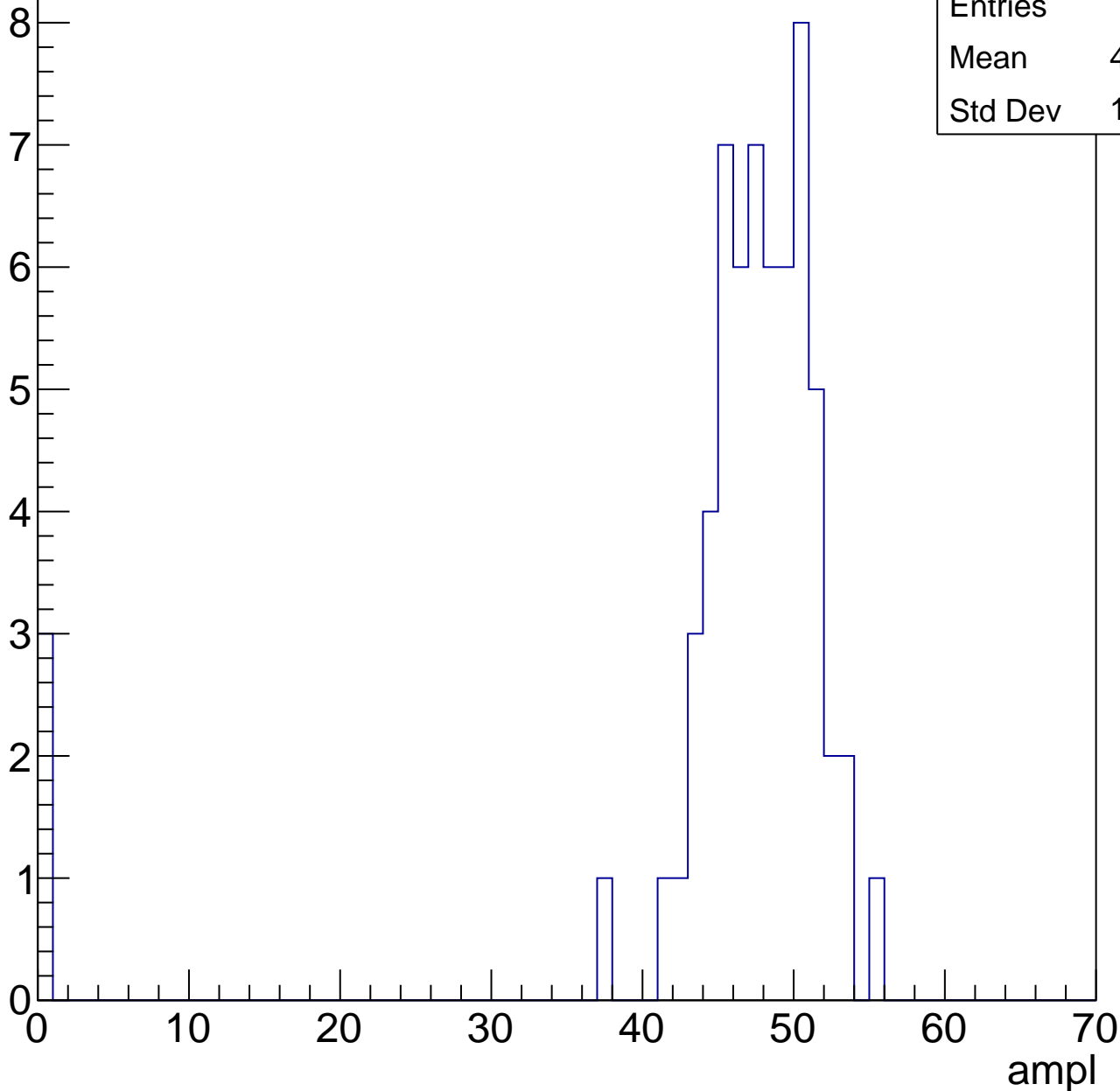


B1L103S, U1-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	45.19
Std Dev	10.59

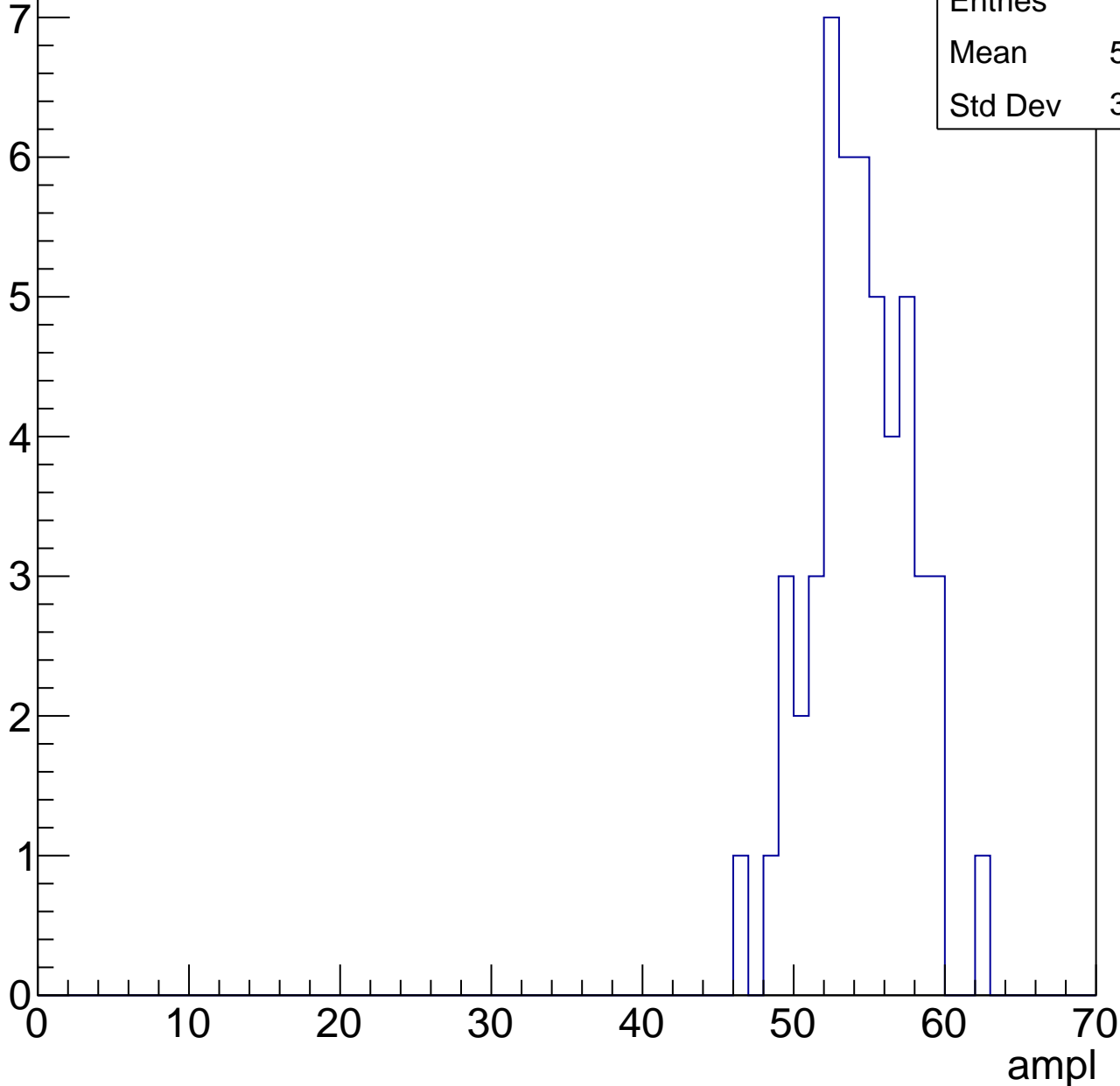


B1L103S, U1-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	53.94
Std Dev	3.227

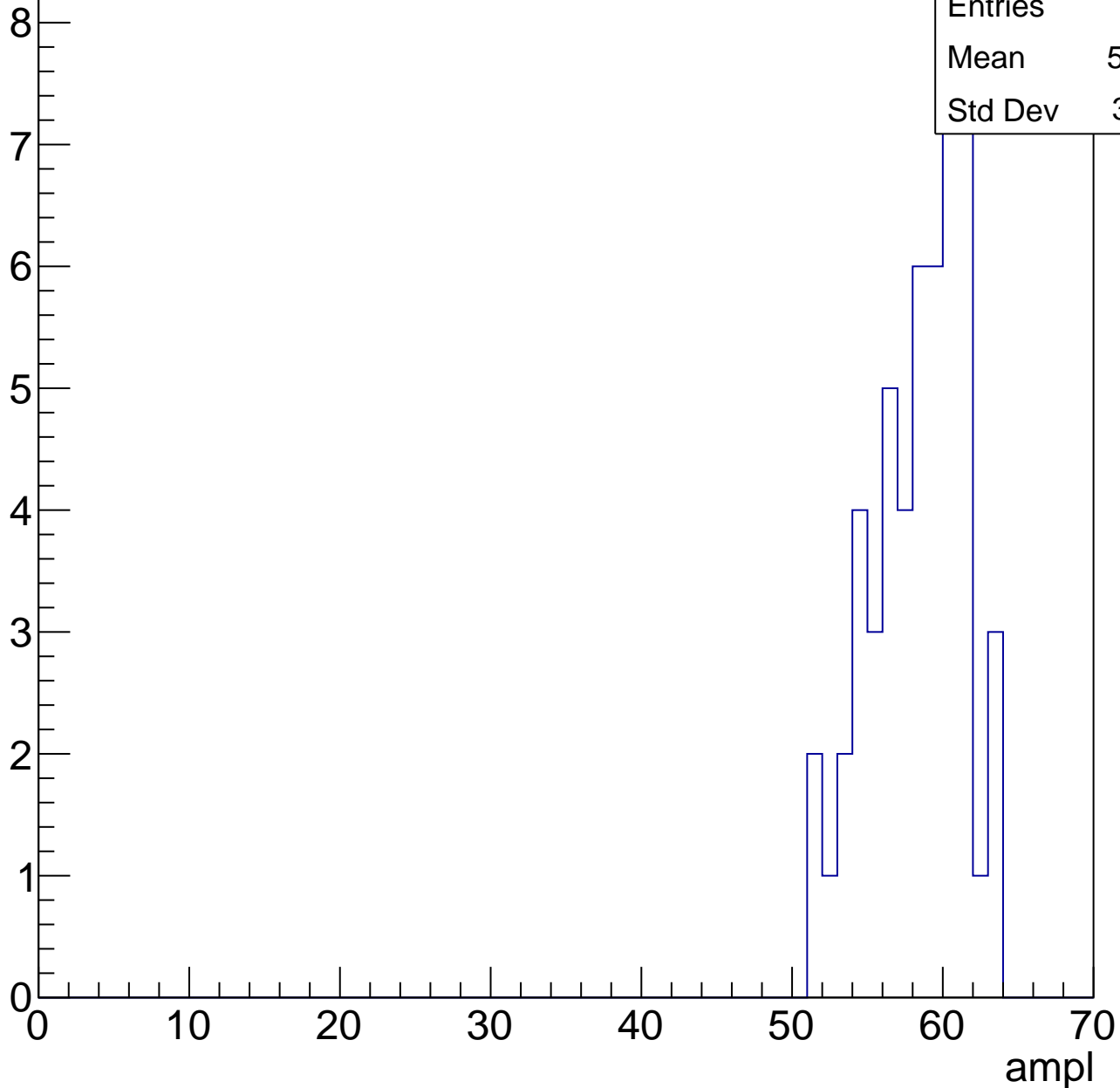


B1L103S, U1-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.92
Std Dev	3.071

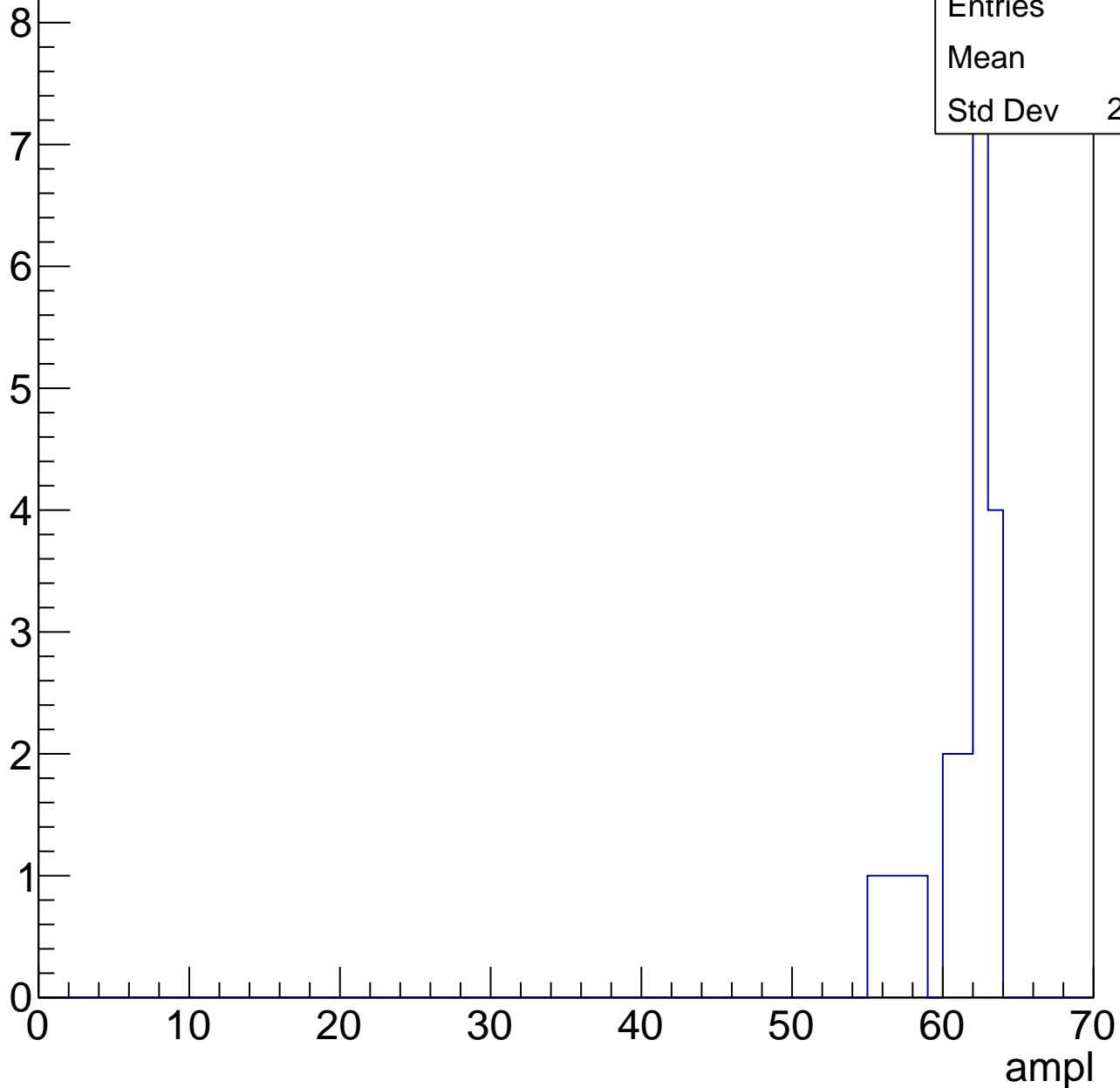


B1L103S, U1-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	20
Mean	60.8
Std Dev	2.358



B1L103S, U1-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

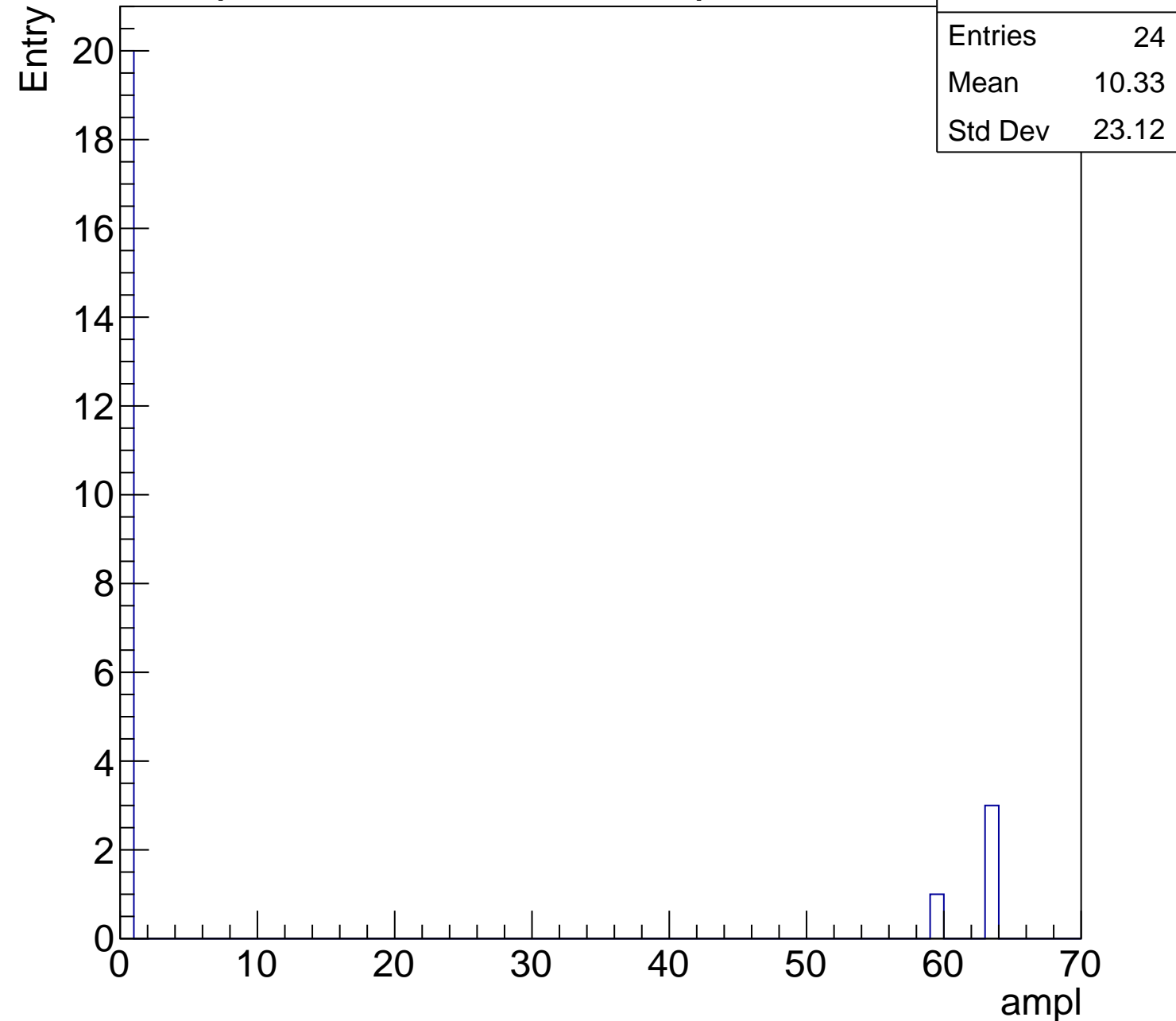
Entries	24
Mean	10.33
Std Dev	23.12

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U1-ch125, adc0

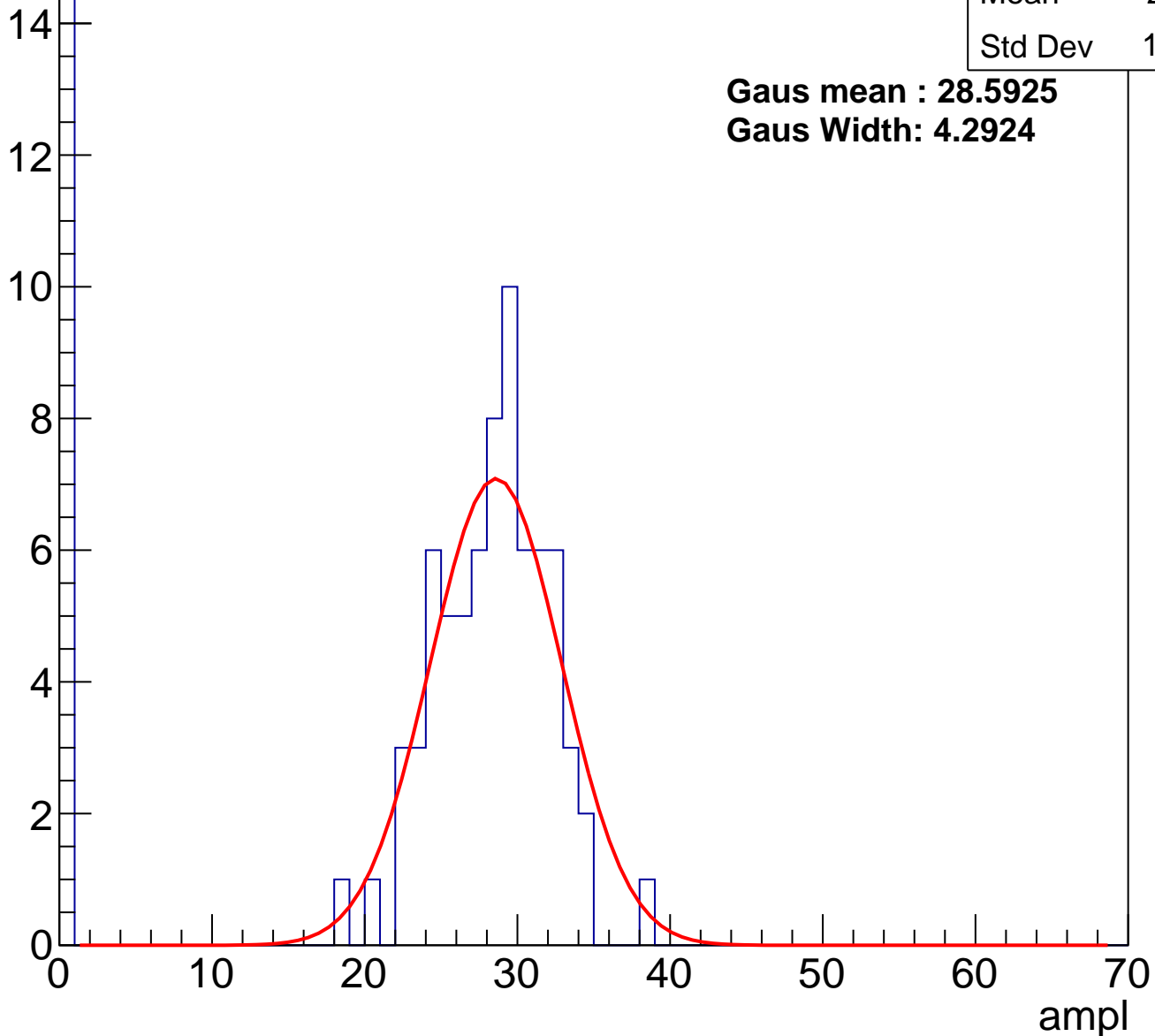
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	23.11
Std Dev	11.05

Gaus mean : 28.5925

Gaus Width: 4.2924

Entry



B1L103S, U1-ch125, adc1

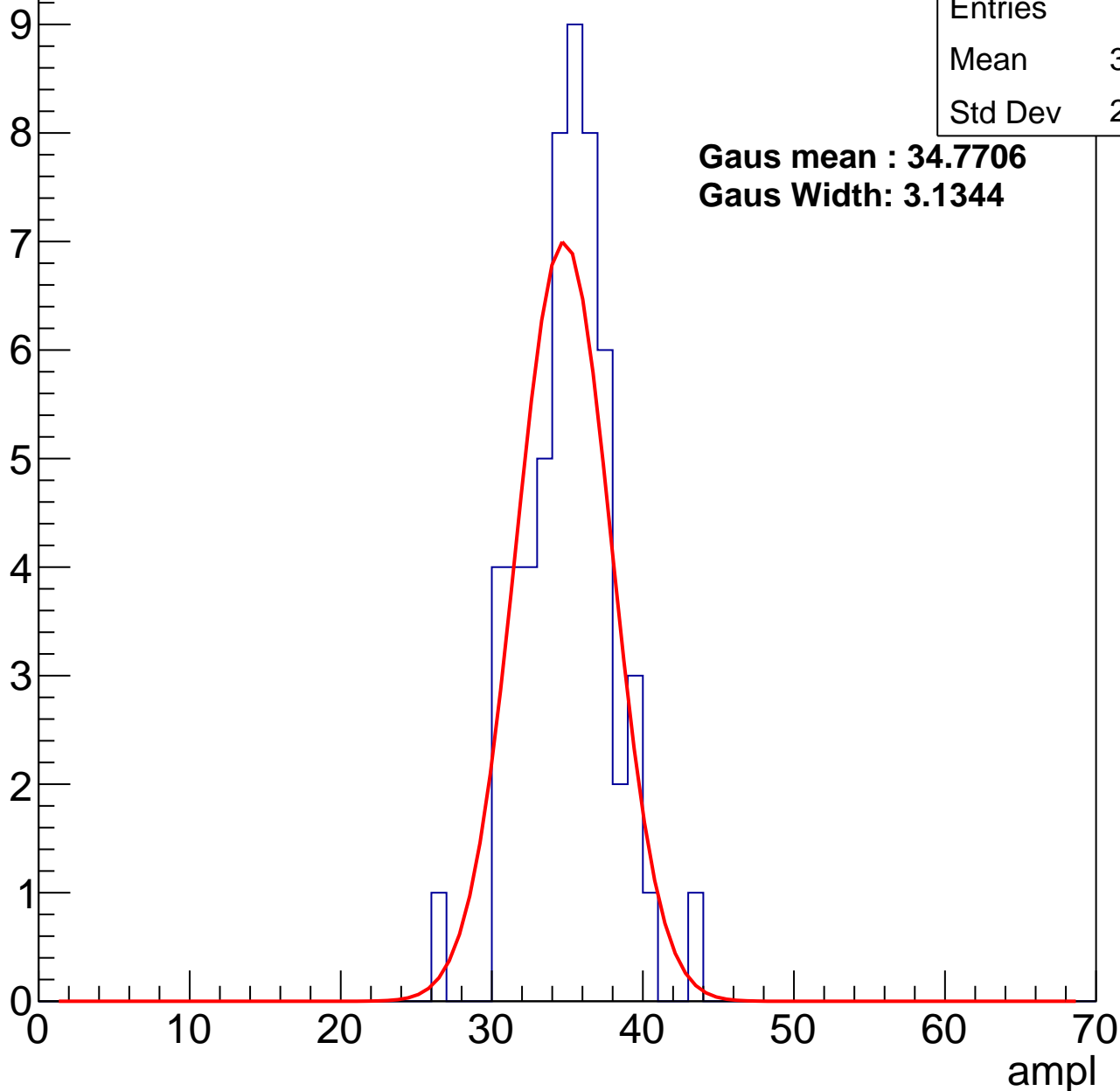
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	34.57
Std Dev	2.945

Gaus mean : 34.7706

Gaus Width: 3.1344



B1L103S, U1-ch125, adc2

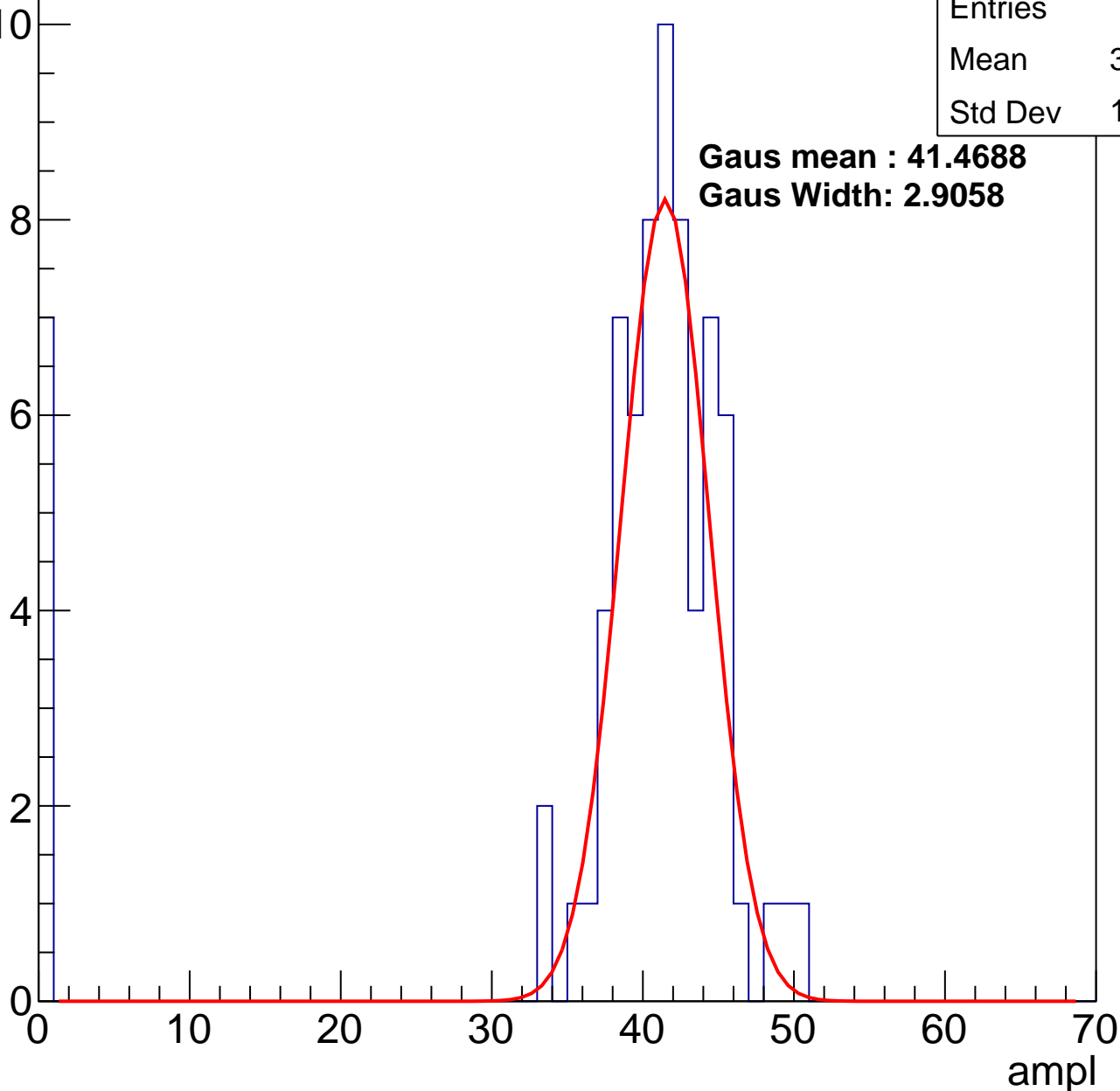
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	37.25
Std Dev	12.36

Gaus mean : 41.4688

Gaus Width: 2.9058

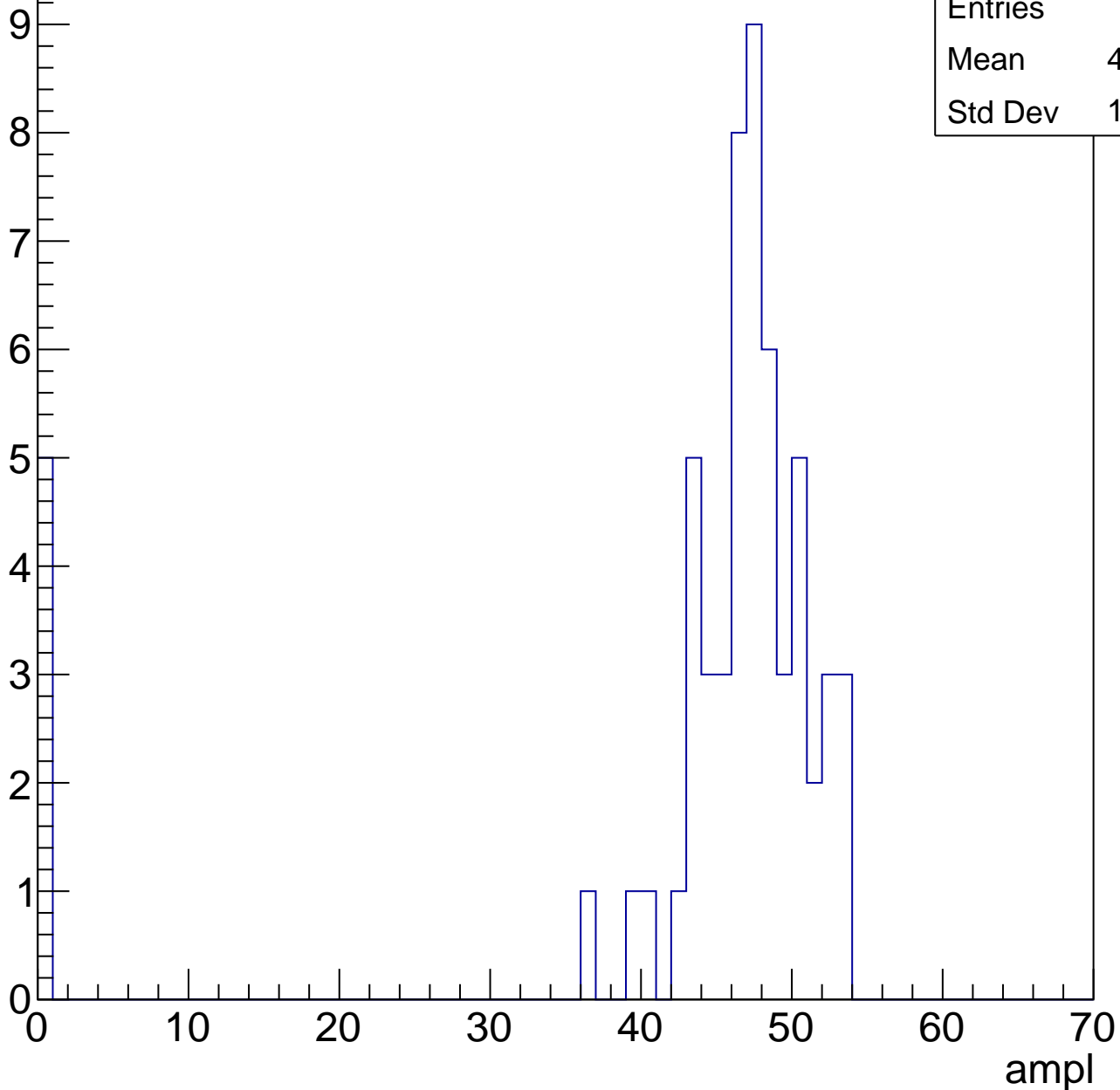


B1L103S, U1-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.92
Std Dev	13.48

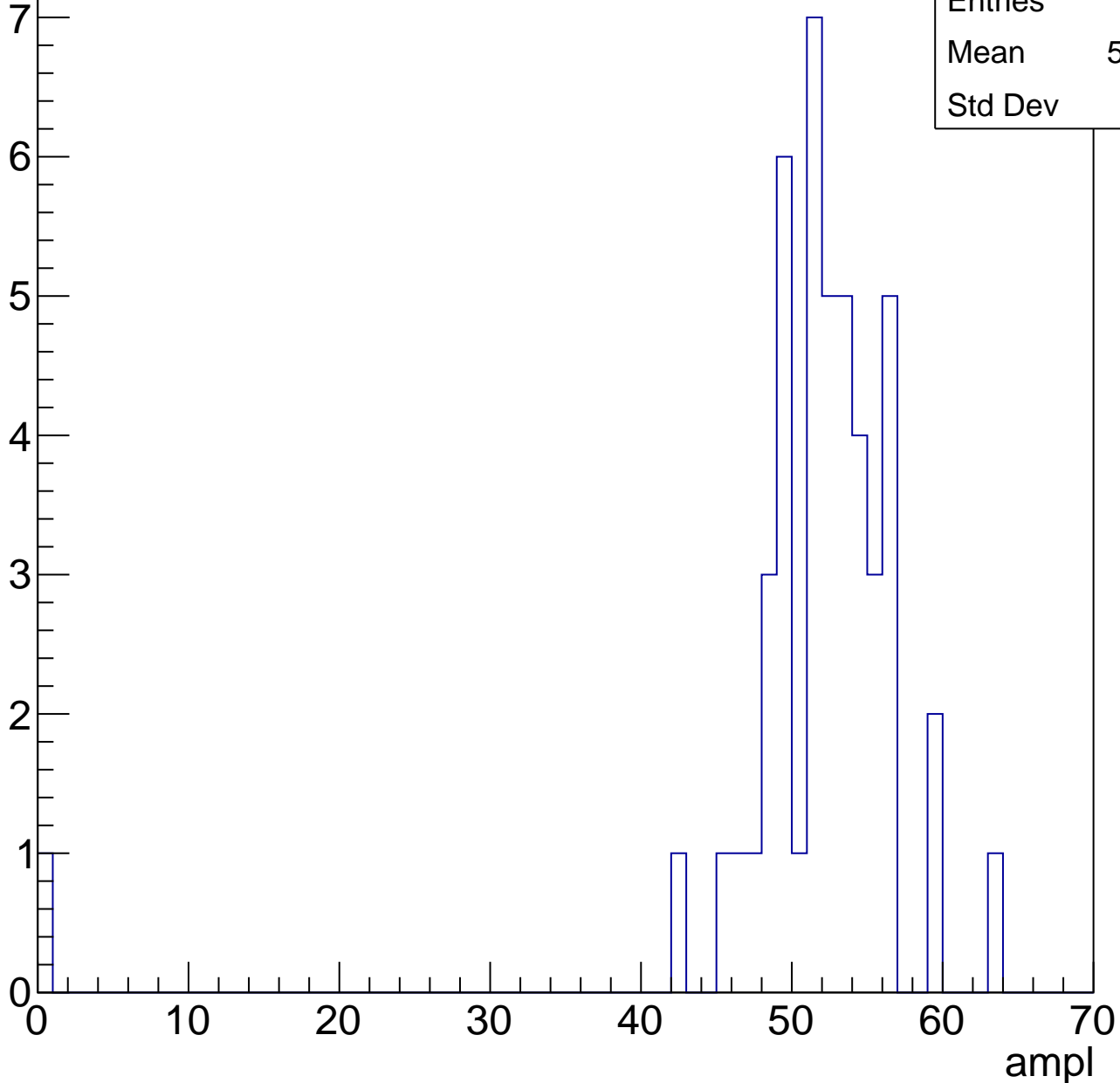


B1L103S, U1-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	50.89
Std Dev	8.4

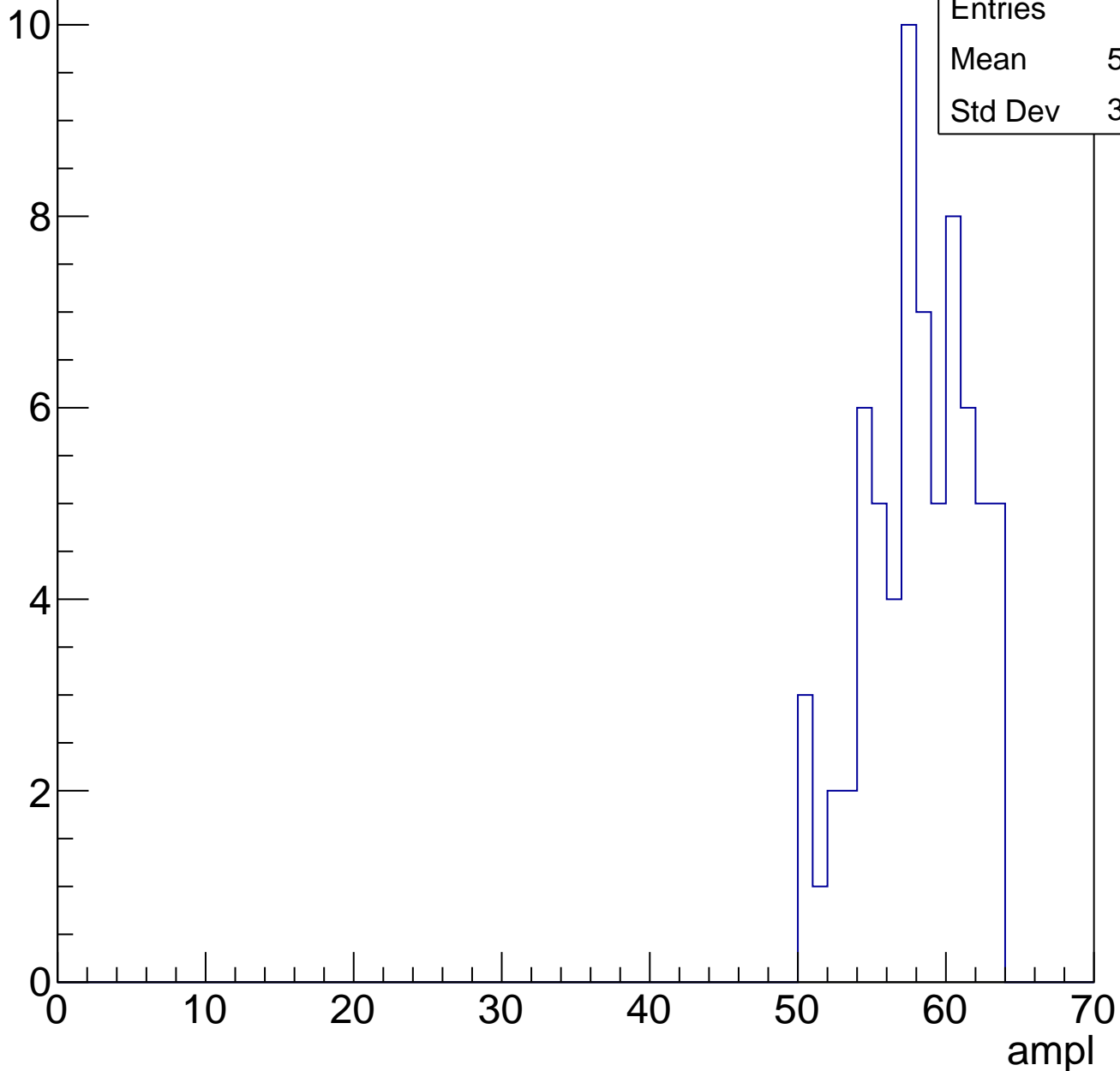


B1L103S, U1-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	57.62
Std Dev	3.439

Entry

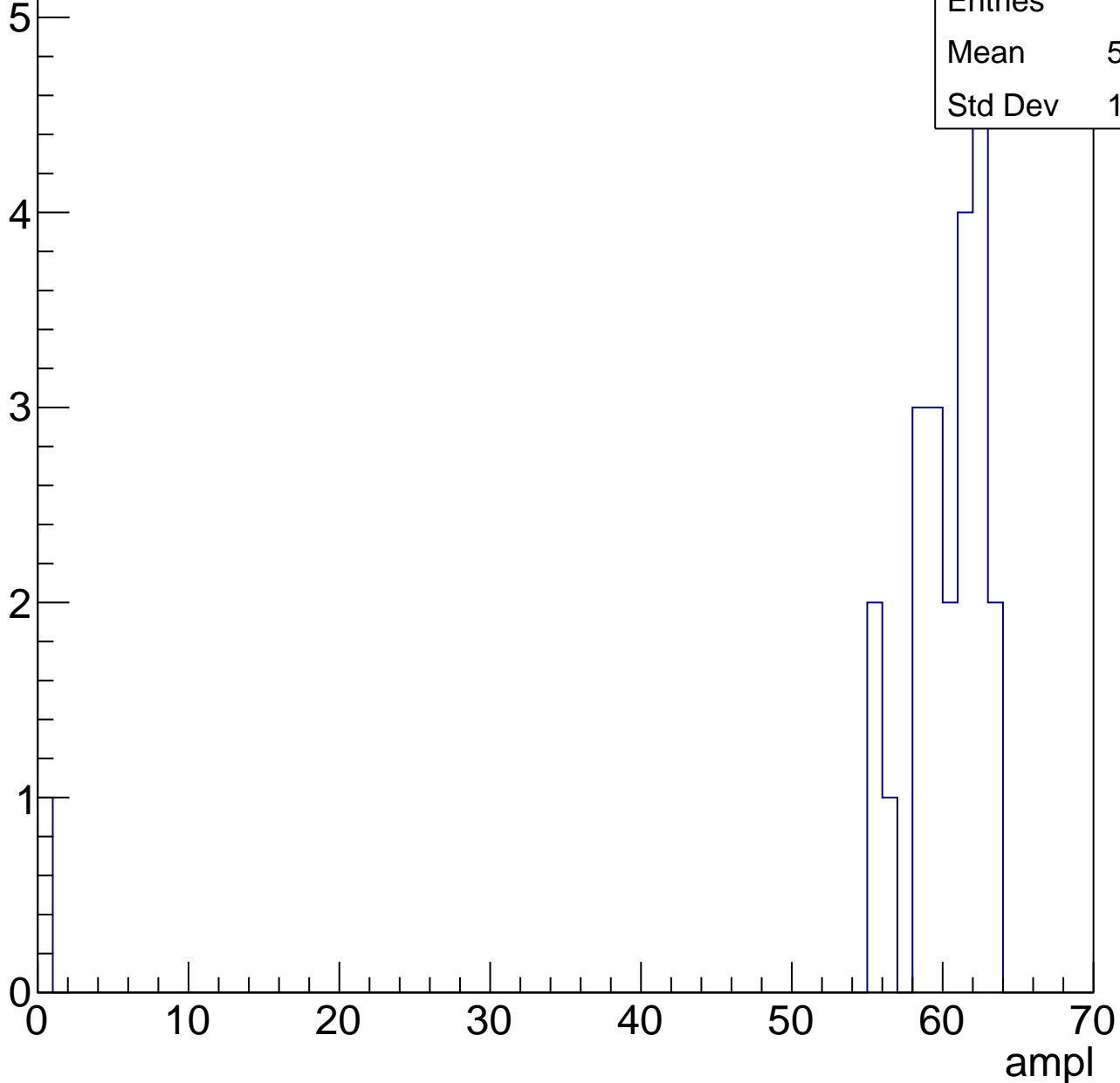


B1L103S, U1-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	57.26
Std Dev	12.42



B1L103S, U1-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U1-ch126, adc0

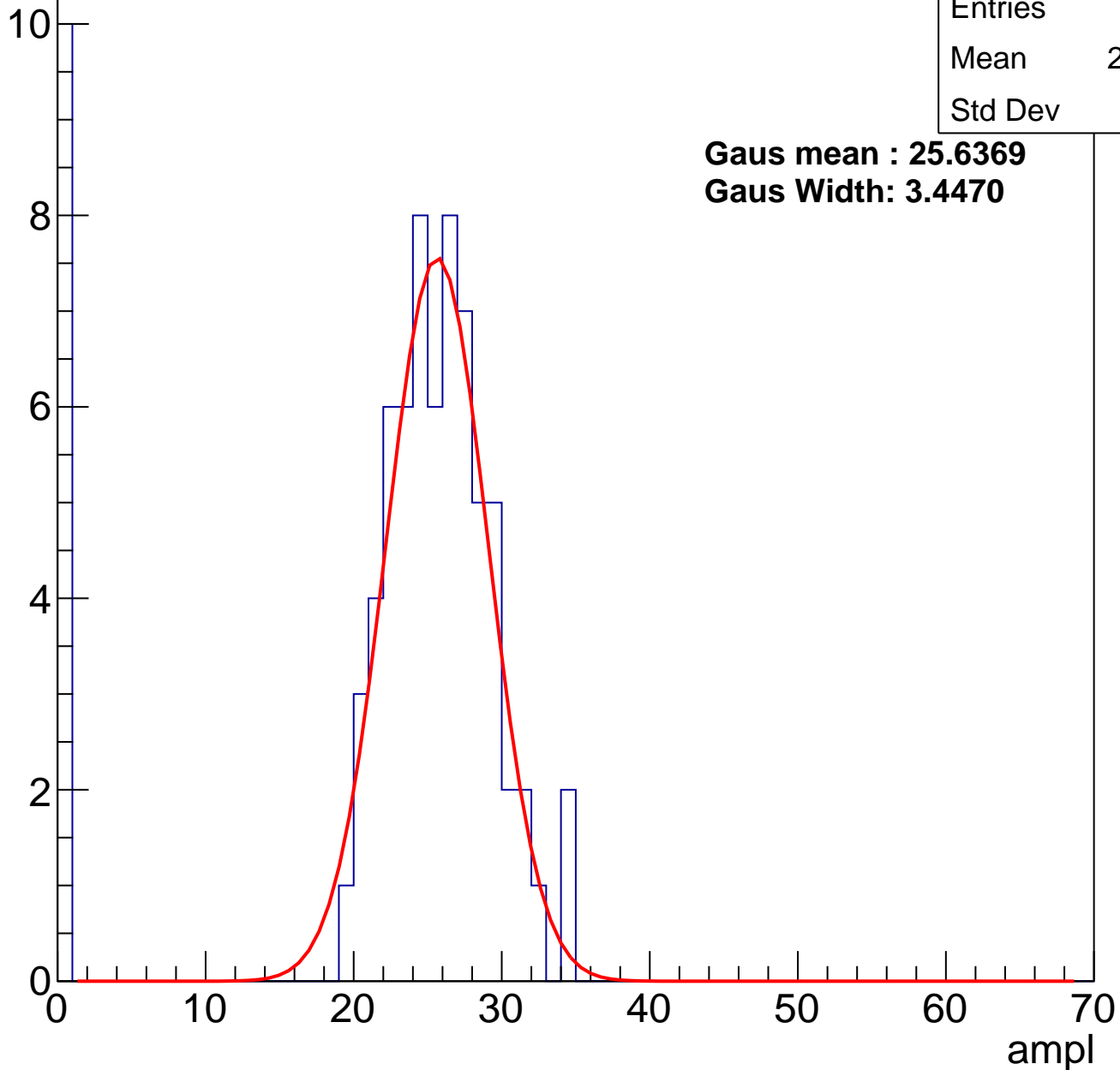
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	22.09
Std Dev	9.15

Gaus mean : 25.6369

Gaus Width: 3.4470

Entry



B1L103S, U1-ch126, adc1

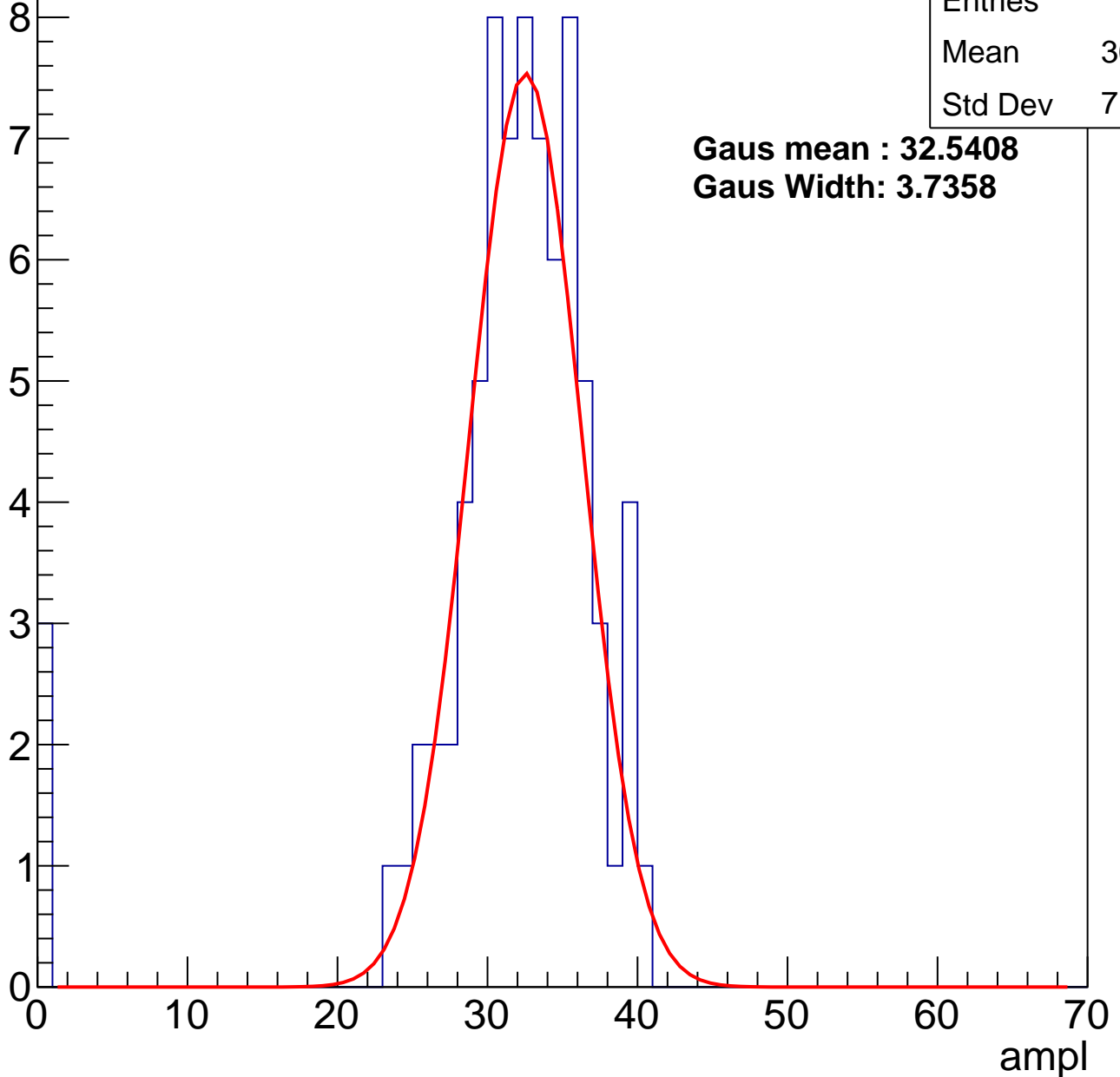
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	30.94
Std Dev	7.206

Gaus mean : 32.5408

Gaus Width: 3.7358



B1L103S, U1-ch126, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	31.64
Std Dev	15.18

Gaus mean : 39.0373

Gaus Width: 3.6747

Entry

10

8

6

4

2

0

0

10

20

30

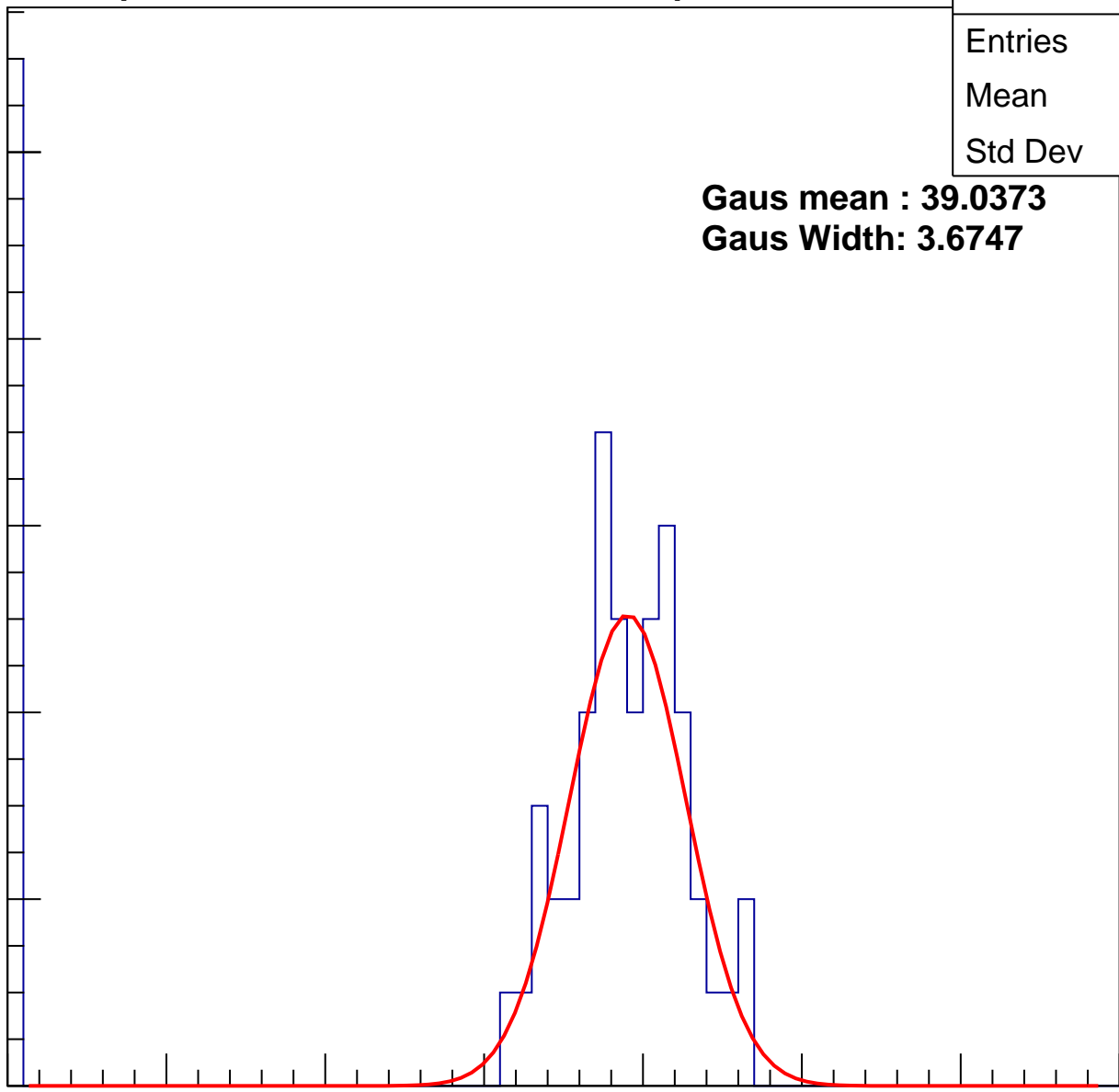
40

50

60

70

ampl

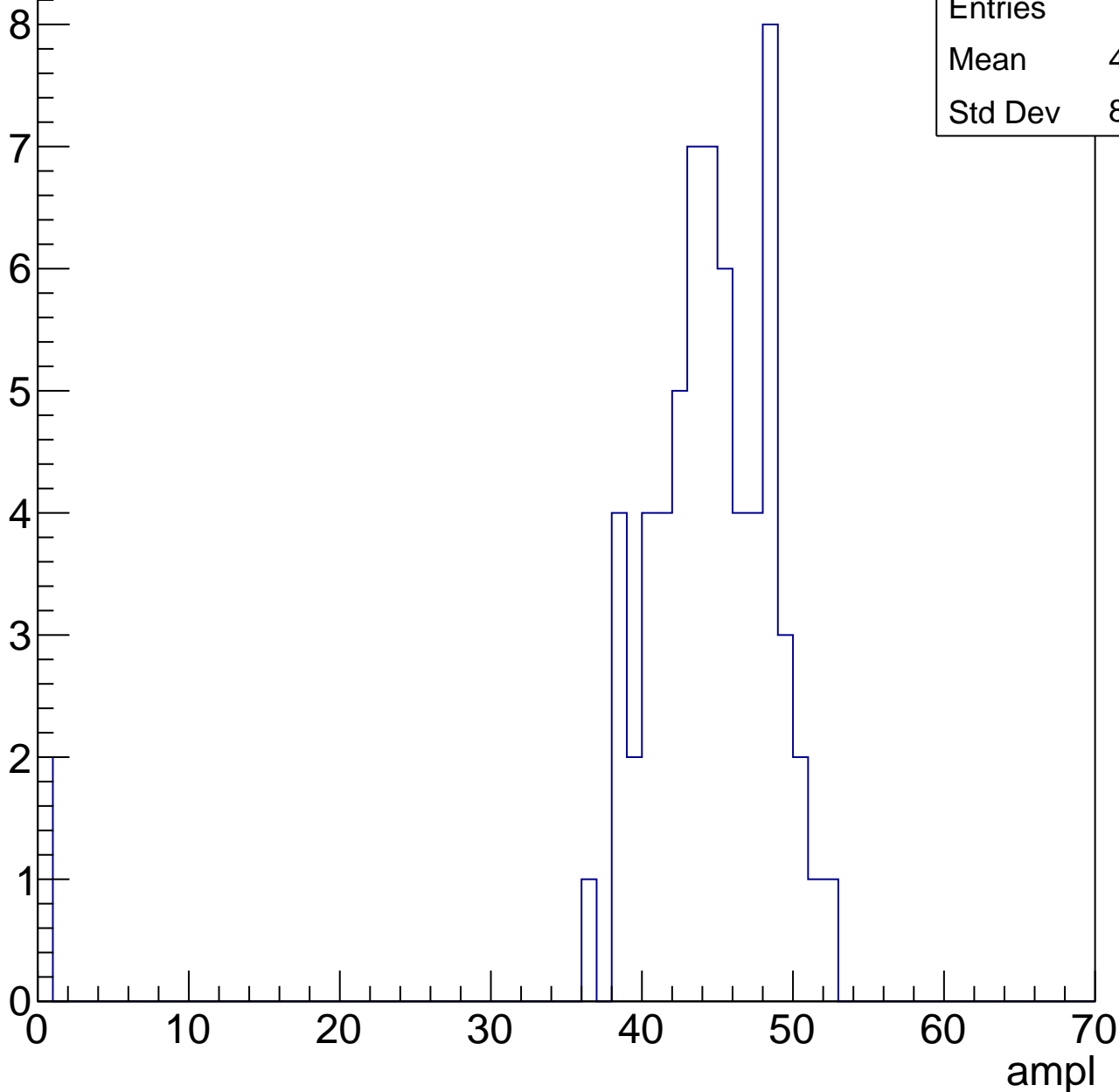


B1L103S, U1-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	42.85
Std Dev	8.427

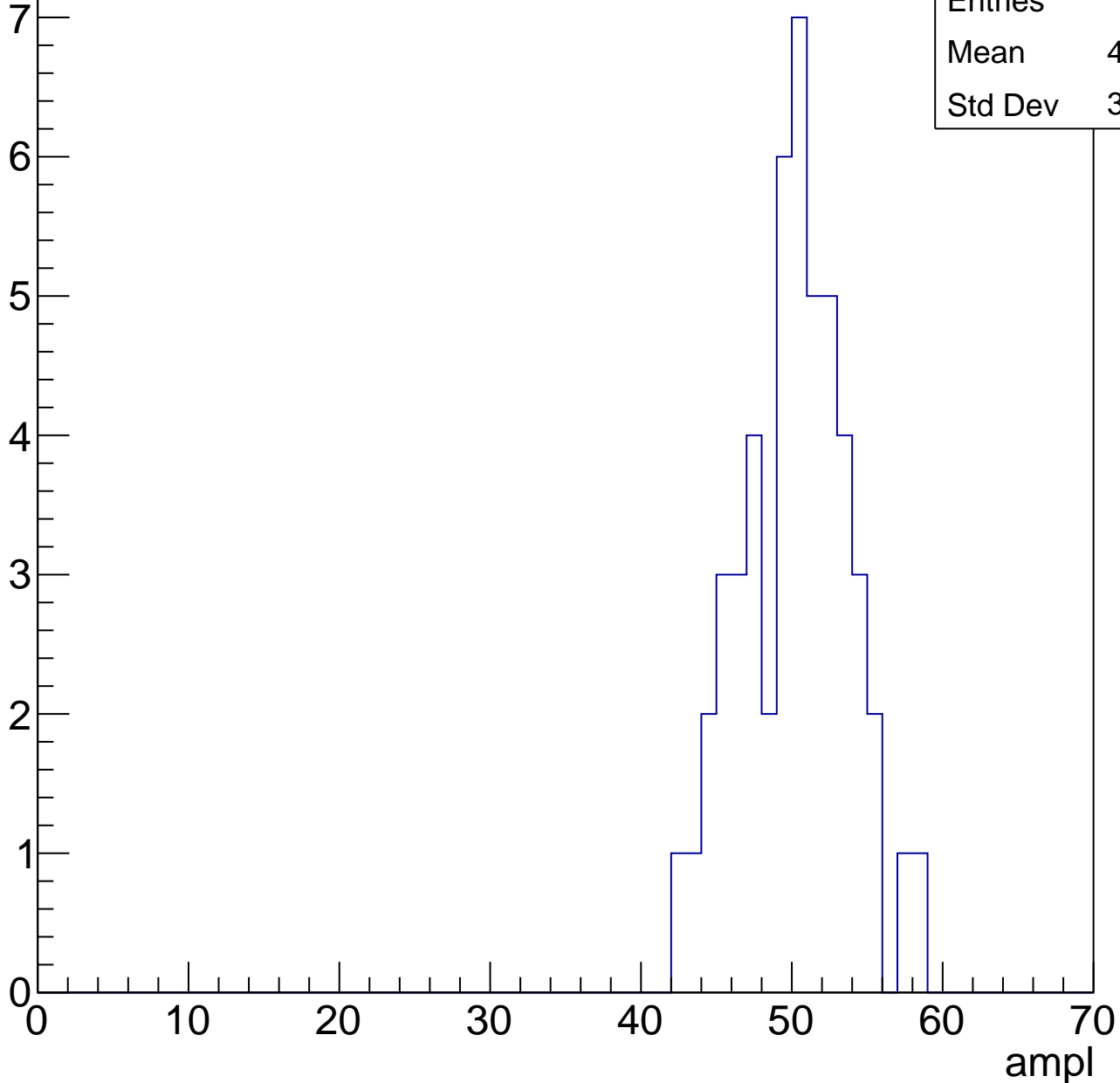


B1L103S, U1-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	49.76
Std Dev	3.542

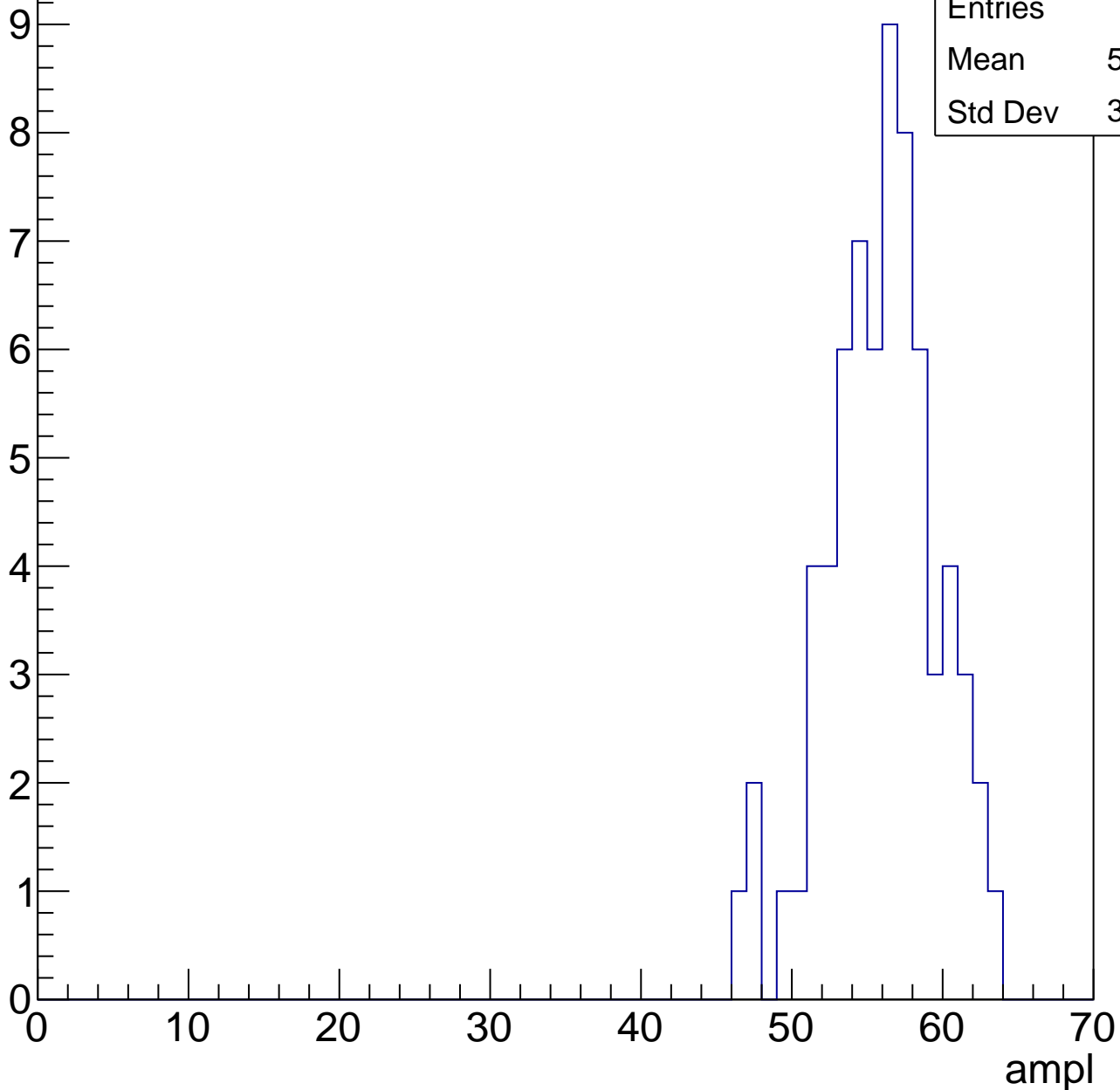


B1L103S, U1-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

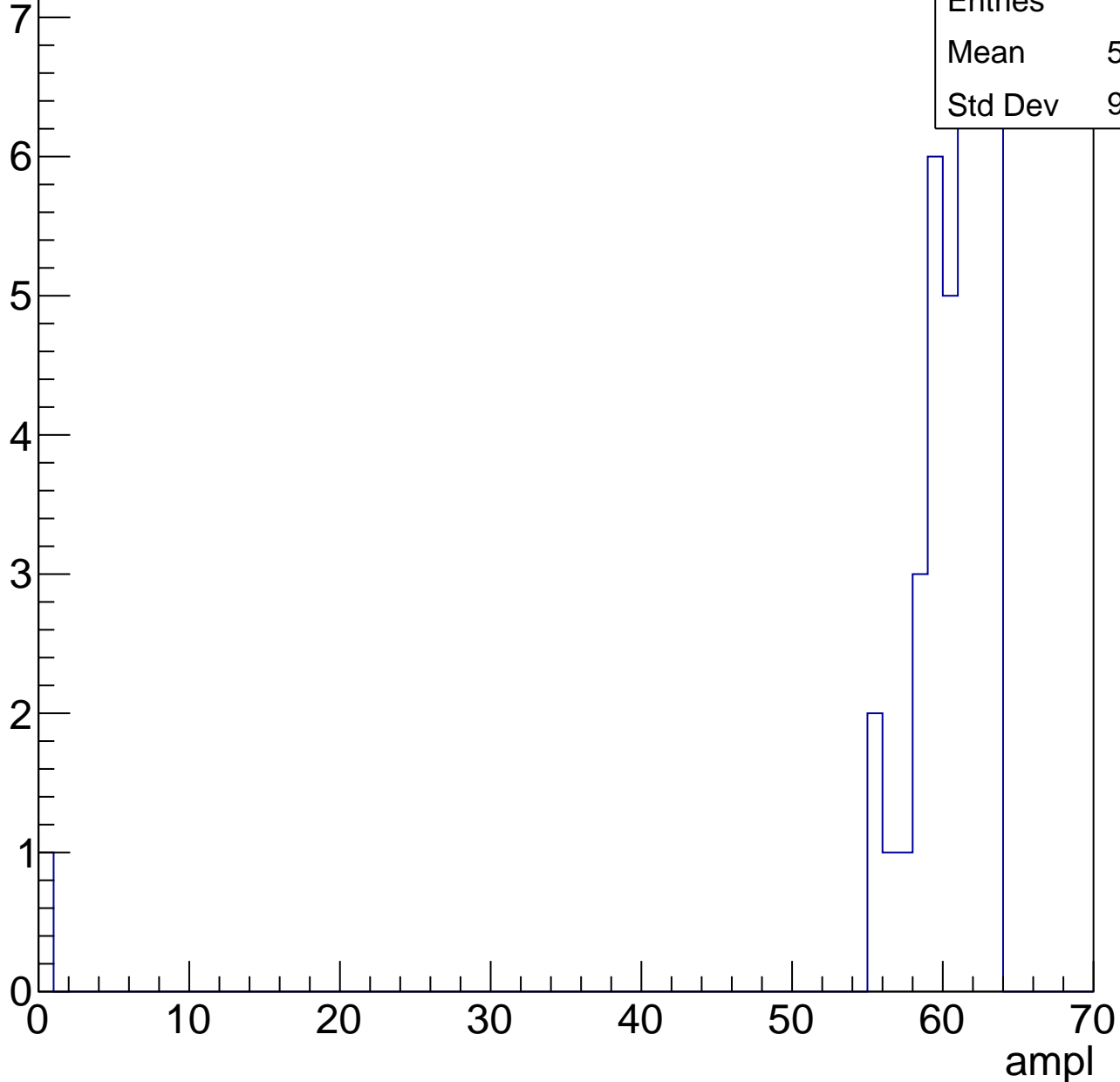
Entries	68
Mean	55.47
Std Dev	3.636



B1L103S, U1-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

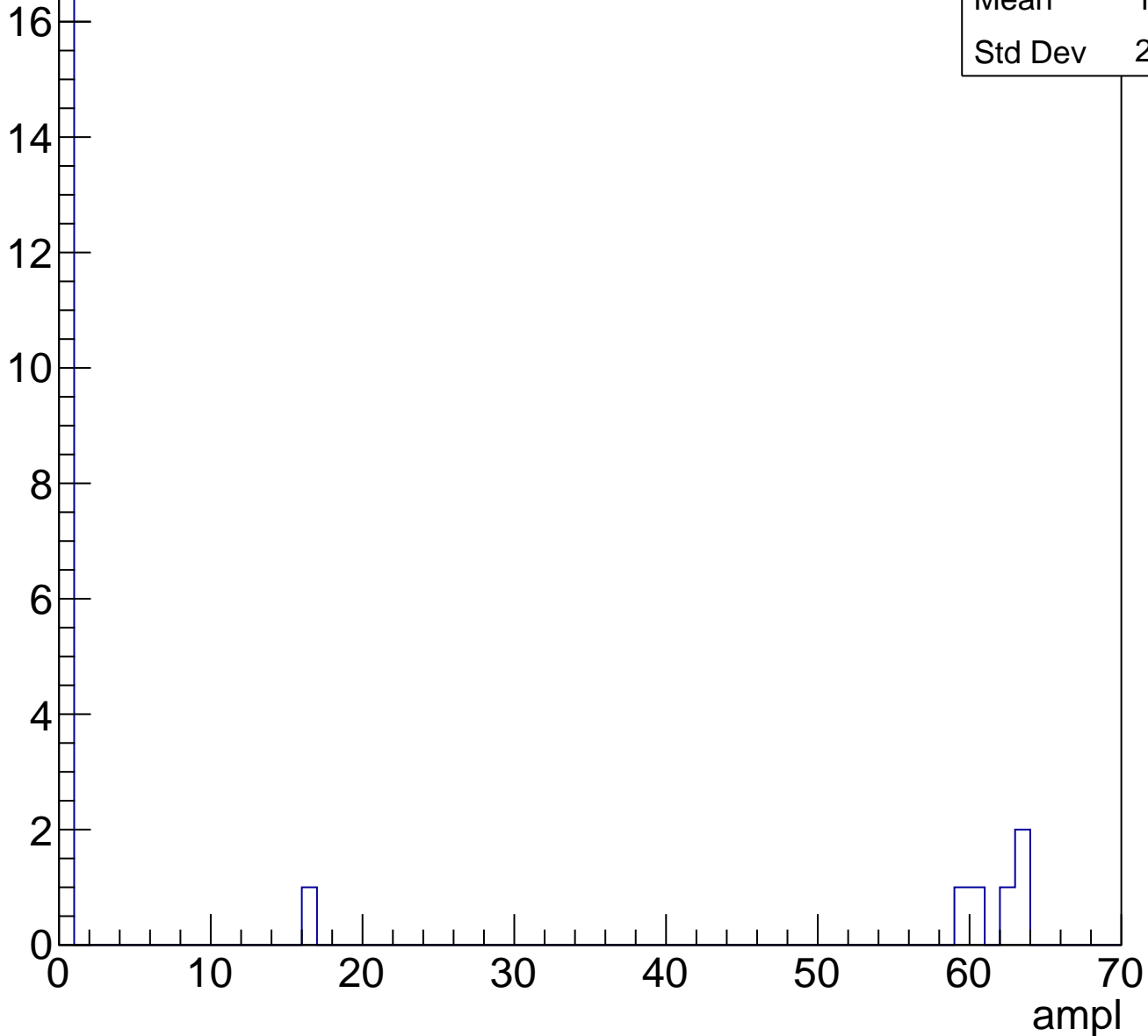


B1L103S, U1-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	14.04
Std Dev	25.18

Entry



B1L103S, U1-ch127, adc0

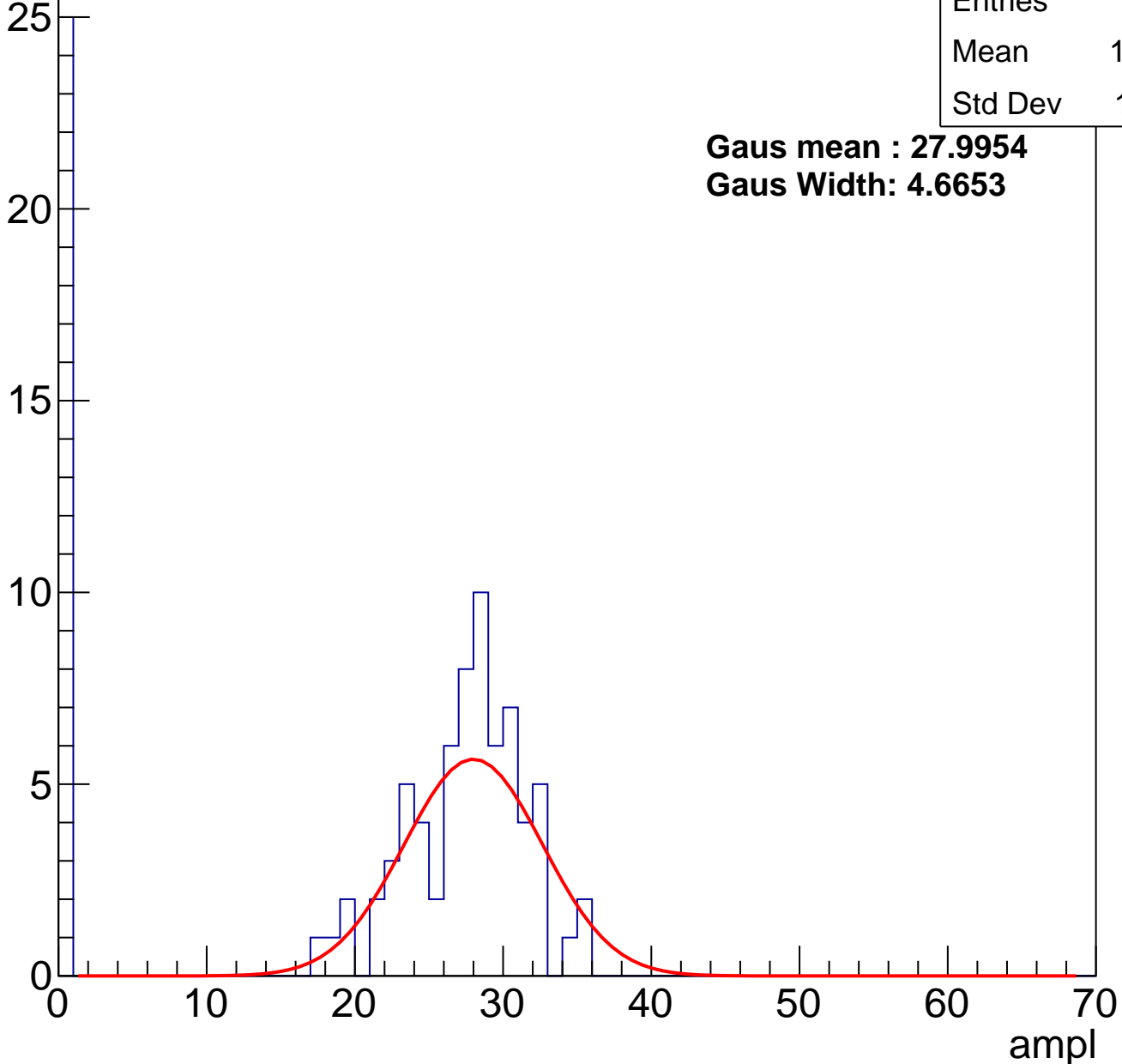
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	19.85
Std Dev	12.41

Gaus mean : 27.9954

Gaus Width: 4.6653

Entry



B1L103S, U1-ch127, adc1

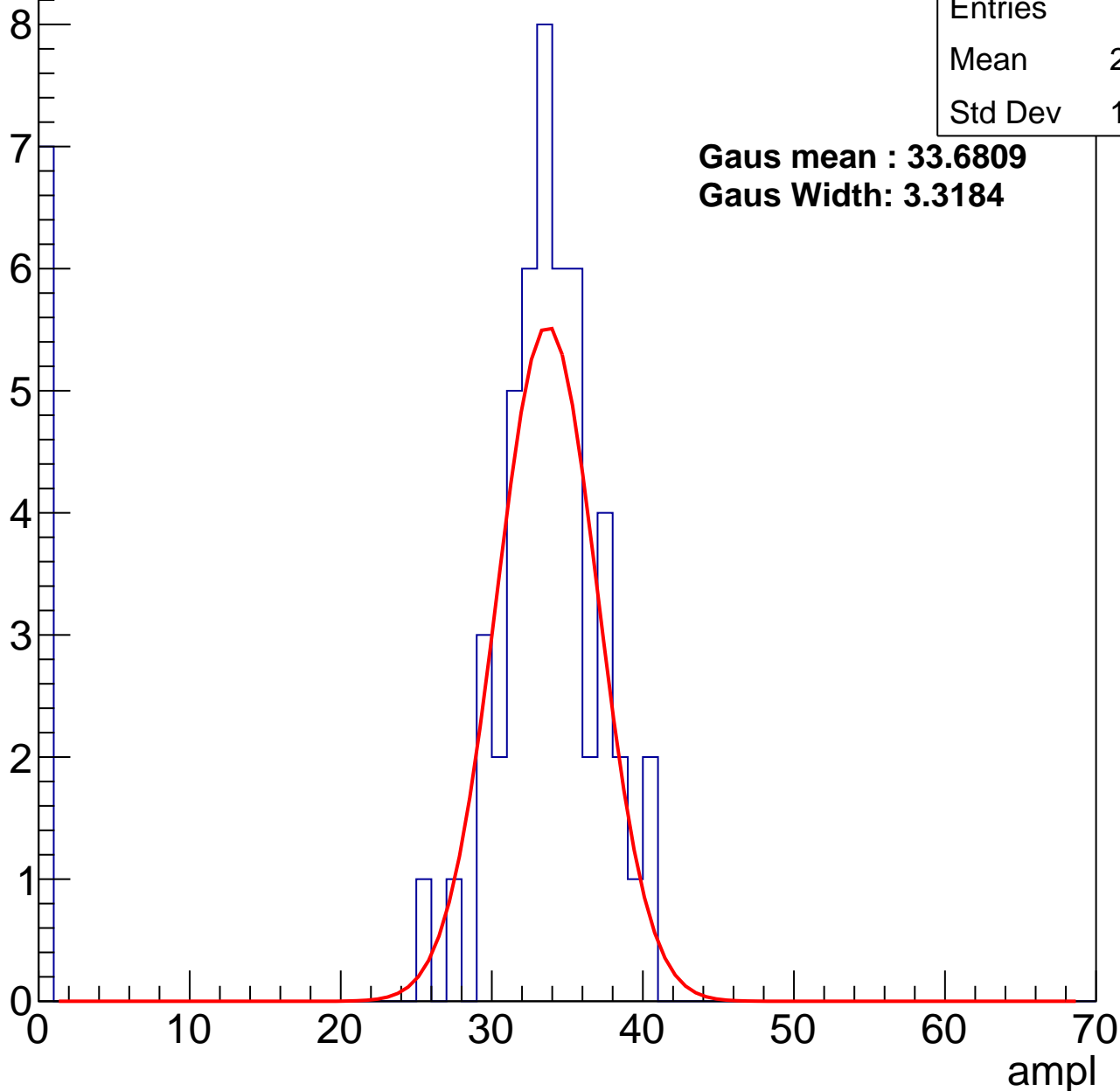
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	29.27
Std Dev	11.44

Gaus mean : 33.6809

Gaus Width: 3.3184



B1L103S, U1-ch127, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	32.44
Std Dev	15.28

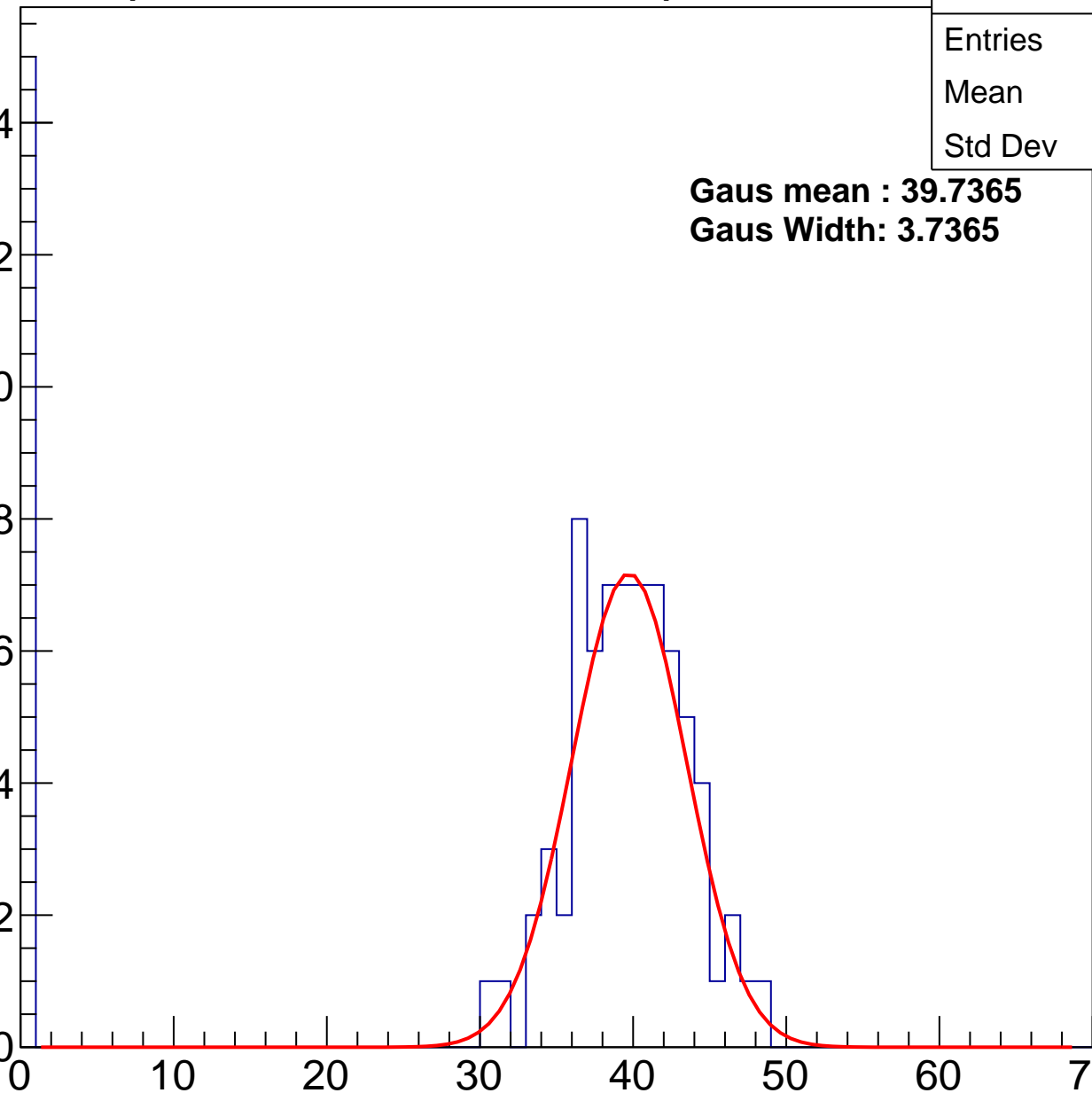
Gaus mean : 39.7365

Gaus Width: 3.7365

Entry

14
12
10
8
6
4
2
0

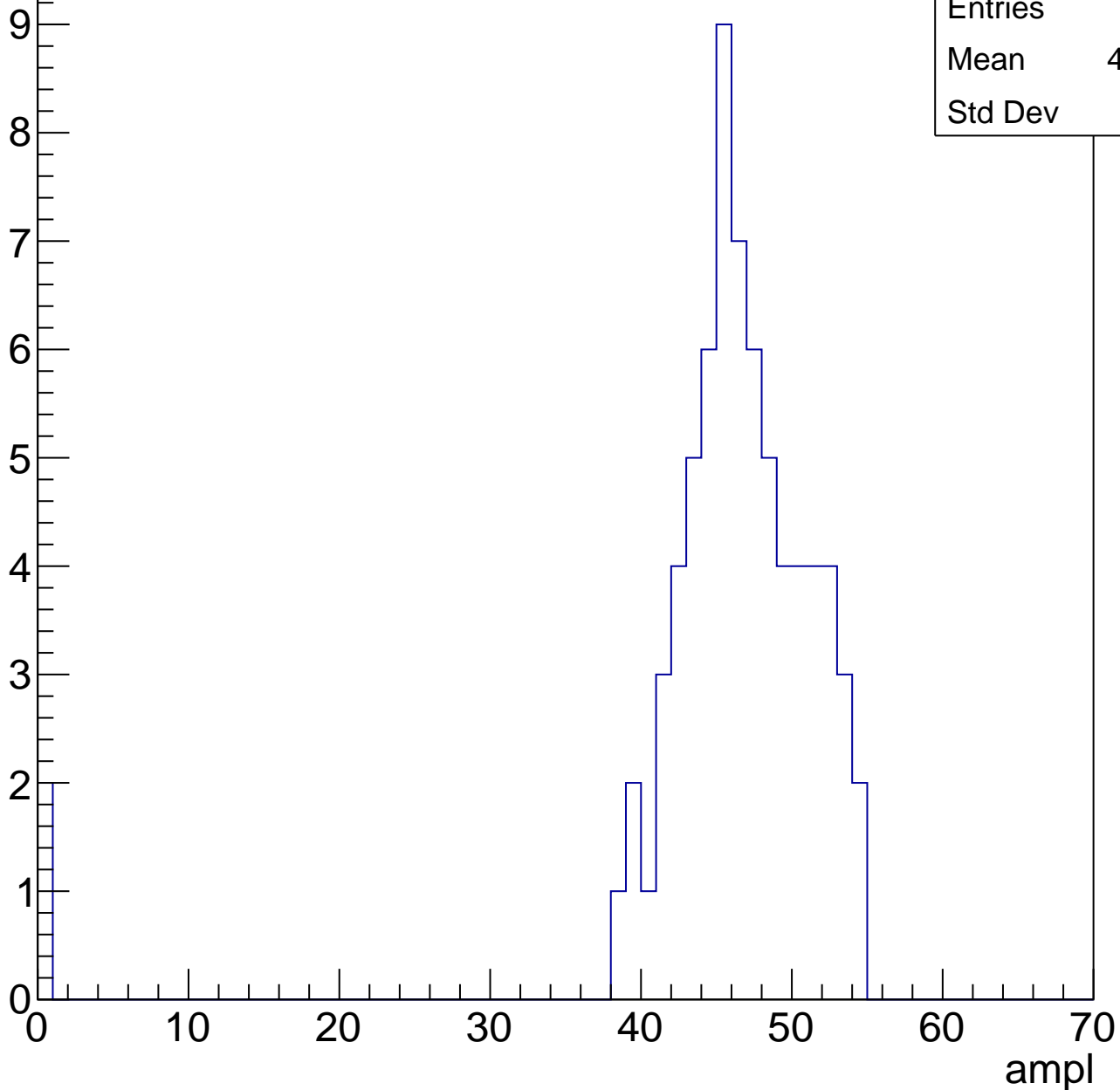
ampl



B1L103S, U1-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

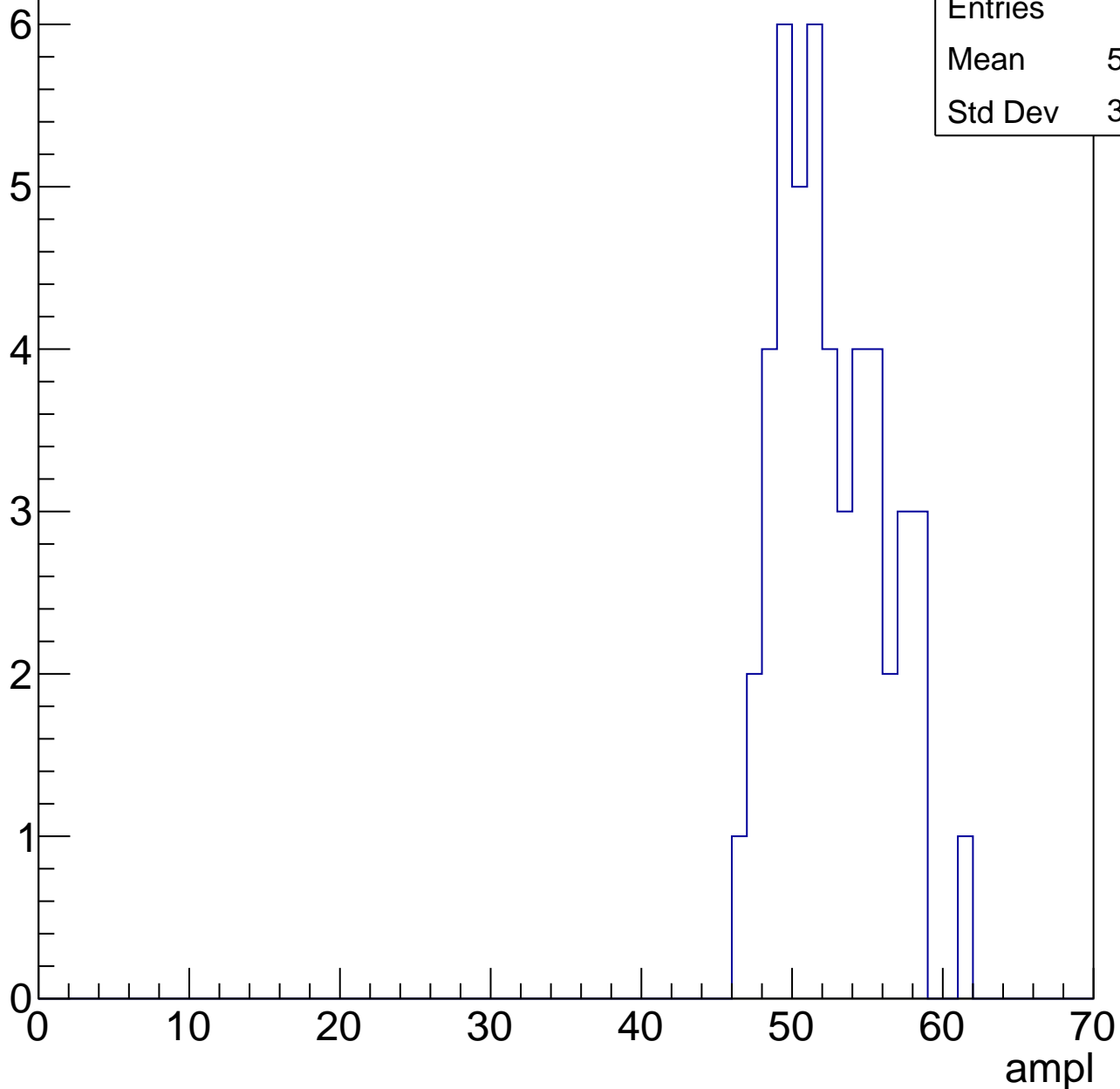


B1L103S, U1-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	52.15
Std Dev	3.476

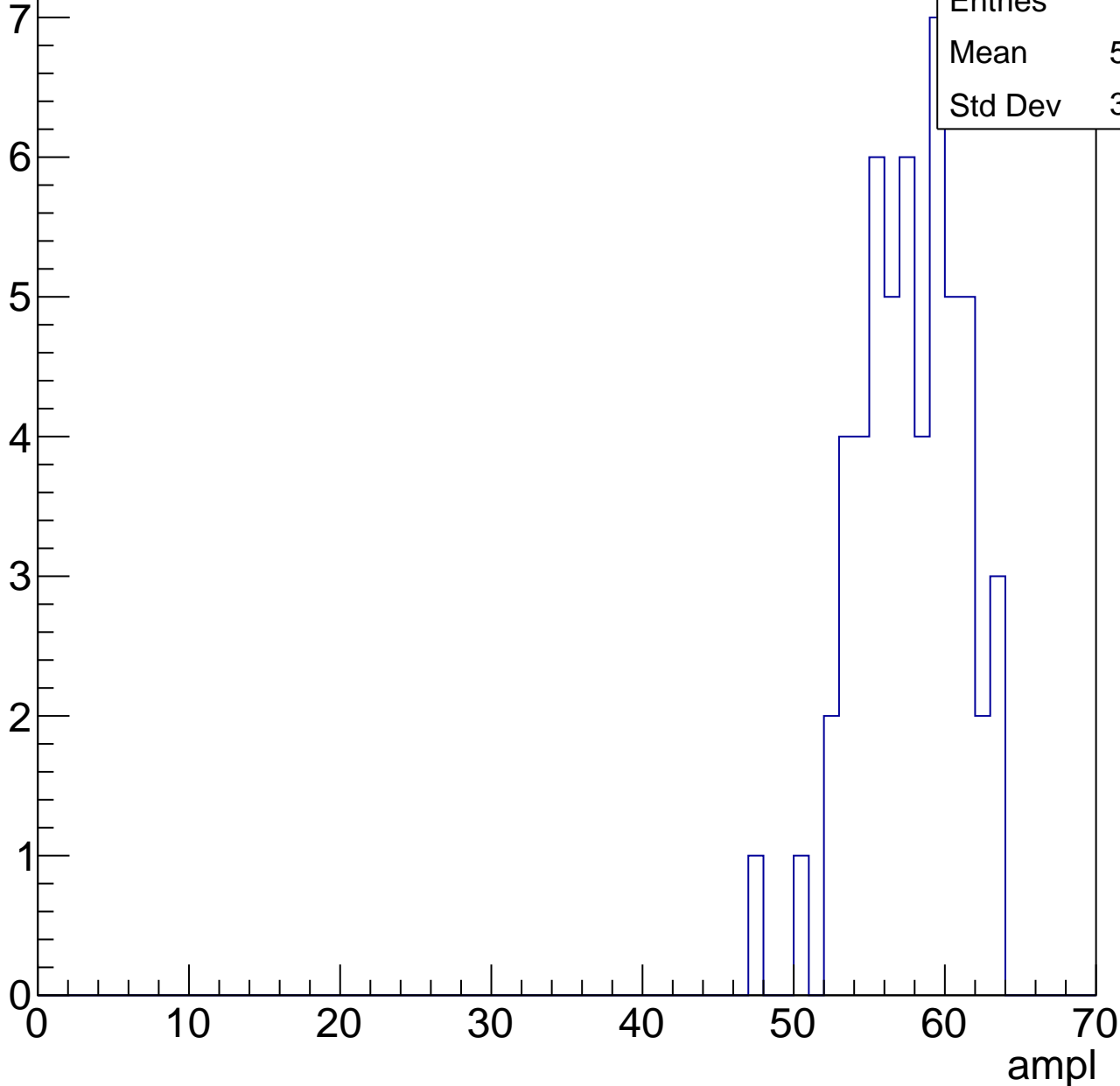


B1L103S, U1-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.16
Std Dev	3.415

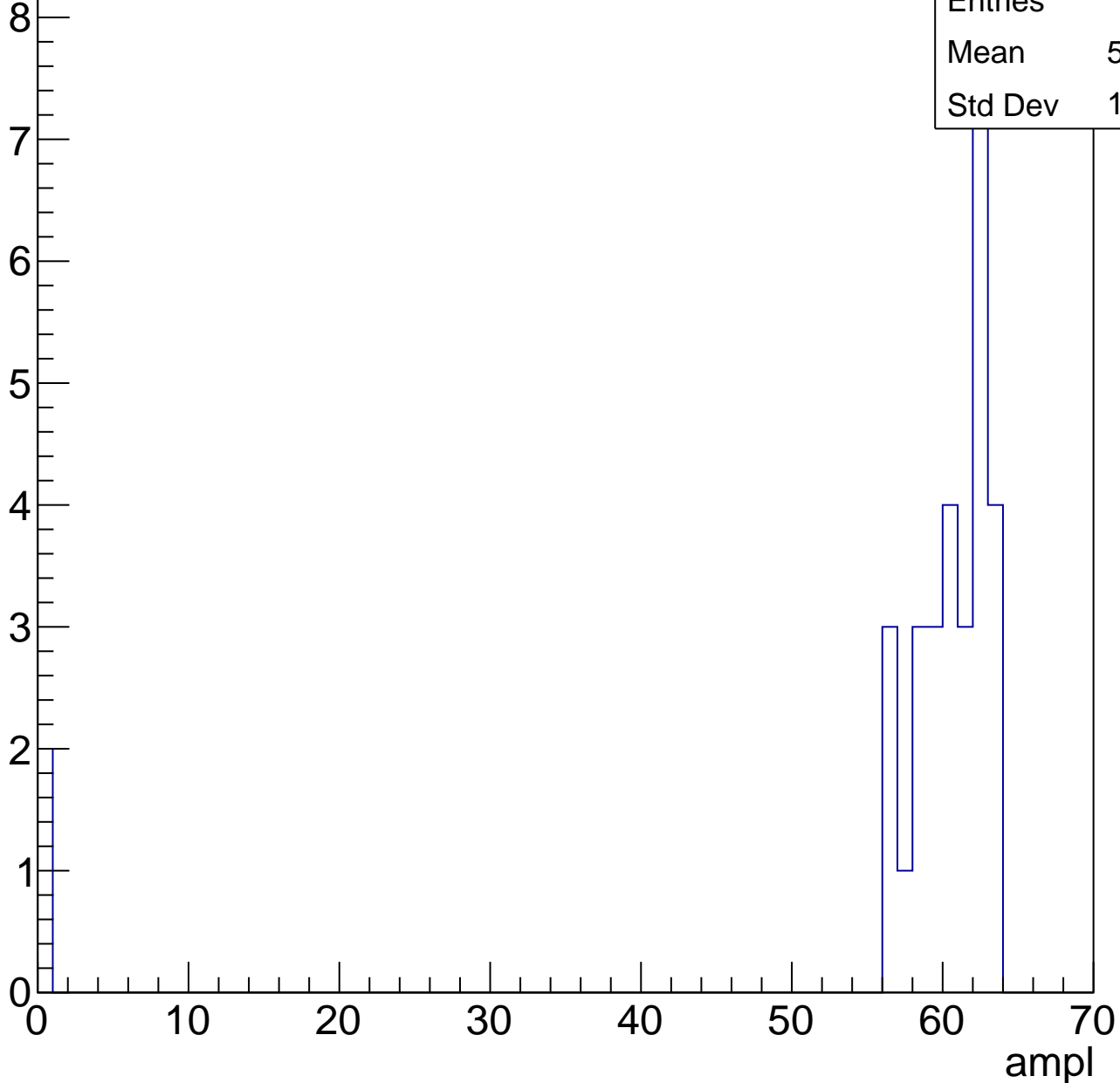


B1L103S, U1-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	56.35
Std Dev	14.95

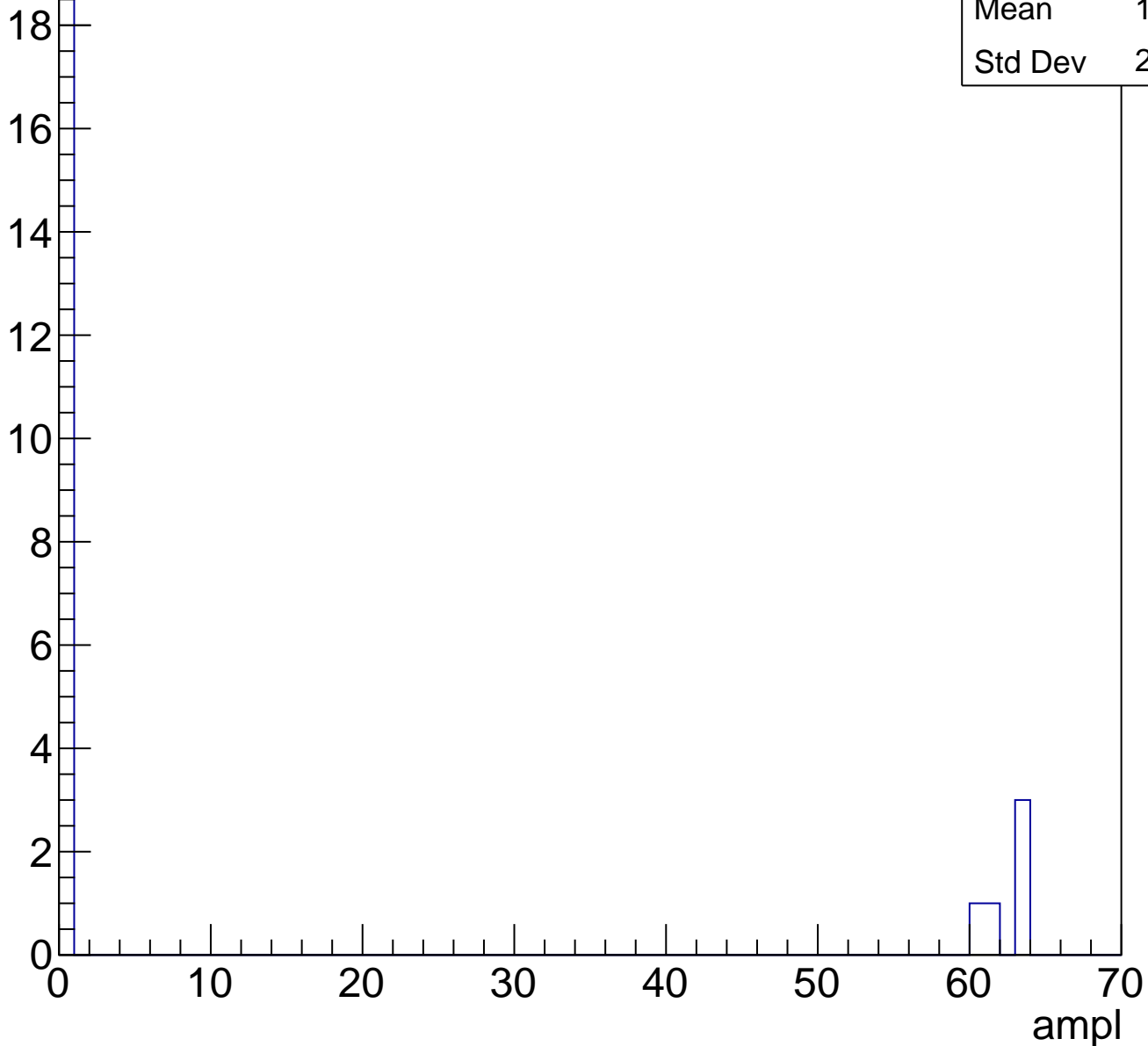


B1L103S, U1-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.92
Std Dev	25.19

Entry



B1L103S, U1-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	12.92
Std Dev	25.19

Entry

