

B0L100S, U23-ch0

calib_packv5_042523_0143.root, FC#6, port A1

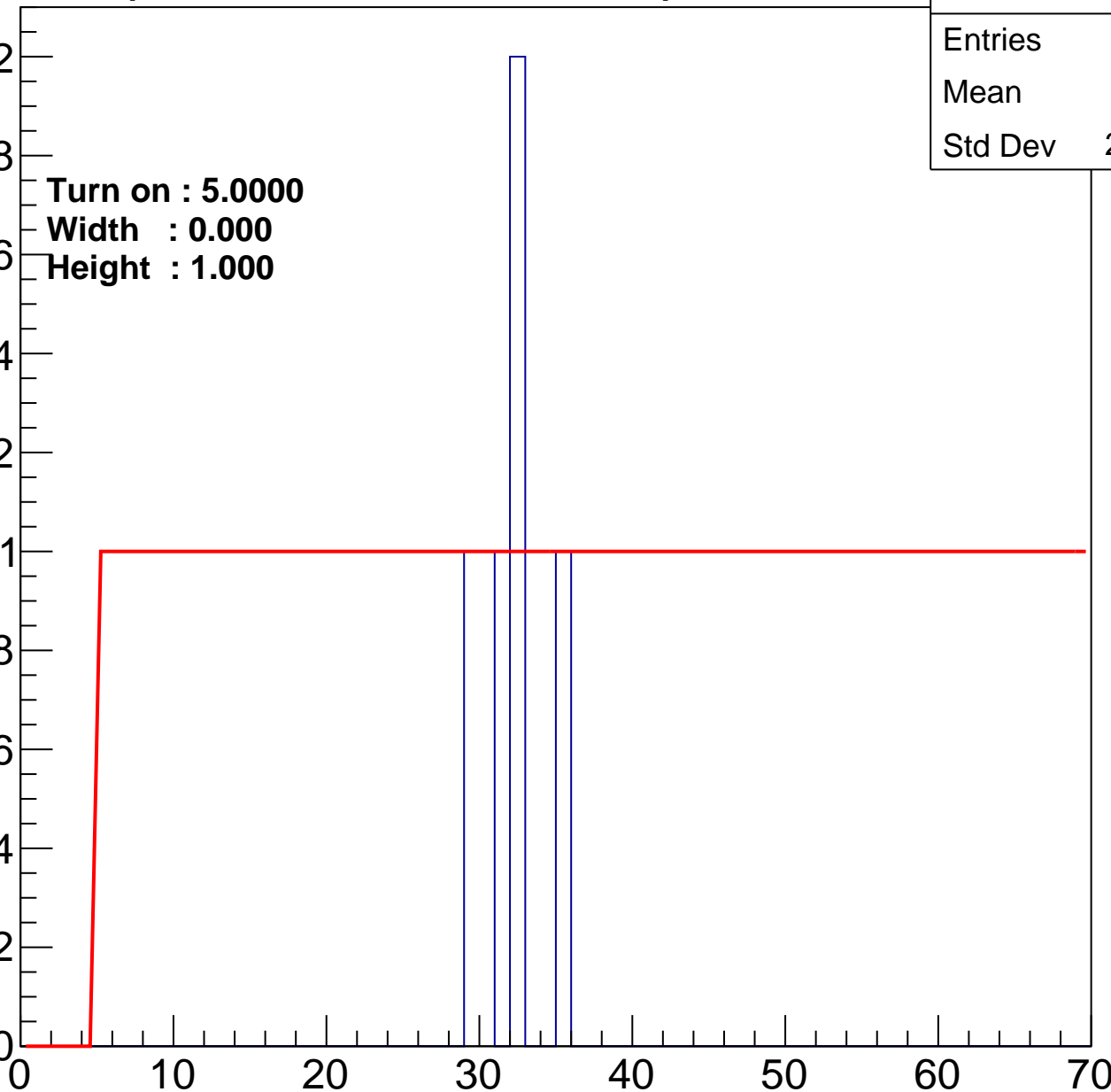
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	5
Mean	31.6
Std Dev	2.059

ampl



B0L100S, U23-ch1

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch2

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch3

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch4

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch5

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch6

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch7

calib_packv5_042523_0143.root, FC#6, port A1

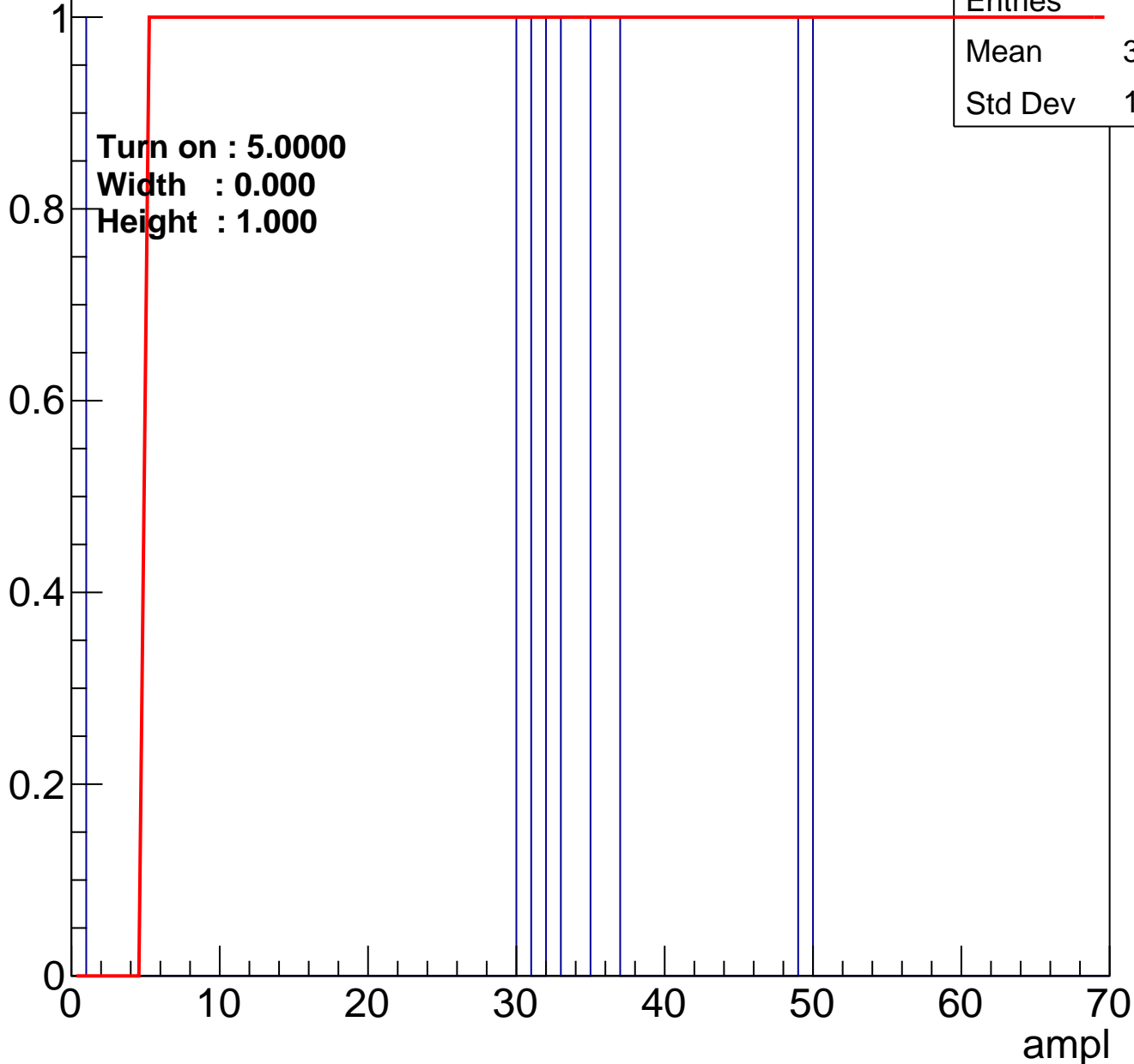
Entry



B0L100S, U23-ch8

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch9

calib_packv5_042523_0143.root, FC#6, port A1

Entry

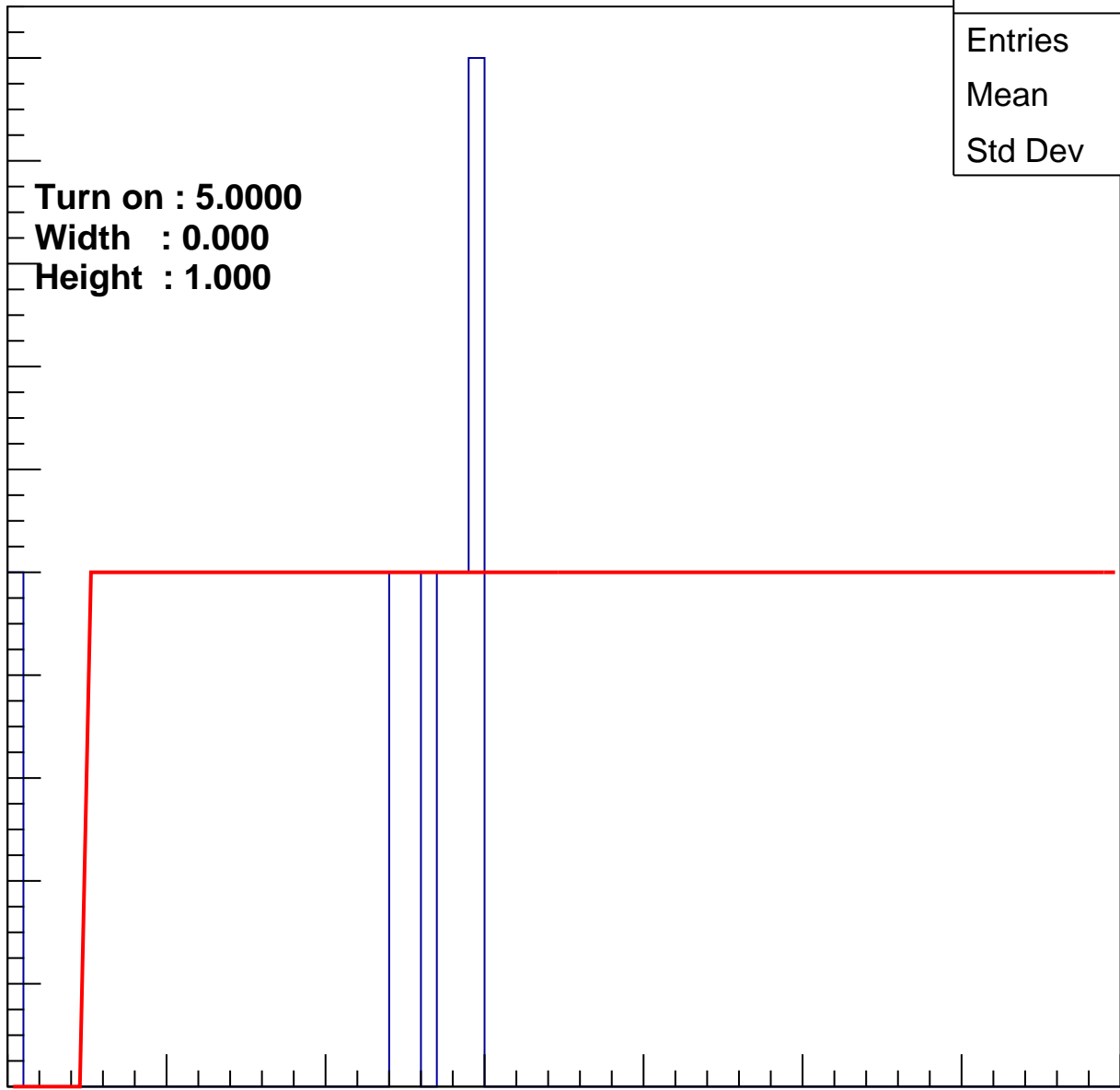
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	7
Mean	23.14
Std Dev	9.613

0 10 20 30 40 50 60 70

ampl



B0L100S, U23-ch10

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch11

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch12

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch13

calib_packv5_042523_0143.root, FC#6, port A1

Entry

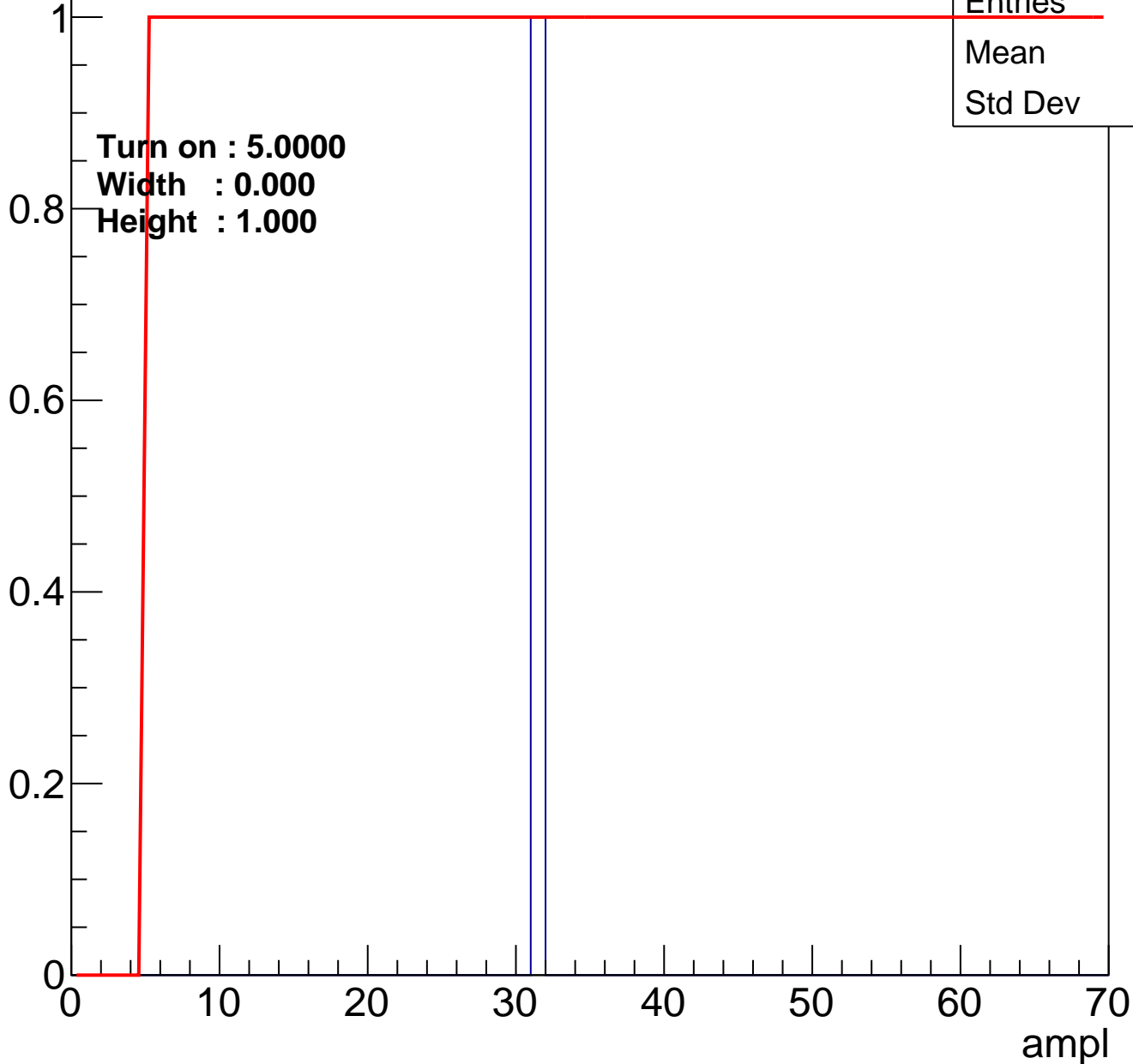


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch14

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch15

calib_packv5_042523_0143.root, FC#6, port A1

Entry

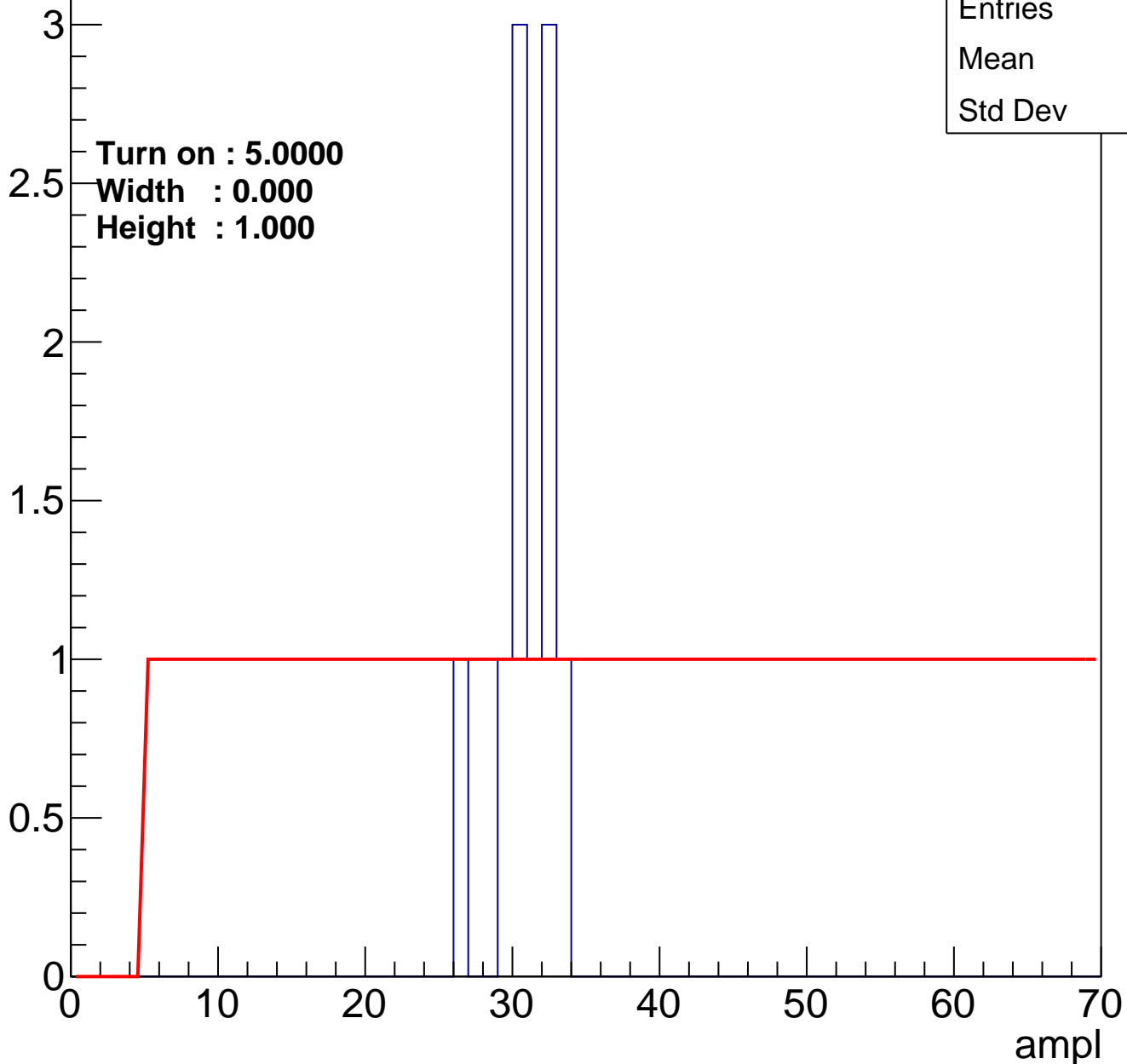


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch16

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch17

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch18

calib_packv5_042523_0143.root, FC#6, port A1

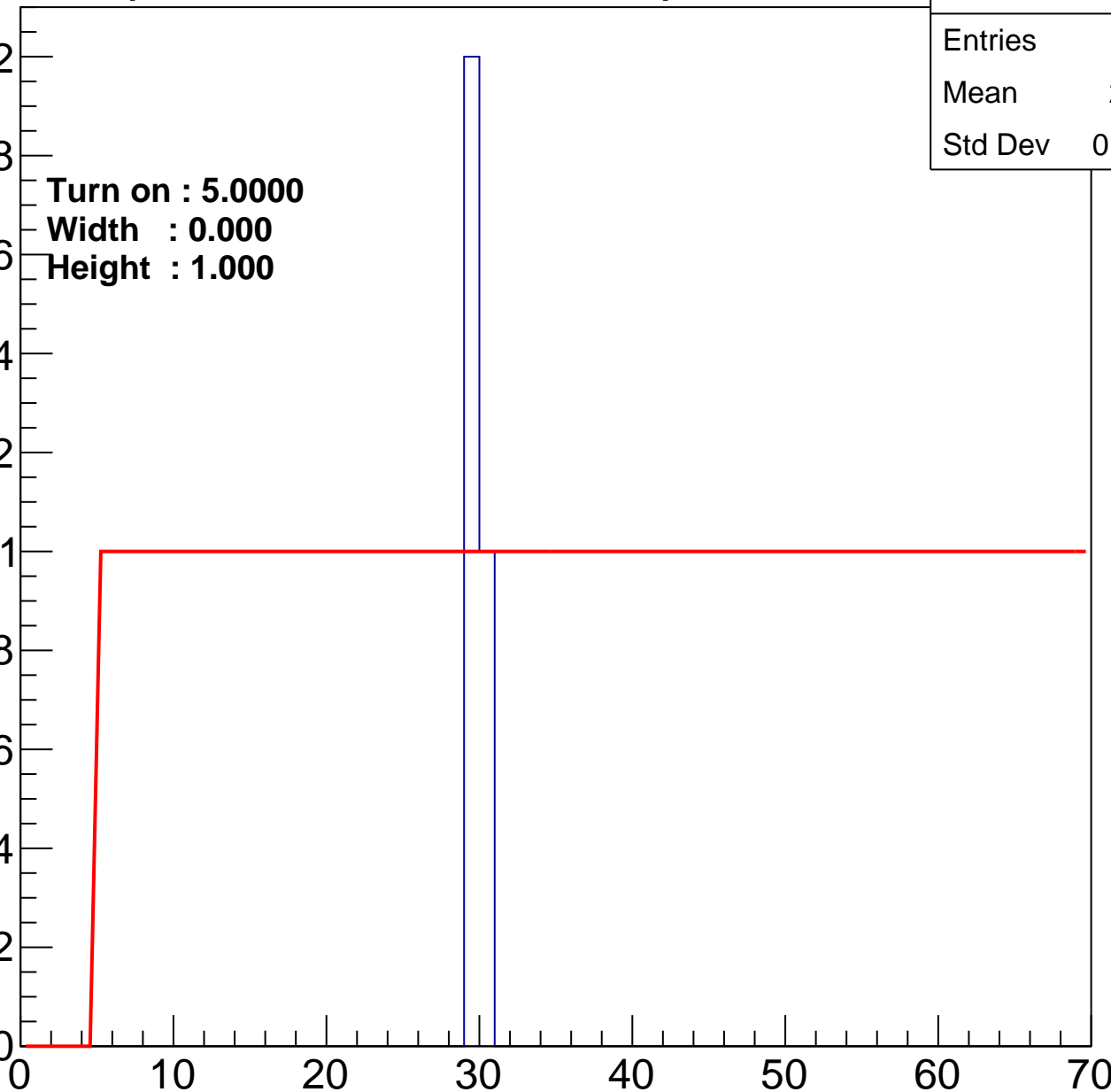
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	3
Mean	29.33
Std Dev	0.4714

ampl



B0L100S, U23-ch19

calib_packv5_042523_0143.root, FC#6, port A1

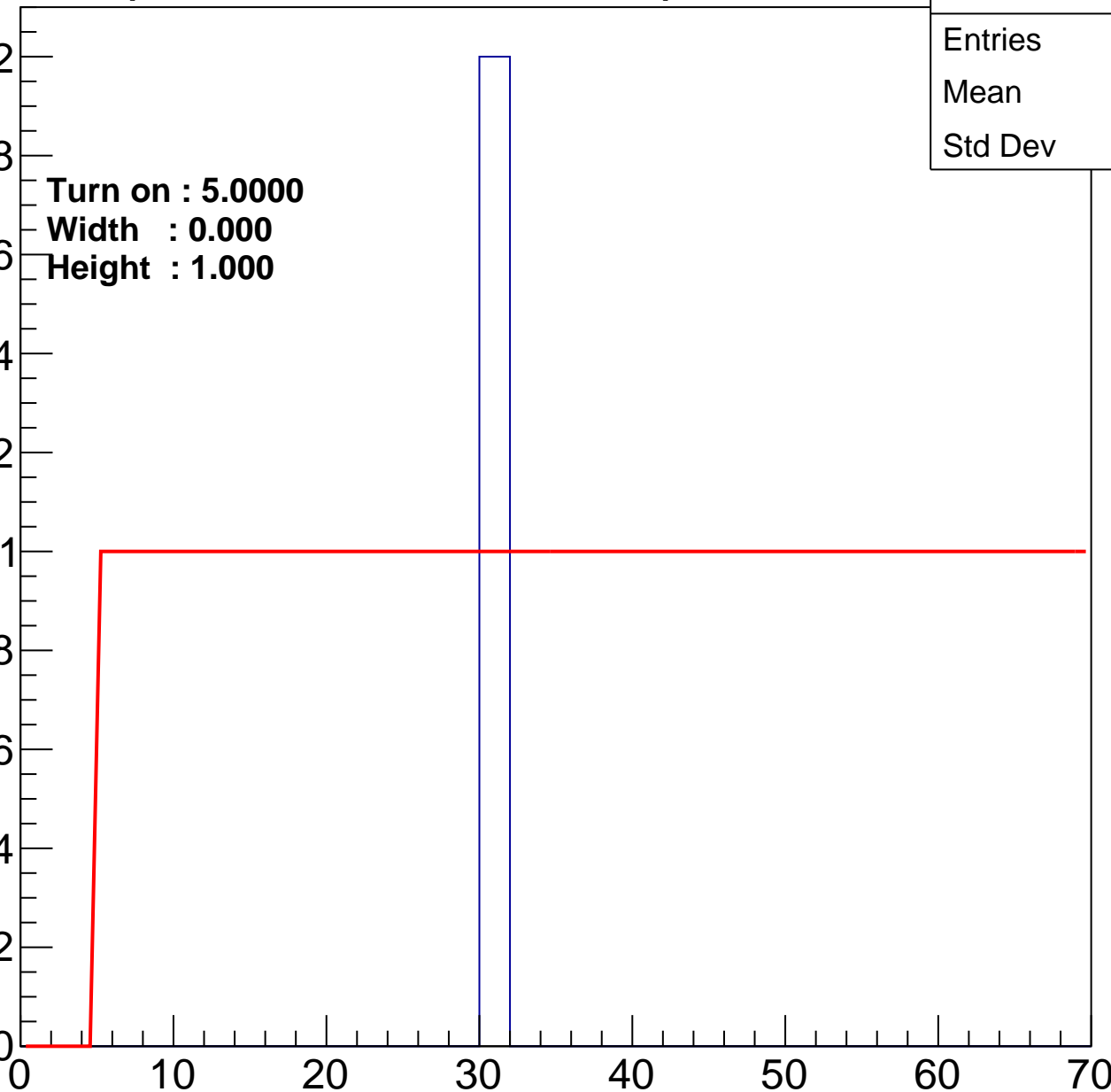
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	4
Mean	30.5
Std Dev	0.5

ampl



B0L100S, U23-ch20

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch21

calib_packv5_042523_0143.root, FC#6, port A1

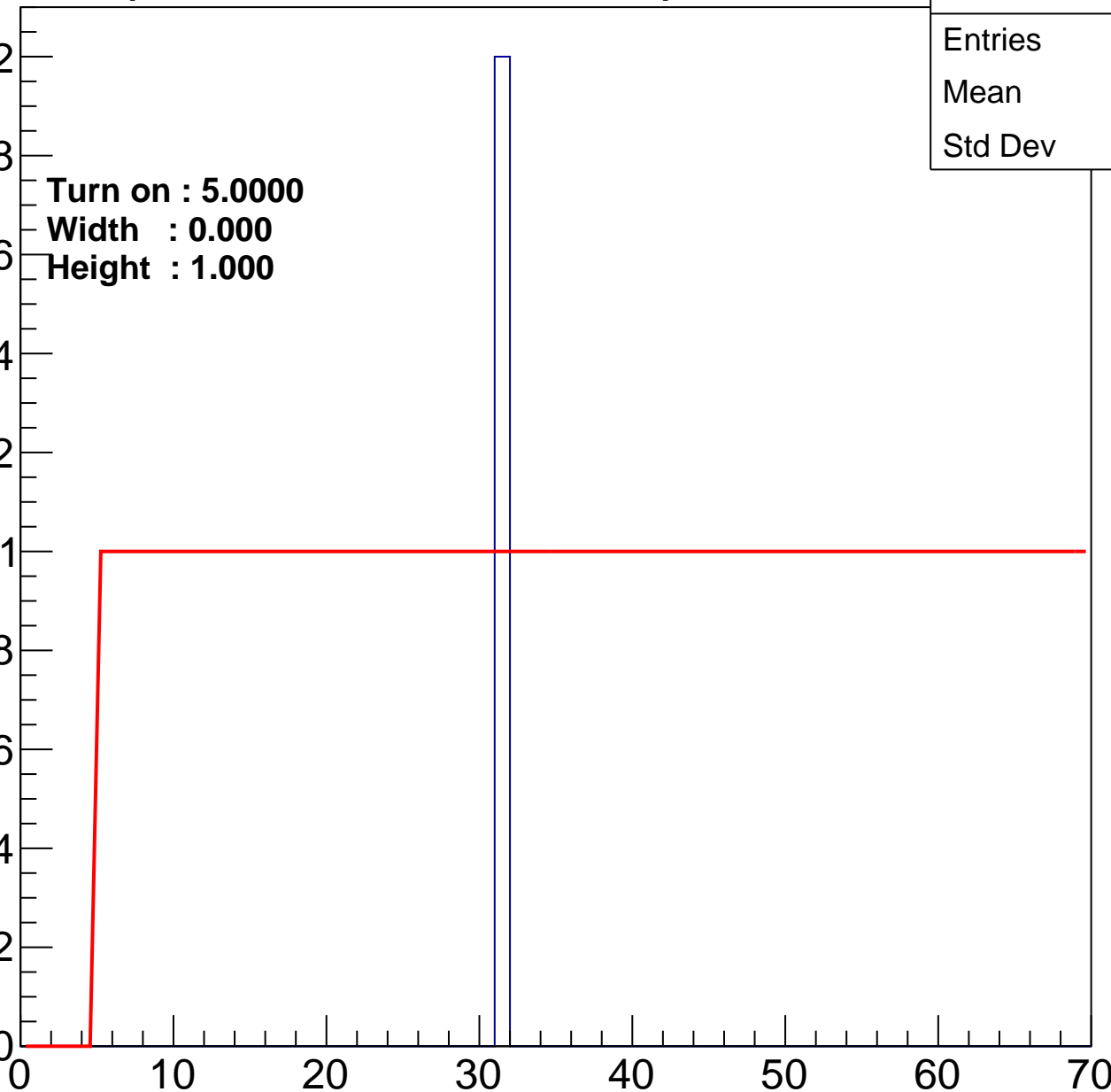
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	2
Mean	31
Std Dev	0

ampl



B0L100S, U23-ch22

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch23

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch24

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch25

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch26

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch27

calib_packv5_042523_0143.root, FC#6, port A1

Entry

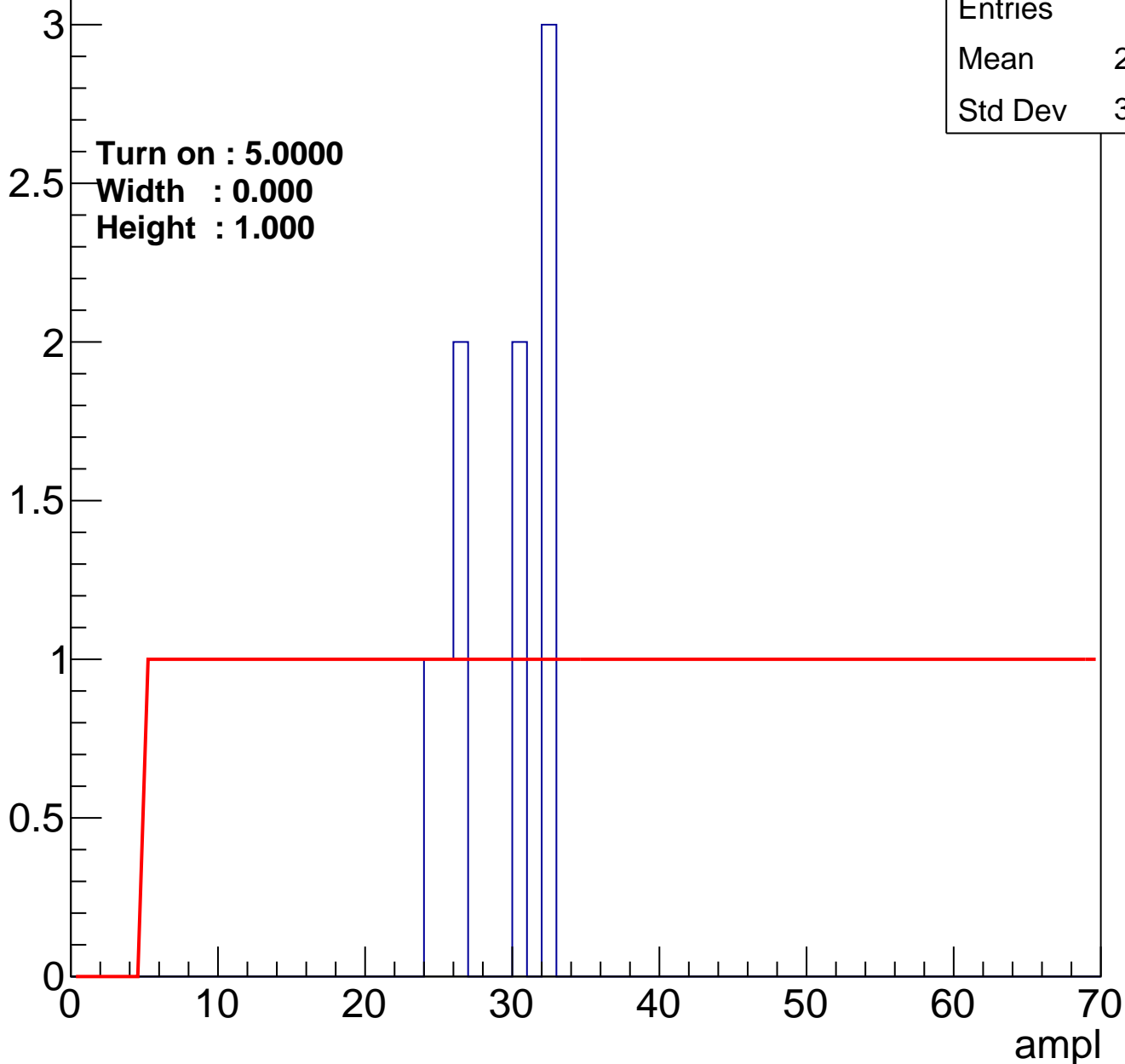


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch28

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch29

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch30

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch31

calib_packv5_042523_0143.root, FC#6, port A1

Entry

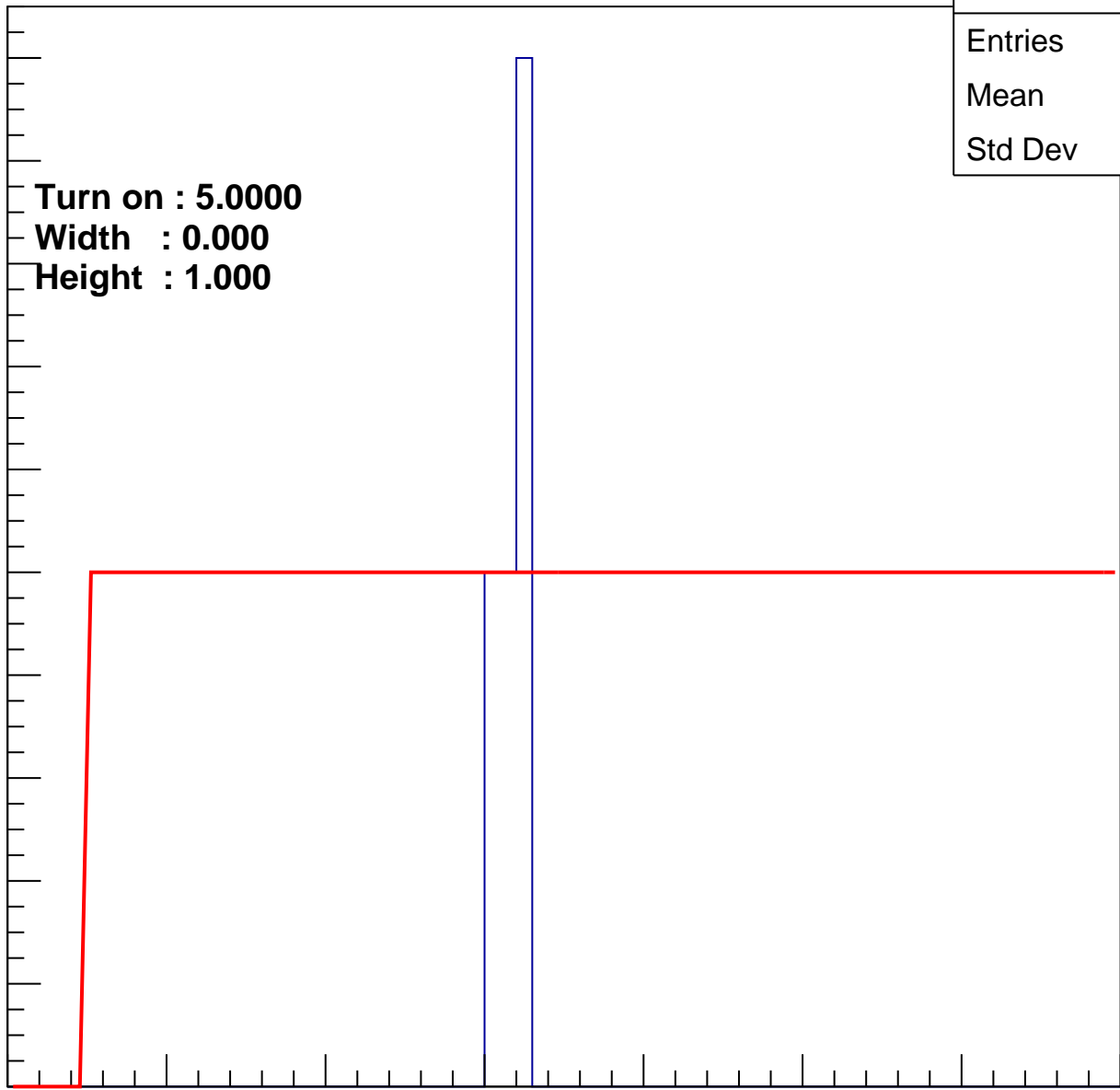
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	4
Mean	31.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



B0L100S, U23-ch32

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch33

calib_packv5_042523_0143.root, FC#6, port A1

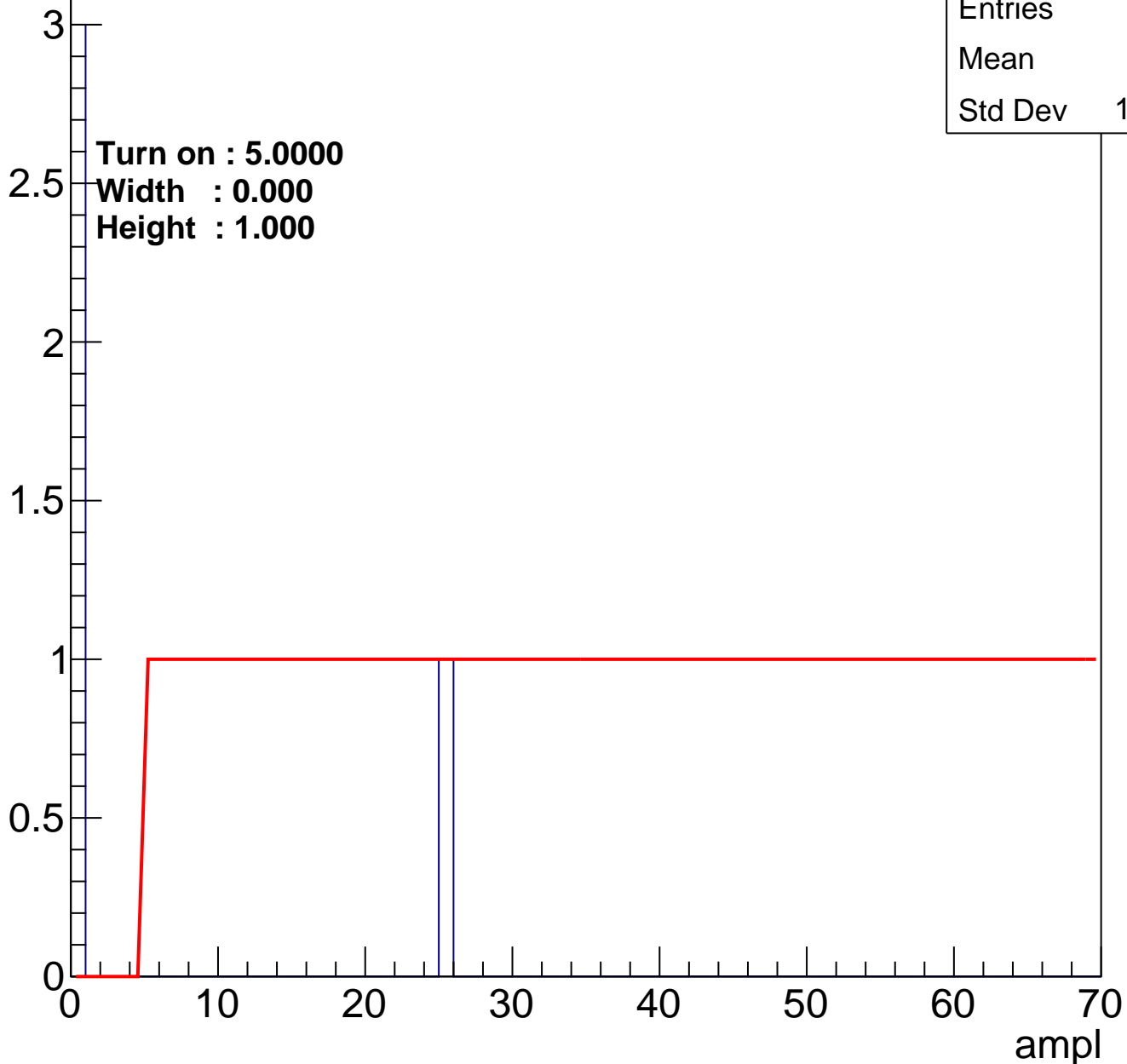
Entry



B0L100S, U23-ch34

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch35

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch36

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch37

calib_packv5_042523_0143.root, FC#6, port A1

Entry

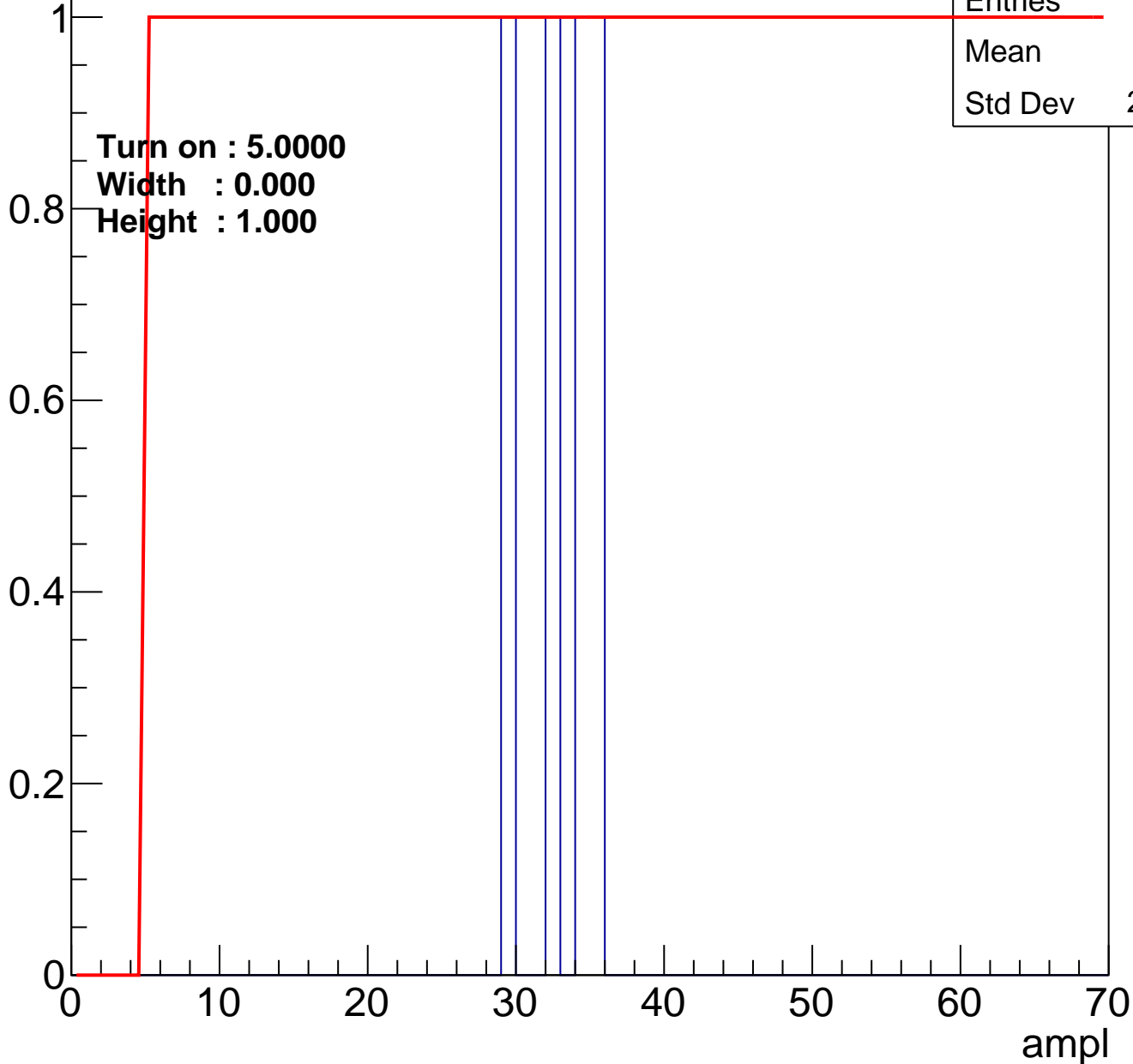


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch38

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	4
Mean	32.5
Std Dev	2.291

B0L100S, U23-ch39

calib_packv5_042523_0143.root, FC#6, port A1

Entry

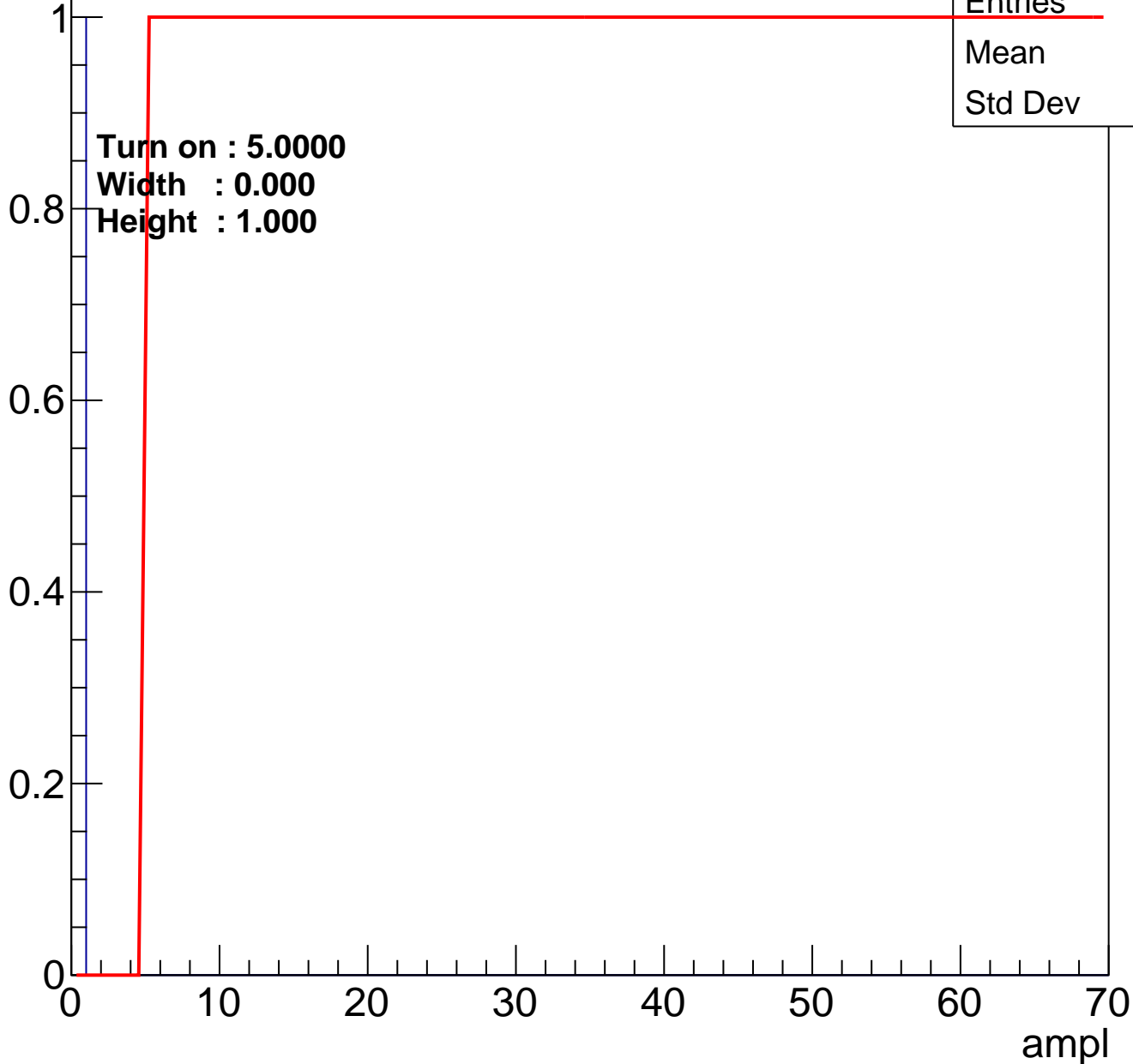


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch40

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch41

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch42

calib_packv5_042523_0143.root, FC#6, port A1

Entry

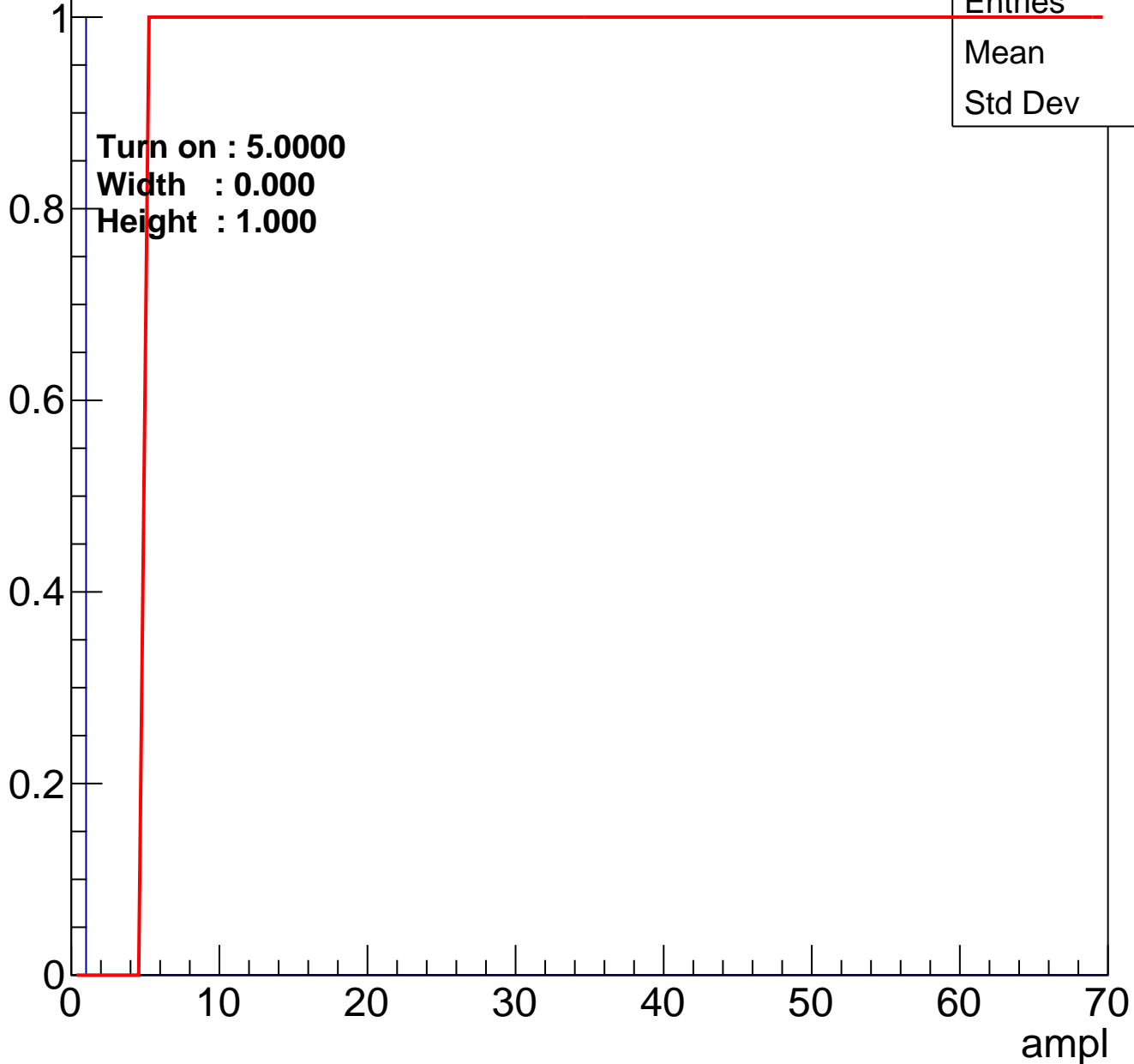


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch43

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch44

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch45

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch46

calib_packv5_042523_0143.root, FC#6, port A1

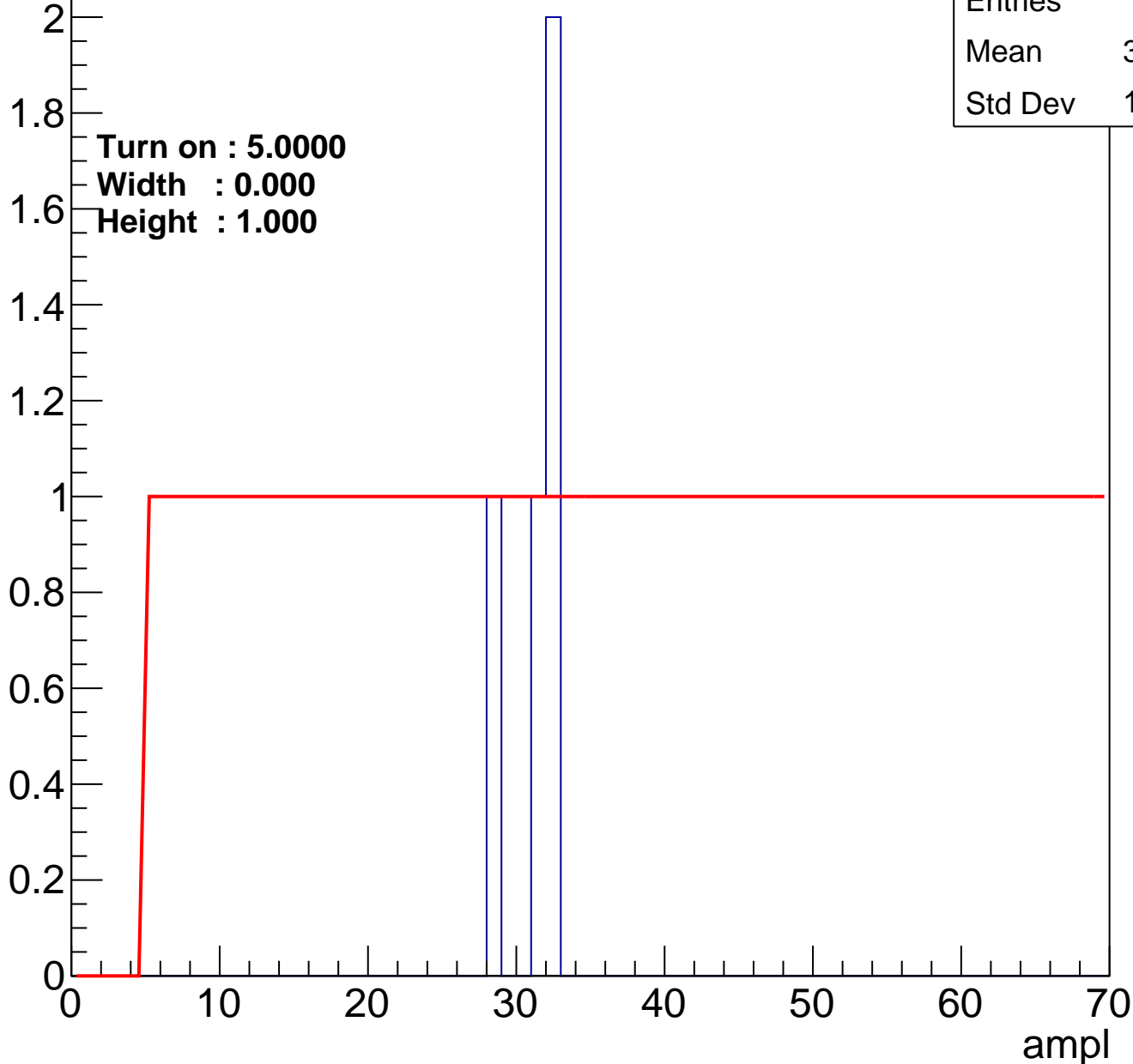
Entry

Entries	4
Mean	30.75
Std Dev	1.639

Turn on : 5.0000

Width : 0.000

Height : 1.000



B0L100S, U23-ch47

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch48

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch49

calib_packv5_042523_0143.root, FC#6, port A1

Entry

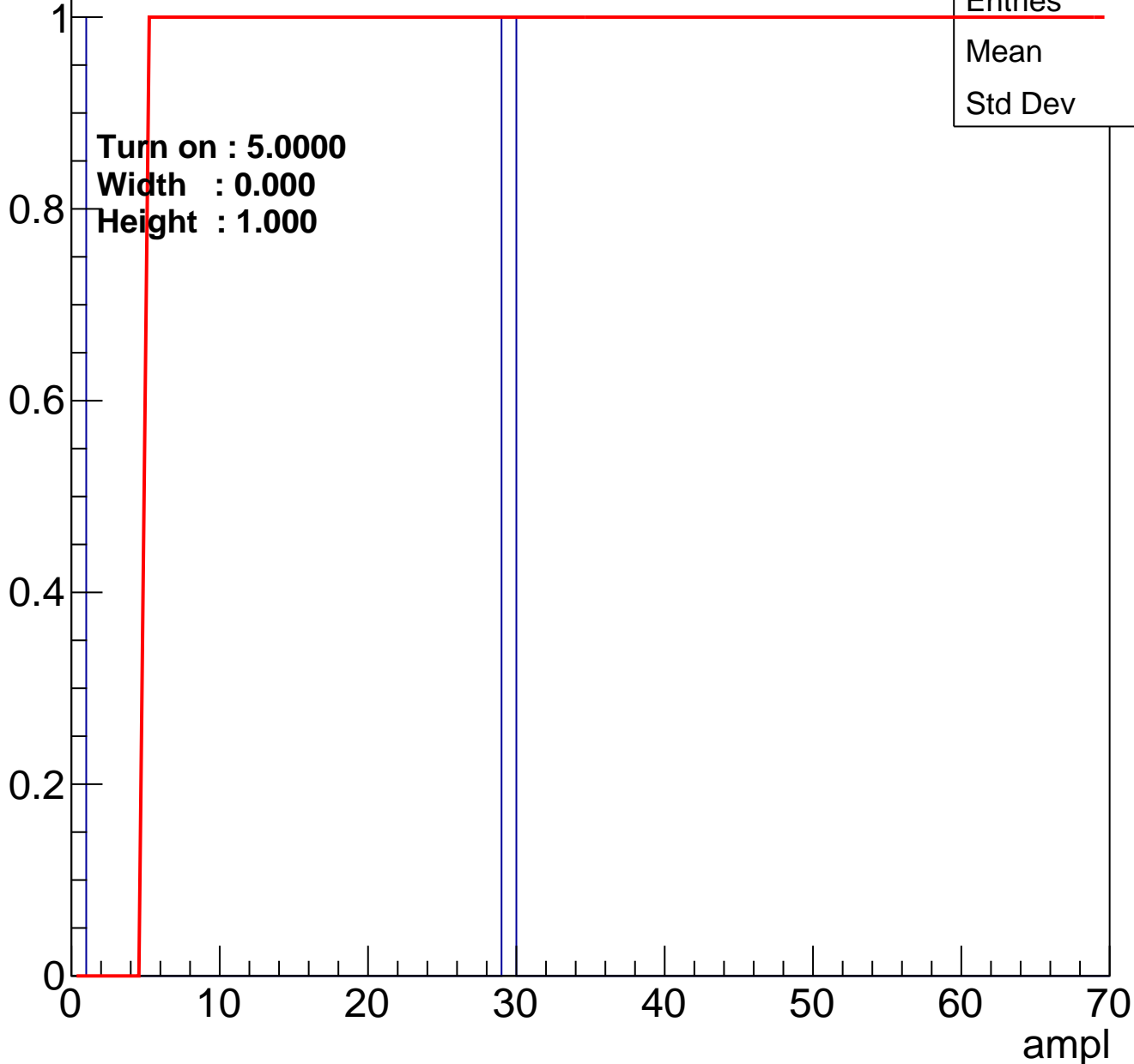


Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch50

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch51

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch52

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch53

calib_packv5_042523_0143.root, FC#6, port A1

Entry

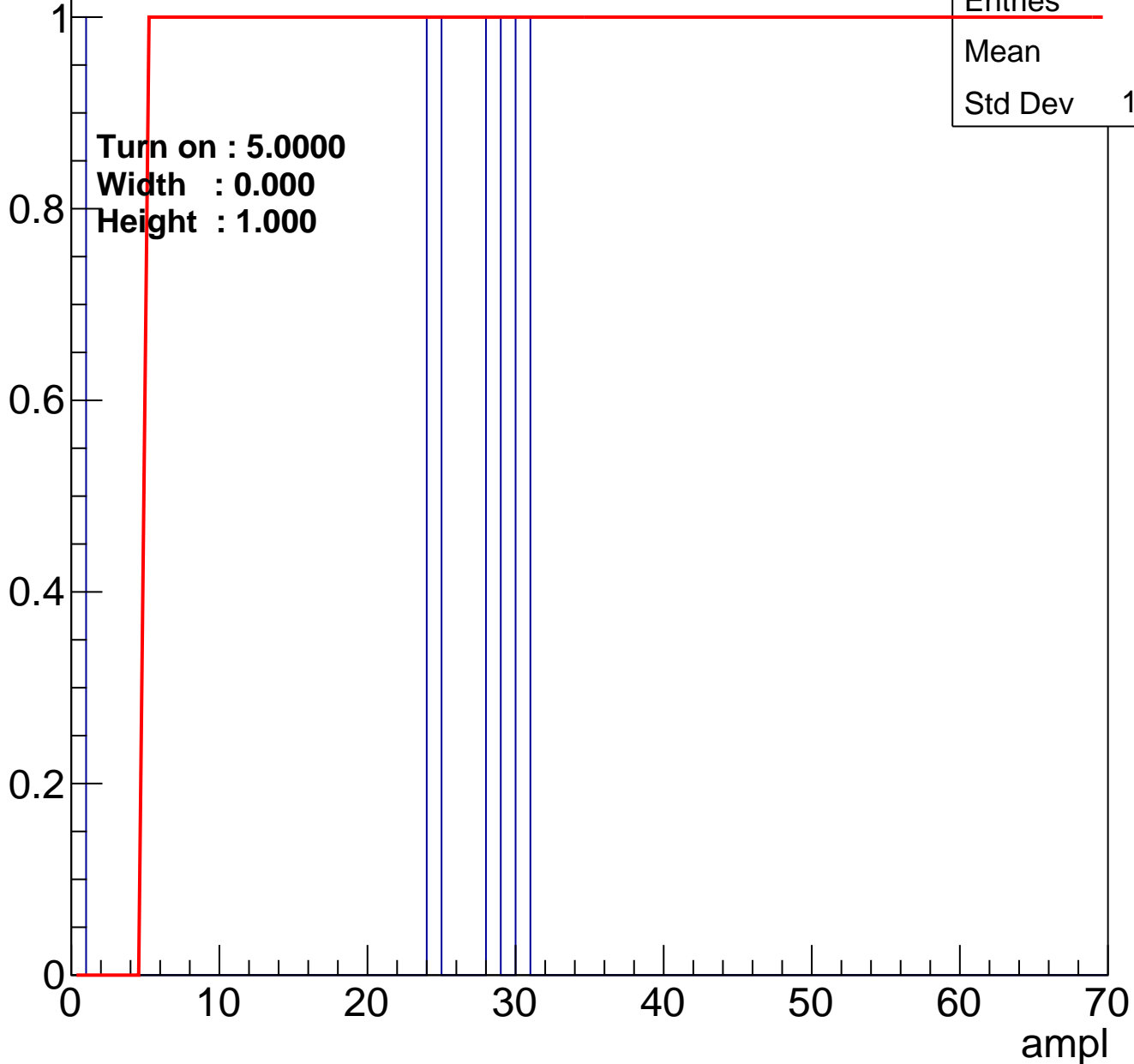


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch54

calib_packv5_042523_0143.root, FC#6, port A1

Entry

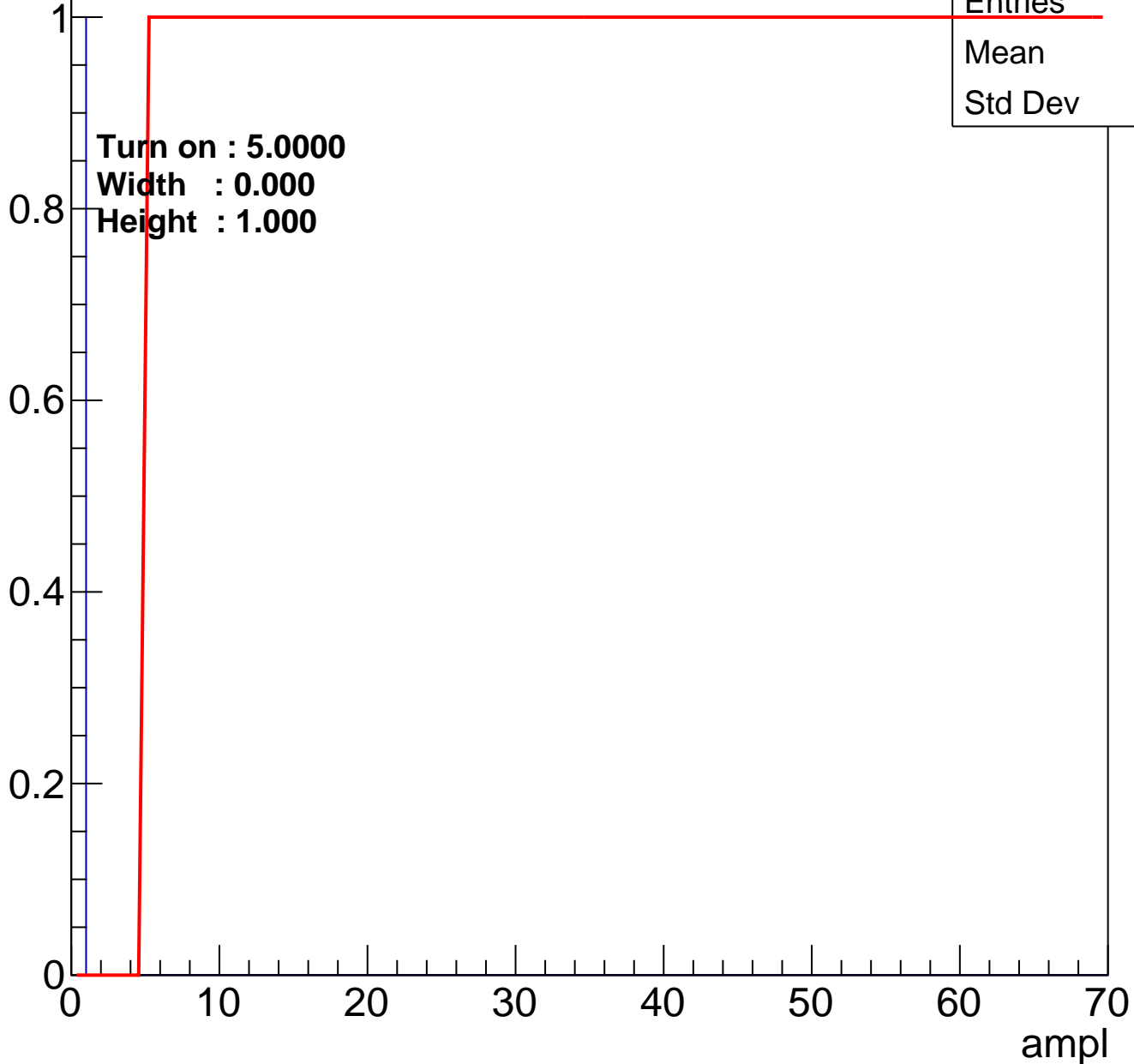


Entries	4
Mean	20.5
Std Dev	12.03

B0L100S, U23-ch55

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch56

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch57

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch58

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch59

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch60

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch61

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch62

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch63

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch64

calib_packv5_042523_0143.root, FC#6, port A1

Entry

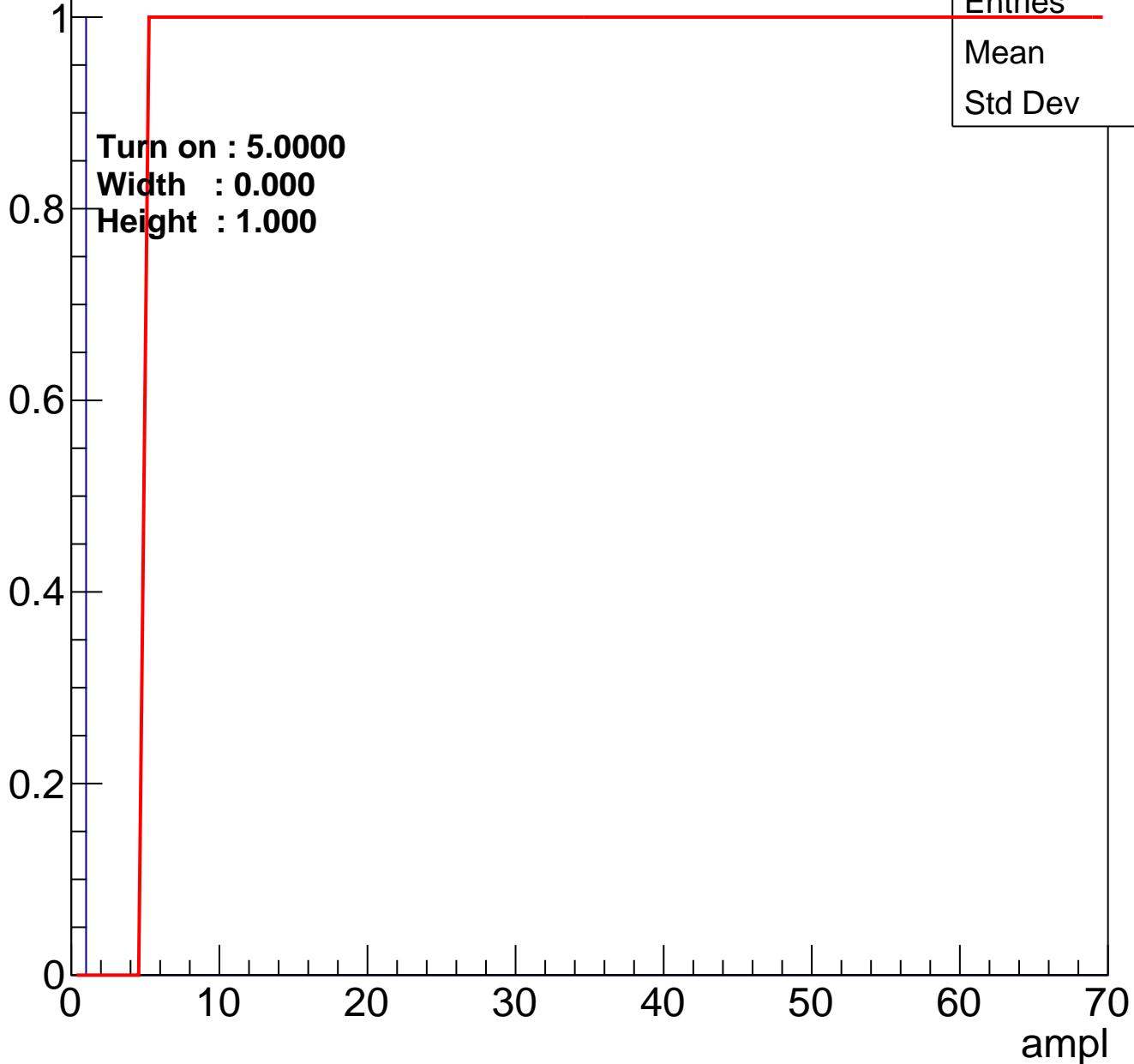


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch65

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch66

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch67

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch68

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch69

calib_packv5_042523_0143.root, FC#6, port A1

Entry

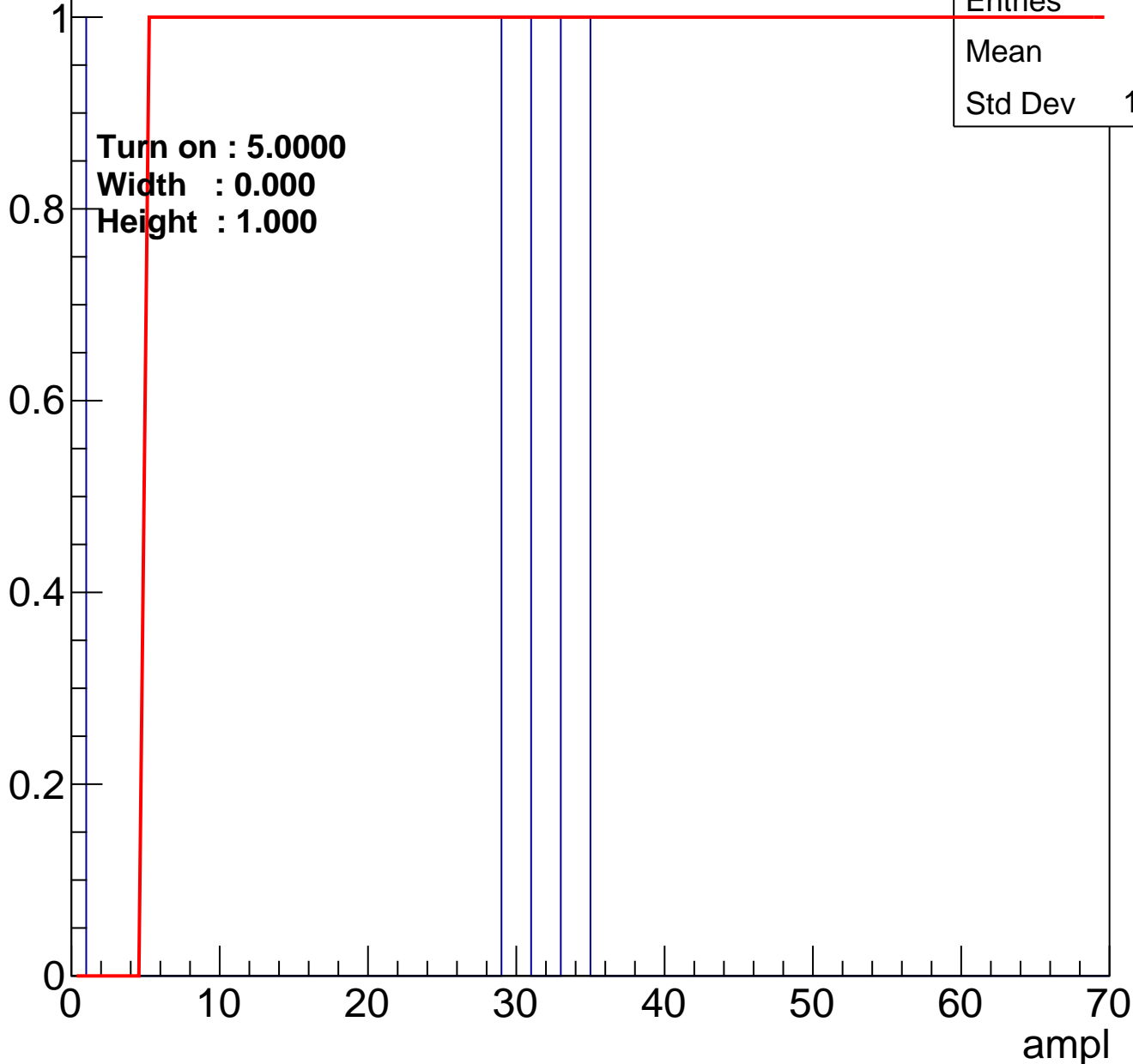


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch70

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	5
Mean	25.2
Std Dev	12.73

B0L100S, U23-ch71

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch72

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch73

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch74

calib_packv5_042523_0143.root, FC#6, port A1

Entry

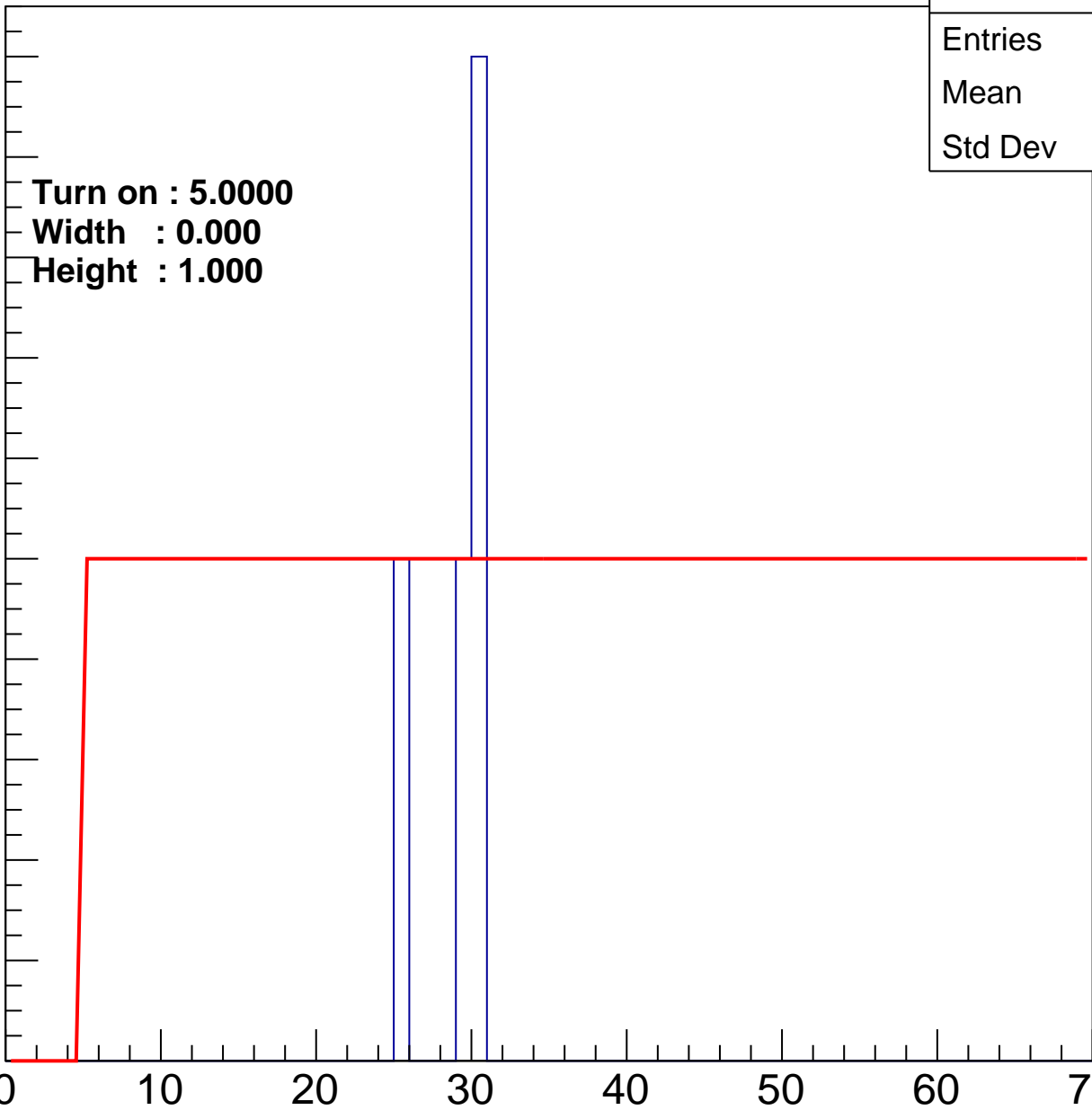
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	4
Mean	28.5
Std Dev	2.062

0 10 20 30 40 50 60 70

ampl



B0L100S, U23-ch75

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch76

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch77

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch78

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch79

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch80

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch81

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch82

calib_packv5_042523_0143.root, FC#6, port A1

Entry

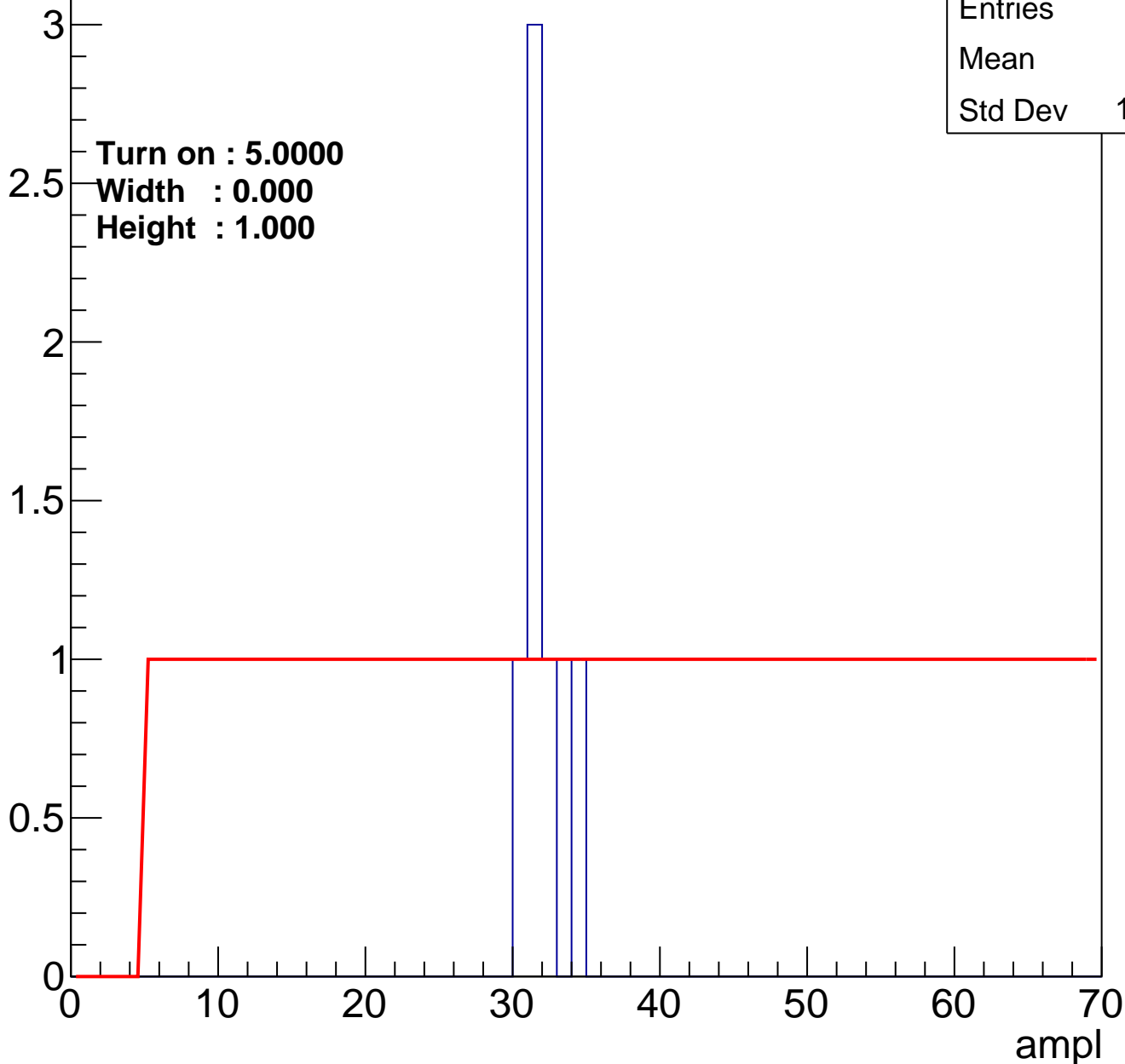


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch83

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch84

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch85

calib_packv5_042523_0143.root, FC#6, port A1

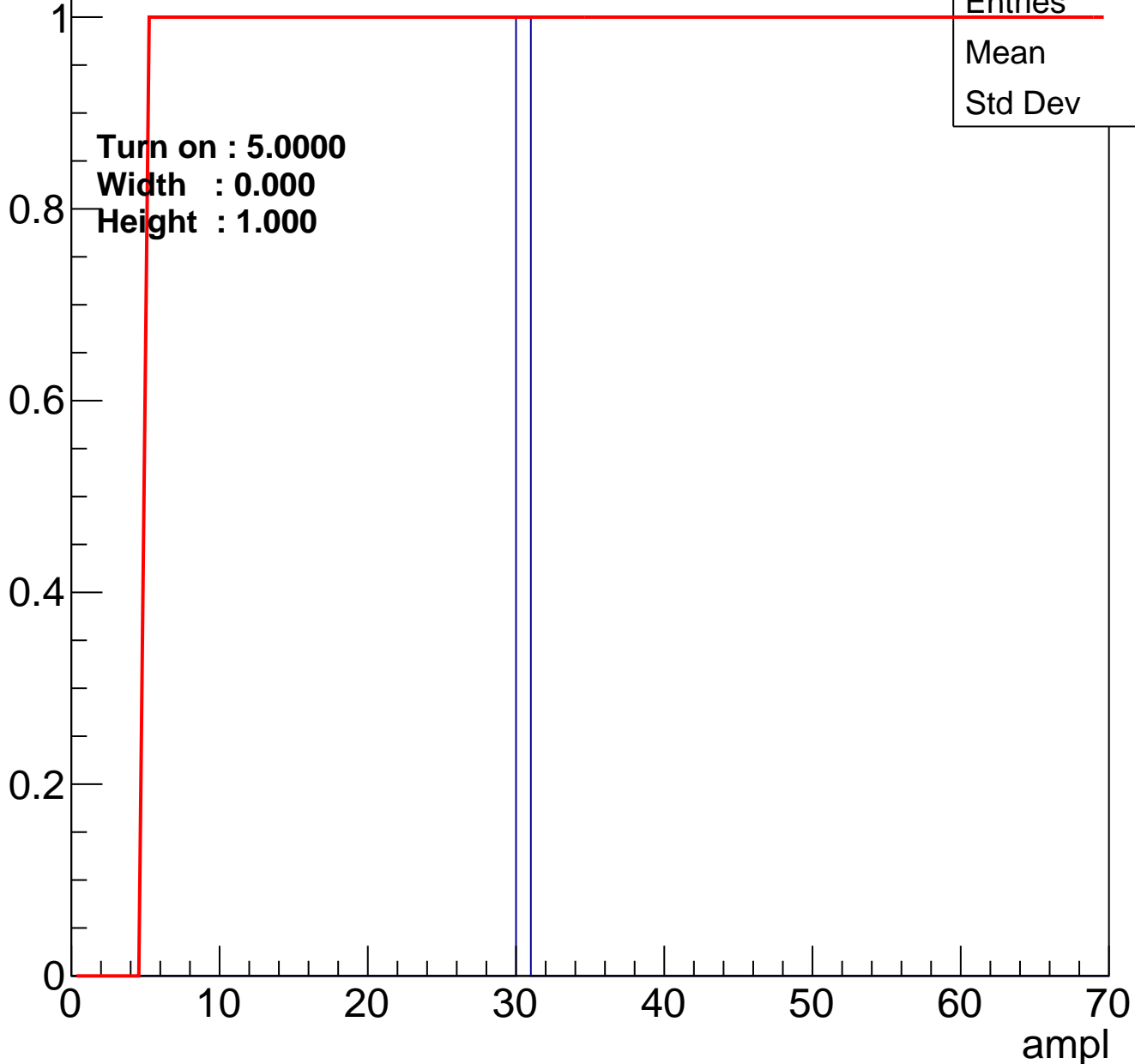
Entry



B0L100S, U23-ch86

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch87

calib_packv5_042523_0143.root, FC#6, port A1

Entry

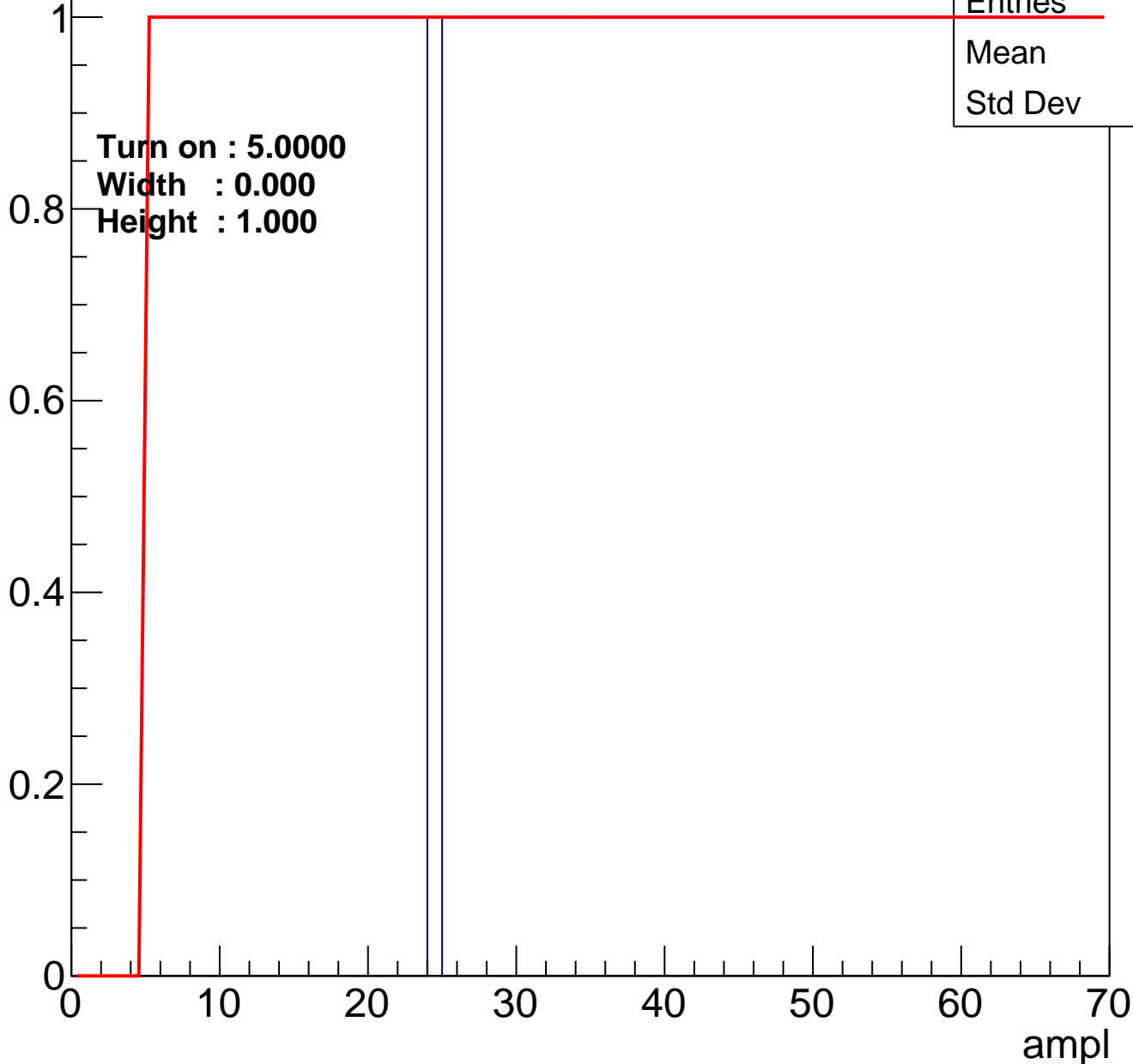


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch88

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	24
Std Dev	0

B0L100S, U23-ch89

calib_packv5_042523_0143.root, FC#6, port A1

Entry

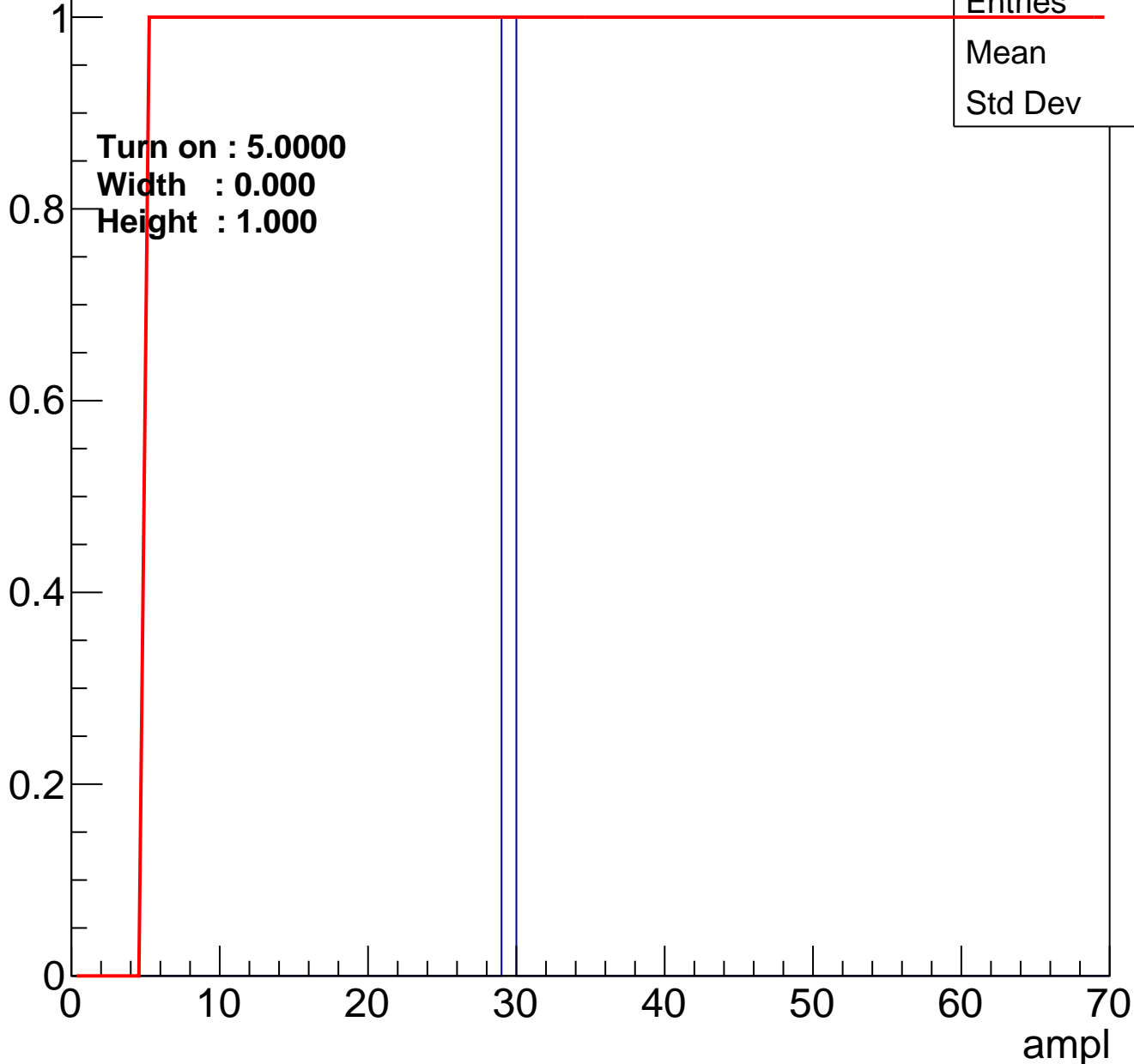


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch90

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	29
Std Dev	0

B0L100S, U23-ch91

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch92

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch93

calib_packv5_042523_0143.root, FC#6, port A1

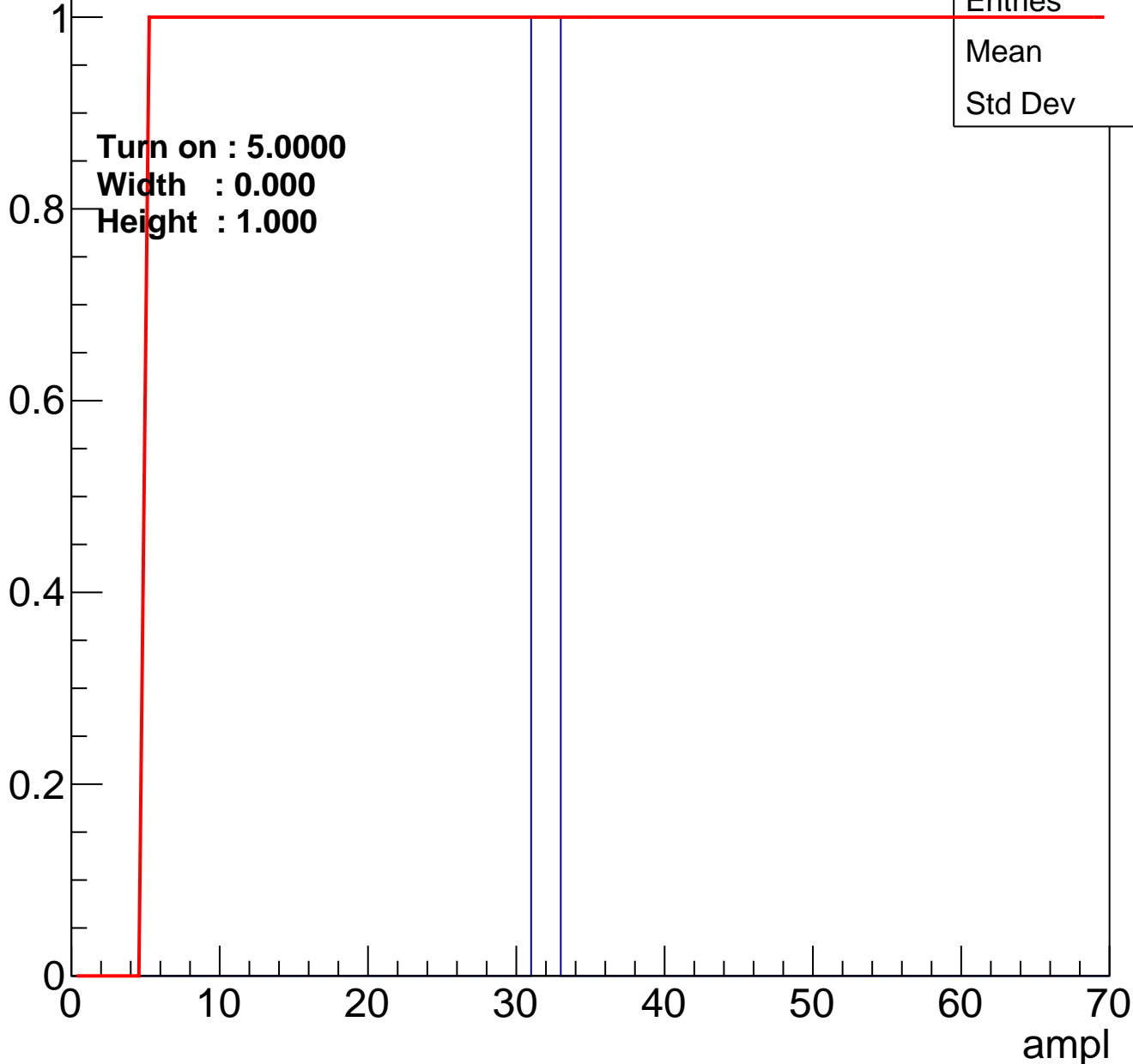
Entry



B0L100S, U23-ch94

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	2
Mean	31.5
Std Dev	0.5

B0L100S, U23-ch95

calib_packv5_042523_0143.root, FC#6, port A1

Entry

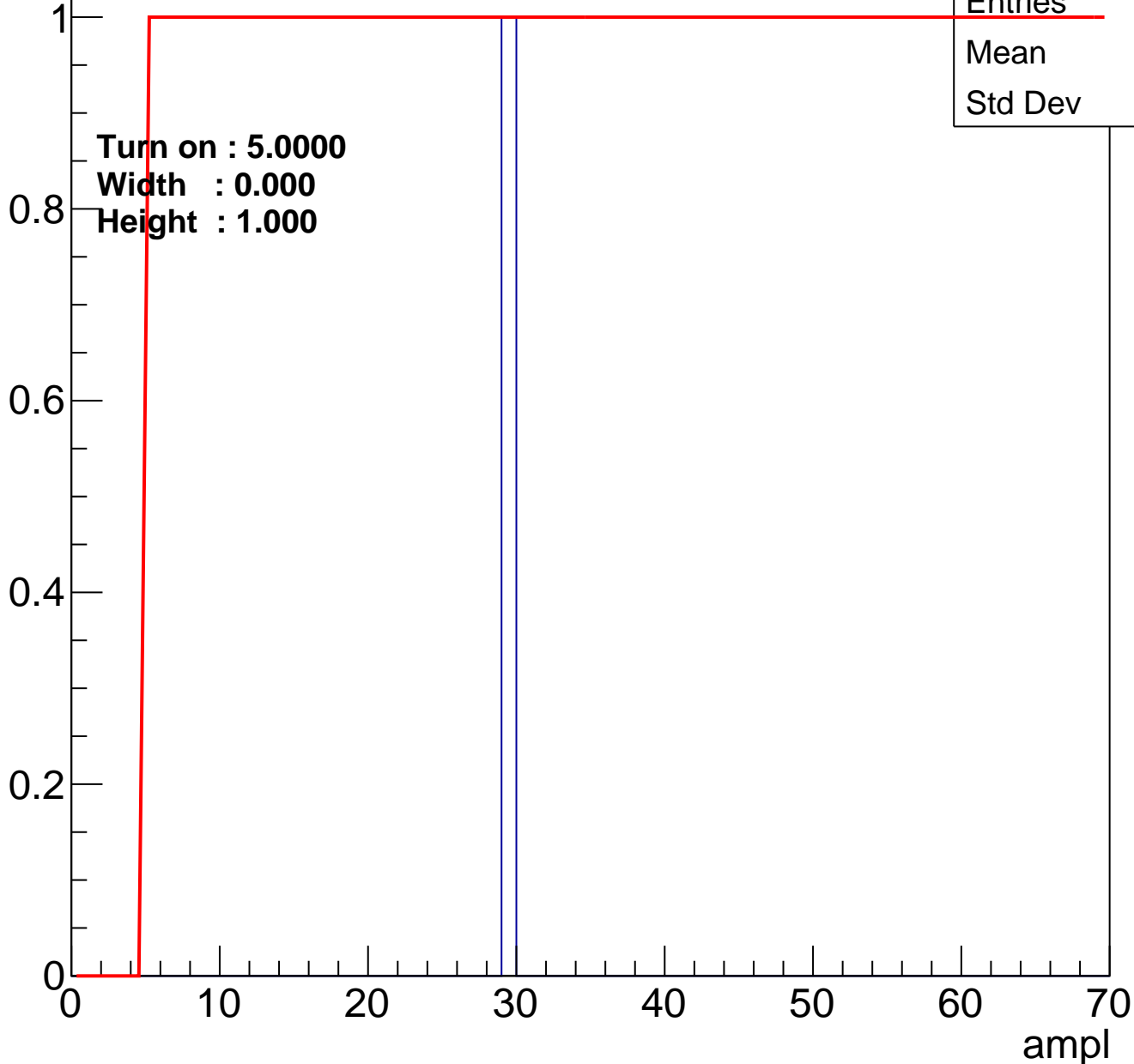


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch96

calib_packv5_042523_0143.root, FC#6, port A1

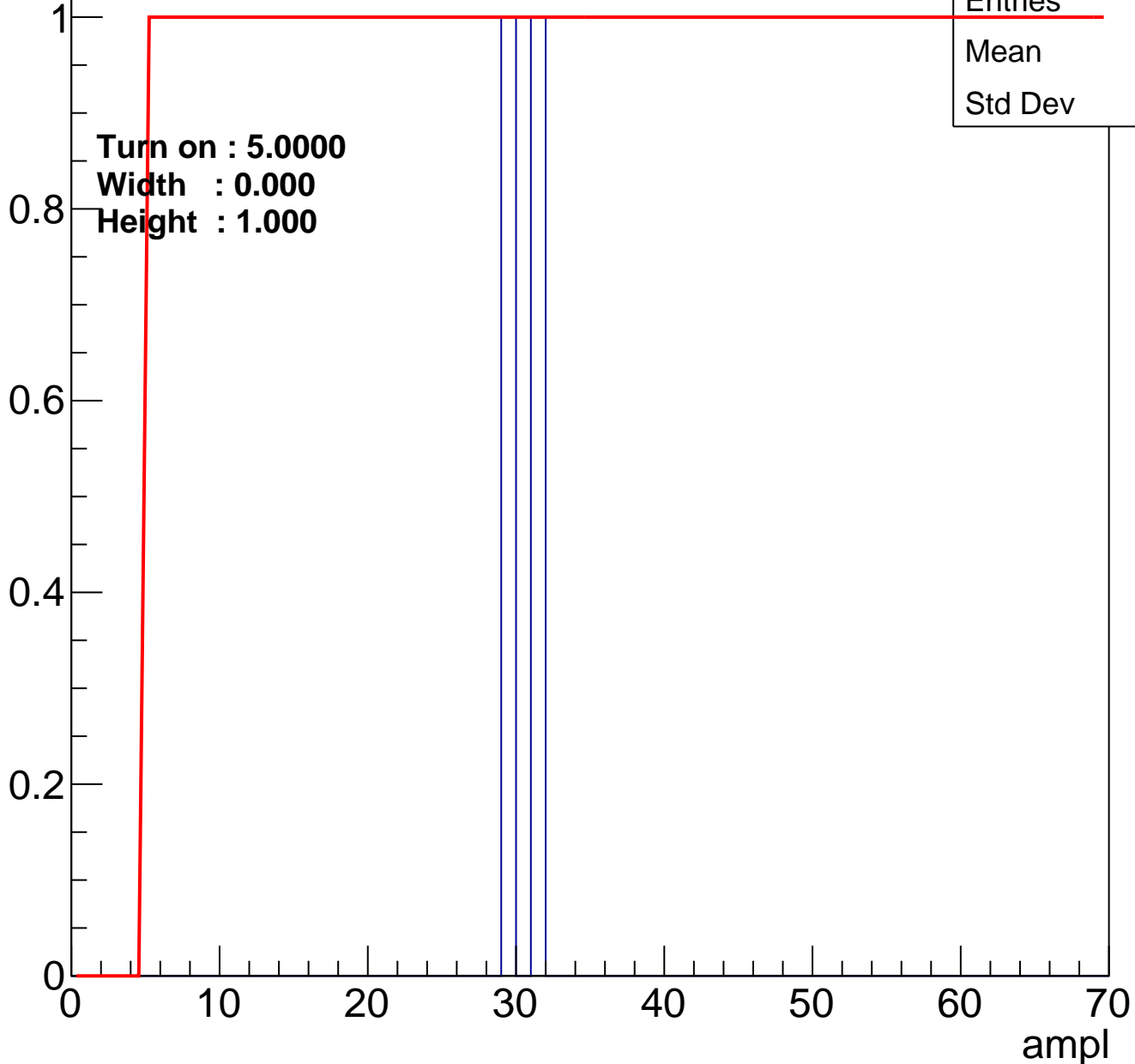
Entry



B0L100S, U23-ch97

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	2
Mean	30
Std Dev	1

B0L100S, U23-ch98

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch99

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch100

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch101

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch102

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch103

calib_packv5_042523_0143.root, FC#6, port A1

Entry

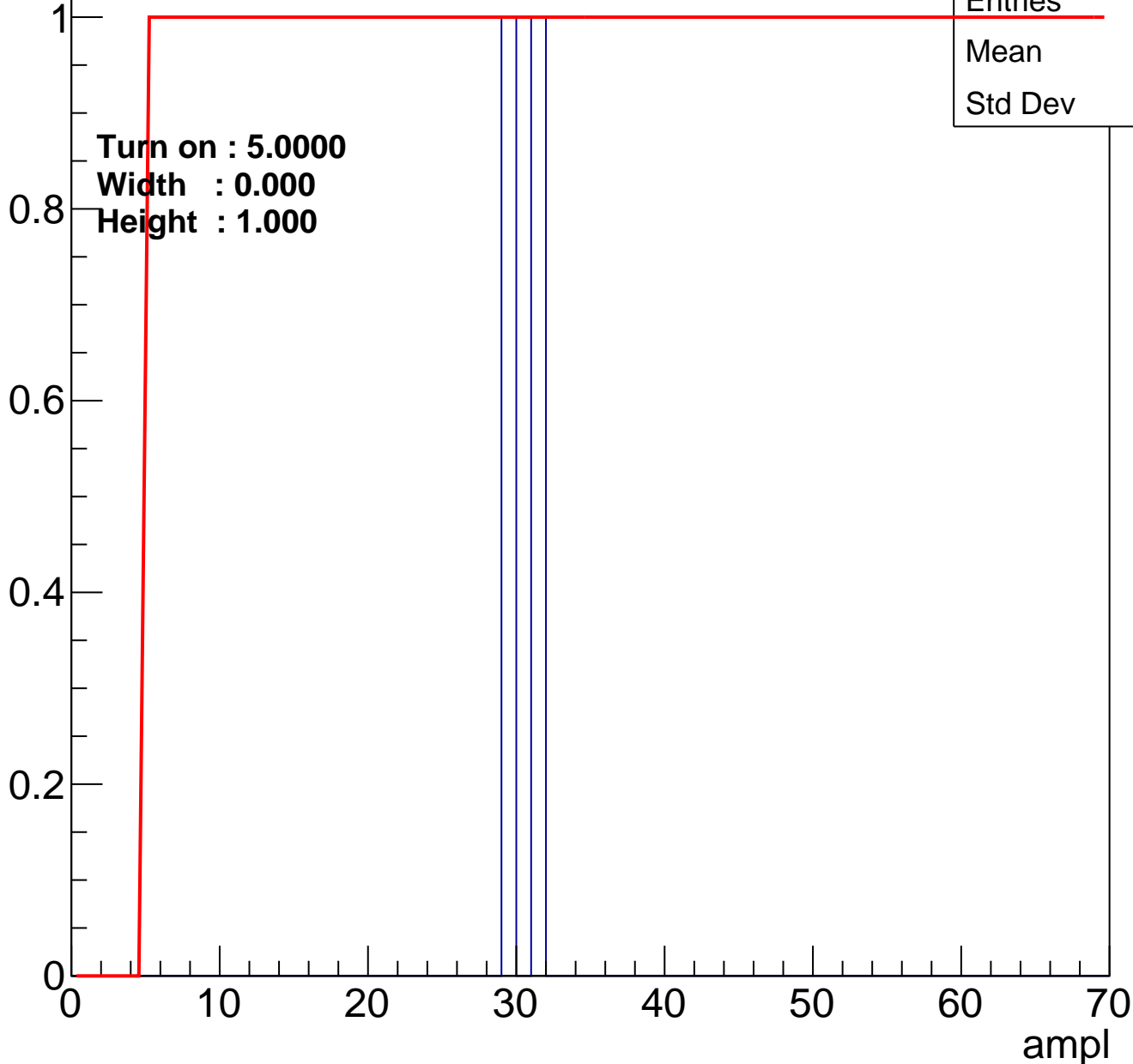


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch104

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	2
Mean	30
Std Dev	1

B0L100S, U23-ch105

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch106

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch107

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch108

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch109

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch110

calib_packv5_042523_0143.root, FC#6, port A1

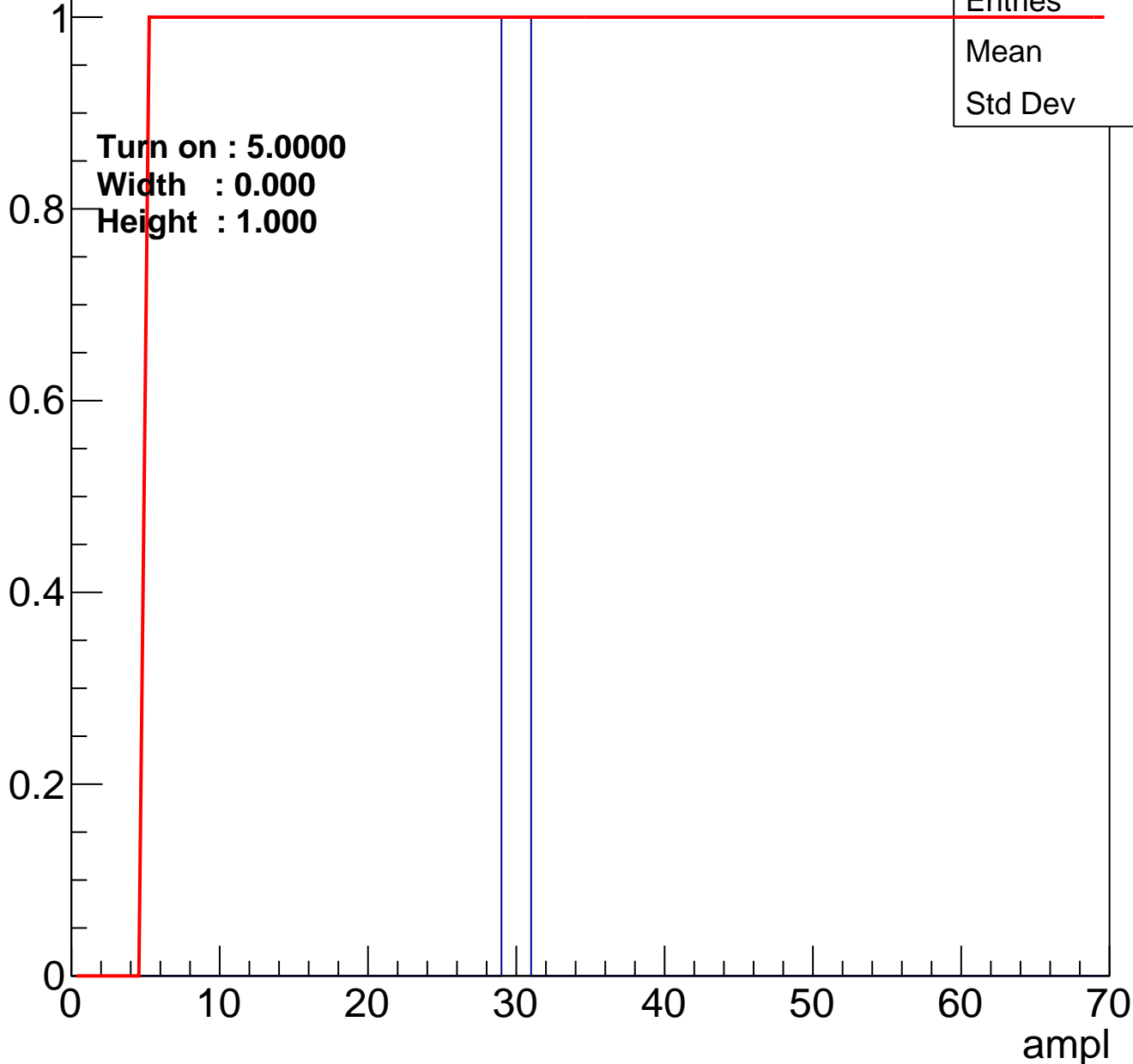
Entry



B0L100S, U23-ch111

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch112

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch113

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch114

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch115

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch116

calib_packv5_042523_0143.root, FC#6, port A1

Entry

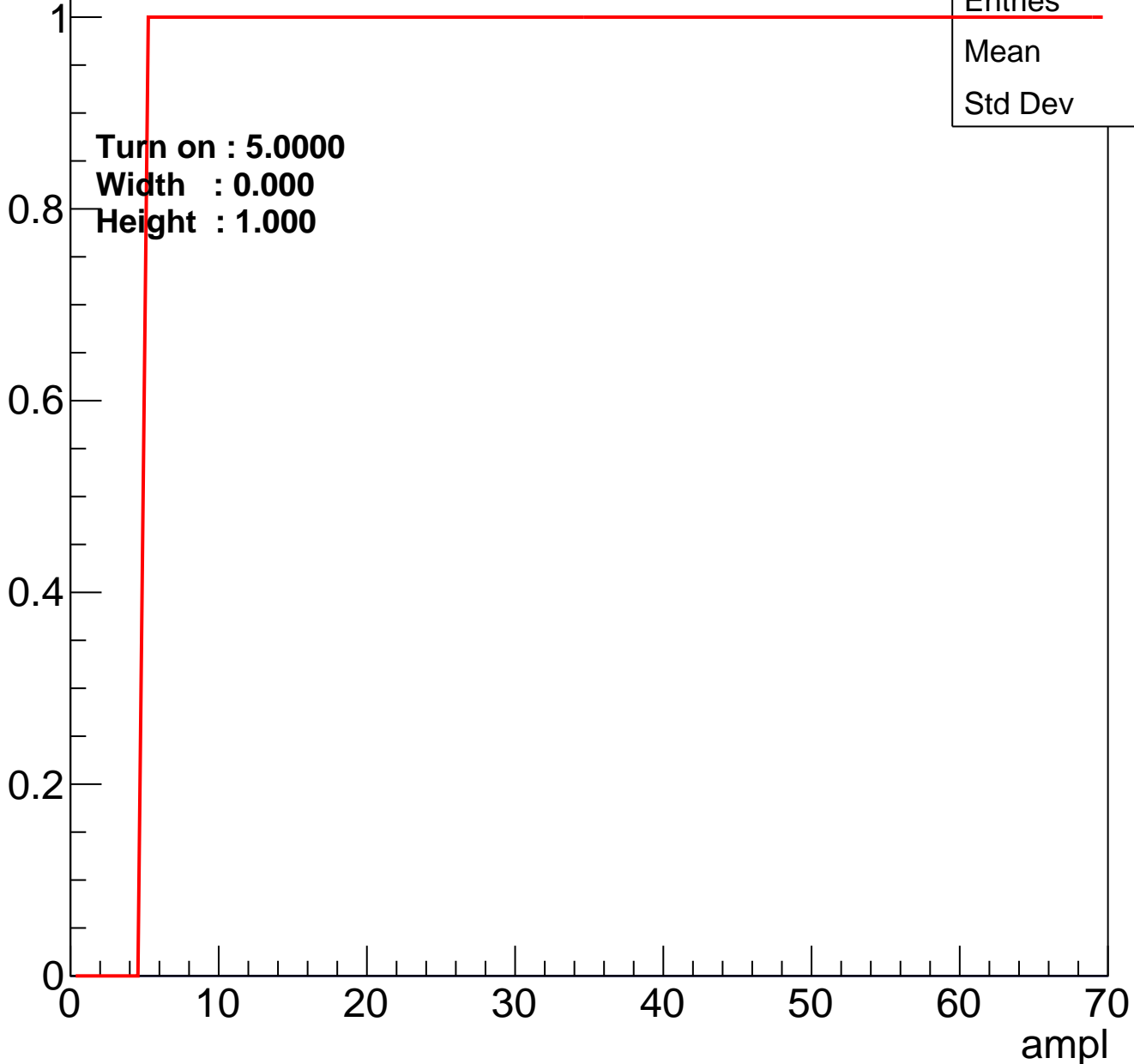


Entries	2
Mean	0
Std Dev	0

B0L100S, U23-ch117

calib_packv5_042523_0143.root, FC#6, port A1

Entry

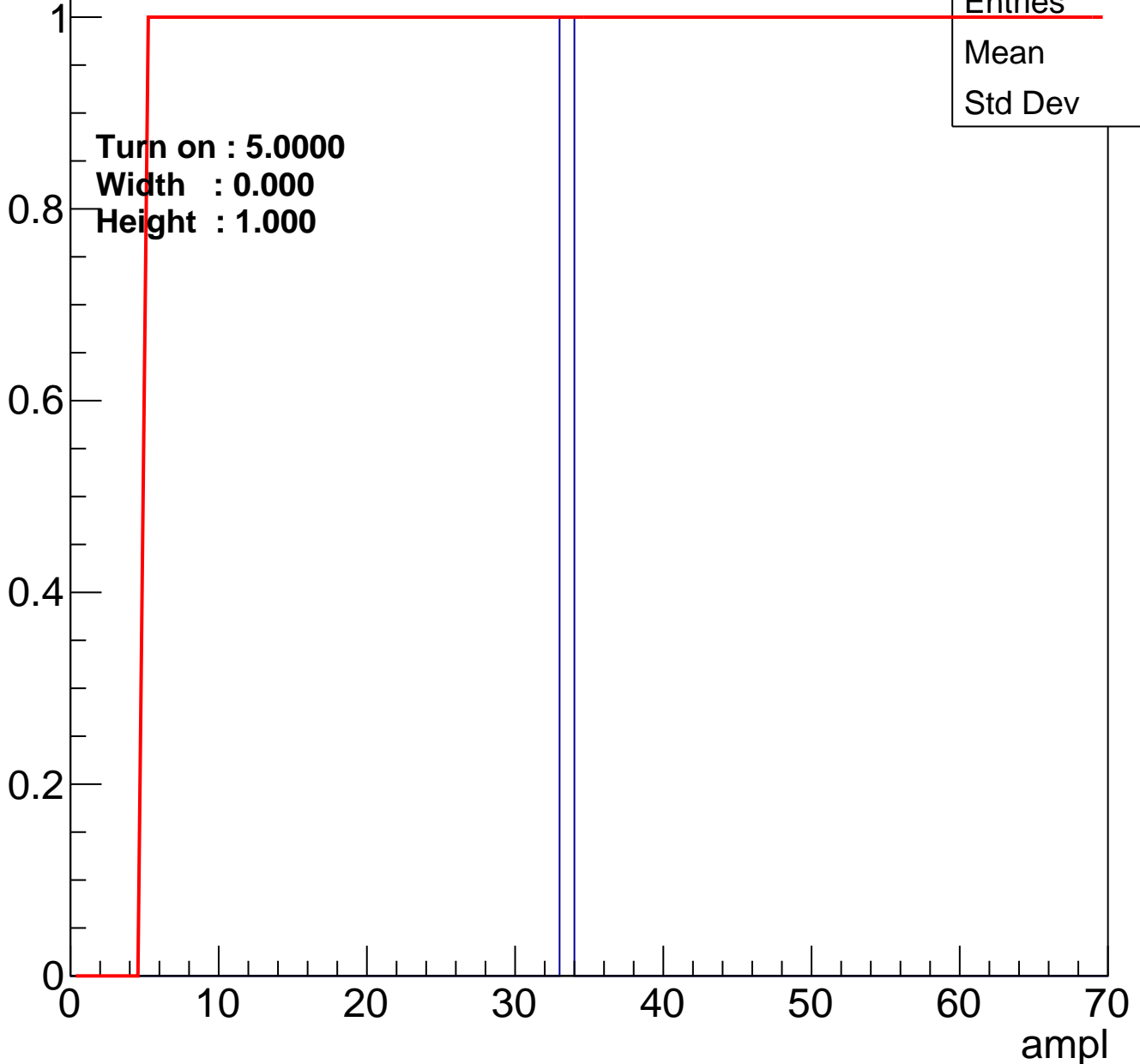


Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch118

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch119

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch120

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch121

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch122

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L100S, U23-ch123

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch124

calib_packv5_042523_0143.root, FC#6, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L100S, U23-ch125

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch126

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entry



B0L100S, U23-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entry

