

B1L000S, U5-ch0

calib_packv5_042523_0143.root, FC#3, port B3

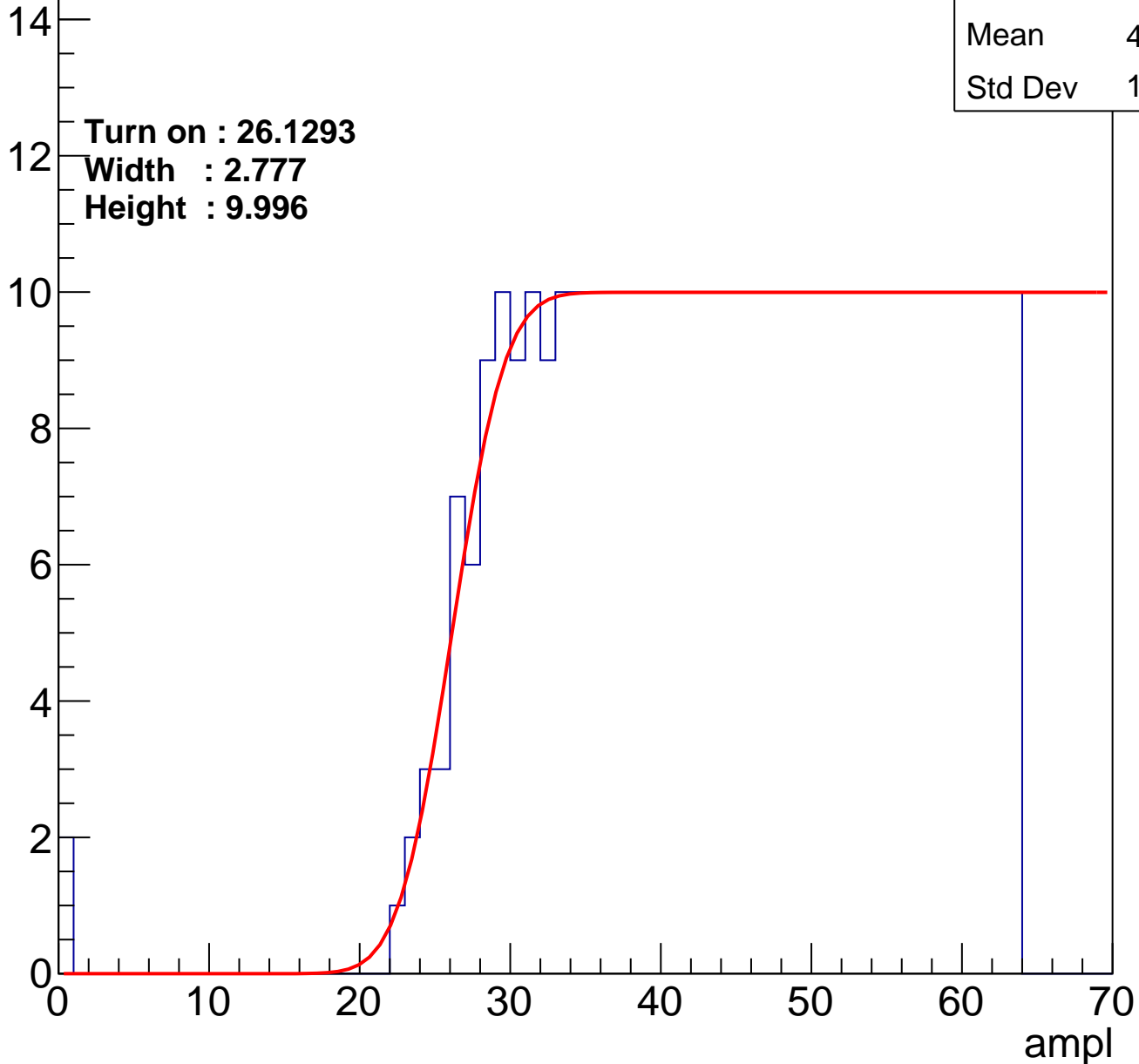
Entries	381
Mean	44.22
Std Dev	11.52

Turn on : 26.1293

Width : 2.777

Height : 9.996

Entry



B1L000S, U5-ch1

calib_packv5_042523_0143.root, FC#3, port B3

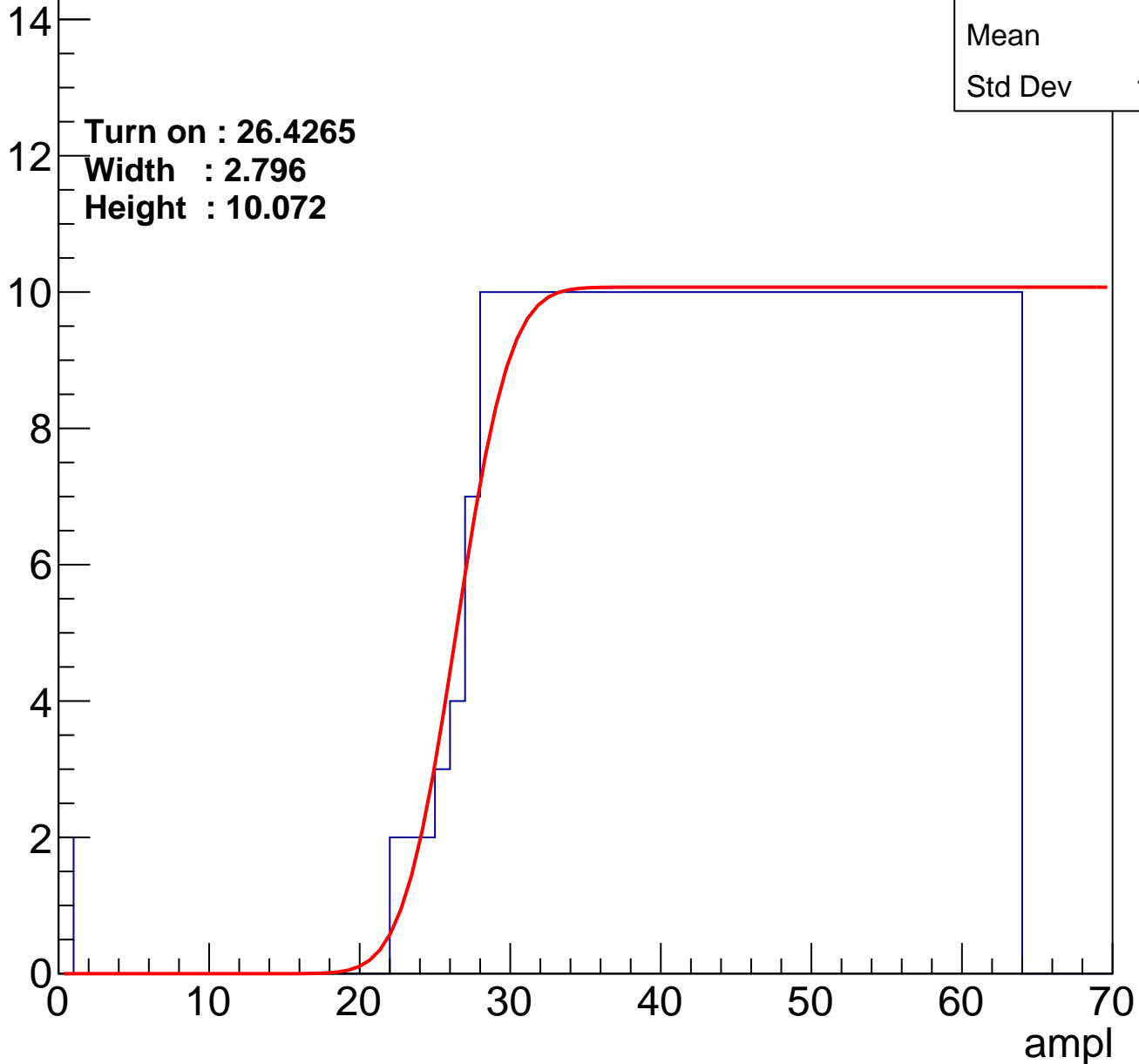
Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 26.4265

Width : 2.796

Height : 10.072

Entry



B1L000S, U5-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.91
Std Dev	11.71

Turn on : 25.8046

Width : 3.376

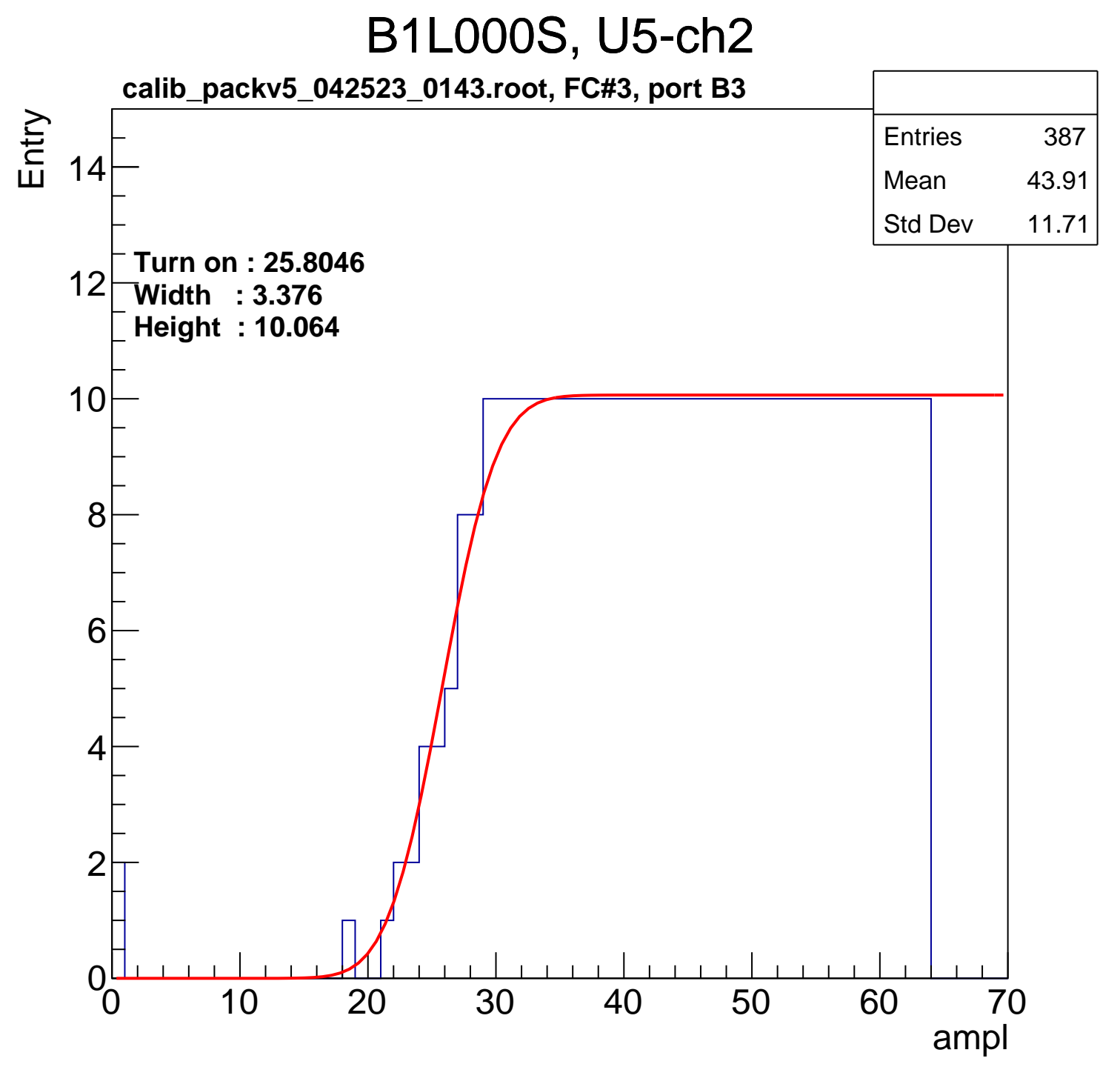
Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U5-ch3

calib_packv5_042523_0143.root, FC#3, port B3

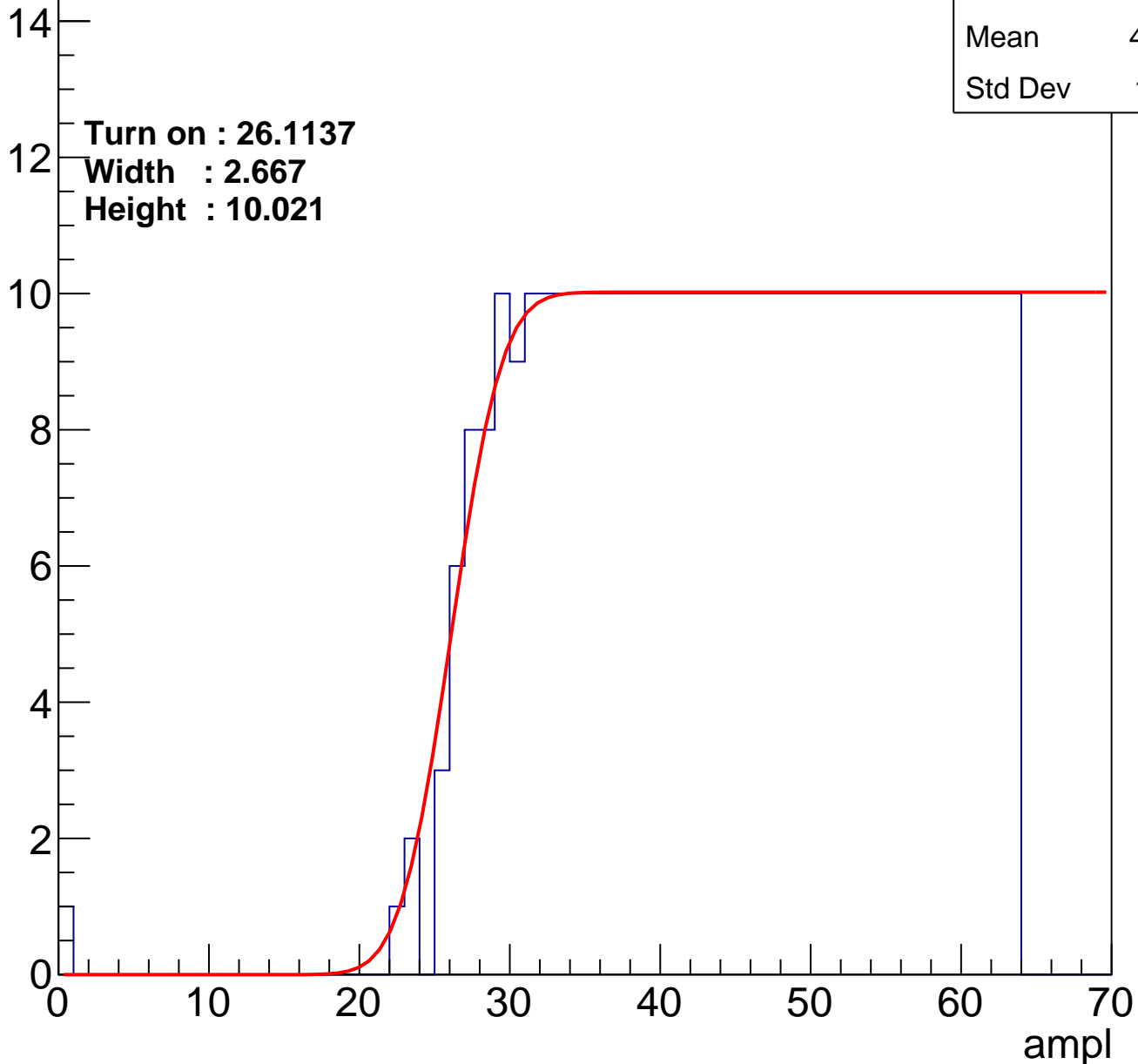
Entries	378
Mean	44.47
Std Dev	11.21

Turn on : 26.1137

Width : 2.667

Height : 10.021

Entry



B1L000S, U5-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.03
Std Dev	12.28

Turn on : 24.2932

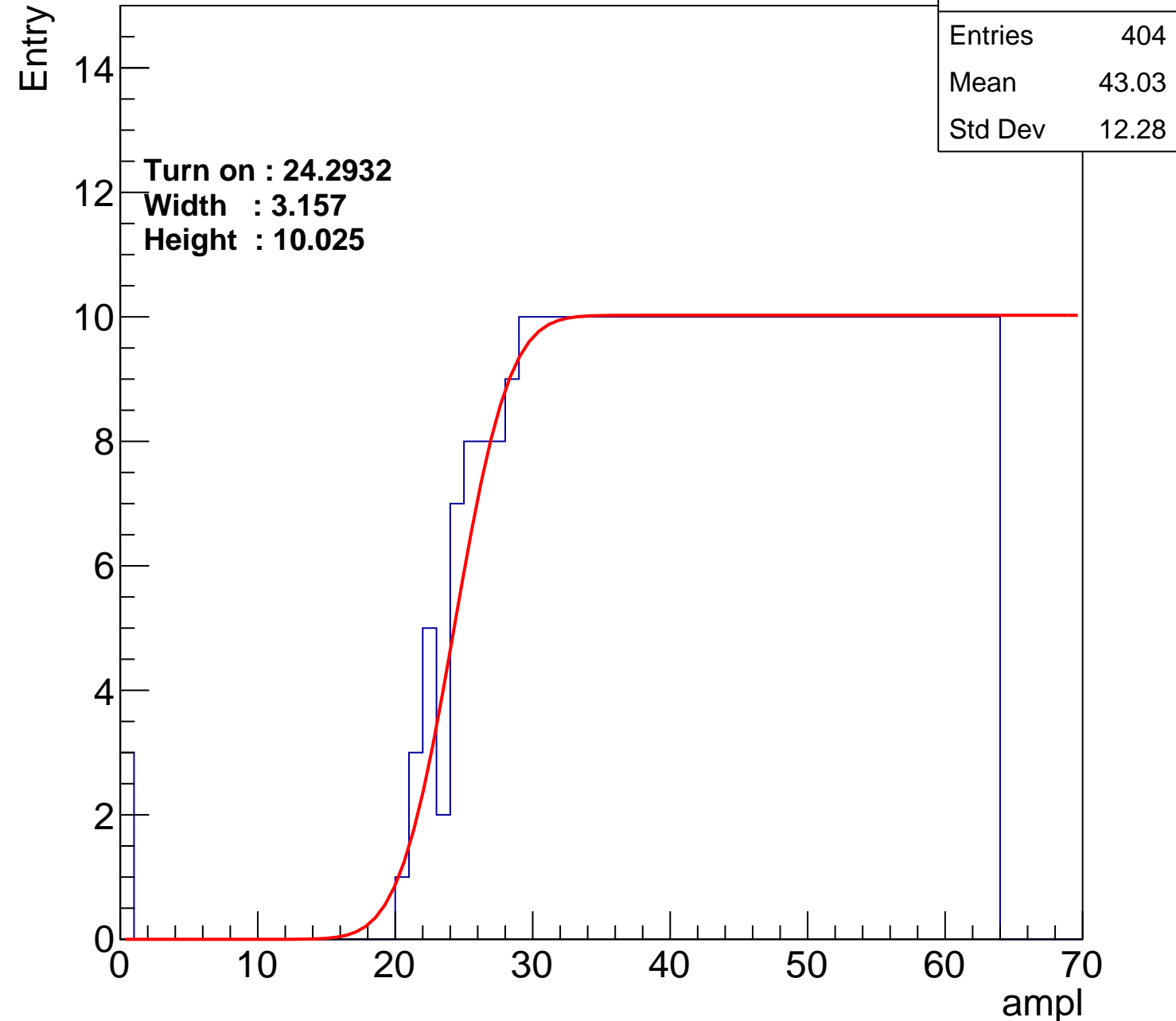
Width : 3.157

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.41
Std Dev	12.08

Turn on : 25.0525

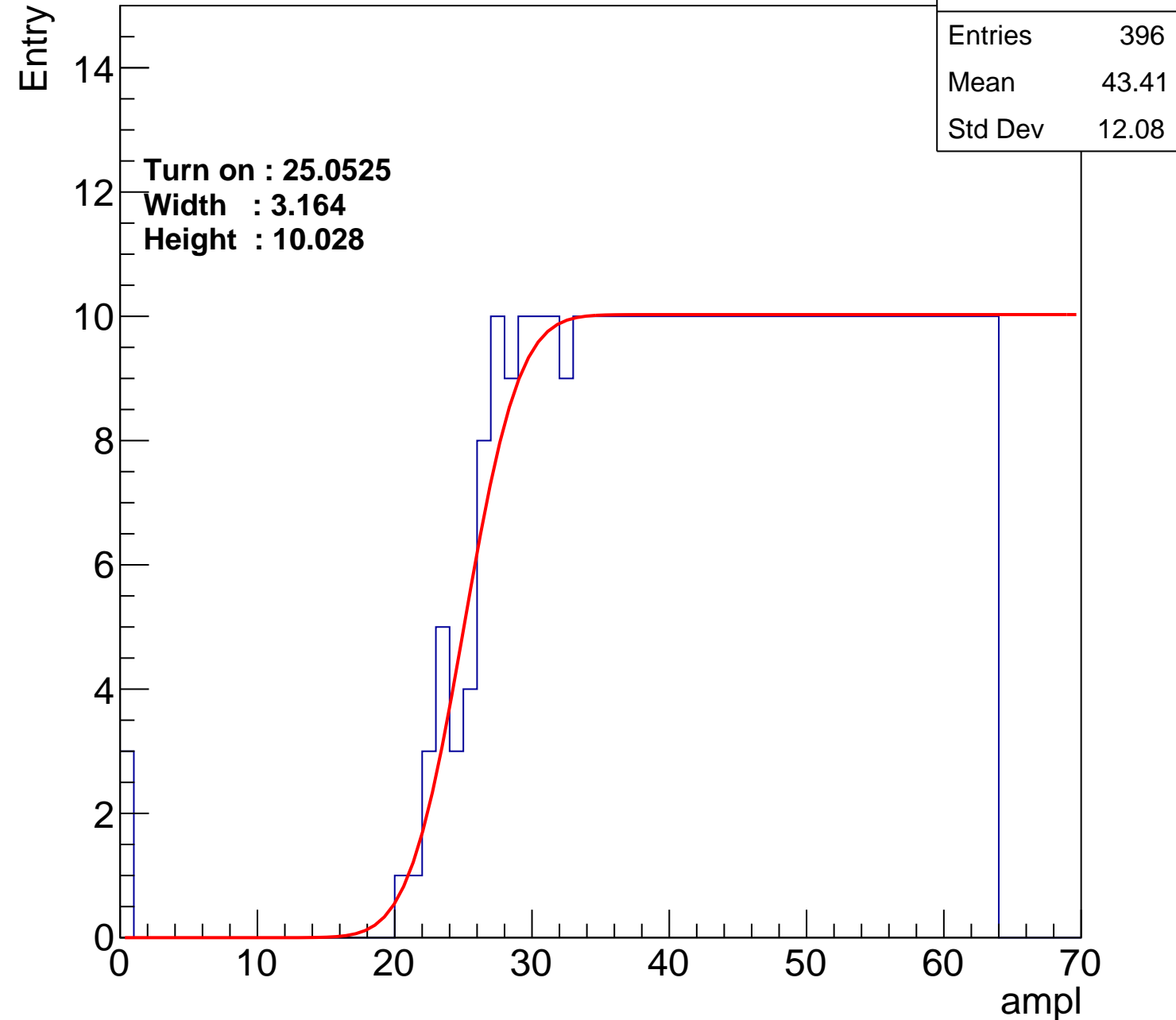
Width : 3.164

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.7
Std Dev	11.82

Turn on : 24.8114

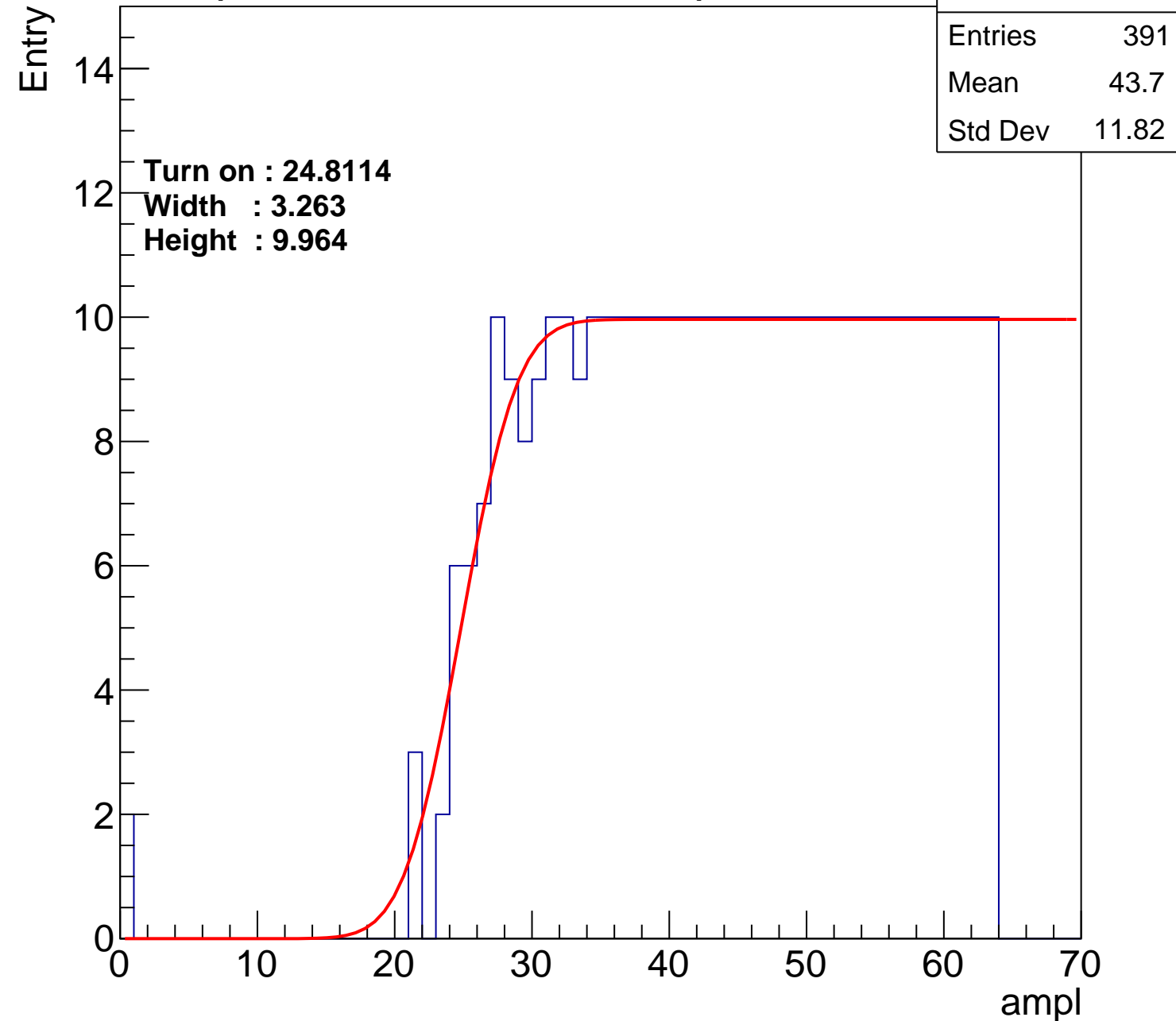
Width : 3.263

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.49
Std Dev	11.56

Turn on : 27.3764

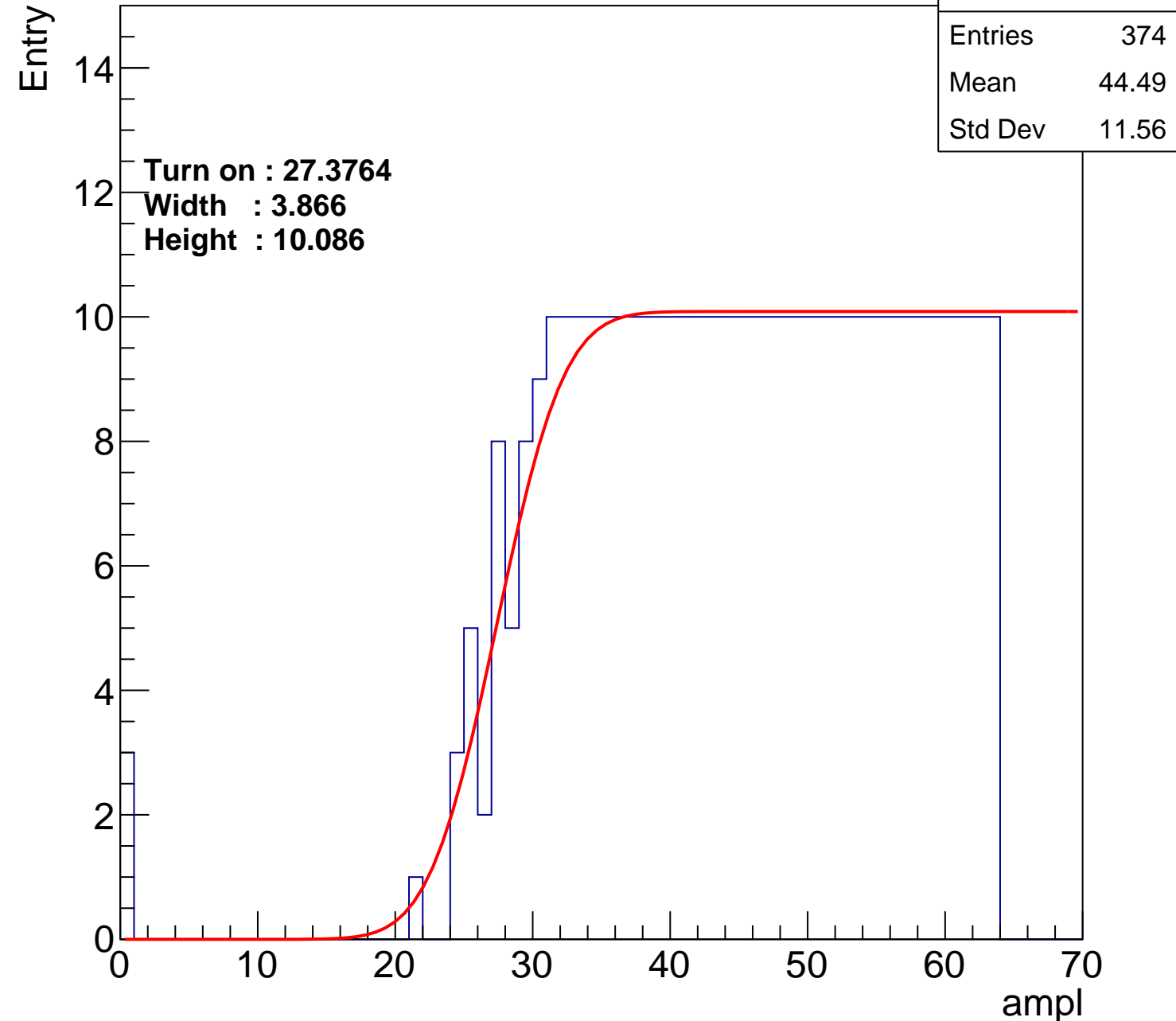
Width : 3.866

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.0281

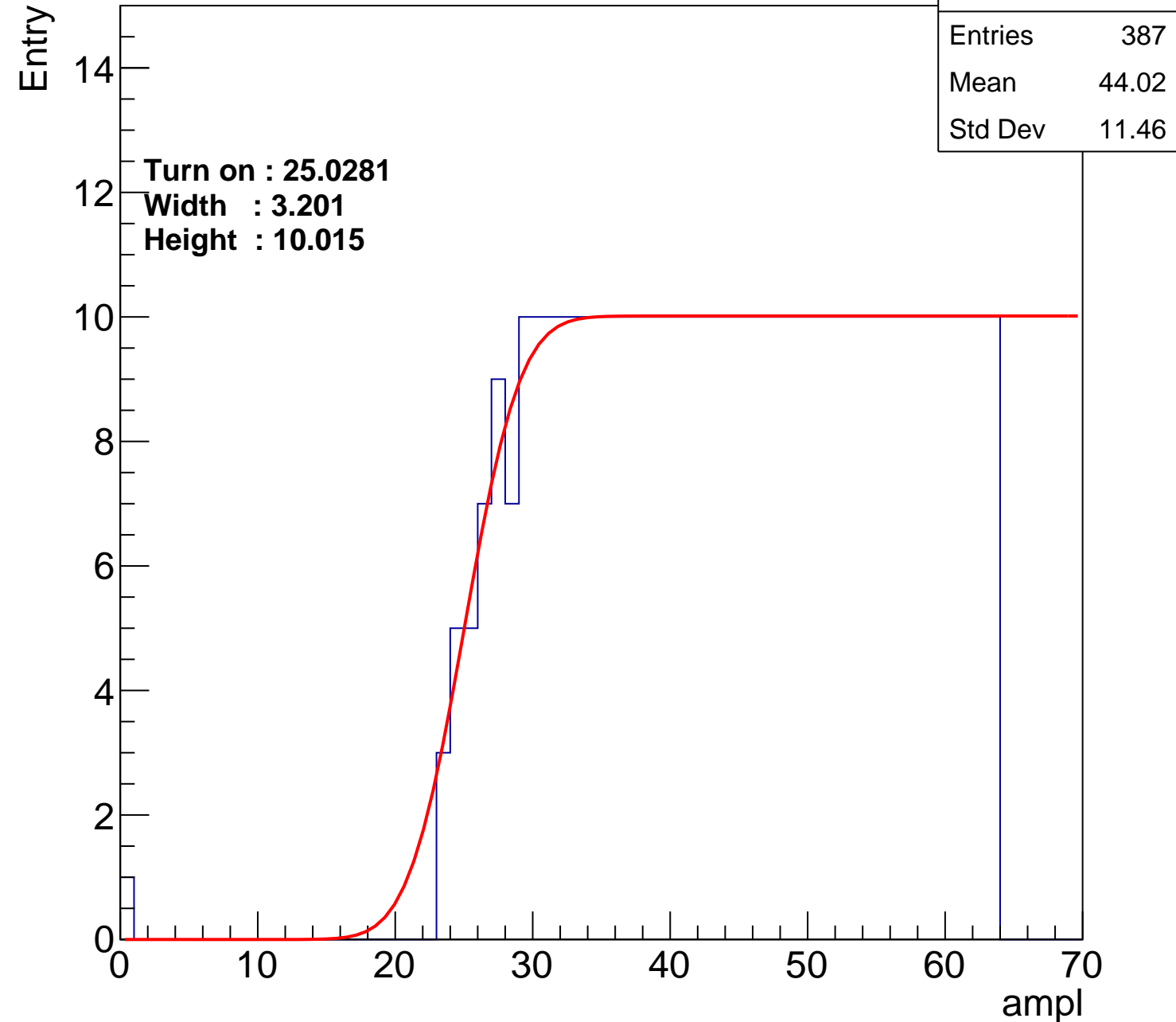
Width : 3.201

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch9

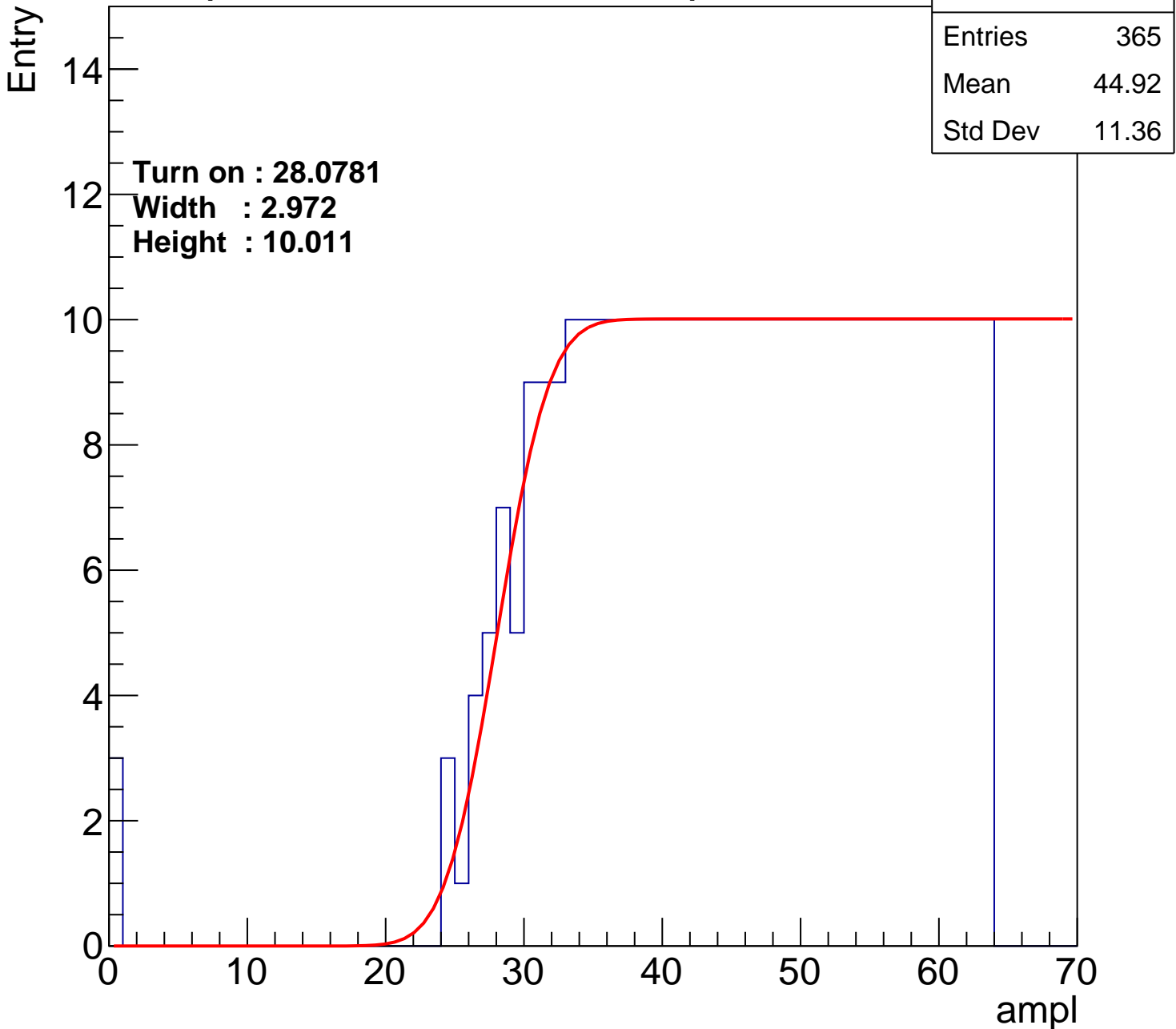
calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.92
Std Dev	11.36

Turn on : 28.0781

Width : 2.972

Height : 10.011



B1L000S, U5-ch10

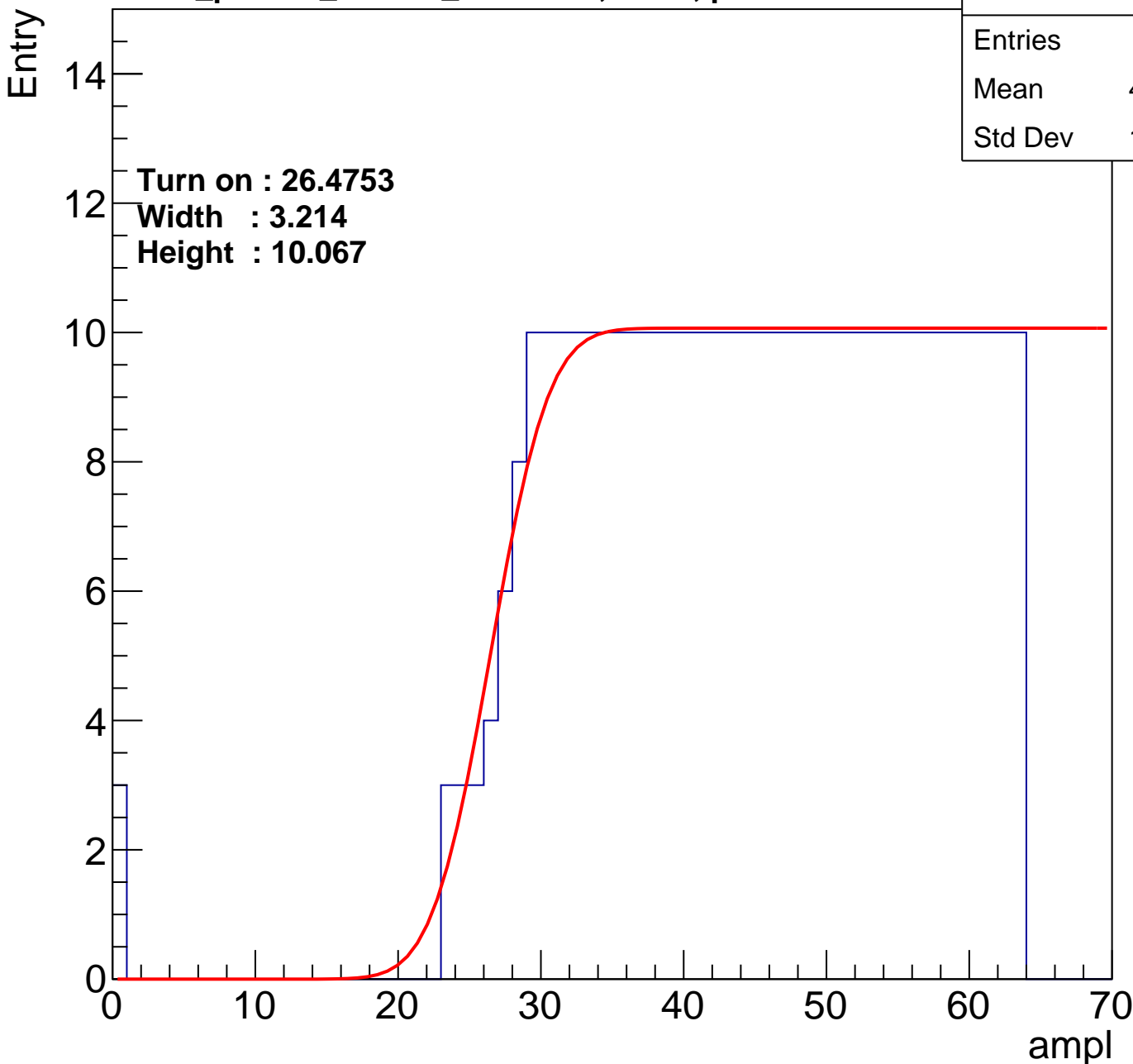
calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.23
Std Dev	11.65

Turn on : 26.4753

Width : 3.214

Height : 10.067



B1L000S, U5-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.58
Std Dev	12.03

Turn on : 25.1274

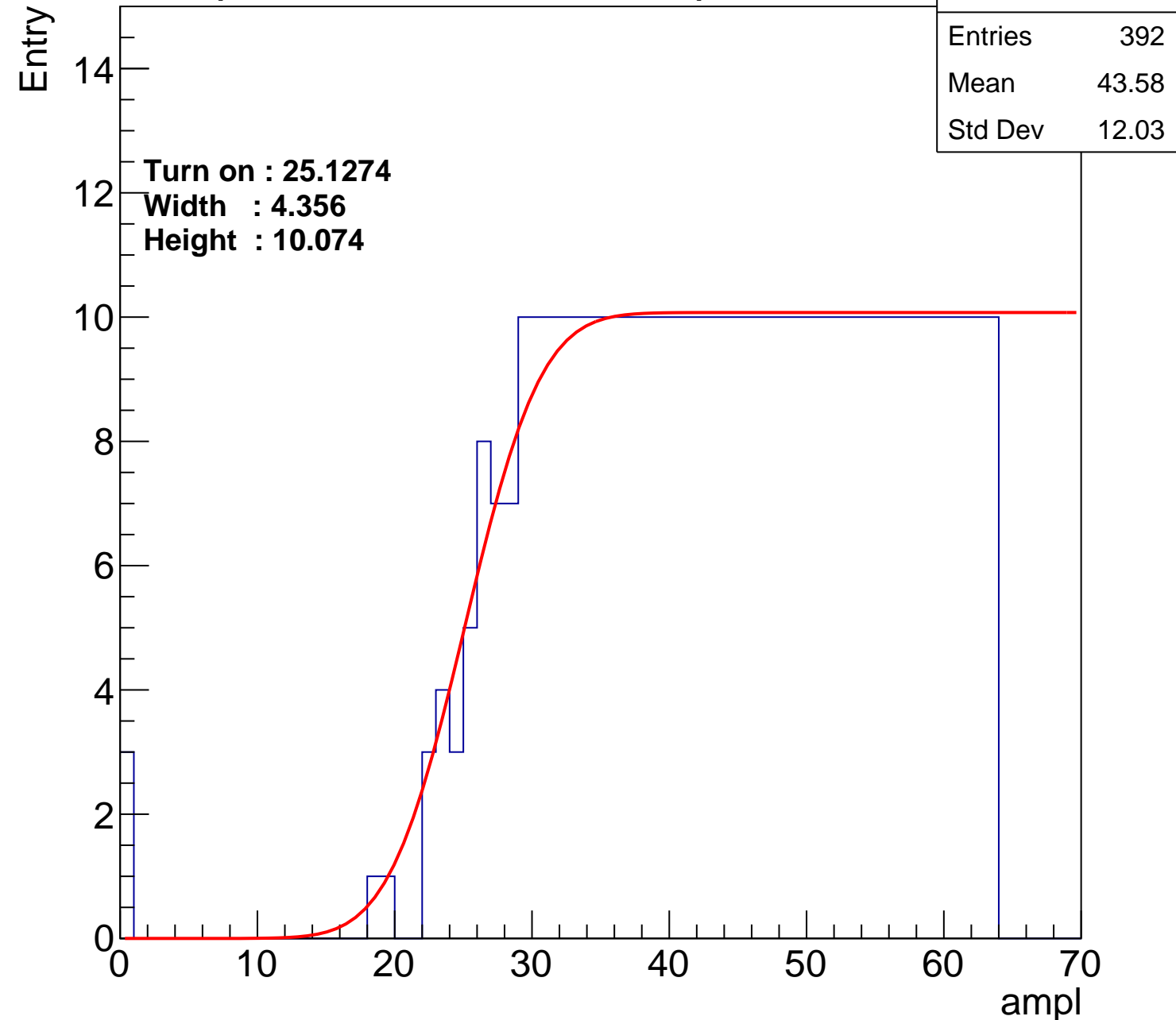
Width : 4.356

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.9388

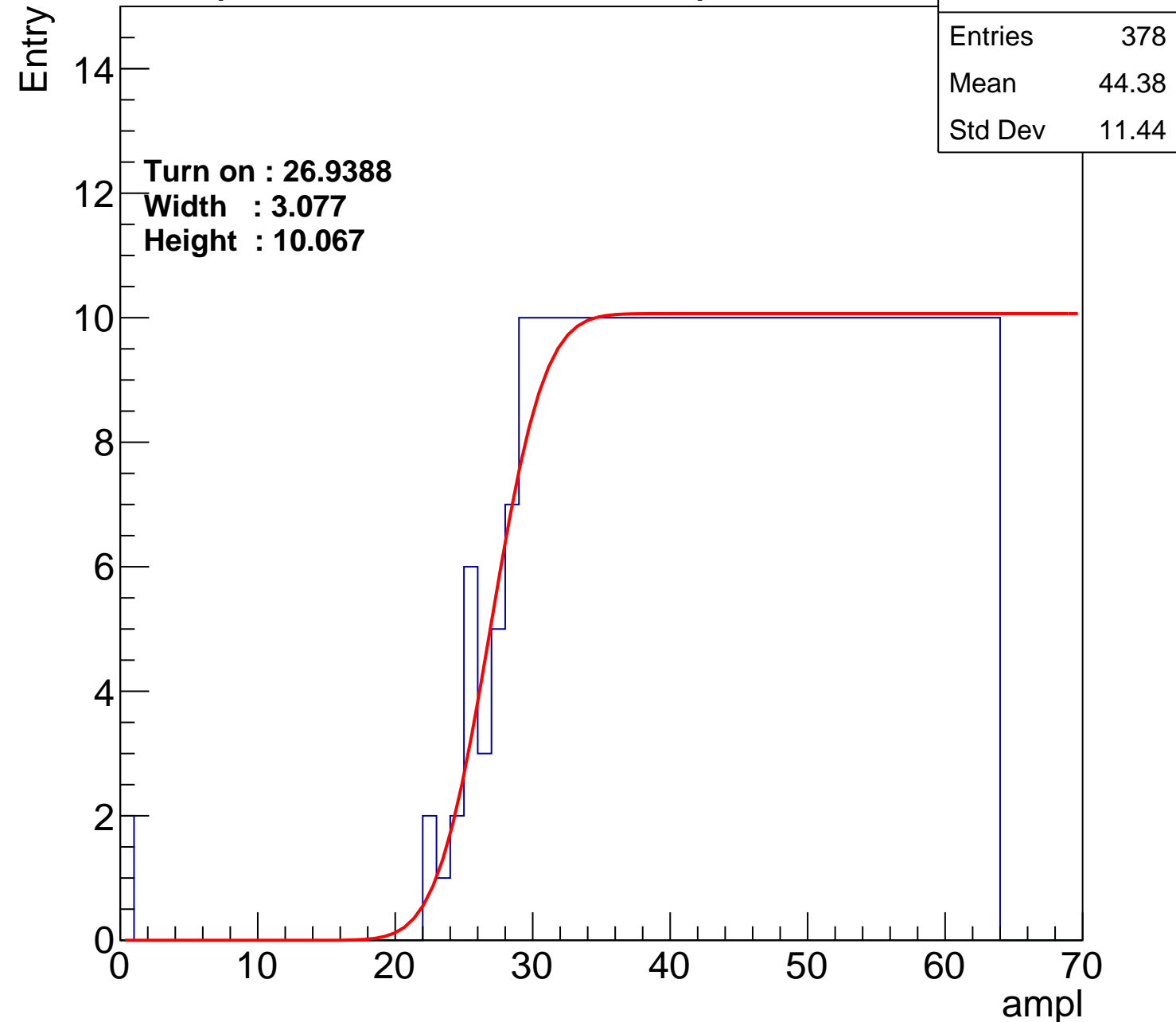
Width : 3.077

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.1317

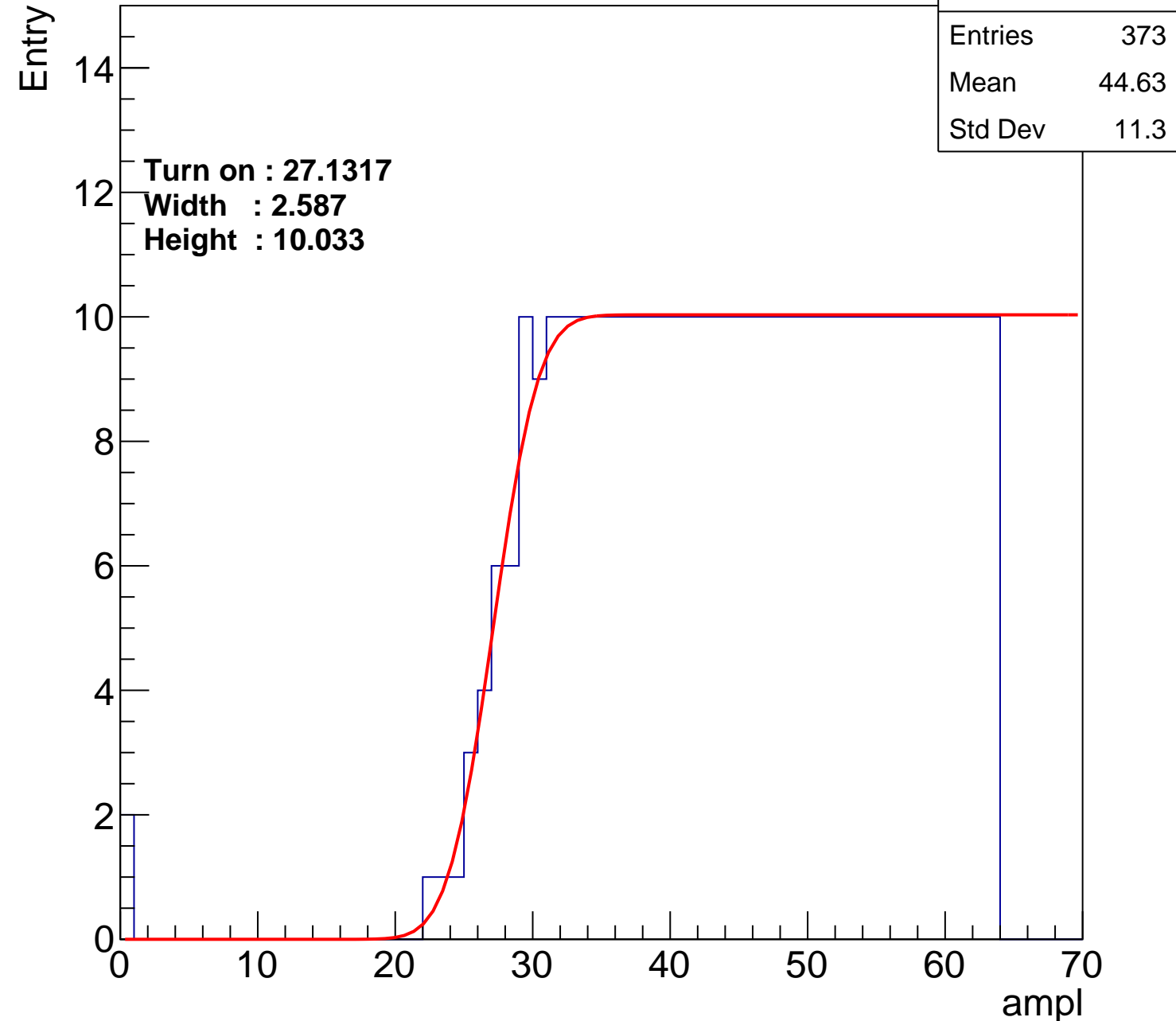
Width : 2.587

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch14

calib_packv5_042523_0143.root, FC#3, port B3

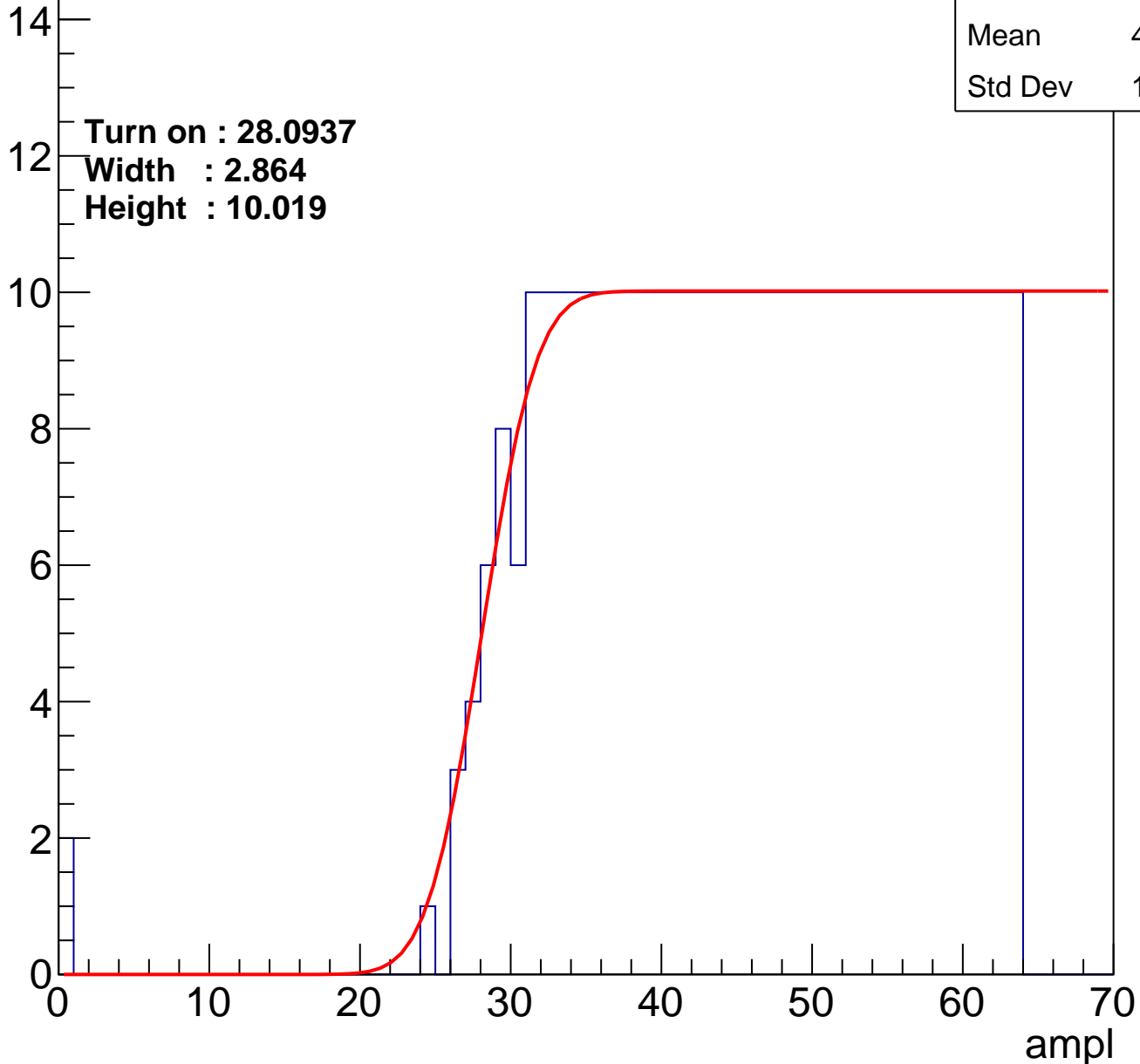
Entries	360
Mean	45.28
Std Dev	10.96

Turn on : 28.0937

Width : 2.864

Height : 10.019

Entry



B1L000S, U5-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.3905

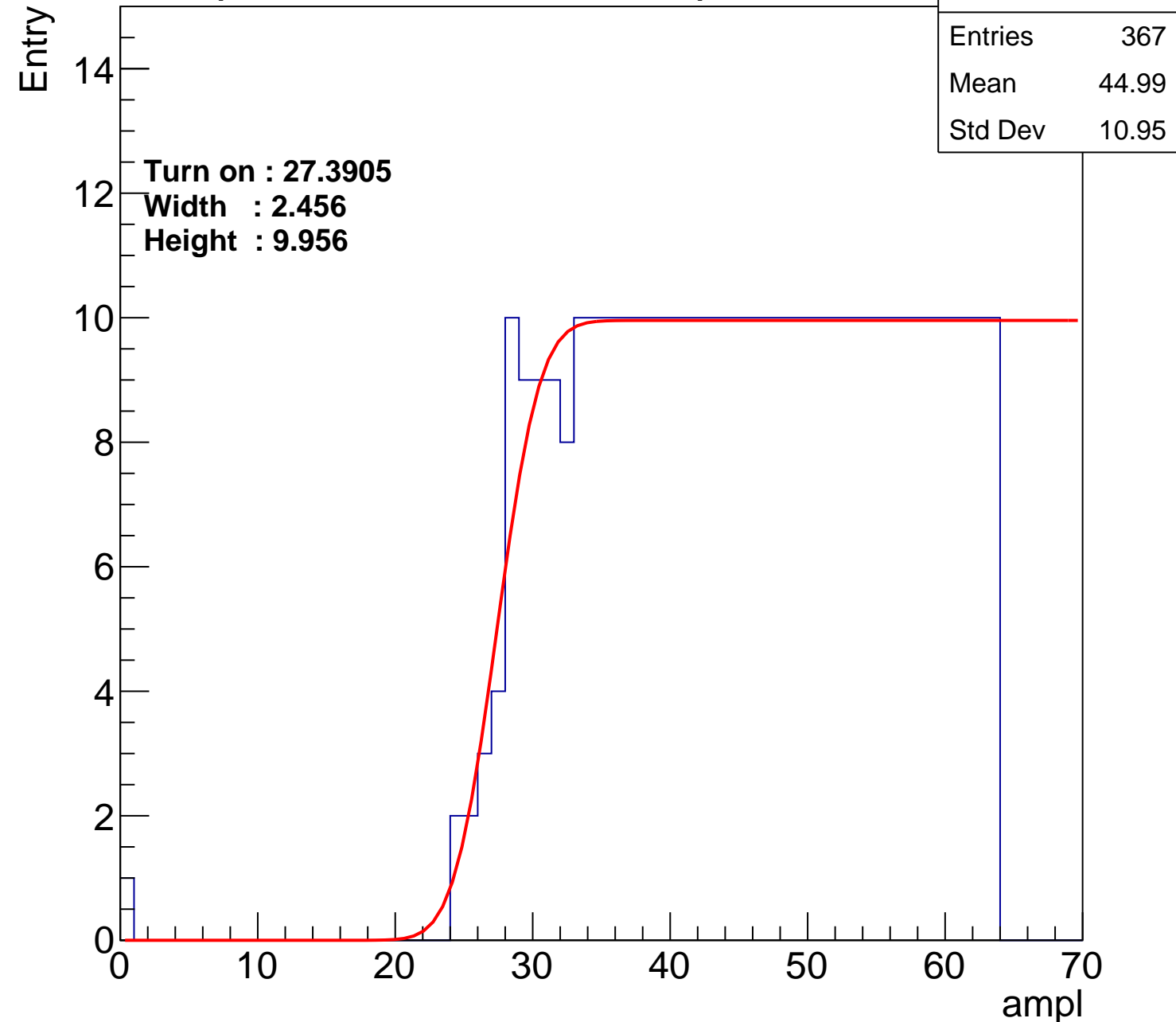
Width : 2.456

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch16

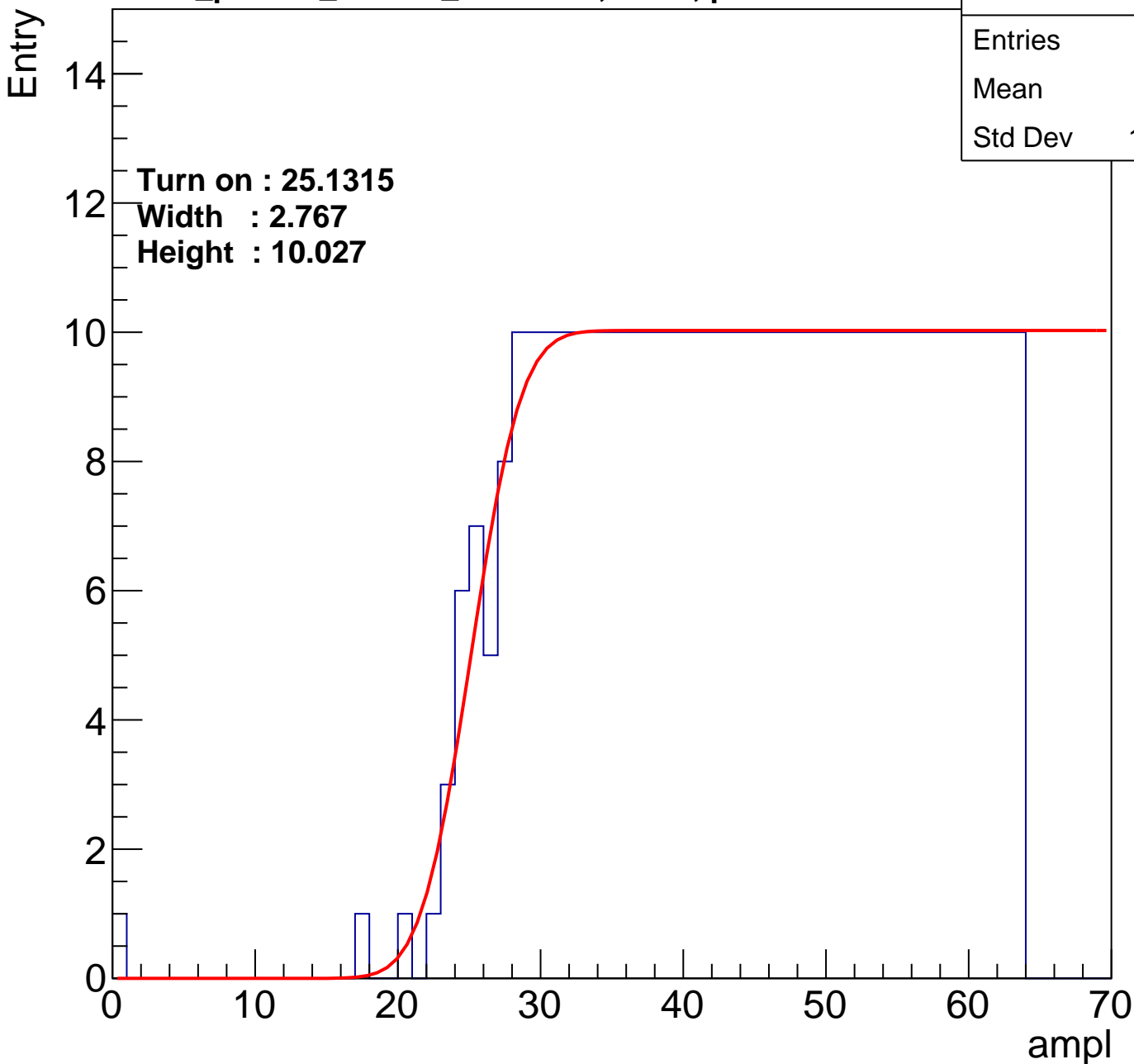
calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.7
Std Dev	11.67

Turn on : 25.1315

Width : 2.767

Height : 10.027



B1L000S, U5-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.2085

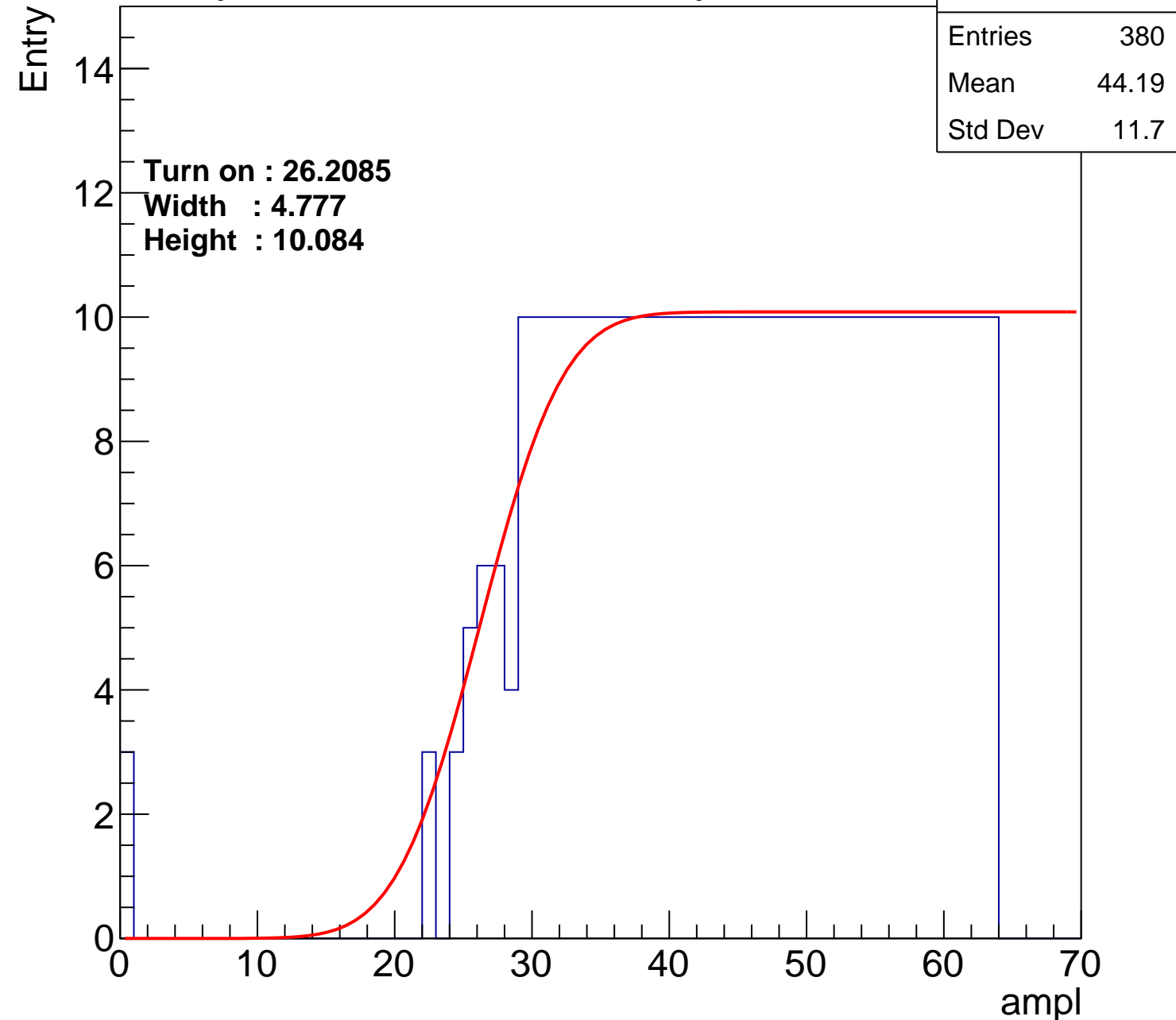
Width : 4.777

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.27
Std Dev	12.38

Turn on : 25.1271

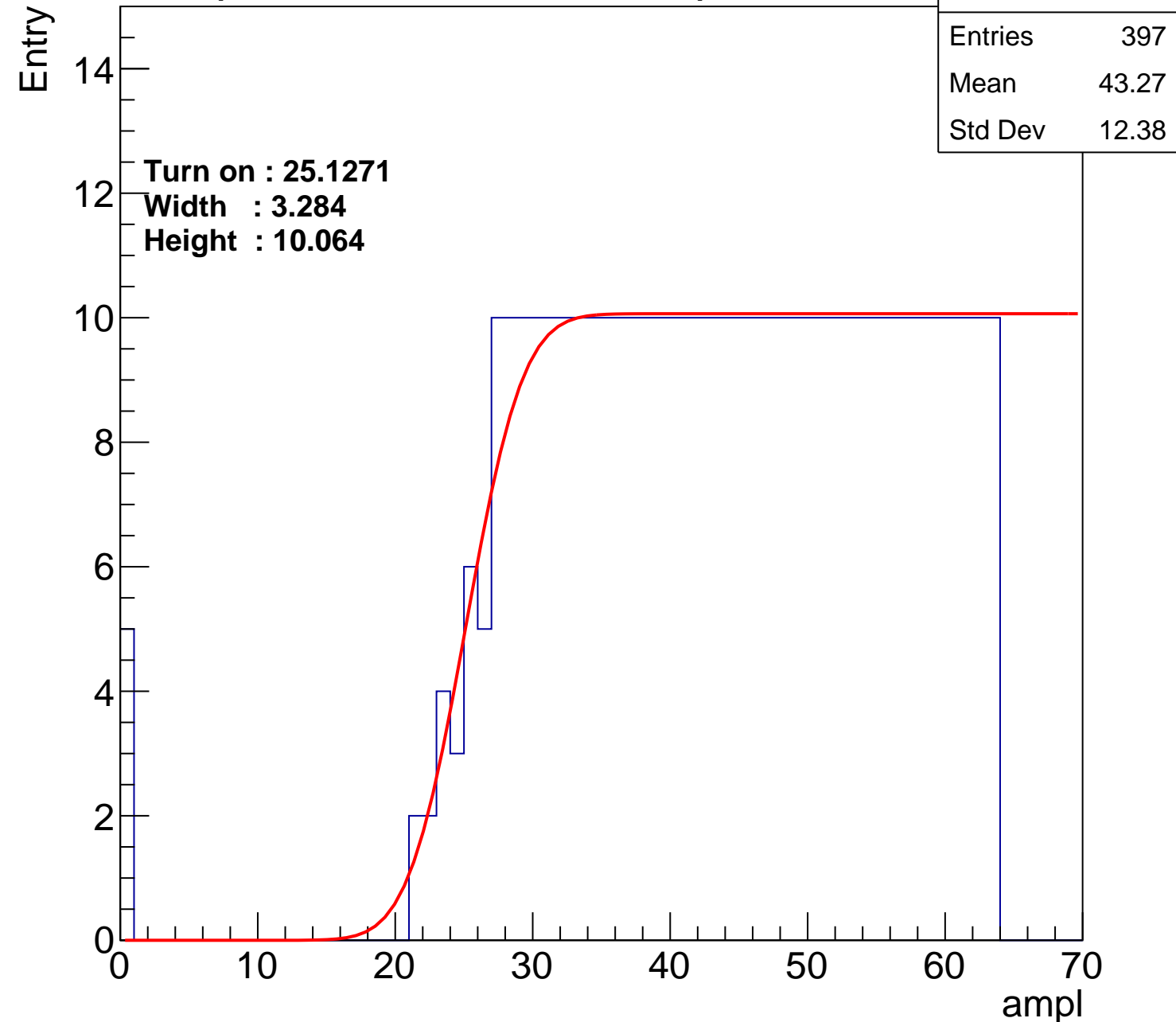
Width : 3.284

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch19

calib_packv5_042523_0143.root, FC#3, port B3

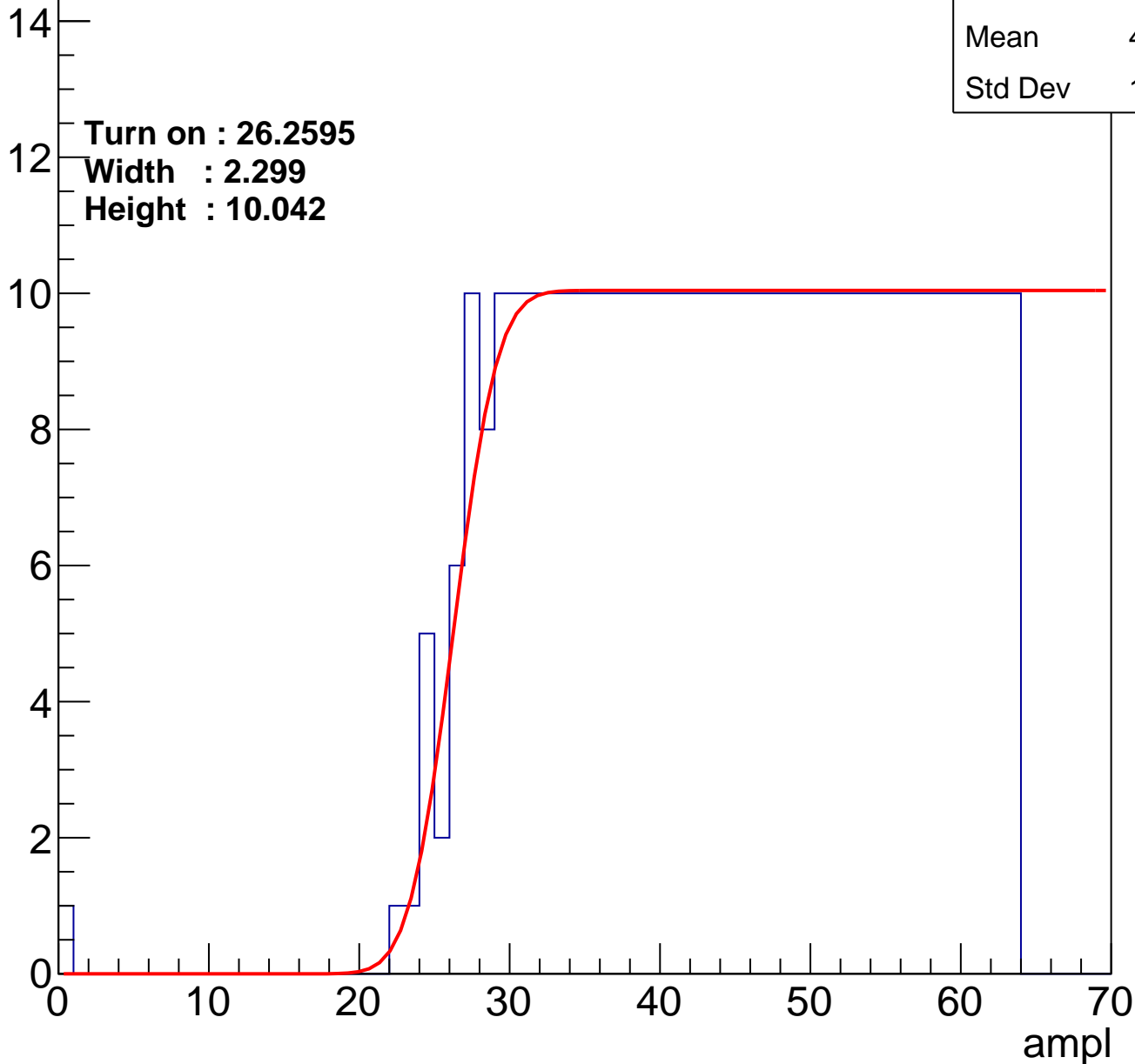
Entry

Entries	384
Mean	44.18
Std Dev	11.36

Turn on : 26.2595

Width : 2.299

Height : 10.042



B1L000S, U5-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.79
Std Dev	11.87

Turn on : 26.0876

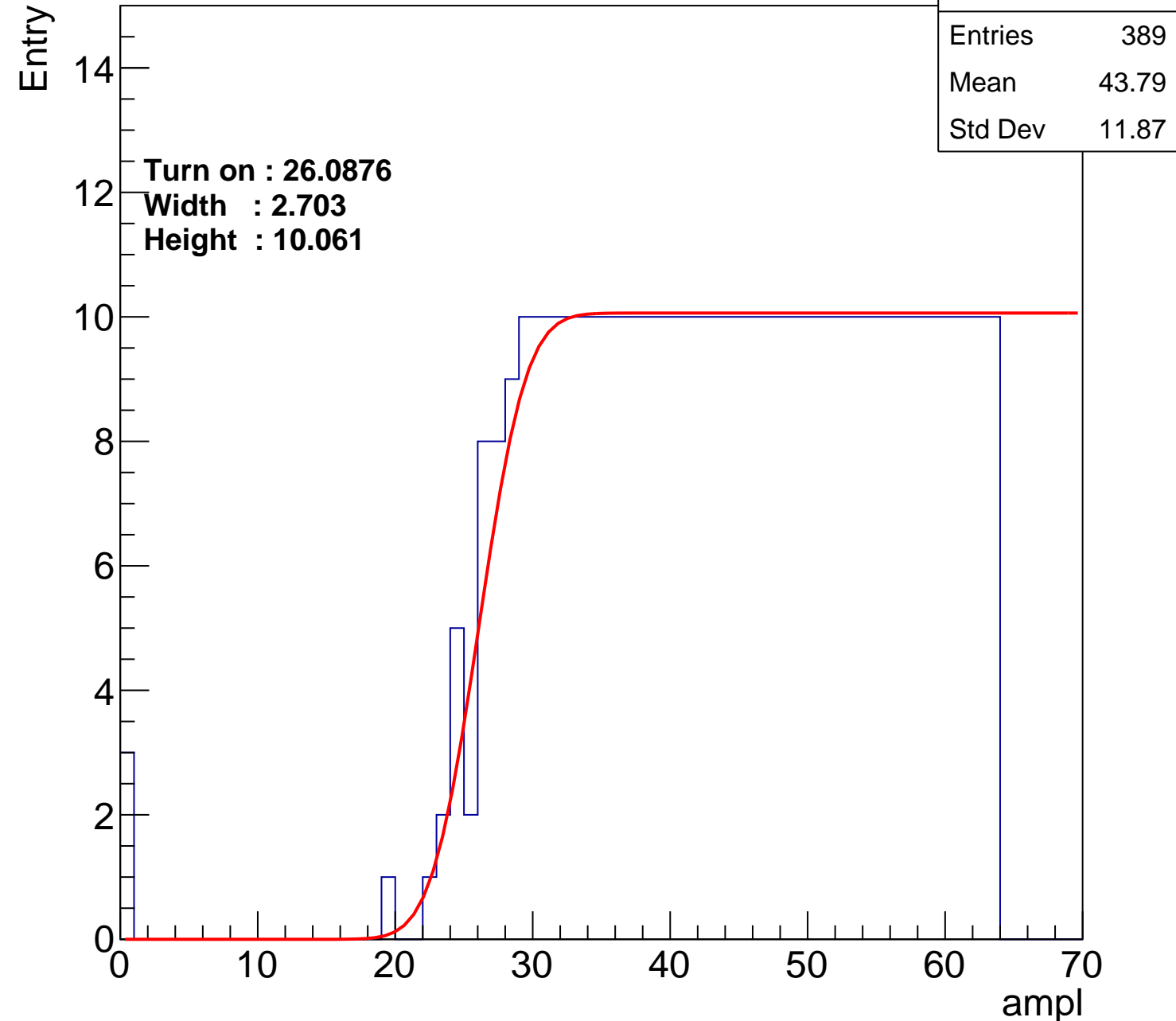
Width : 2.703

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch21

calib_packv5_042523_0143.root, FC#3, port B3

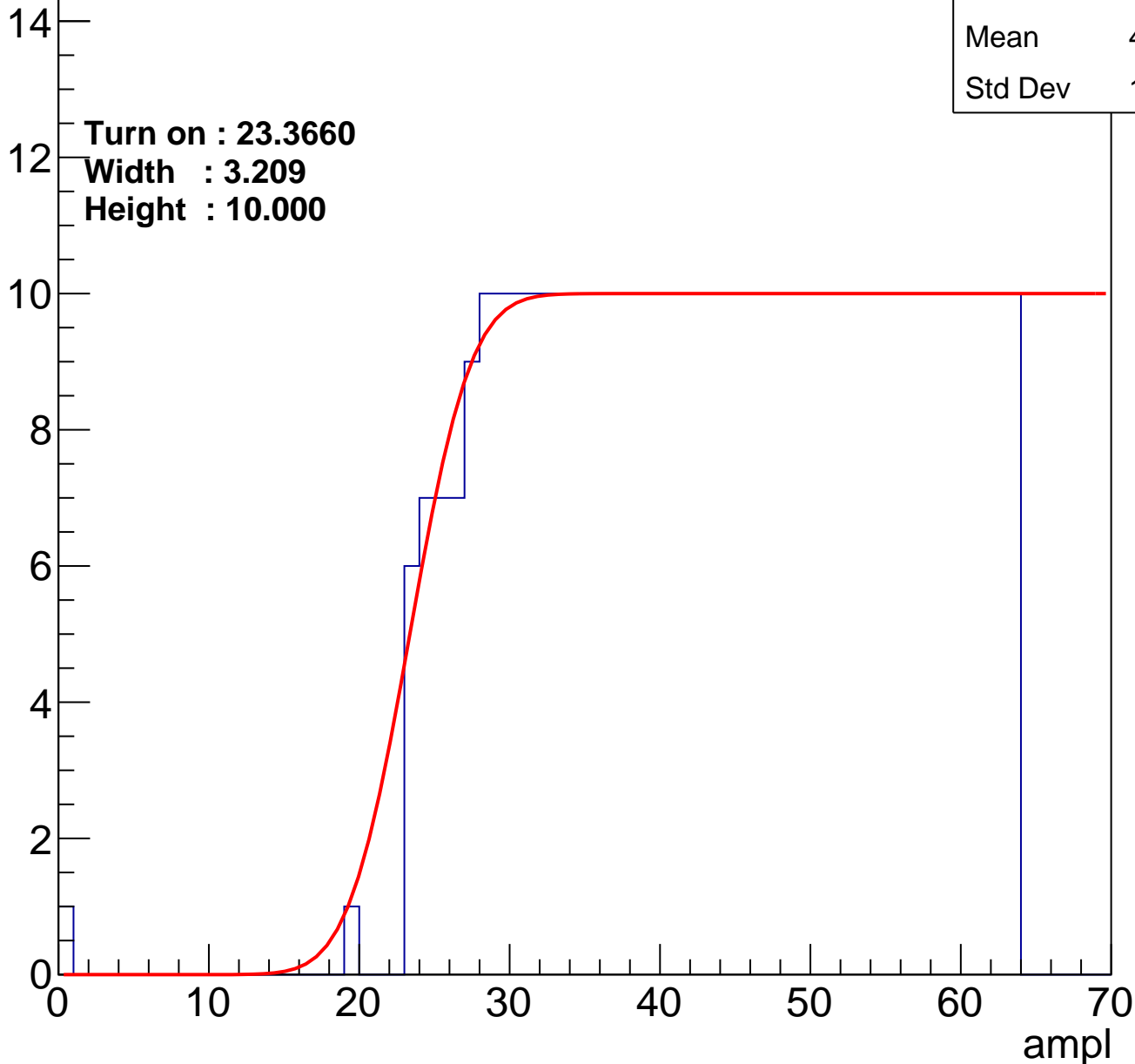
Entries	398
Mean	43.48
Std Dev	11.75

Turn on : 23.3660

Width : 3.209

Height : 10.000

Entry



B1L000S, U5-ch22

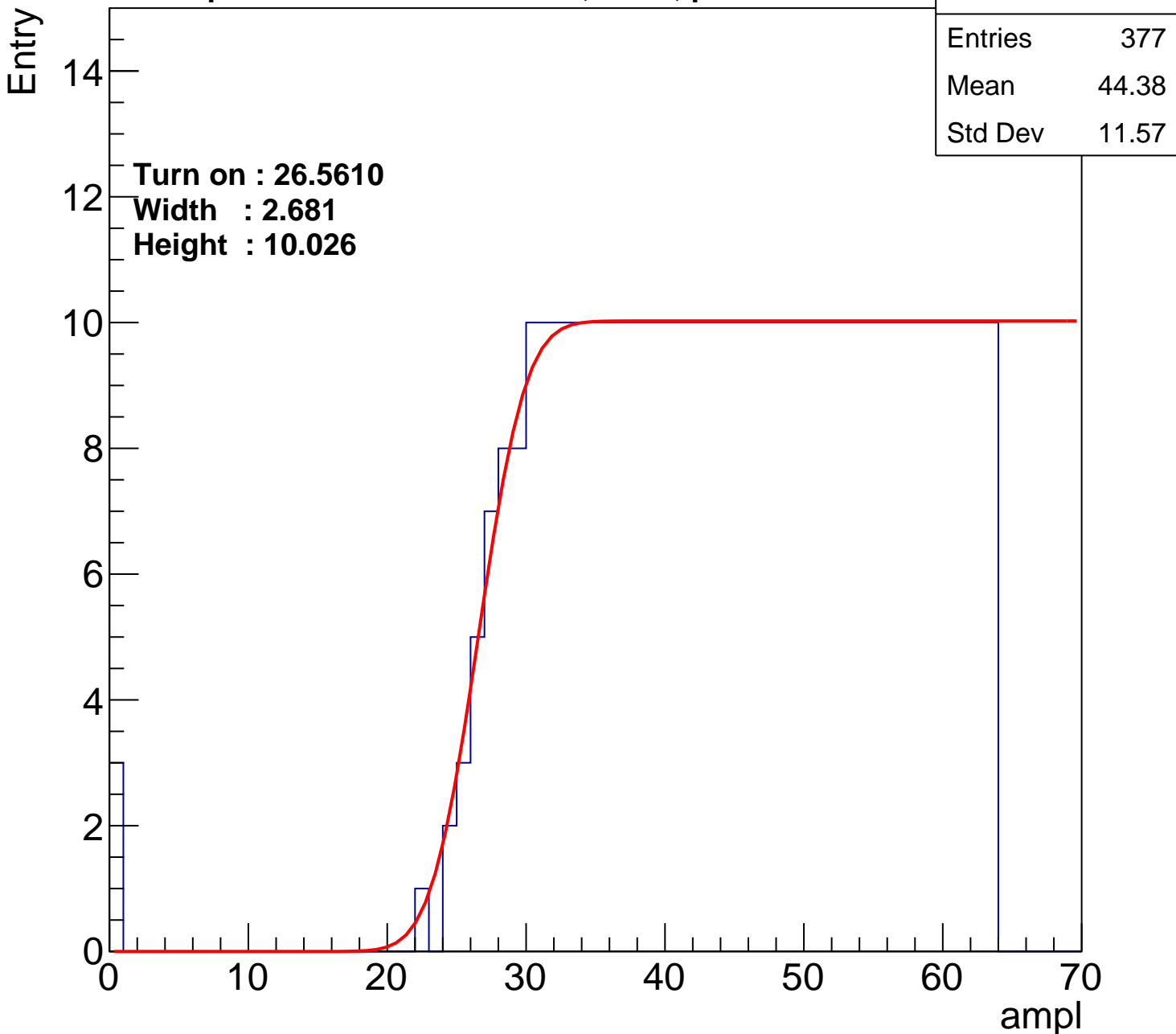
calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.38
Std Dev	11.57

Turn on : 26.5610

Width : 2.681

Height : 10.026



B1L000S, U5-ch23

calib_packv5_042523_0143.root, FC#3, port B3

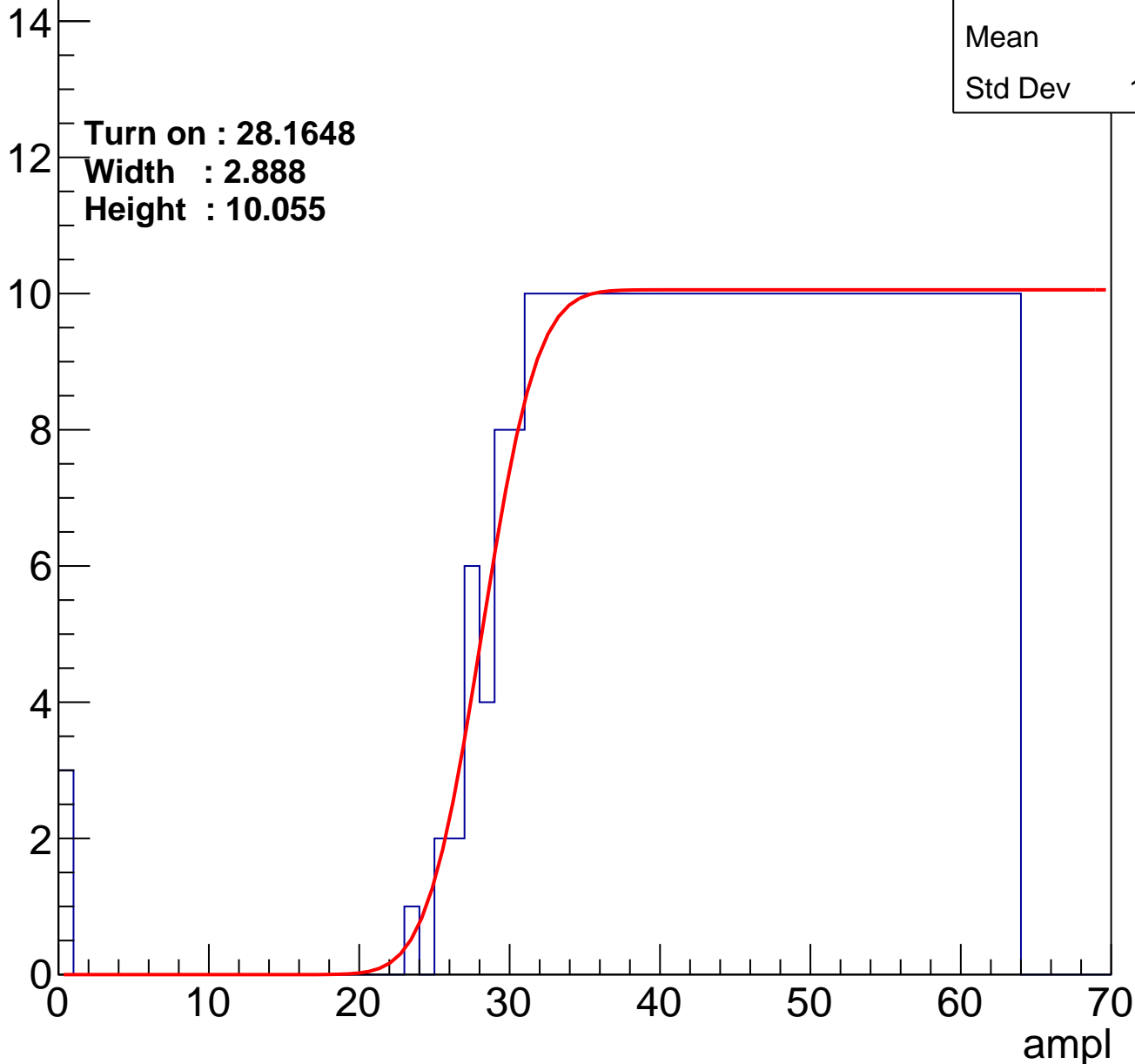
Entry

Entries	364
Mean	45
Std Dev	11.27

Turn on : 28.1648

Width : 2.888

Height : 10.055



B1L000S, U5-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	417
Mean	42.44
Std Dev	12.48

Turn on : 22.6361

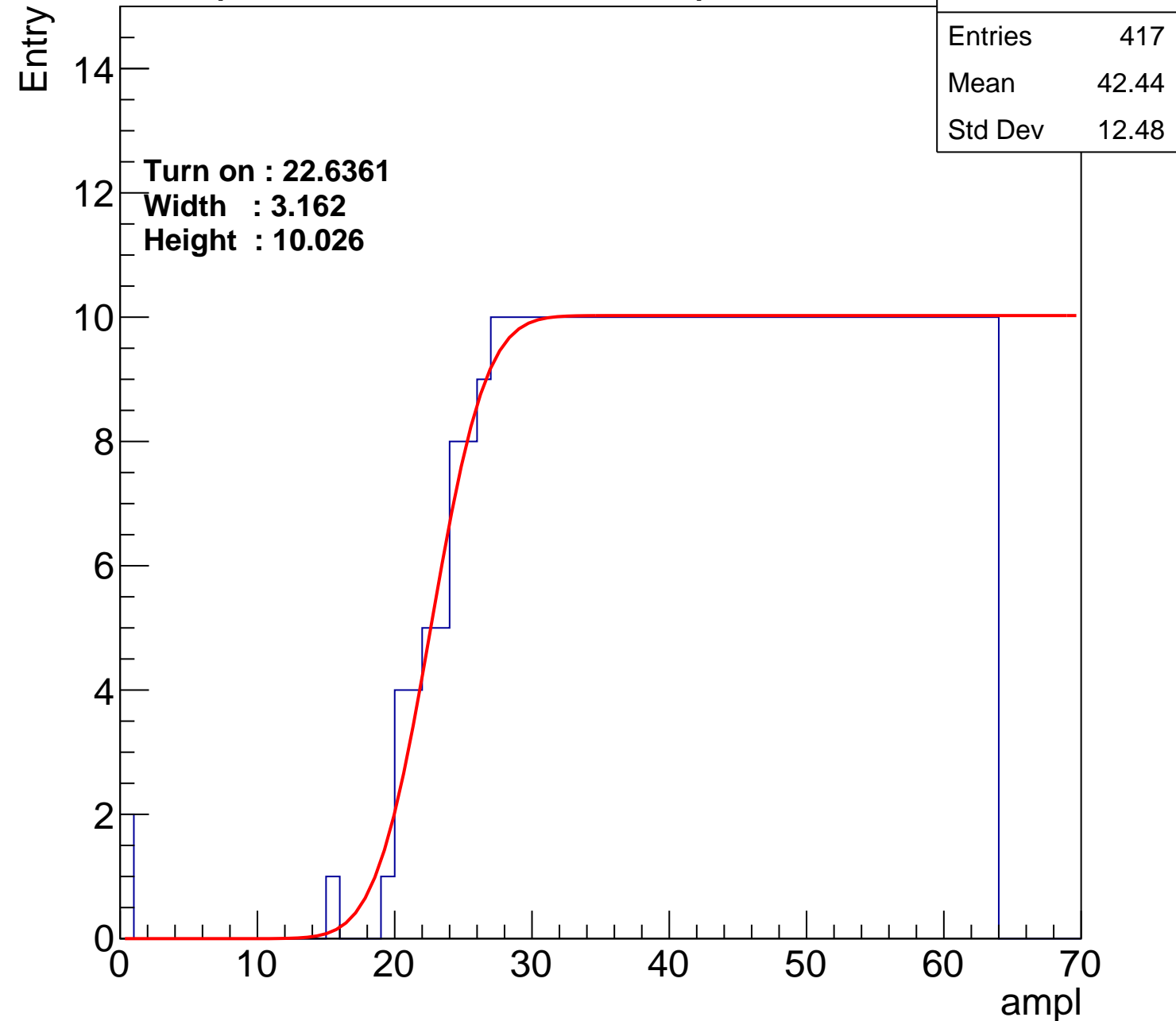
Width : 3.162

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.34
Std Dev	12.33

Turn on : 25.0302

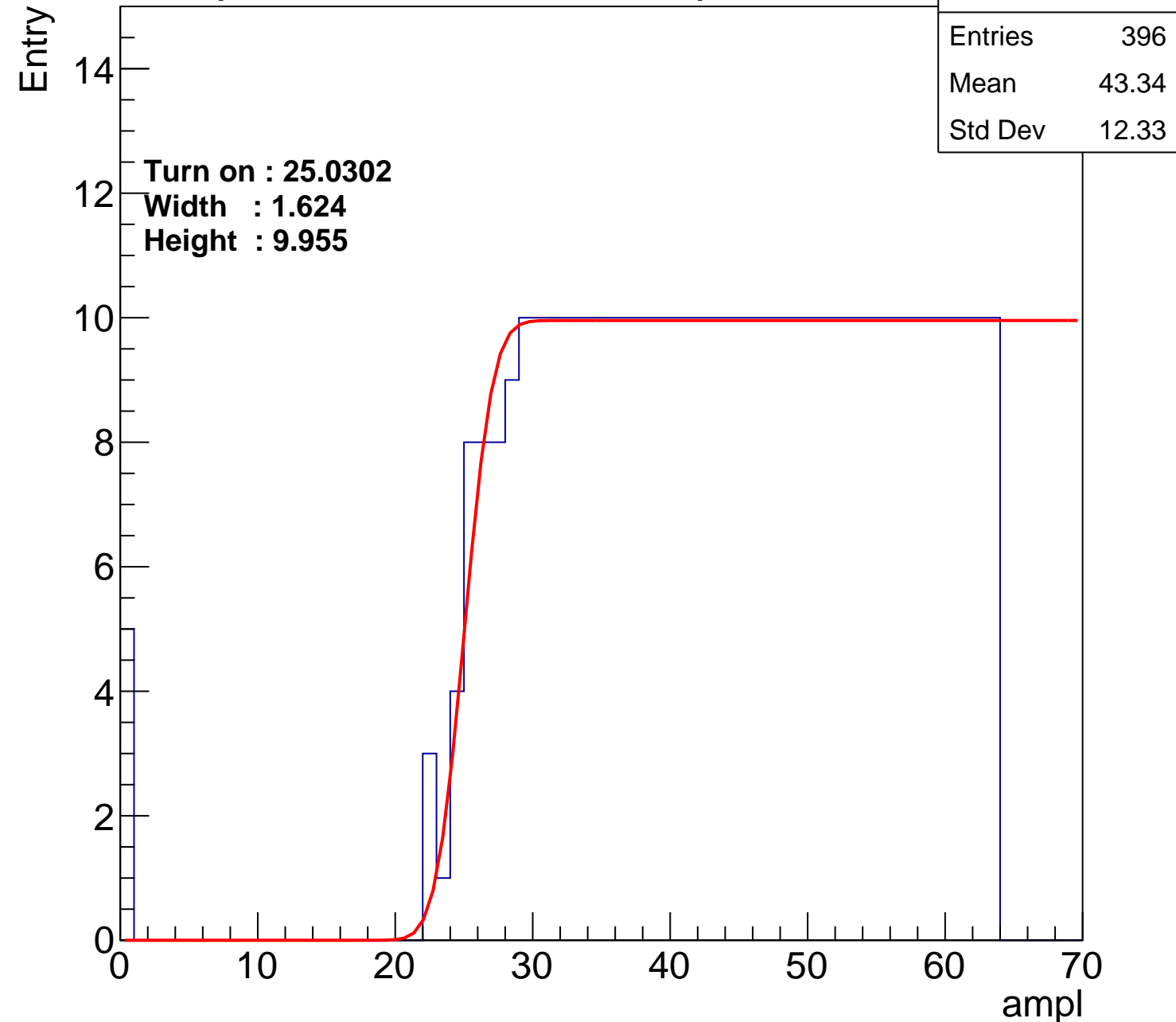
Width : 1.624

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.73
Std Dev	12.07

Turn on : 25.7865

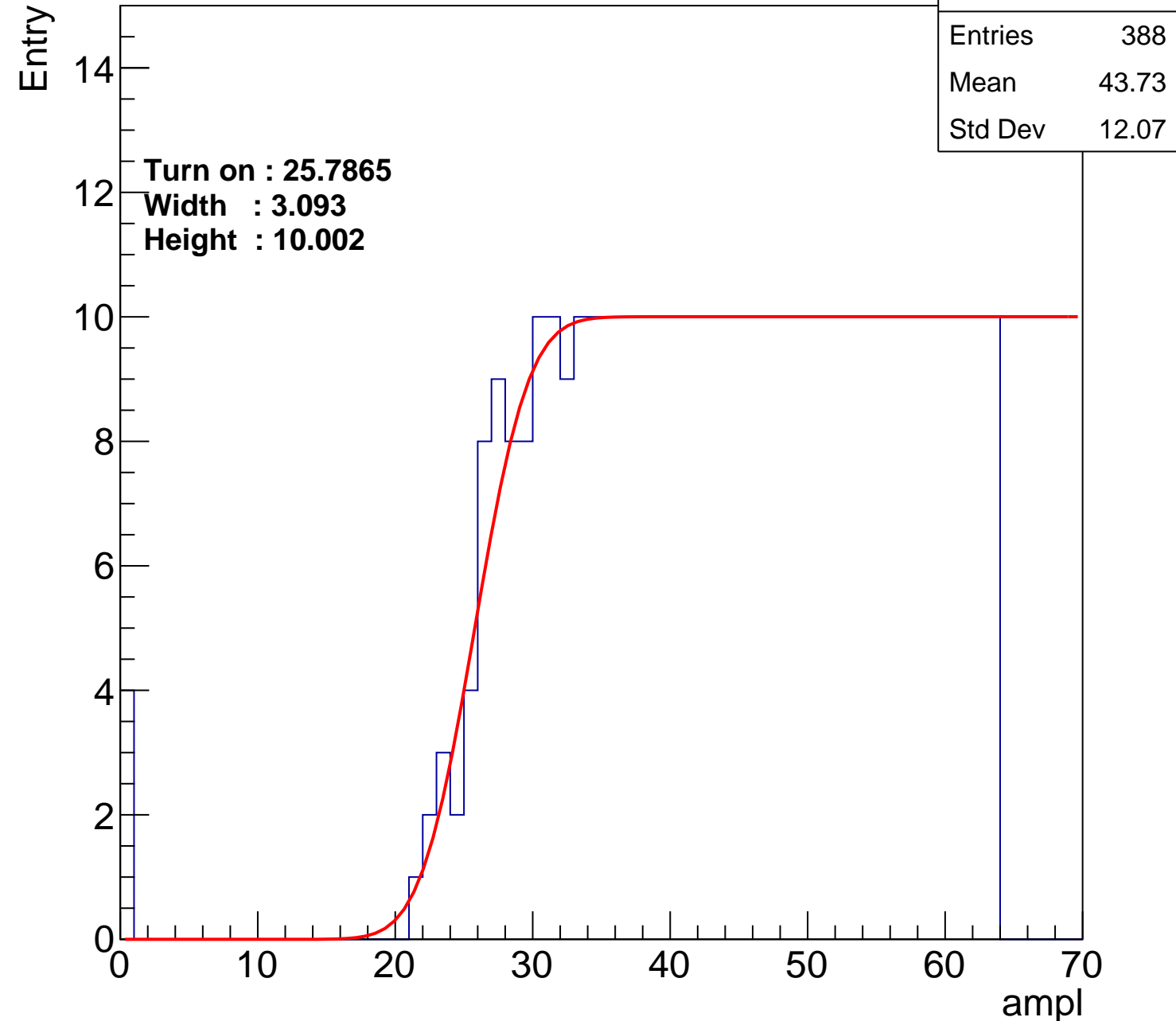
Width : 3.093

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.86
Std Dev	11.61

Turn on : 25.6308

Width : 3.483

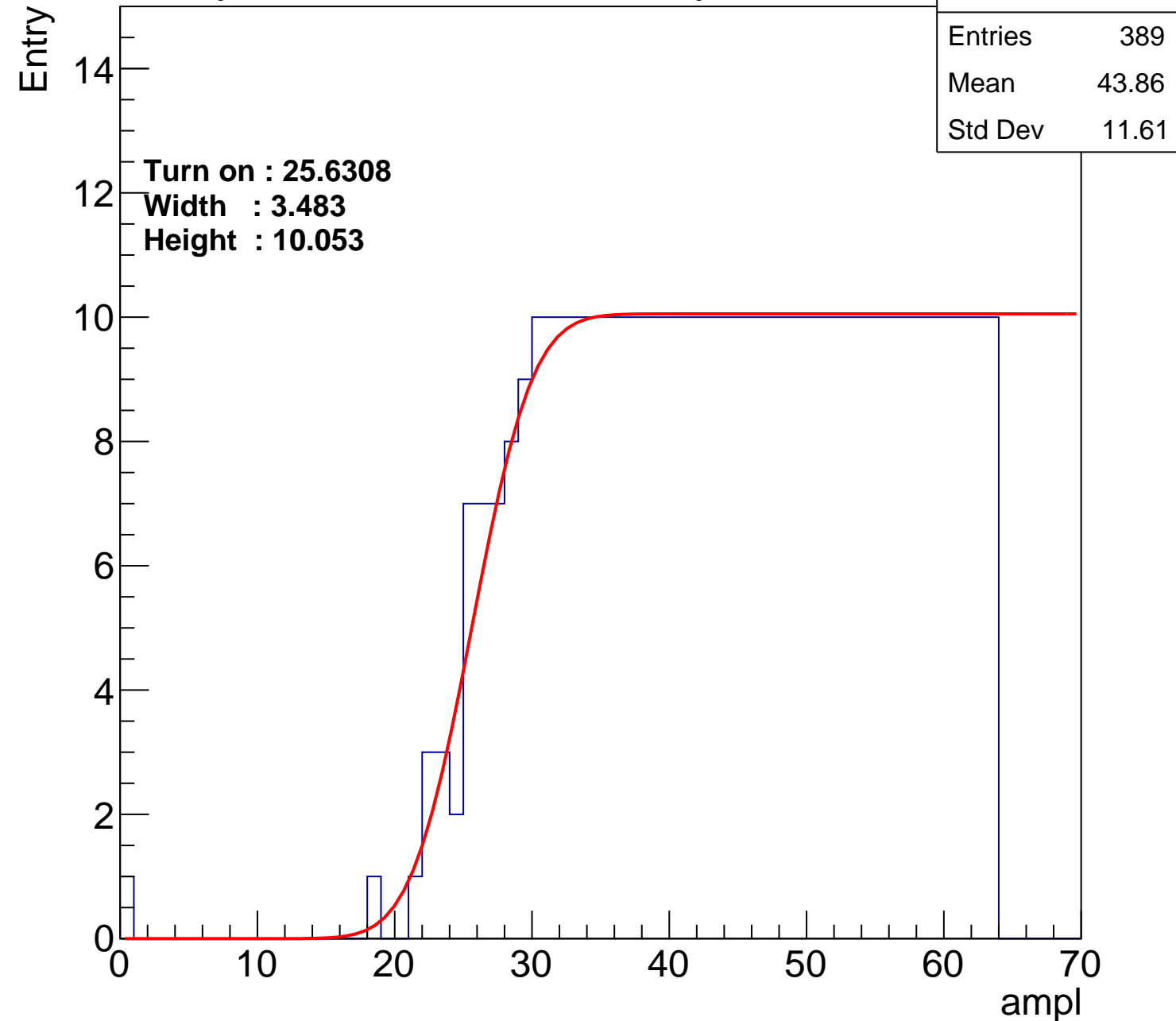
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U5-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.57
Std Dev	11.71

Turn on : 25.2181

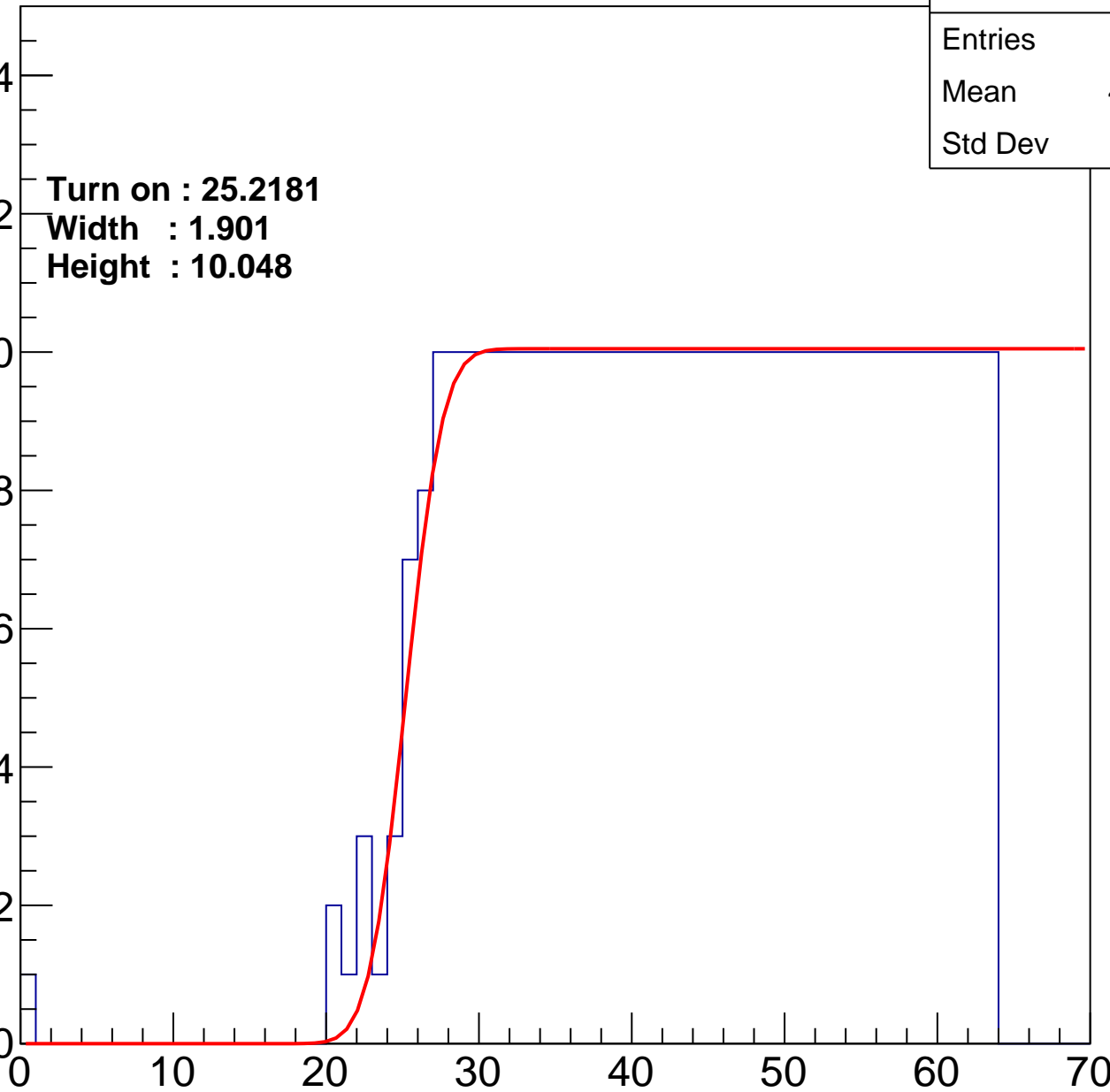
Width : 1.901

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.2
Std Dev	11.63

Turn on : 26.4169

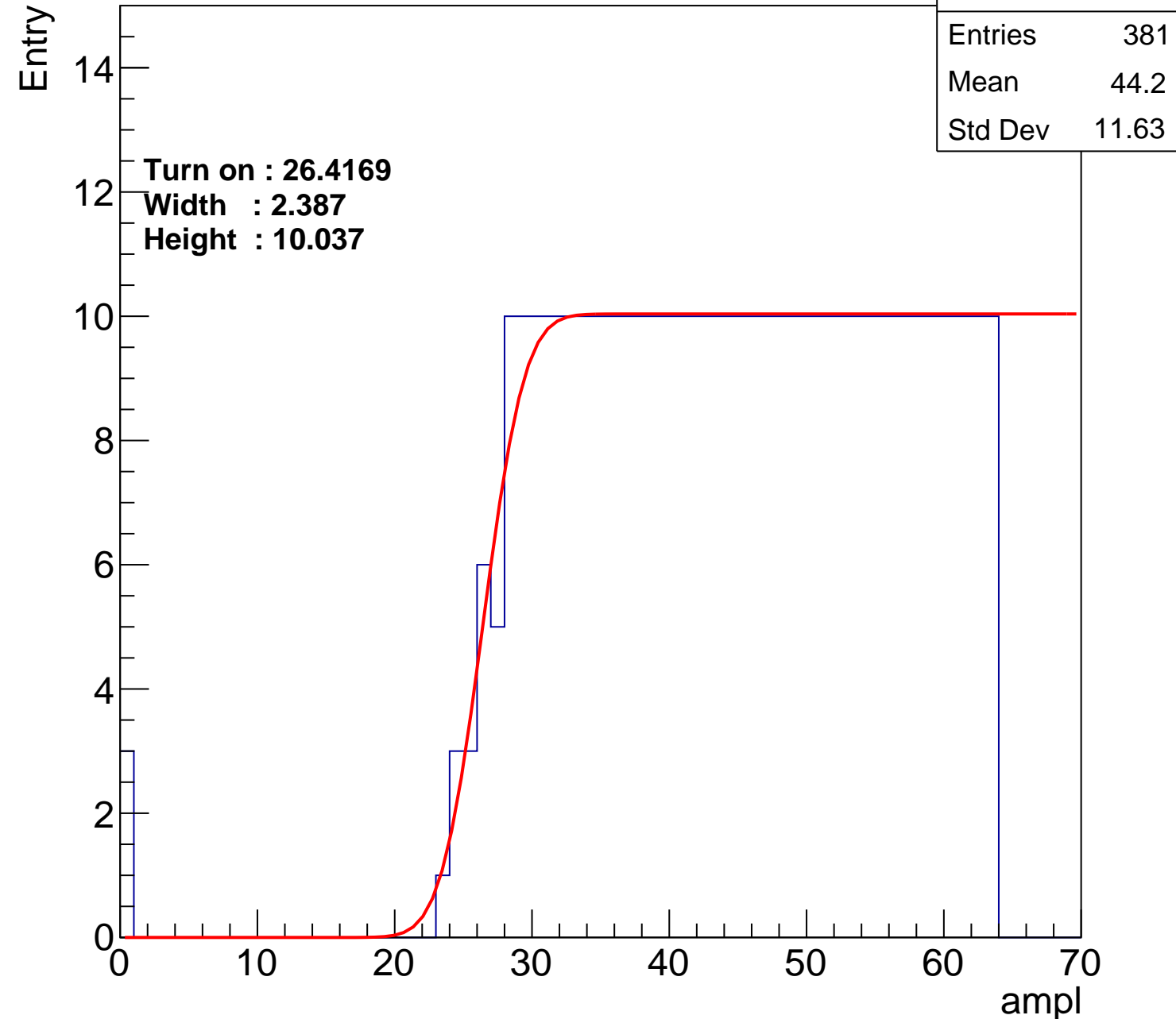
Width : 2.387

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.08
Std Dev	11.89

Turn on : 26.3643

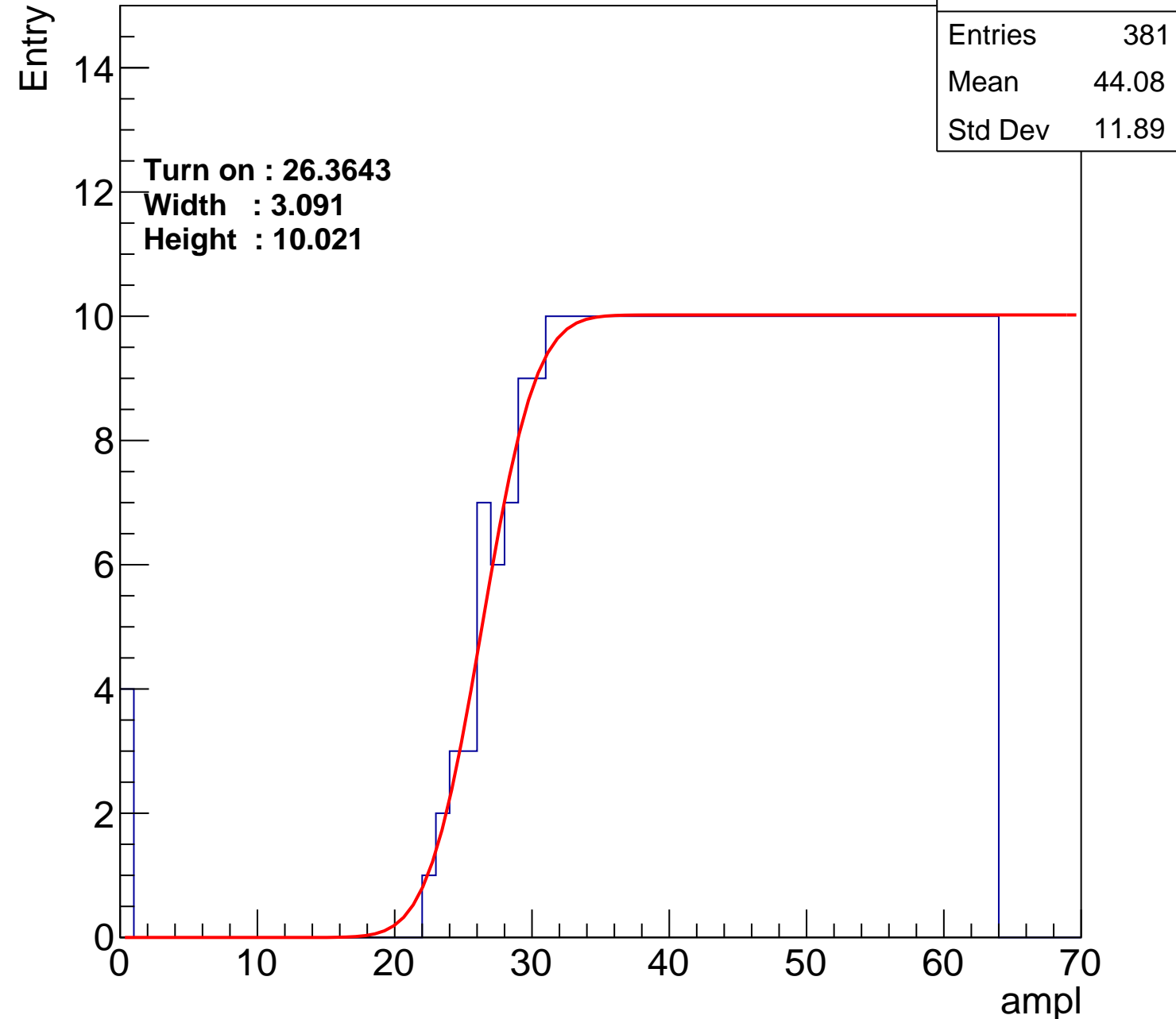
Width : 3.091

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 24.0345

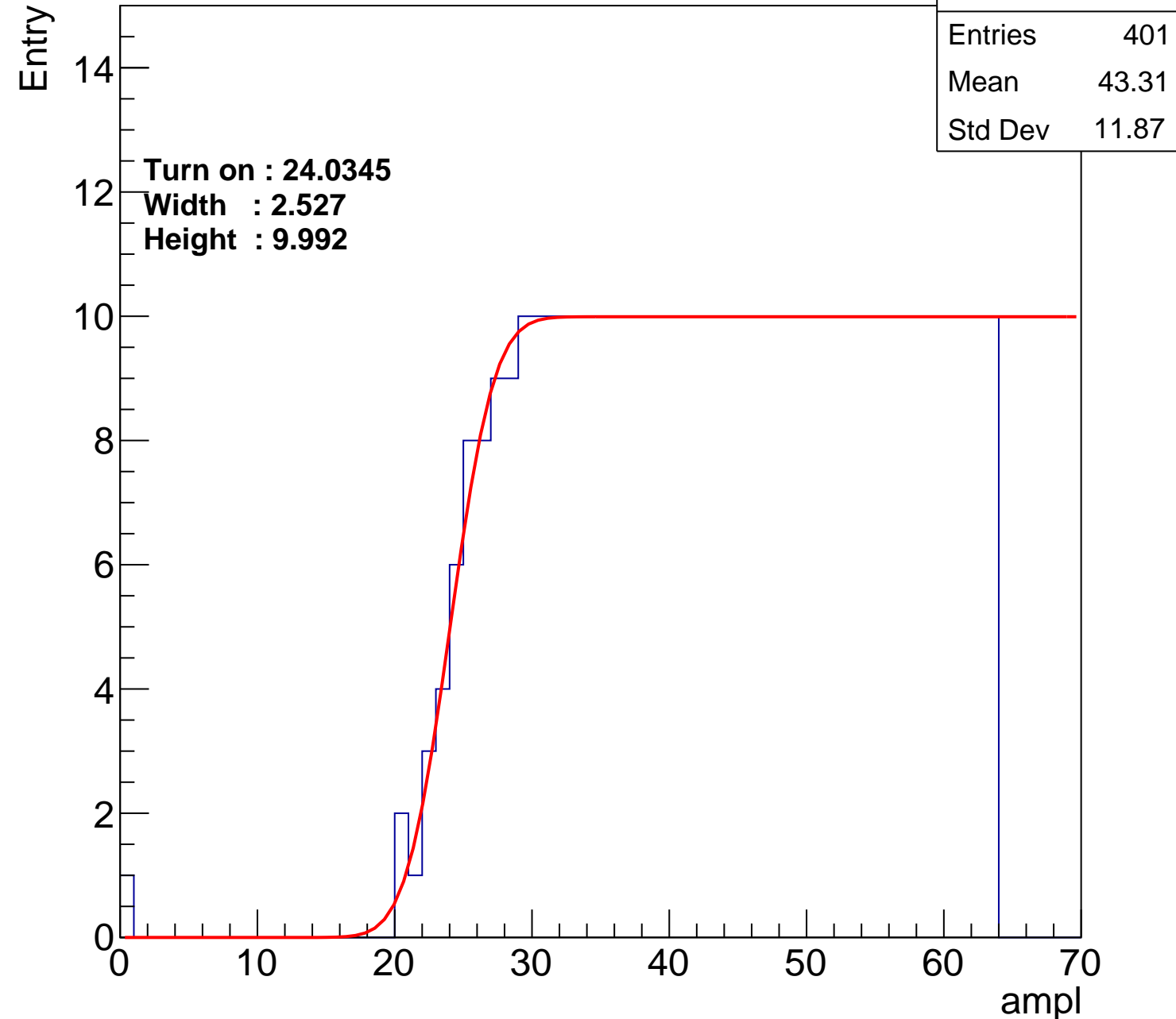
Width : 2.527

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.04
Std Dev	11.6

Turn on : 26.1373

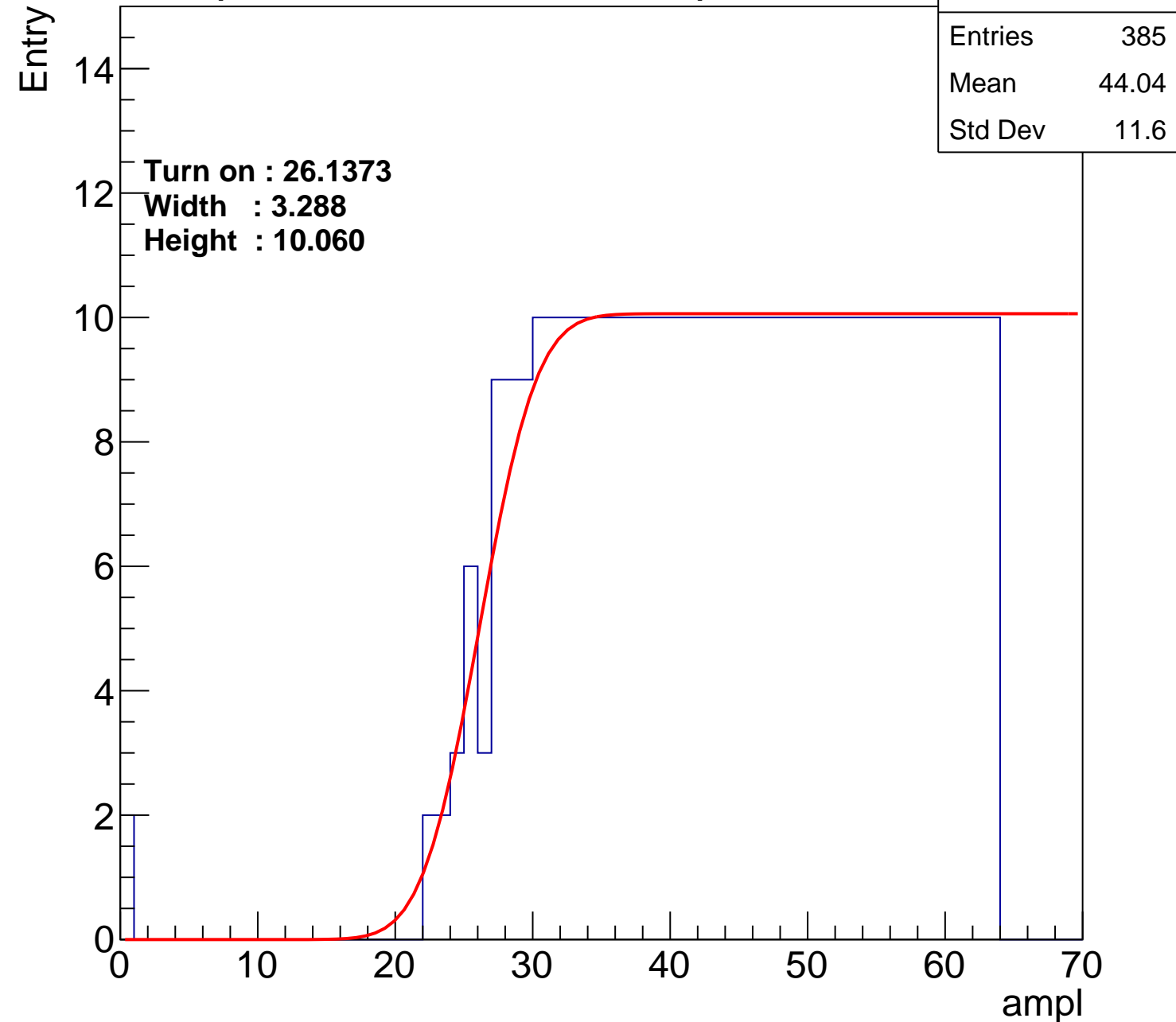
Width : 3.288

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch33

calib_packv5_042523_0143.root, FC#3, port B3

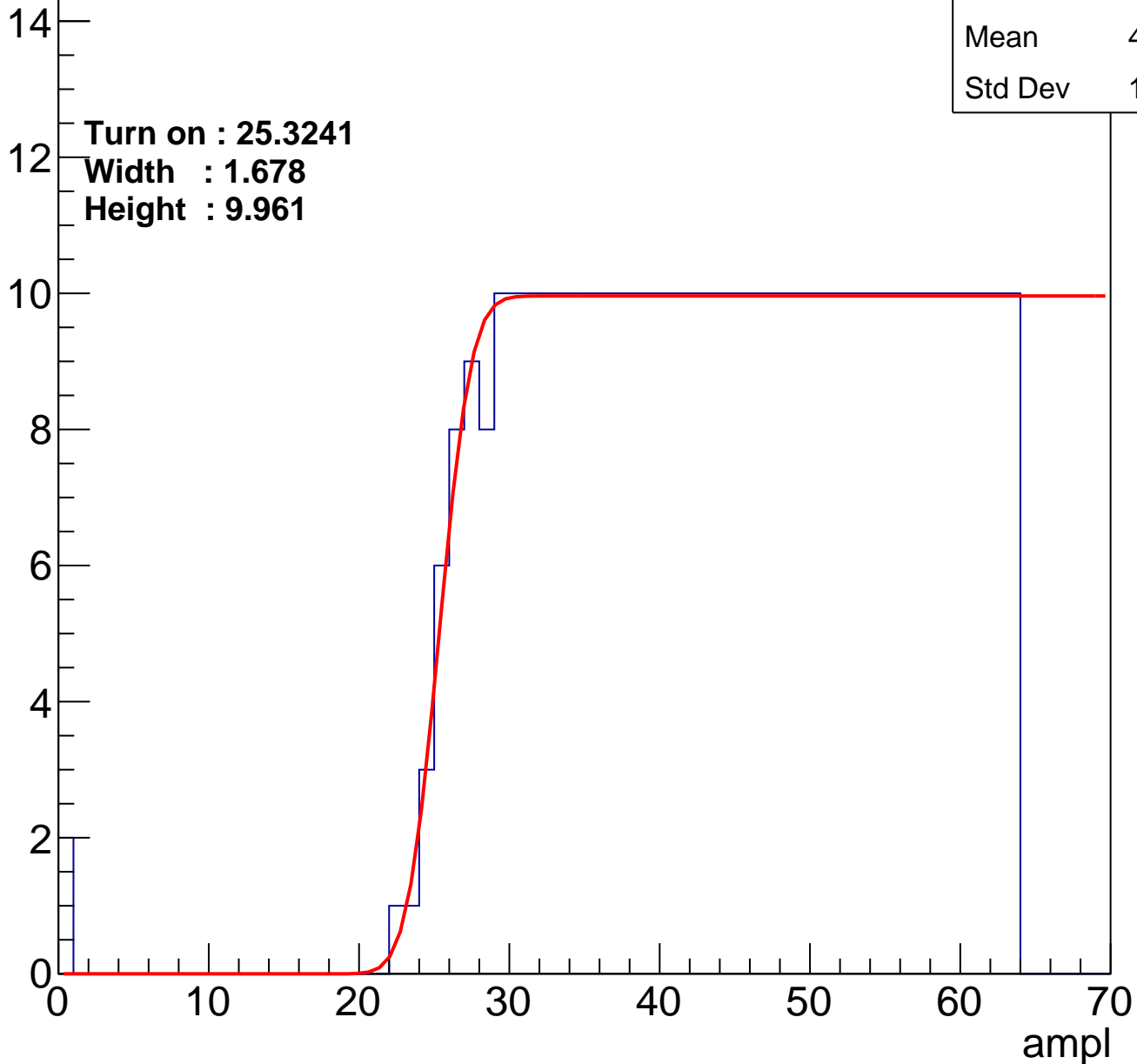
Entries	388
Mean	43.92
Std Dev	11.63

Turn on : 25.3241

Width : 1.678

Height : 9.961

Entry



B1L000S, U5-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.31
Std Dev	11.44

Turn on : 26.2633

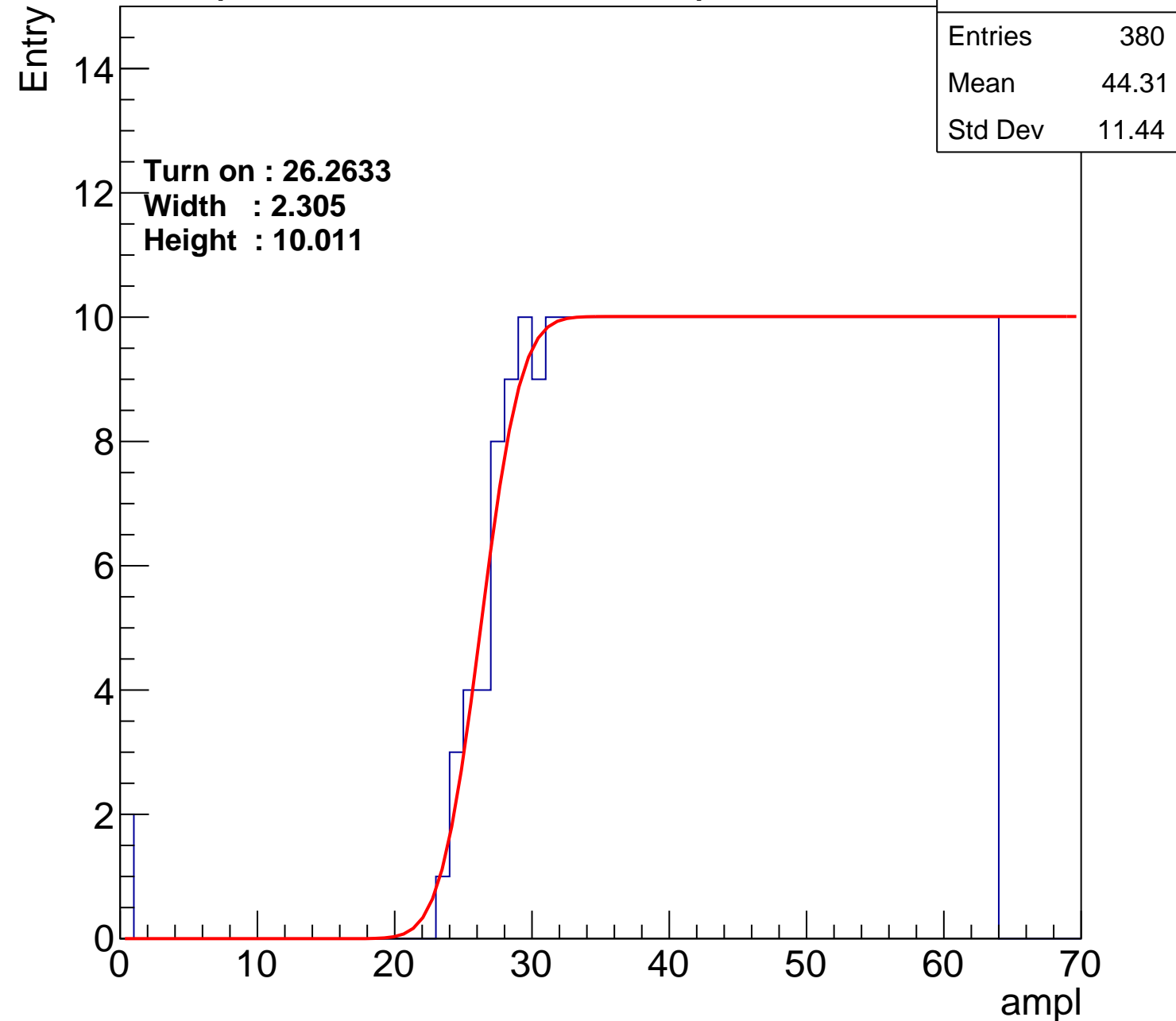
Width : 2.305

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.32
Std Dev	11.97

Turn on : 24.4388

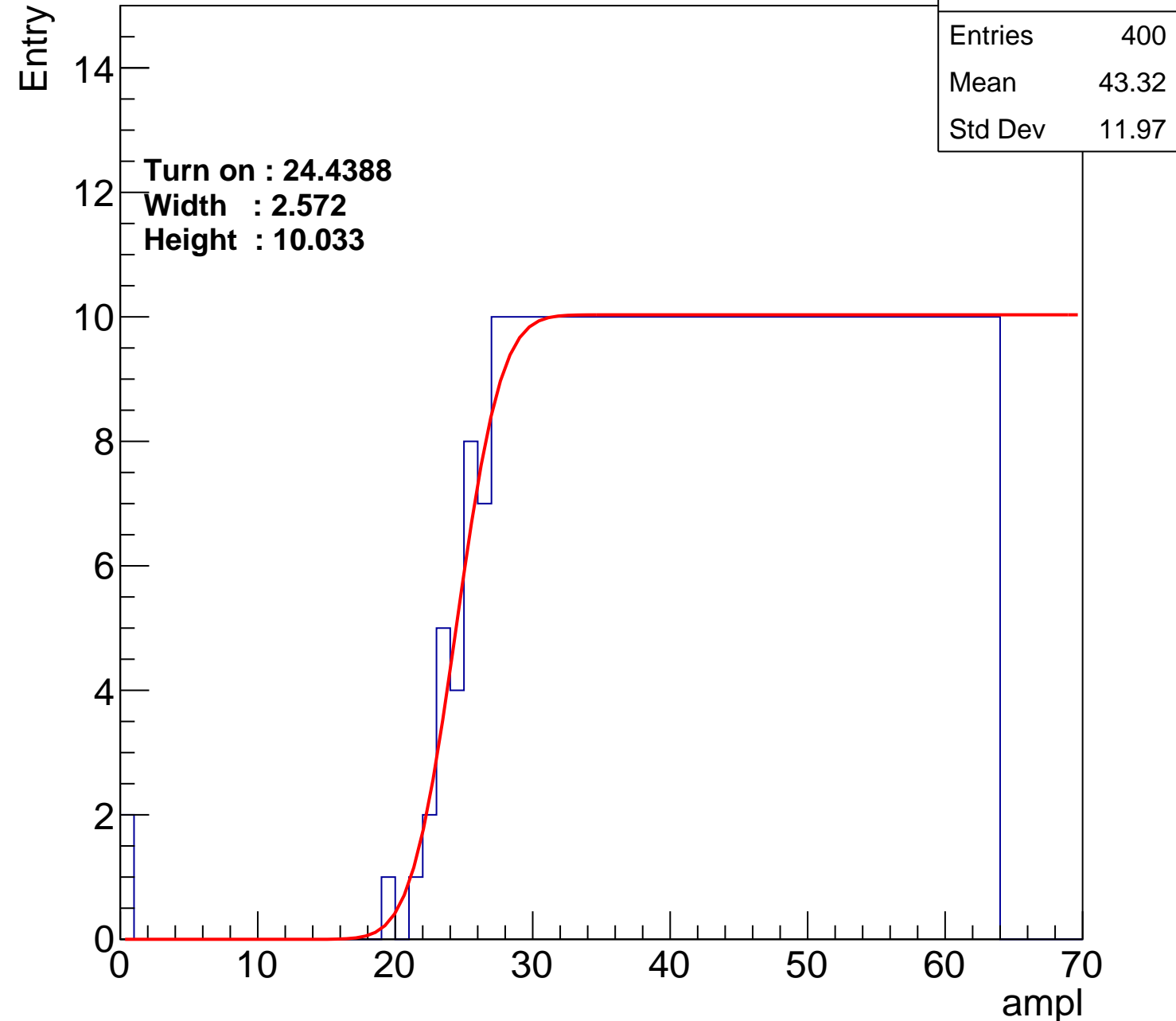
Width : 2.572

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.27
Std Dev	12.49

Turn on : 25.1961

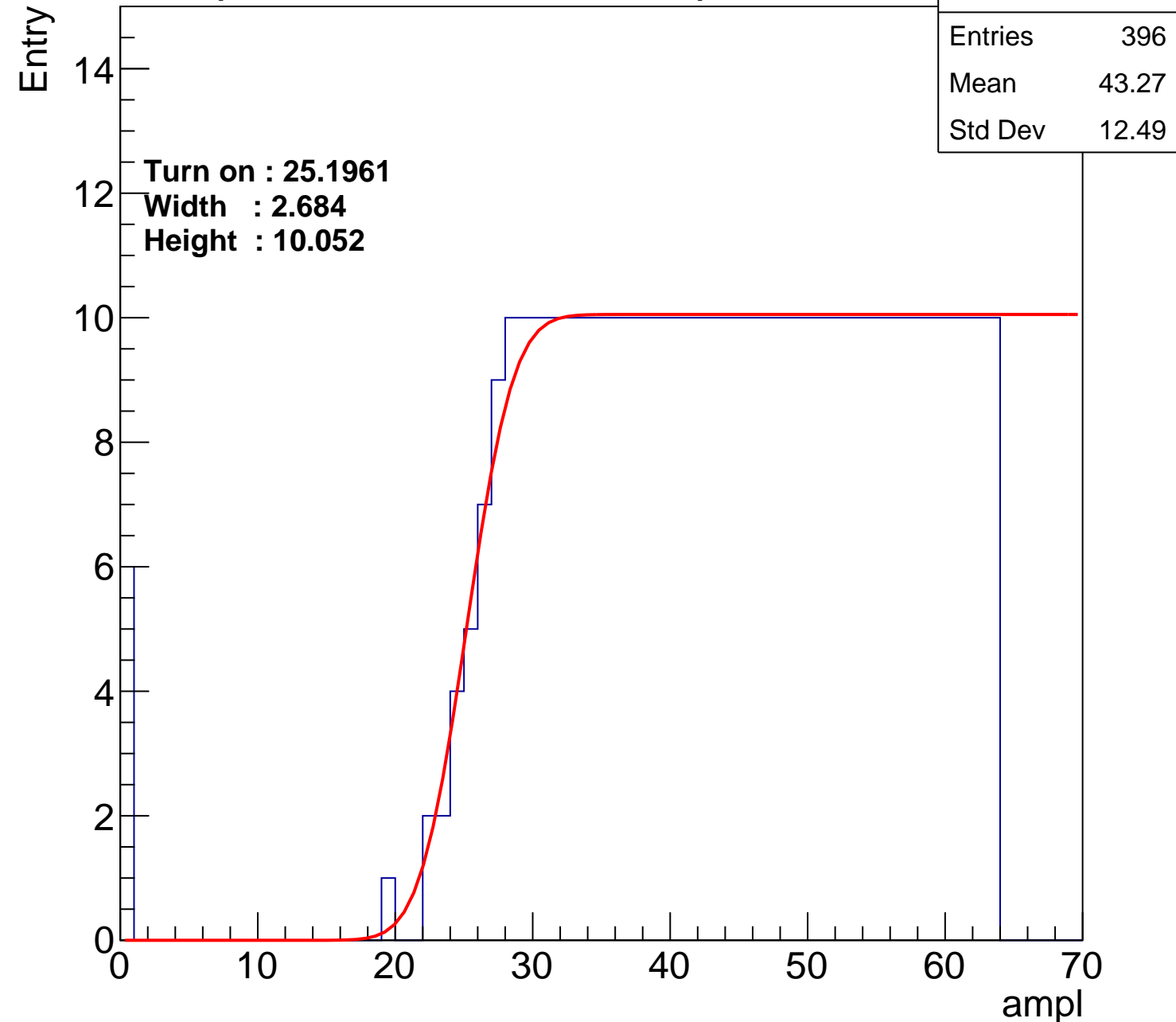
Width : 2.684

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.83
Std Dev	11.2

Turn on : 27.4299

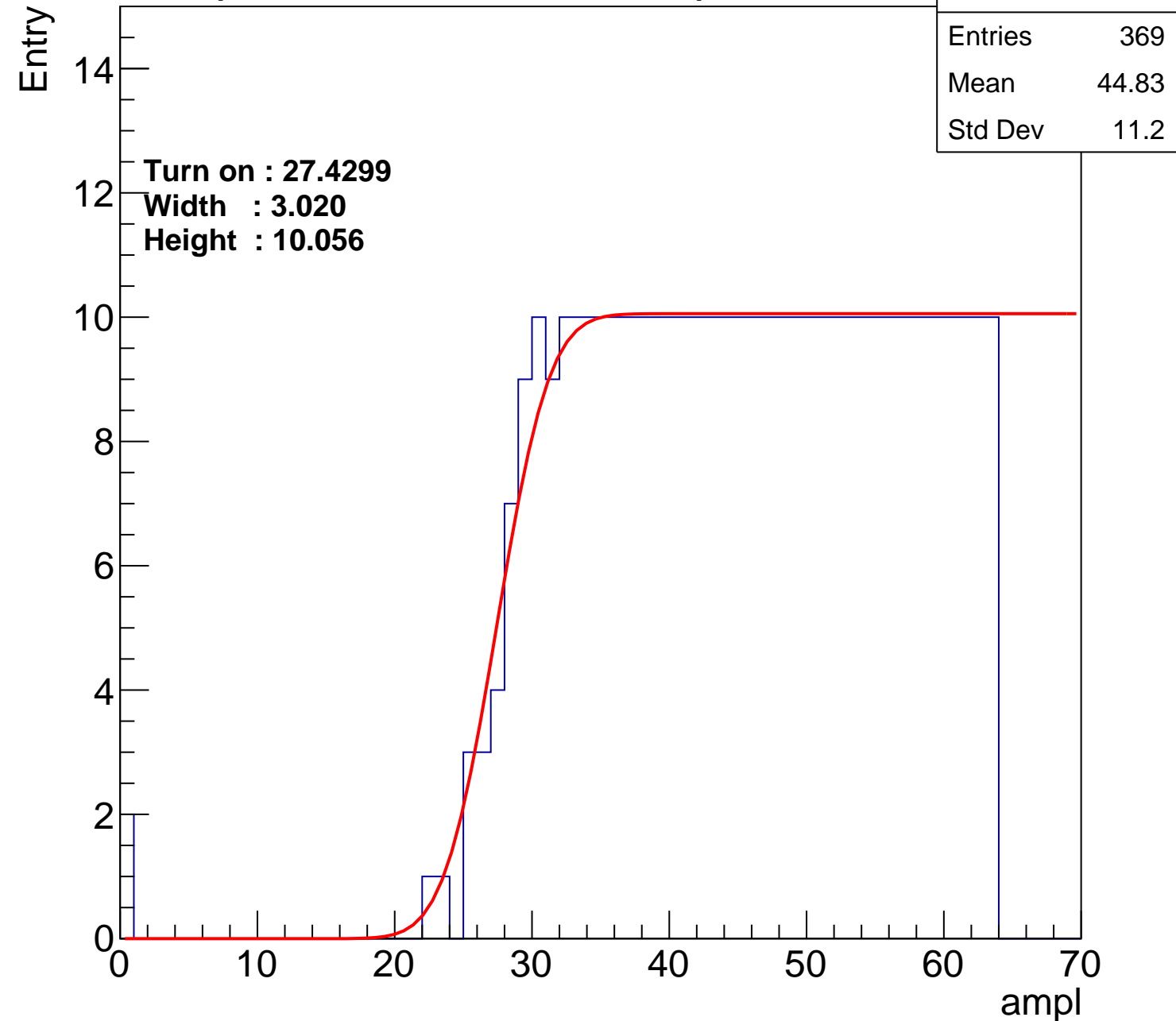
Width : 3.020

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
---------	-----

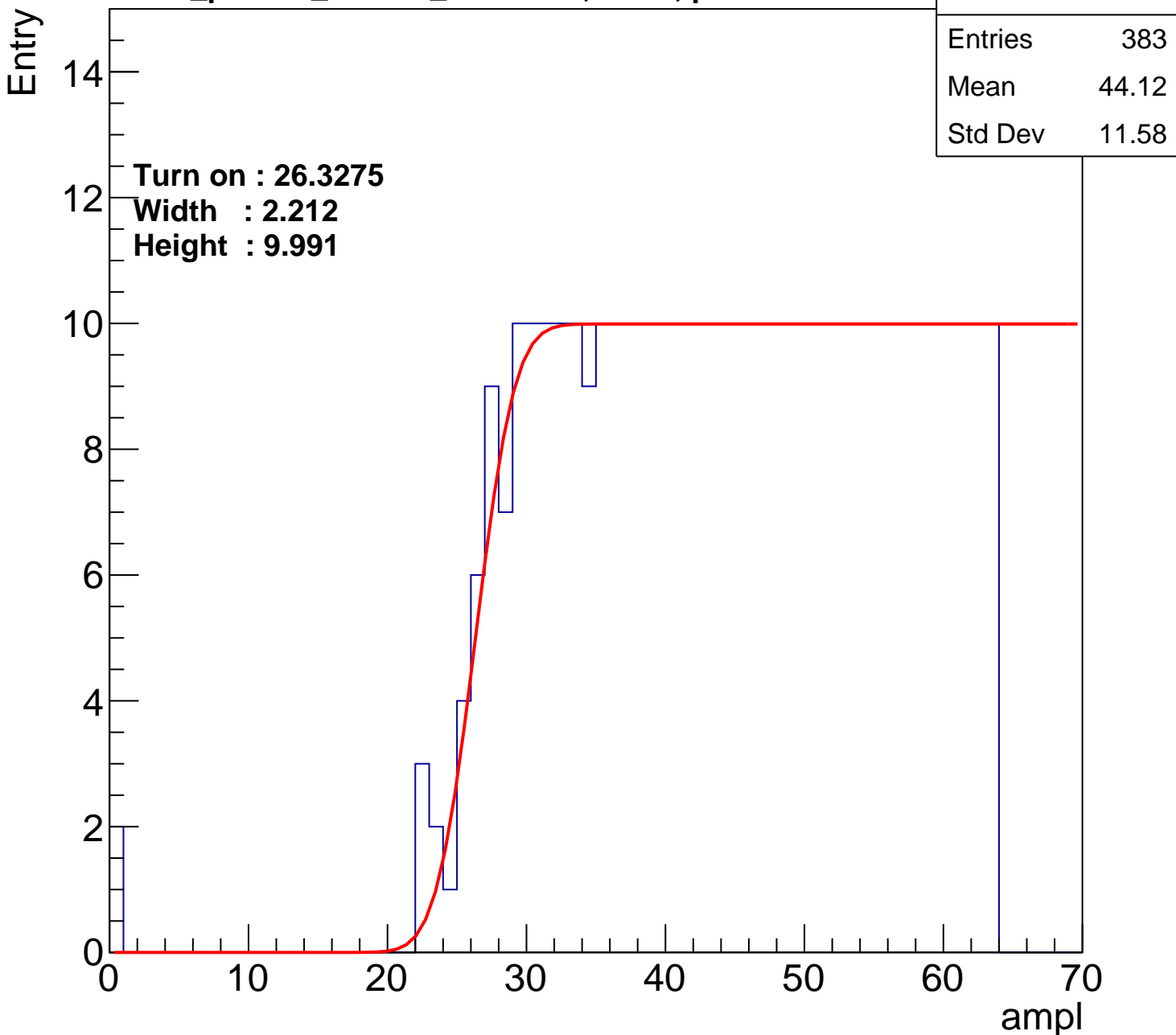
Mean	44.12
------	-------

Std Dev	11.58
---------	-------

Turn on : 26.3275

Width : 2.212

Height : 9.991



B1L000S, U5-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 27.1767

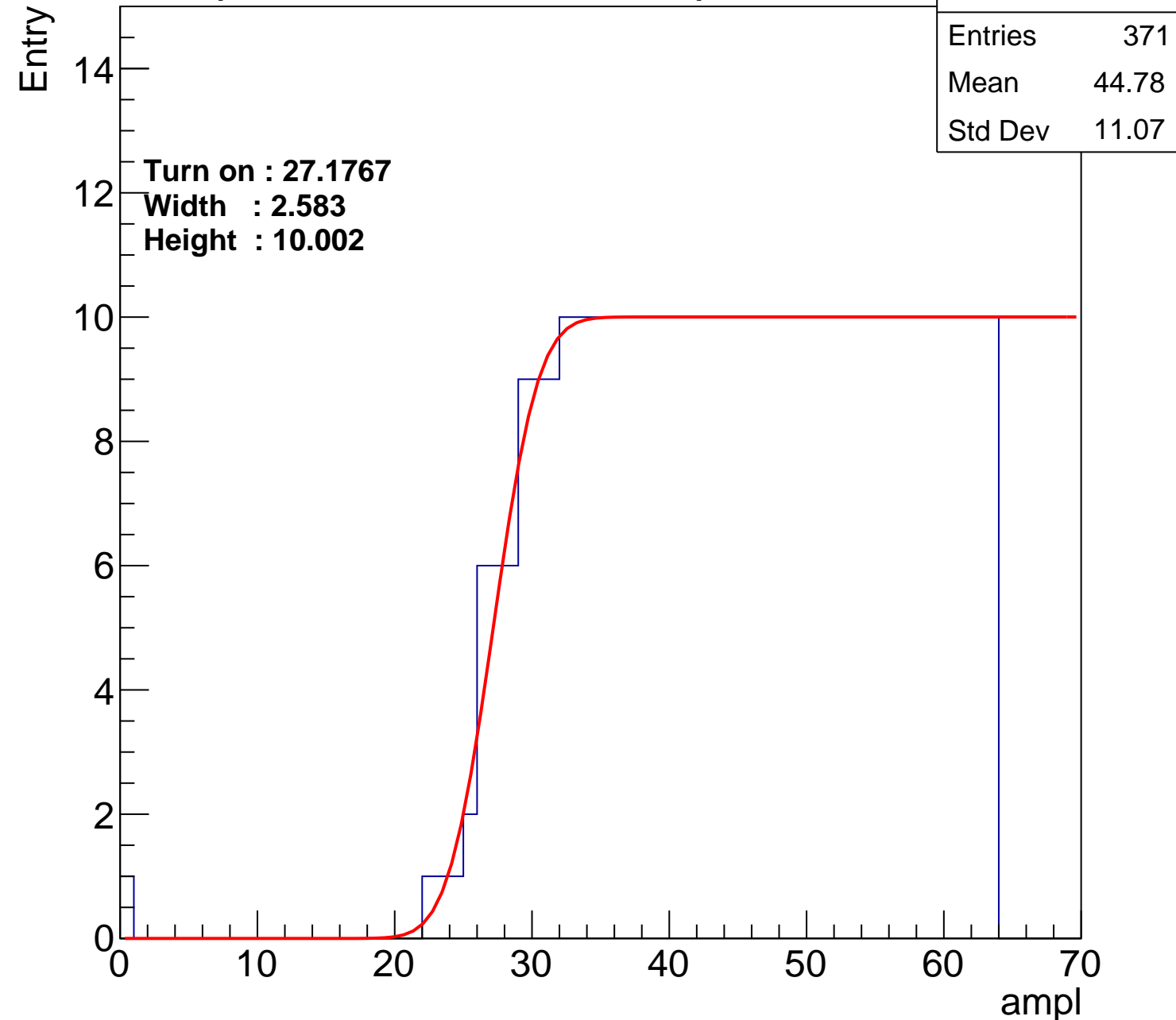
Width : 2.583

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.26
Std Dev	11.94

Turn on : 23.7300

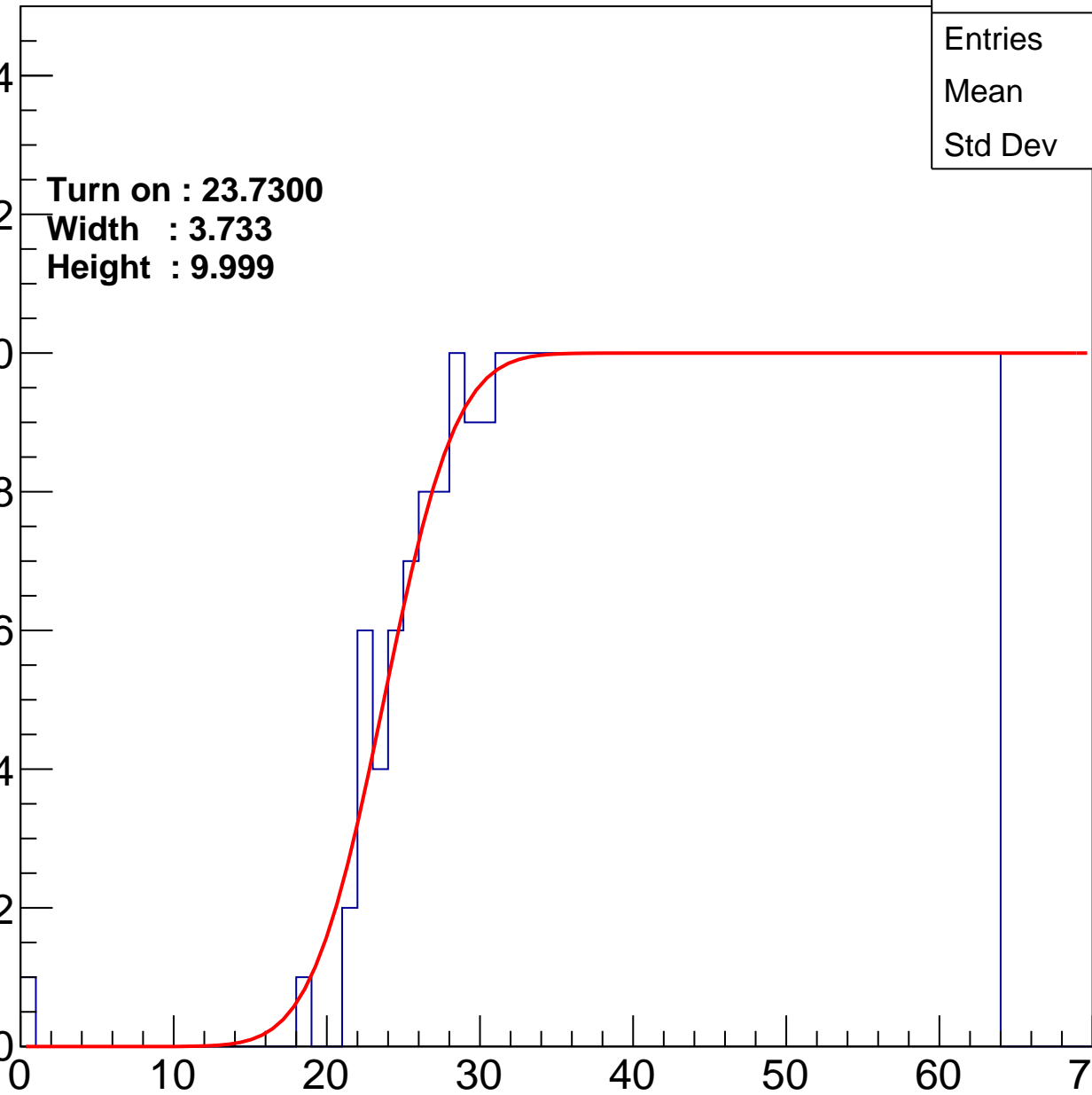
Width : 3.733

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 27.0005

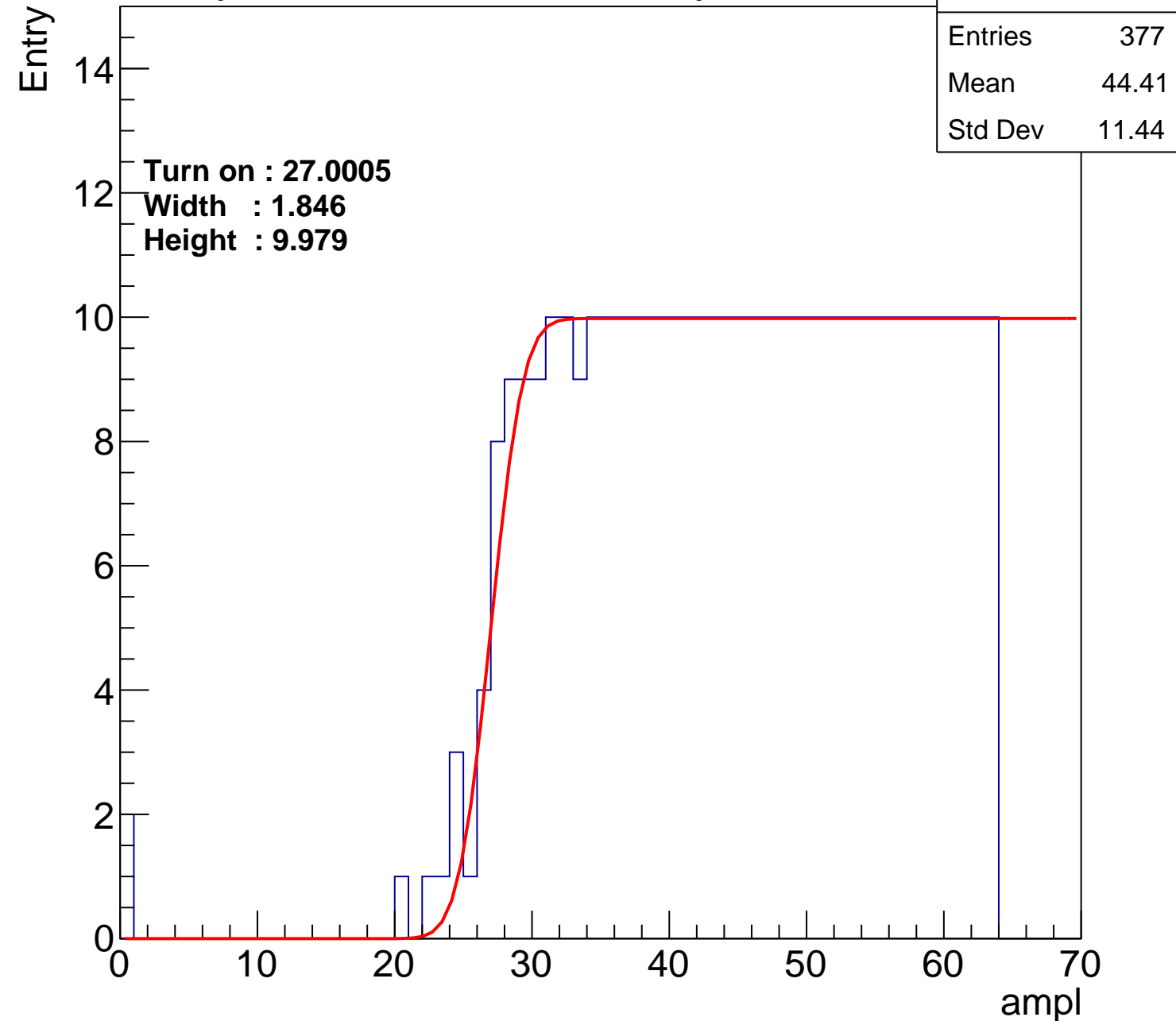
Width : 1.846

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch42

calib_packv5_042523_0143.root, FC#3, port B3

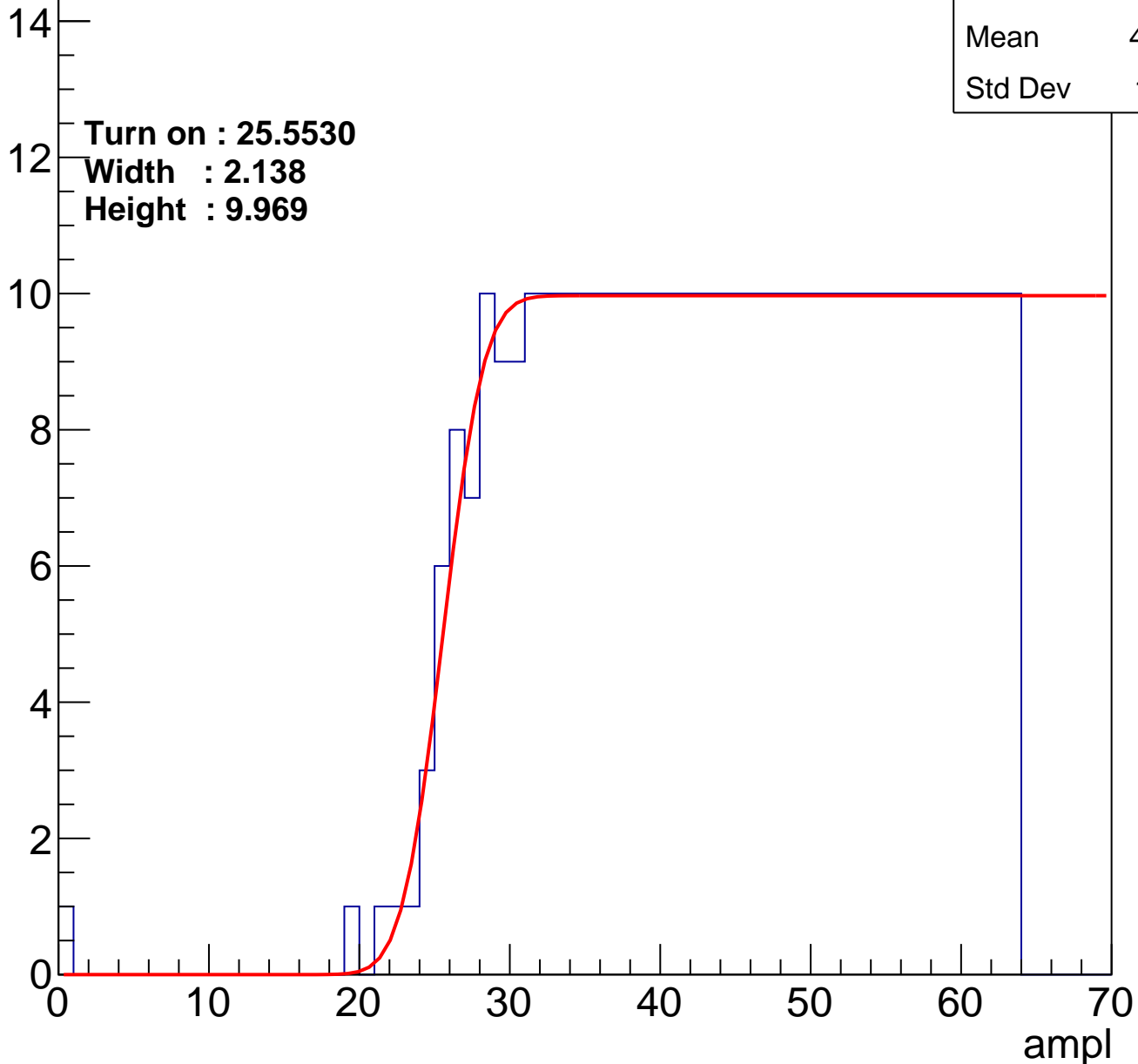
Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.5530

Width : 2.138

Height : 9.969

Entry



B1L000S, U5-ch43

calib_packv5_042523_0143.root, FC#3, port B3

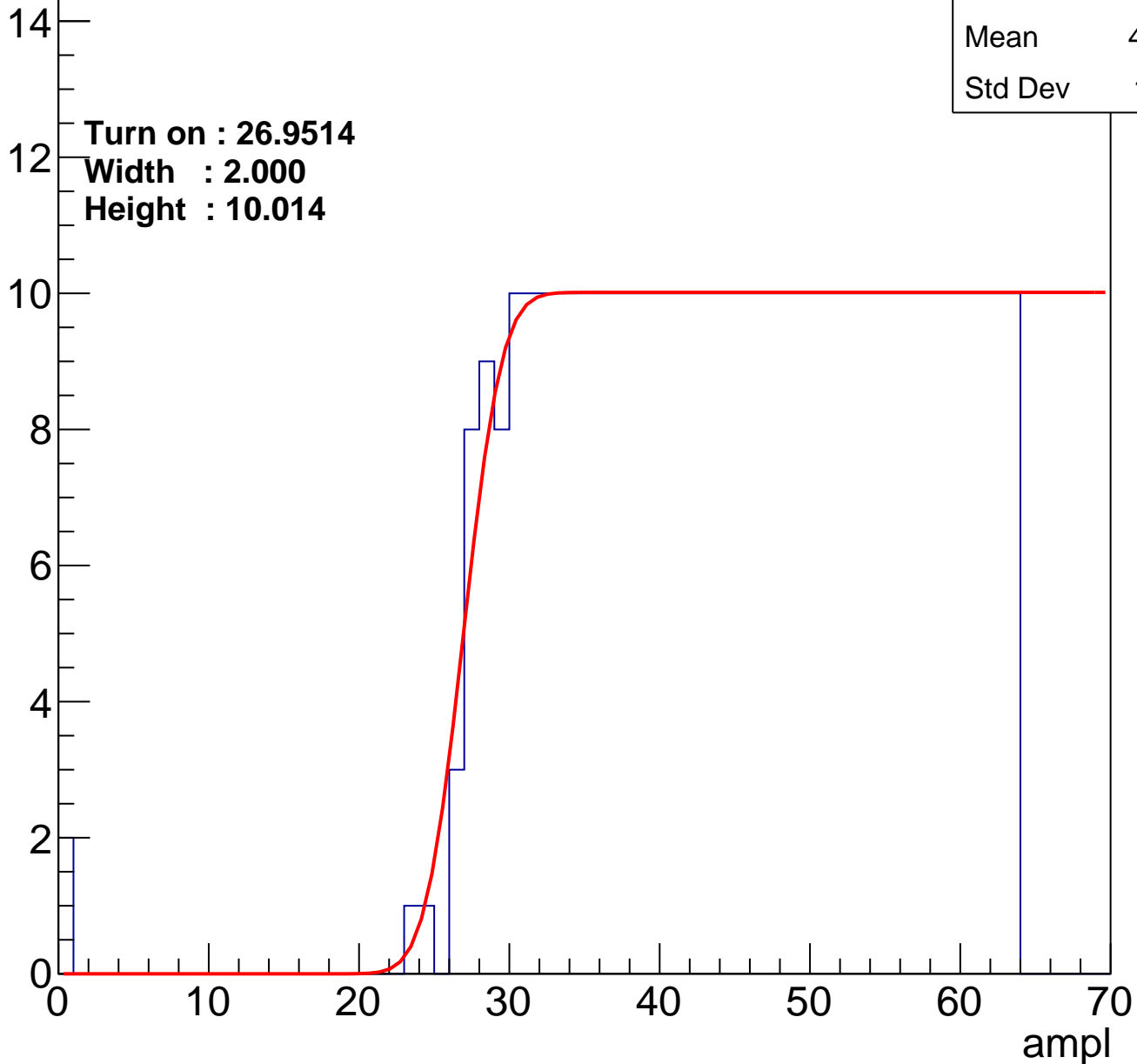
Entry

Entries	372
Mean	44.72
Std Dev	11.21

Turn on : 26.9514

Width : 2.000

Height : 10.014



B1L000S, U5-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.51
Std Dev	11.59

Turn on : 27.3572

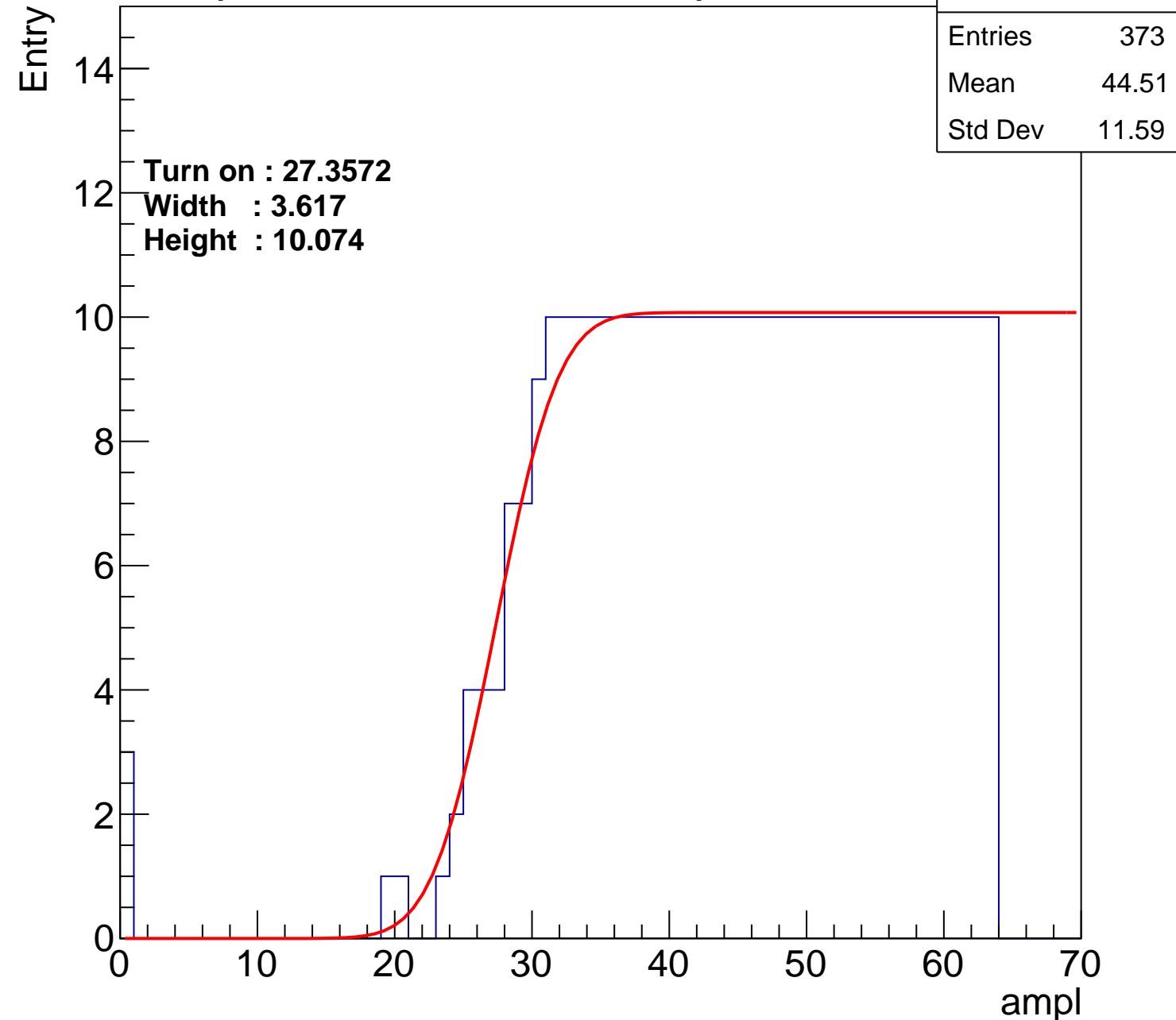
Width : 3.617

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
---------	-----

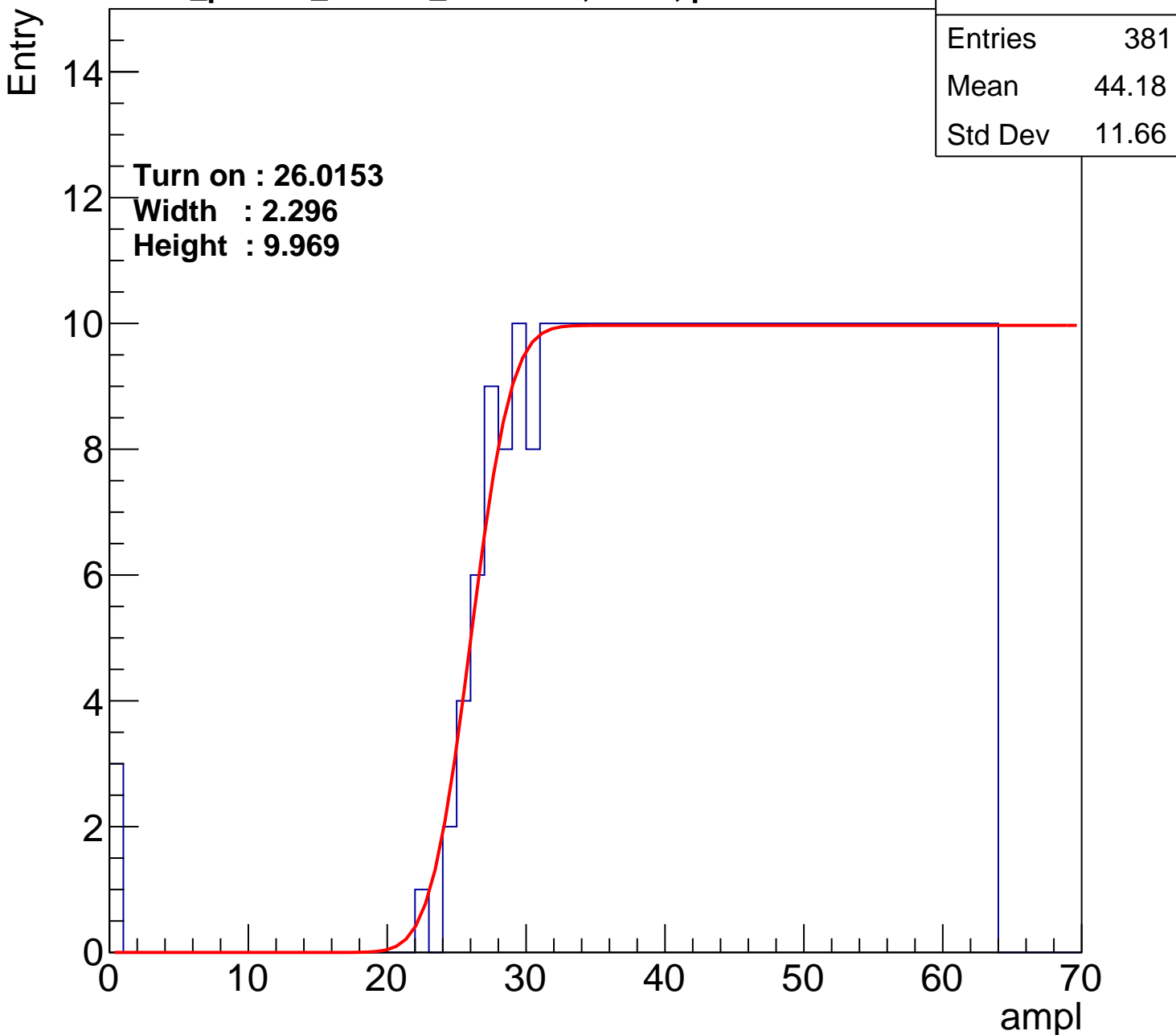
Mean	44.18
------	-------

Std Dev	11.66
---------	-------

Turn on : 26.0153

Width : 2.296

Height : 9.969



B1L000S, U5-ch46

calib_packv5_042523_0143.root, FC#3, port B3

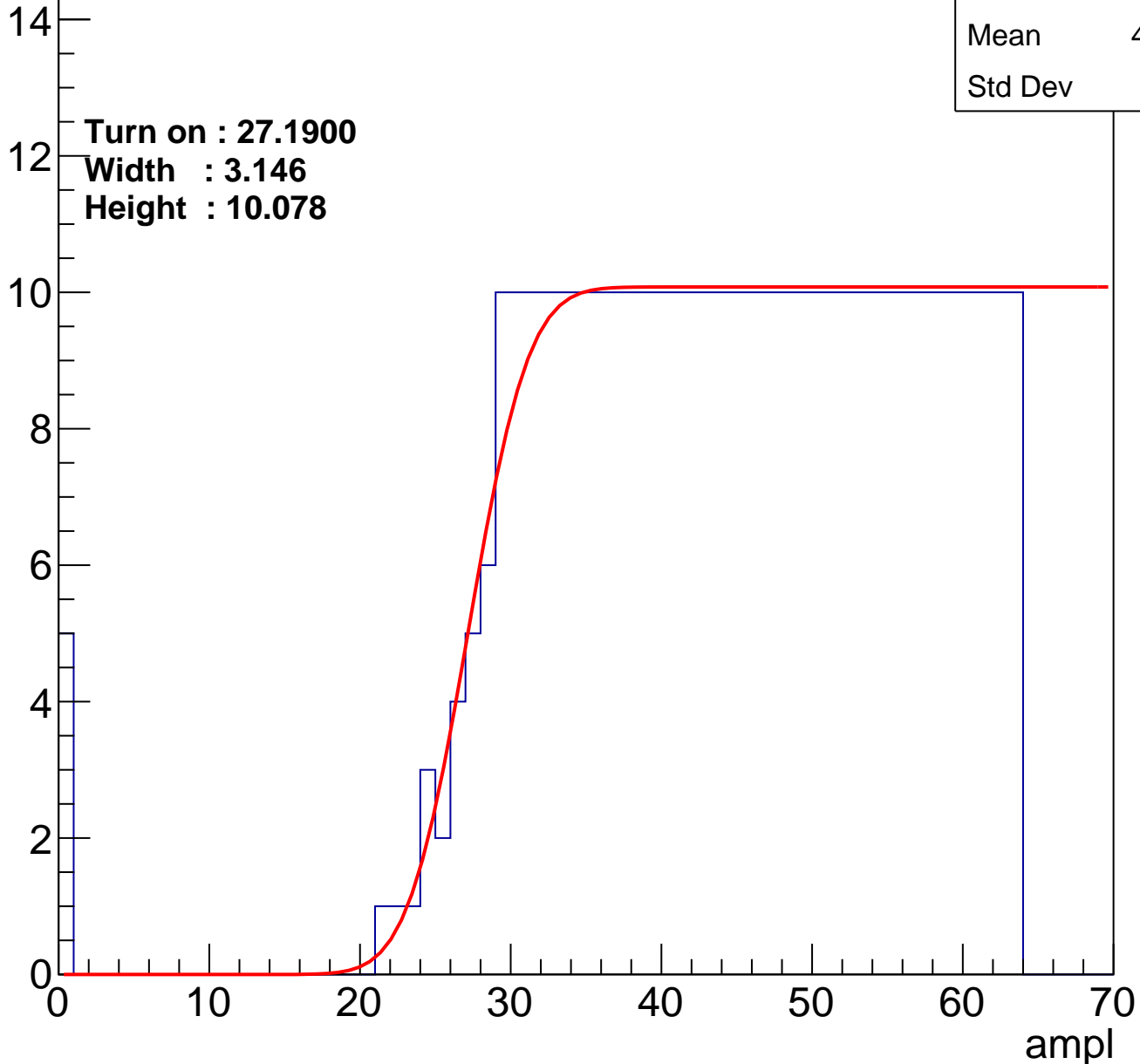
Entries	378
Mean	44.17
Std Dev	12

Turn on : 27.1900

Width : 3.146

Height : 10.078

Entry



B1L000S, U5-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.37
Std Dev	11.39

Turn on : 25.3404

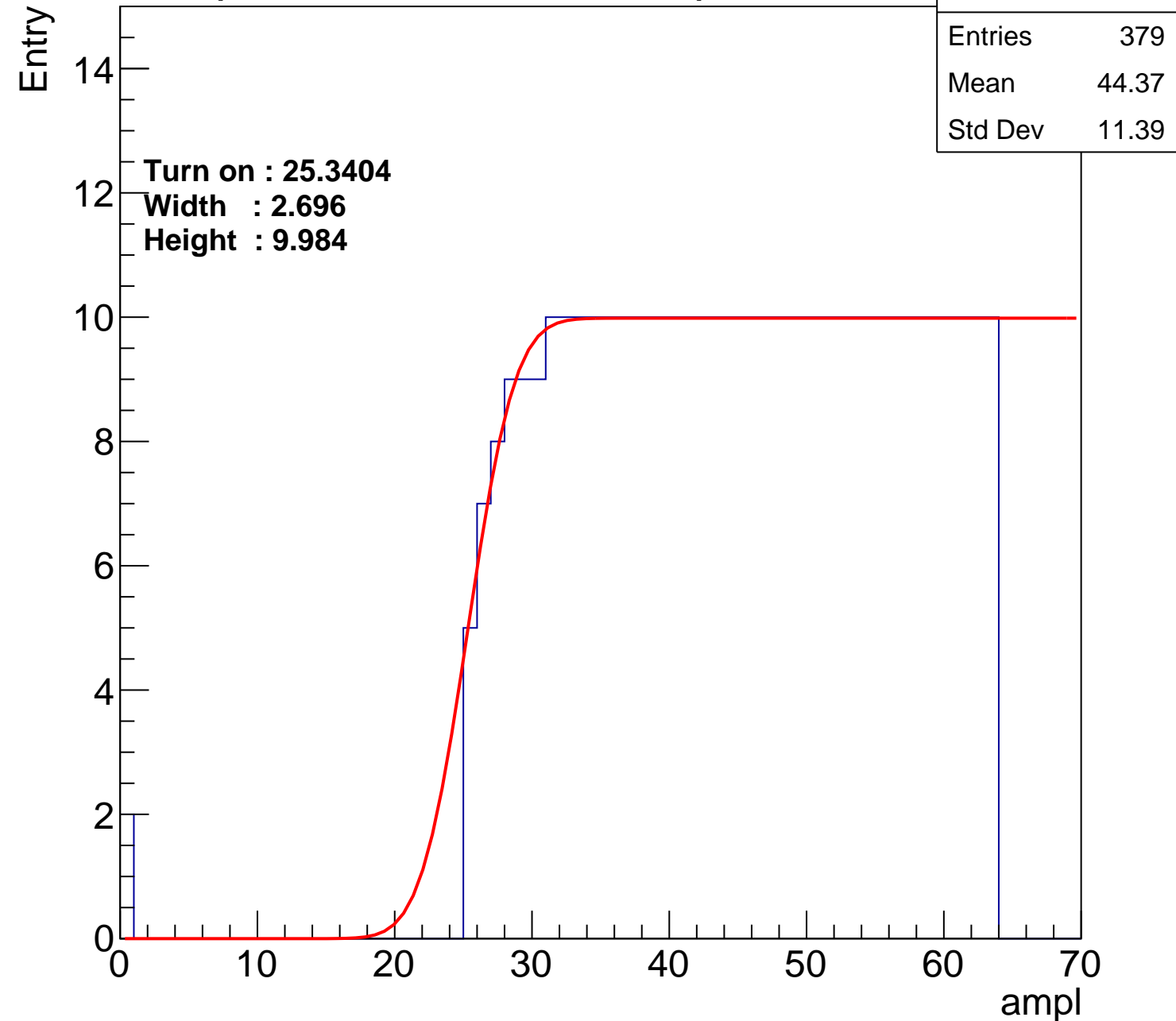
Width : 2.696

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.59

Turn on : 26.6478

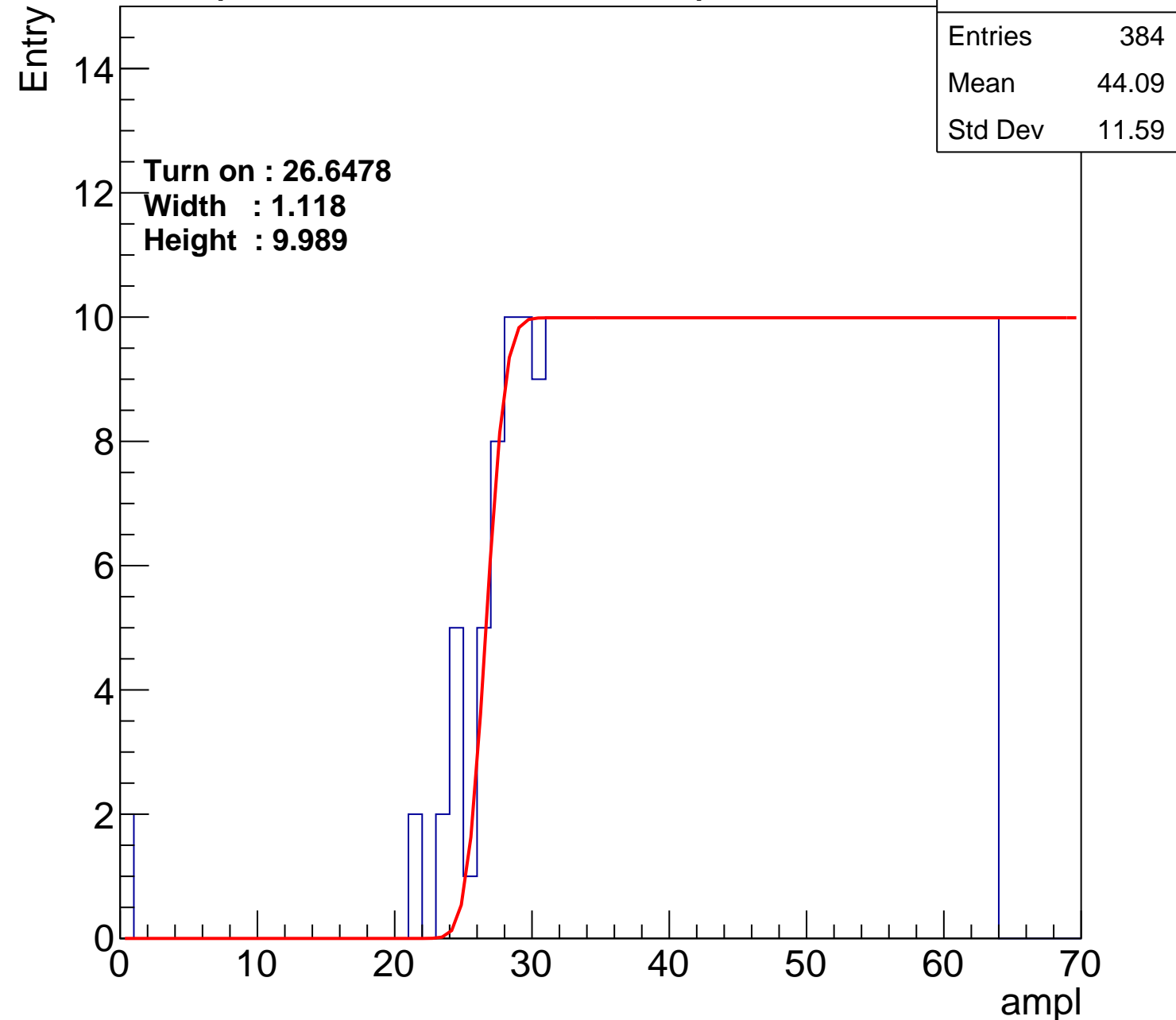
Width : 1.118

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.65
Std Dev	11.38

Turn on : 27.7333

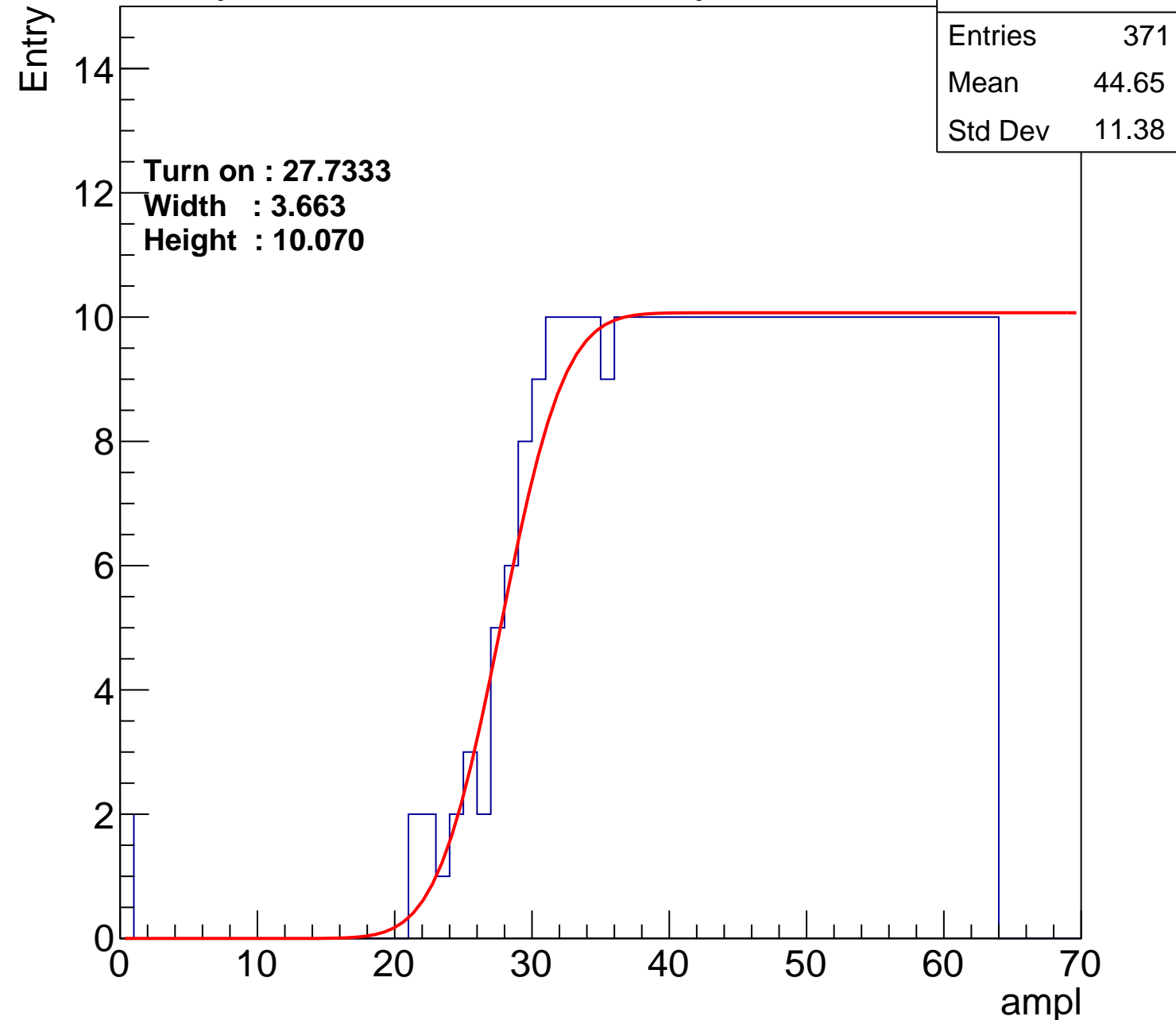
Width : 3.663

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 25.7313

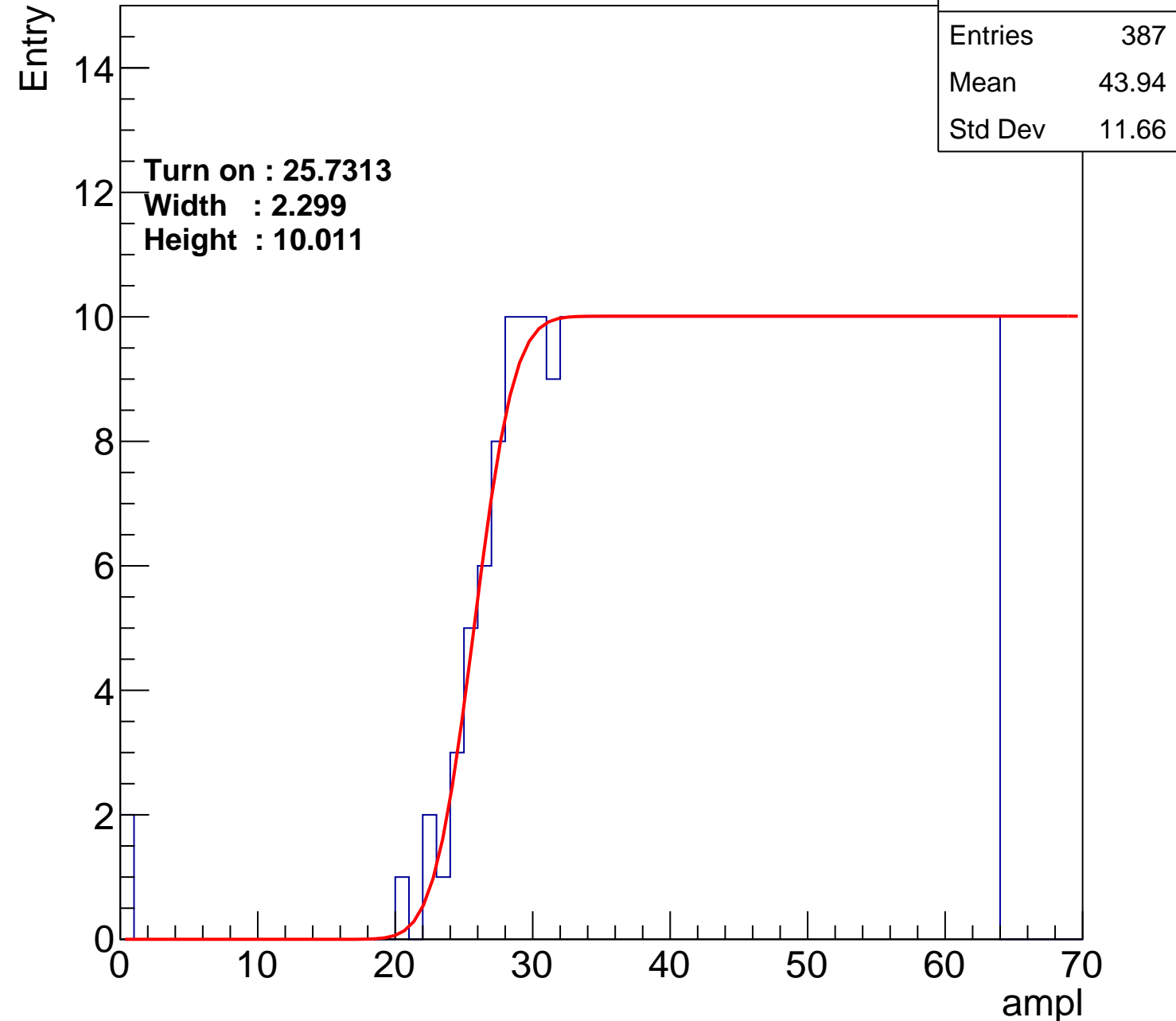
Width : 2.299

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.97
Std Dev	11.84

Turn on : 26.1689

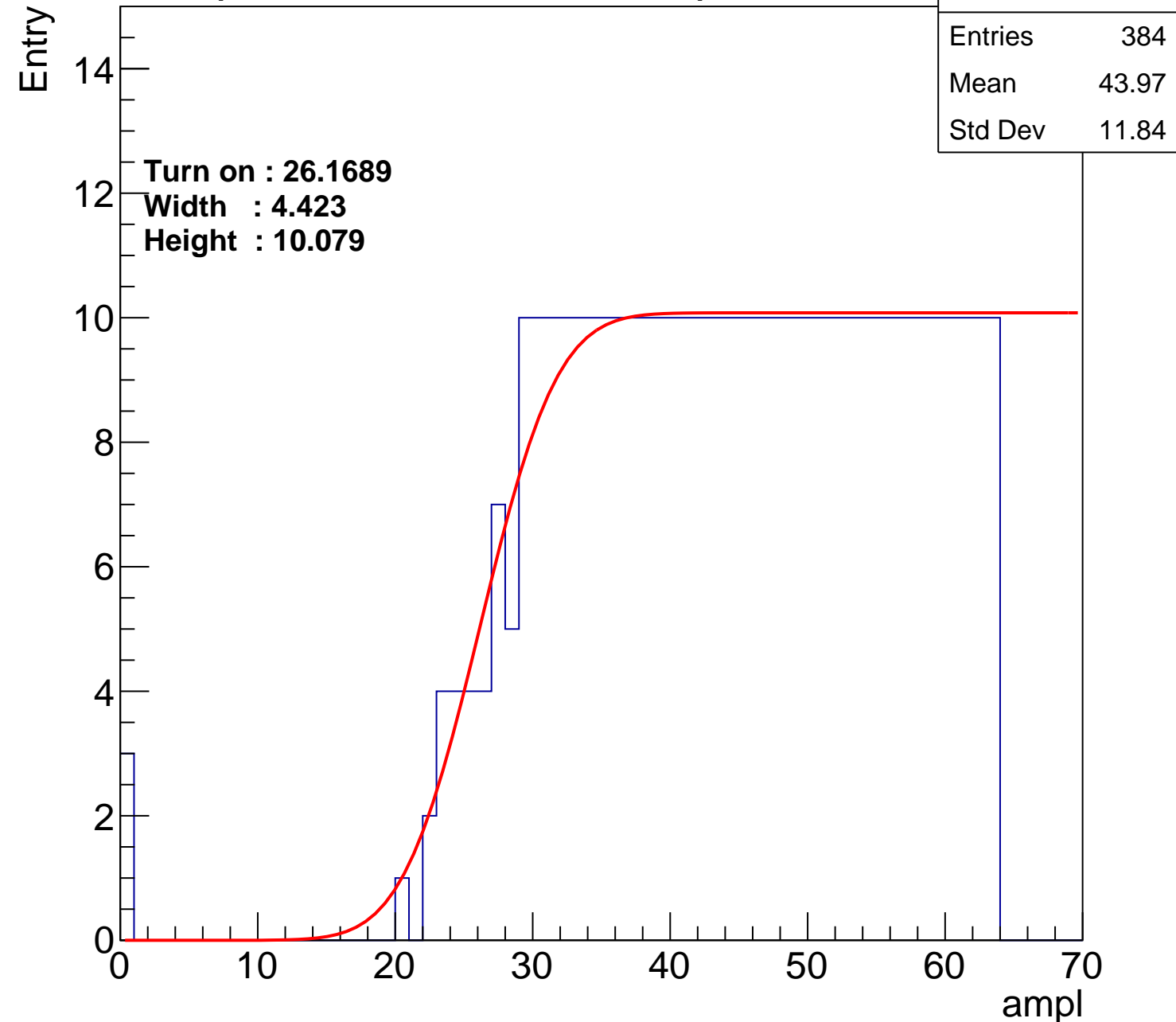
Width : 4.423

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	44.9
Std Dev	11.57

Turn on : 28.5771

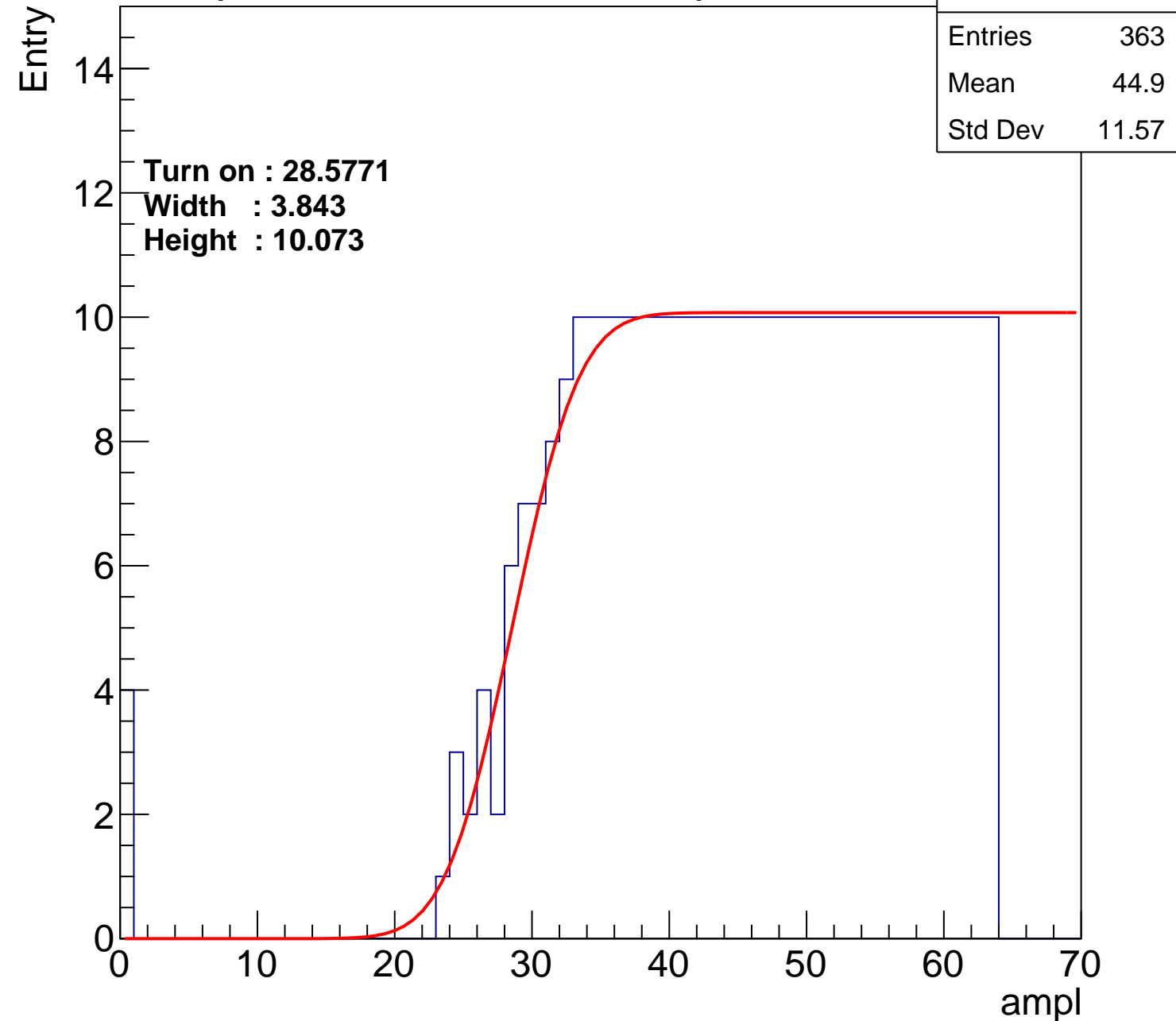
Width : 3.843

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.4797

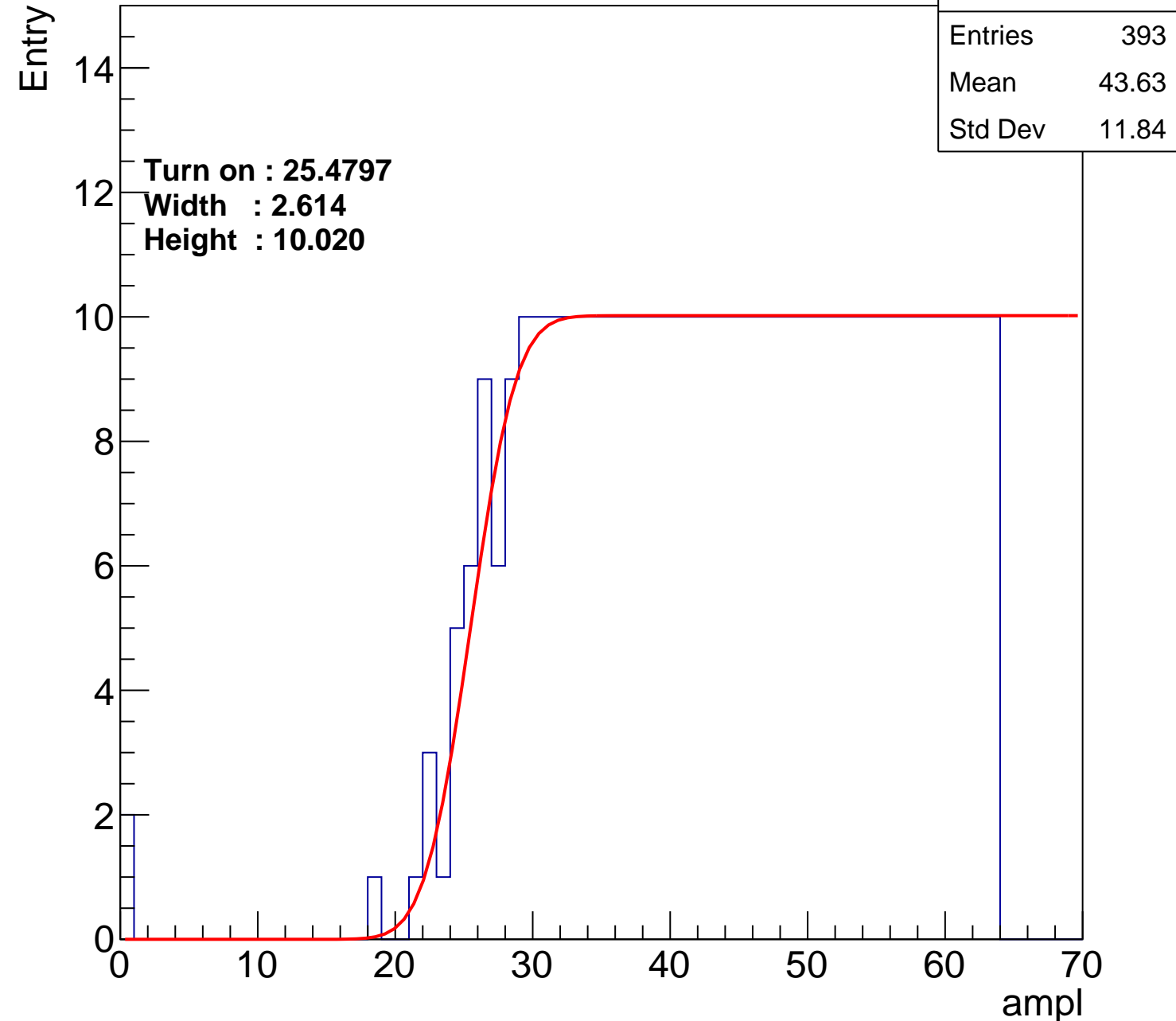
Width : 2.614

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.41
Std Dev	12.32

Turn on : 25.4940

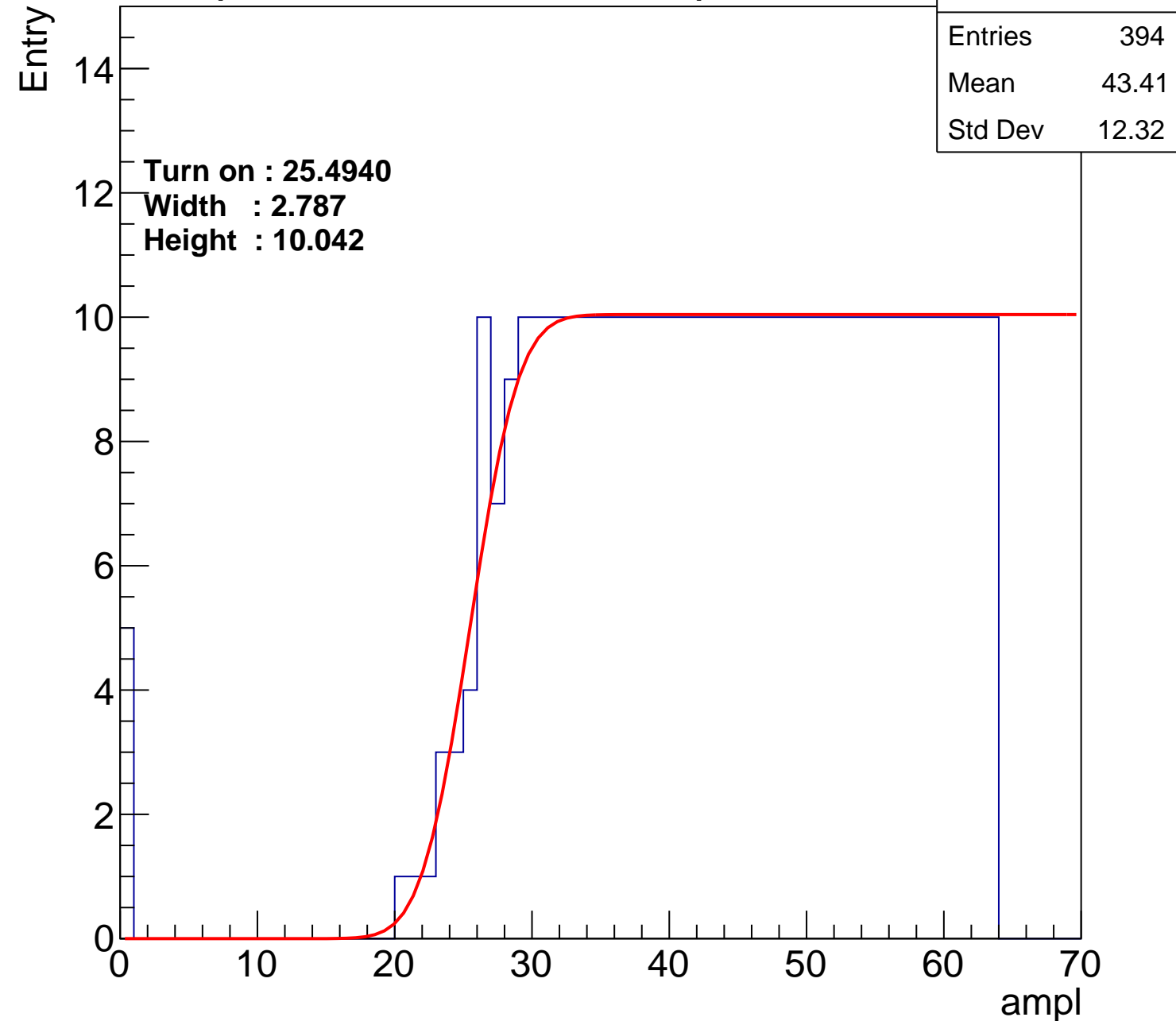
Width : 2.787

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.0448

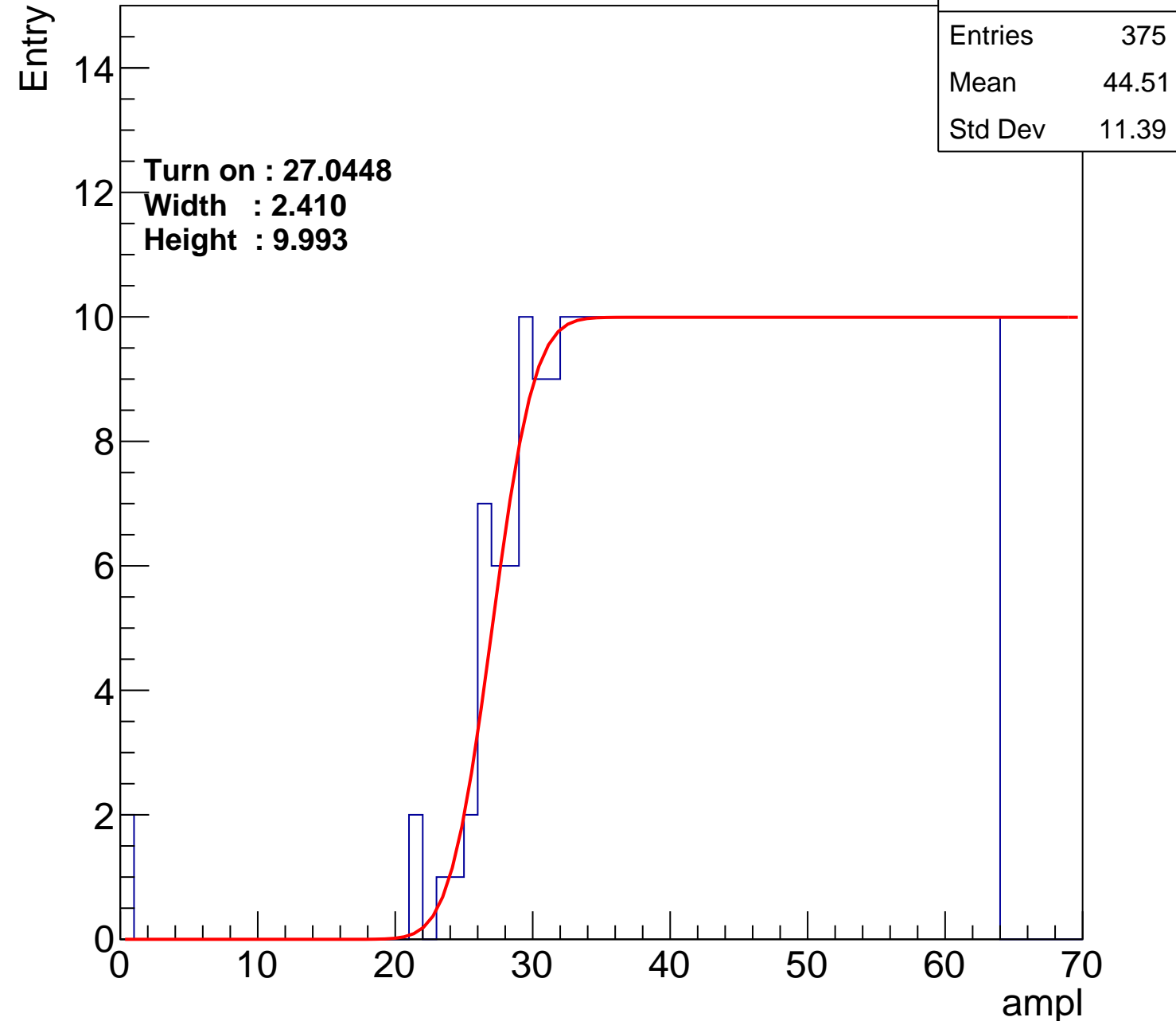
Width : 2.410

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch56

calib_packv5_042523_0143.root, FC#3, port B3

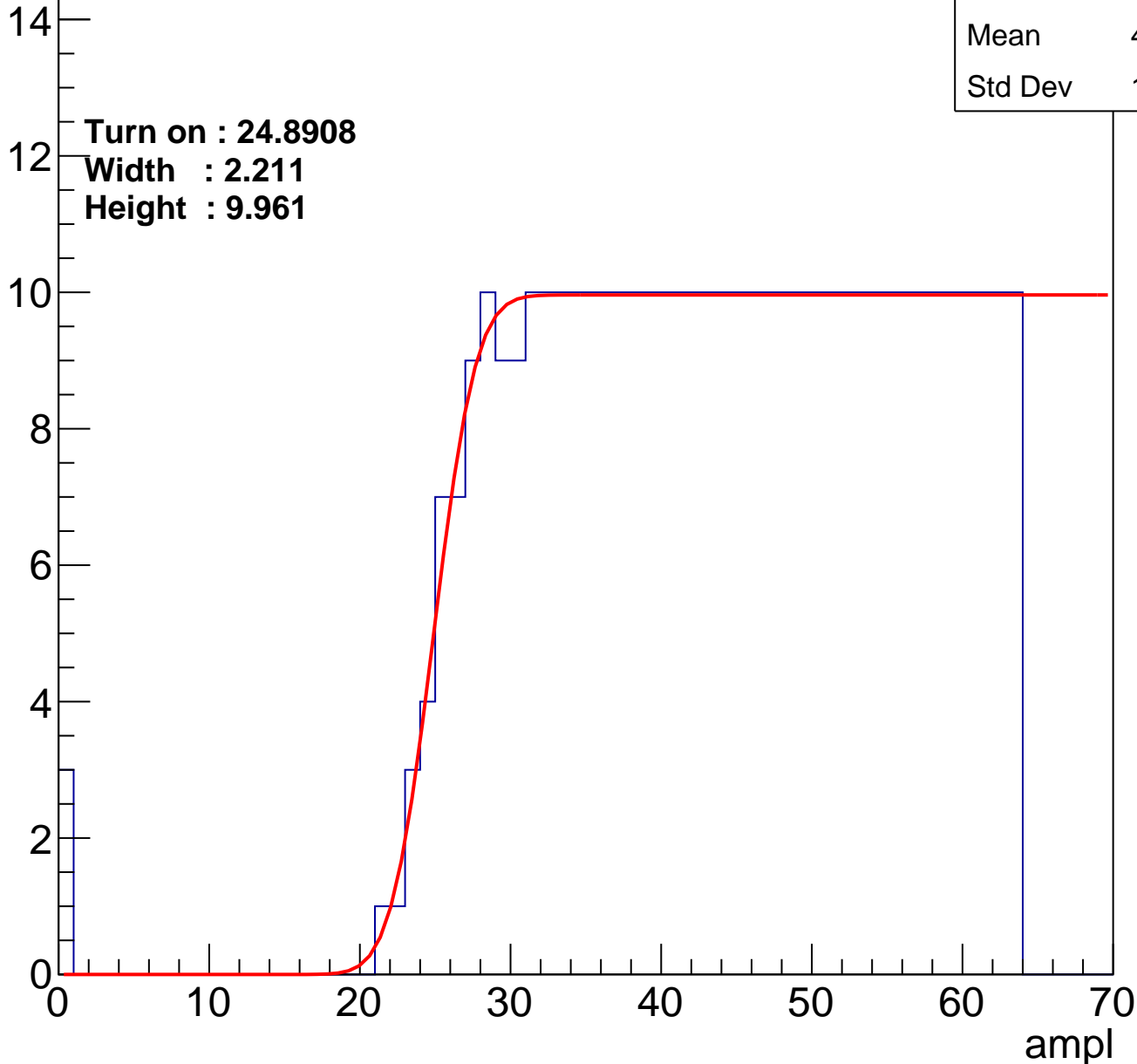
Entry

Entries	393
Mean	43.59
Std Dev	11.97

Turn on : 24.8908

Width : 2.211

Height : 9.961



B1L000S, U5-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.14
Std Dev	11.54

Turn on : 25.8377

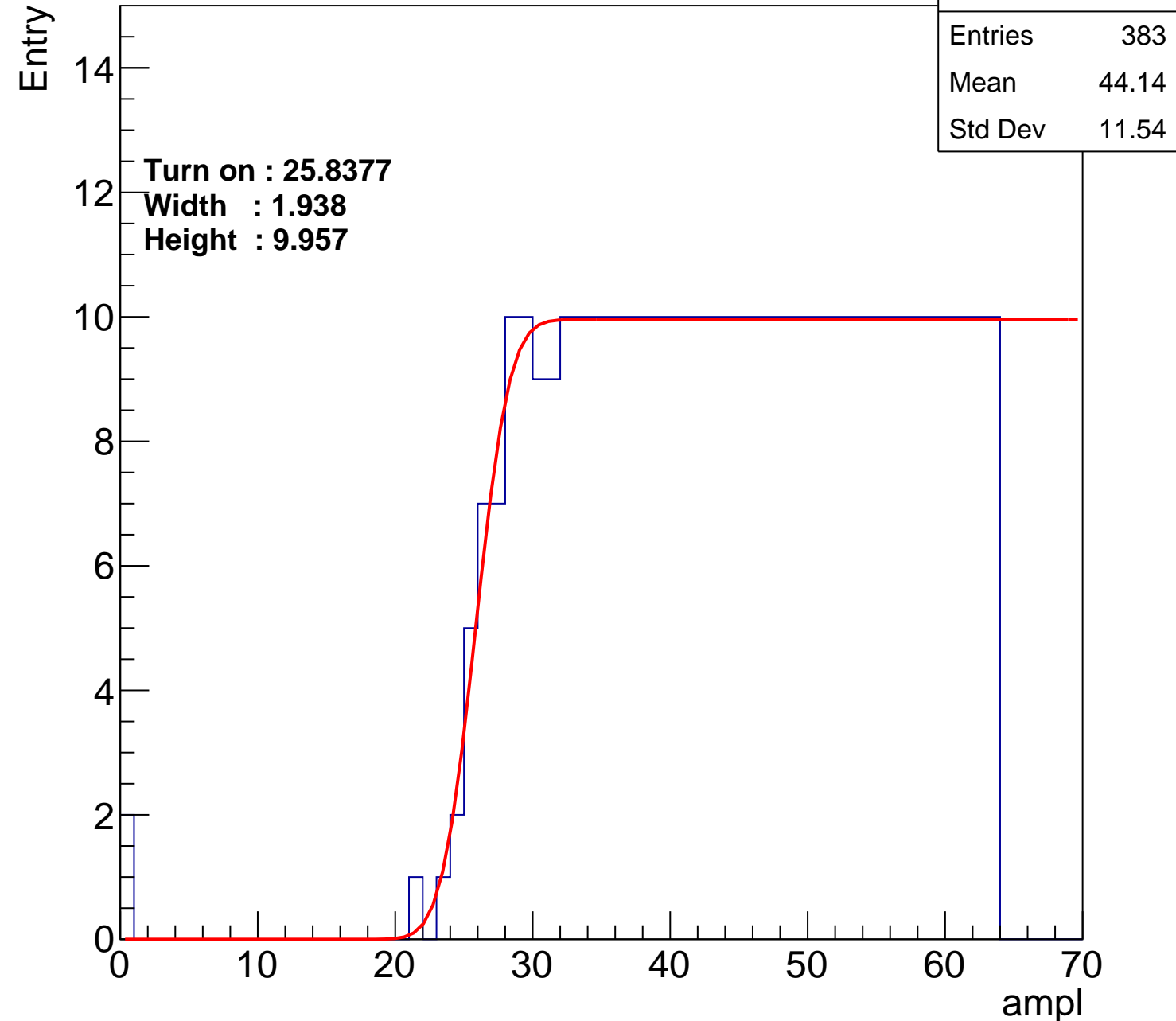
Width : 1.938

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch58

calib_packv5_042523_0143.root, FC#3, port B3

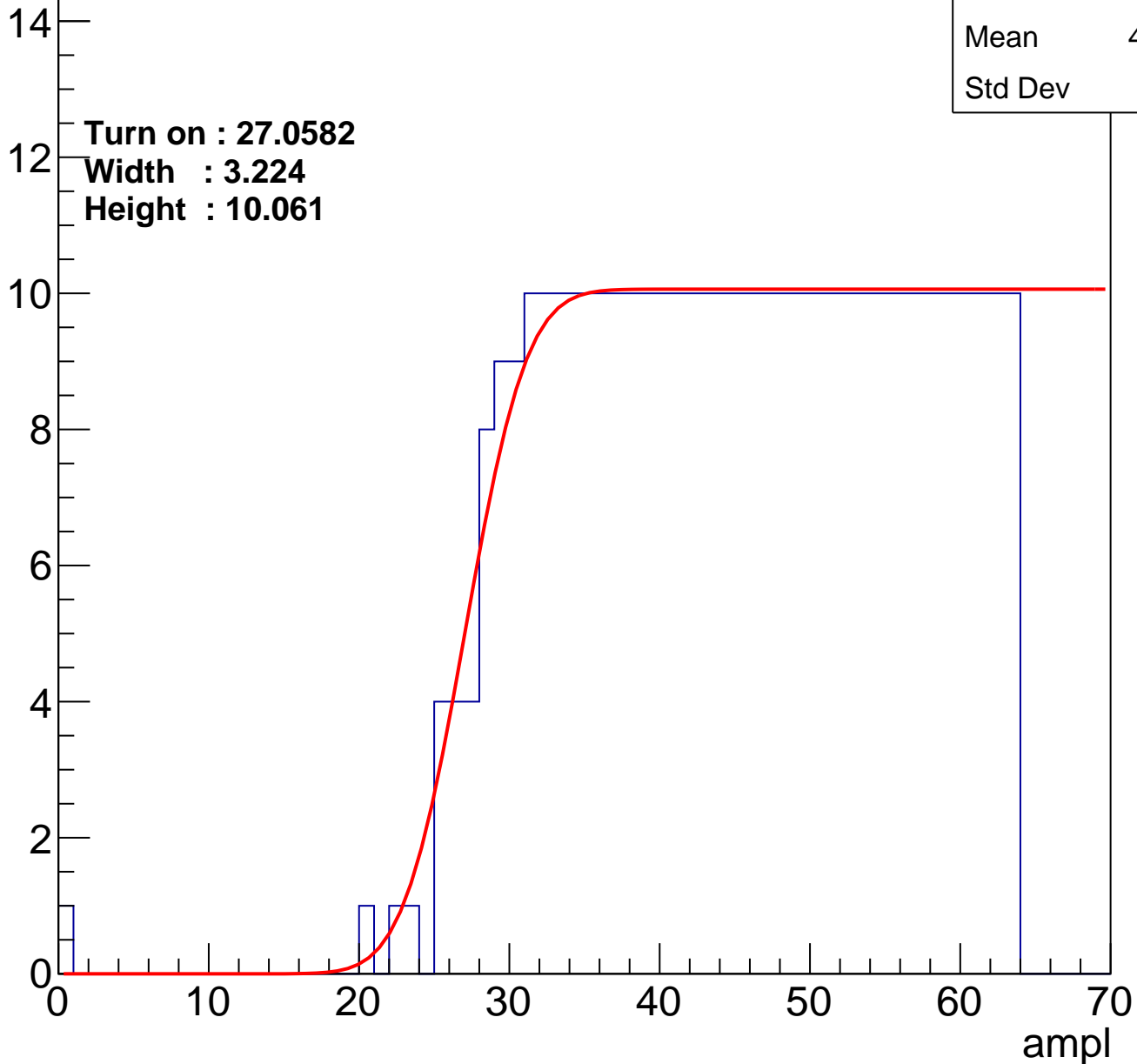
Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 27.0582

Width : 3.224

Height : 10.061

Entry



B1L000S, U5-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.12
Std Dev	12.35

Turn on : 25.2376

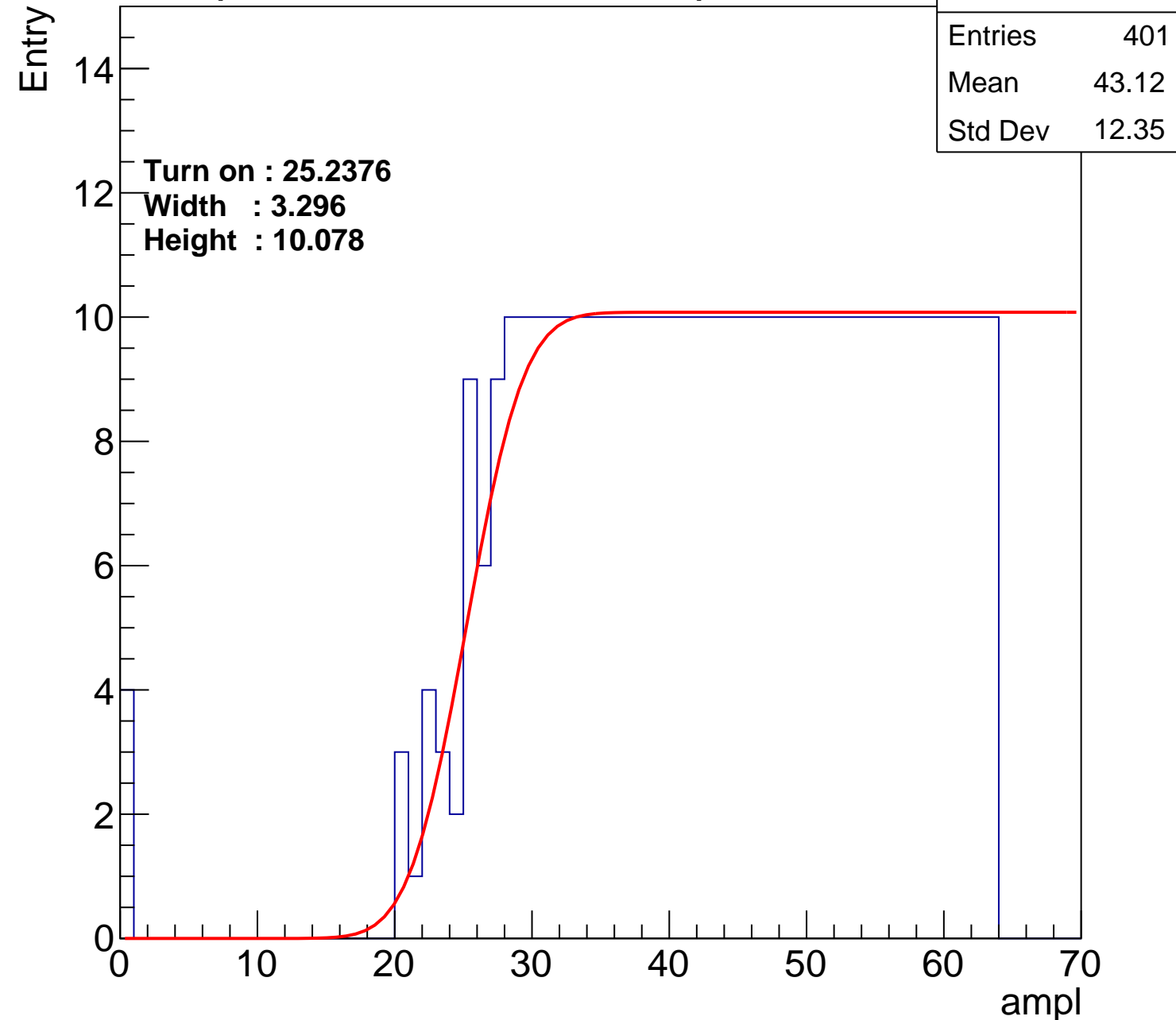
Width : 3.296

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.56
Std Dev	12.02

Turn on : 25.6140

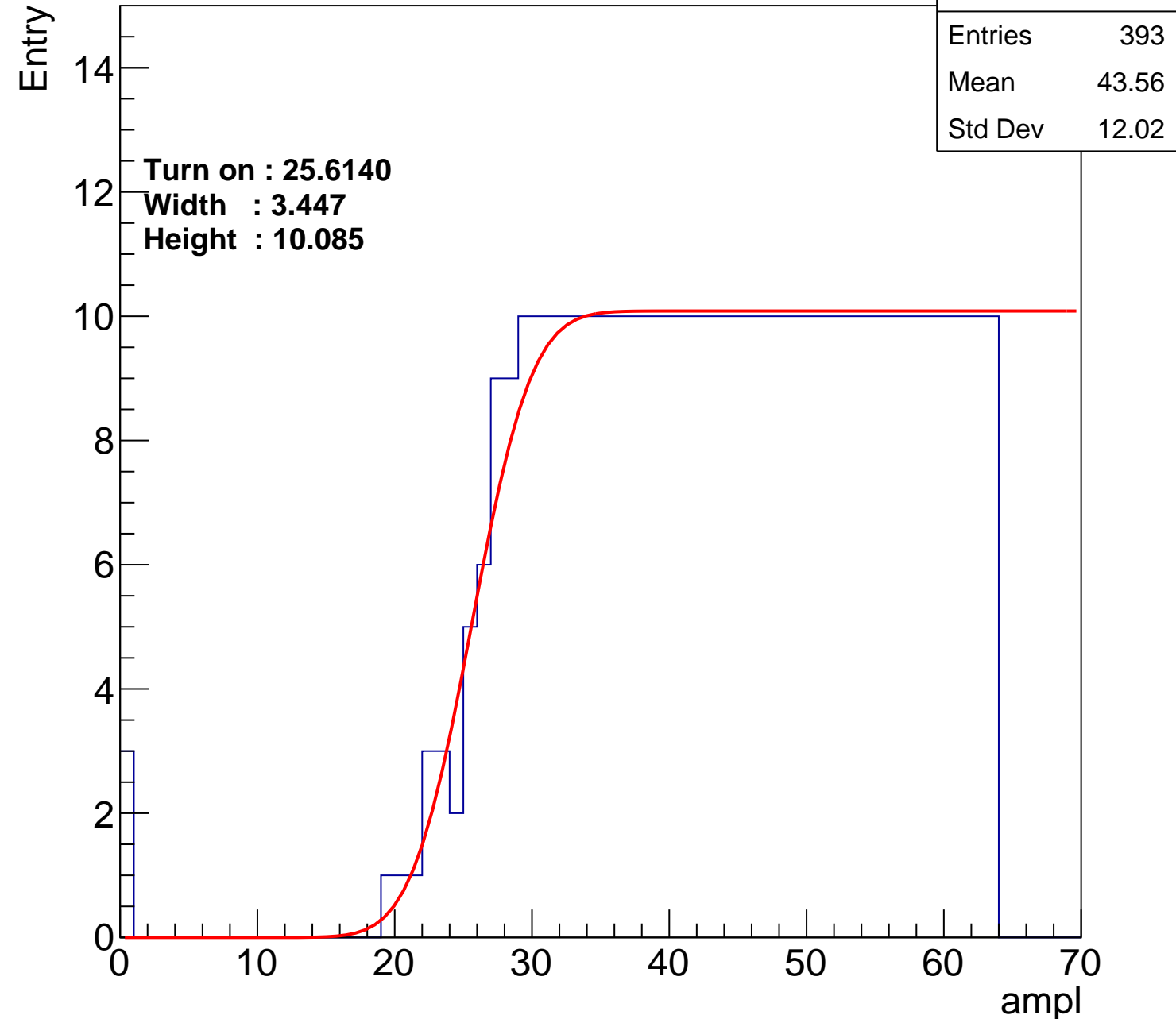
Width : 3.447

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.02
Std Dev	12.28

Turn on : 24.0638

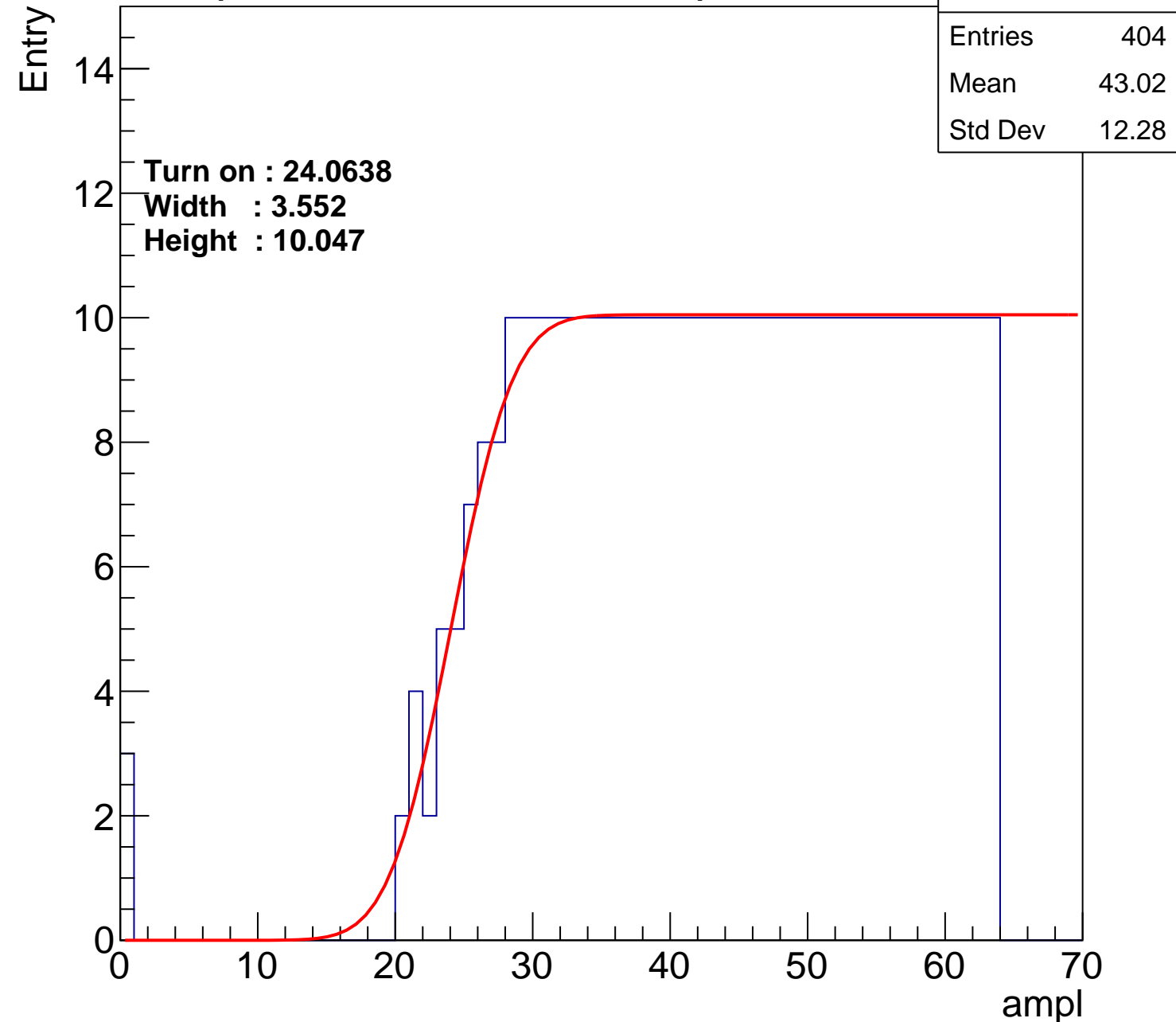
Width : 3.552

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	42.99
Std Dev	12.24

Turn on : 24.0393

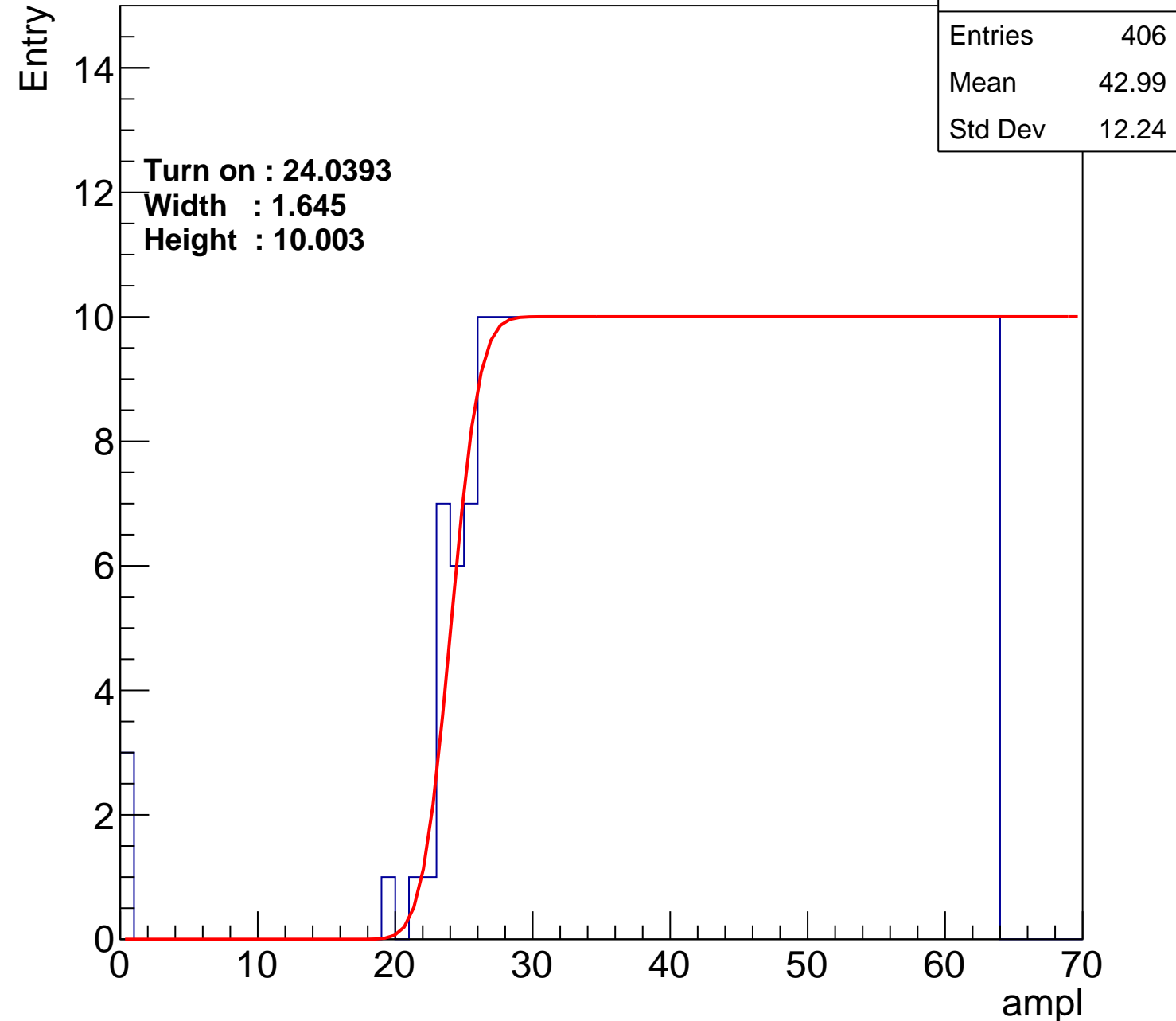
Width : 1.645

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch63

calib_packv5_042523_0143.root, FC#3, port B3

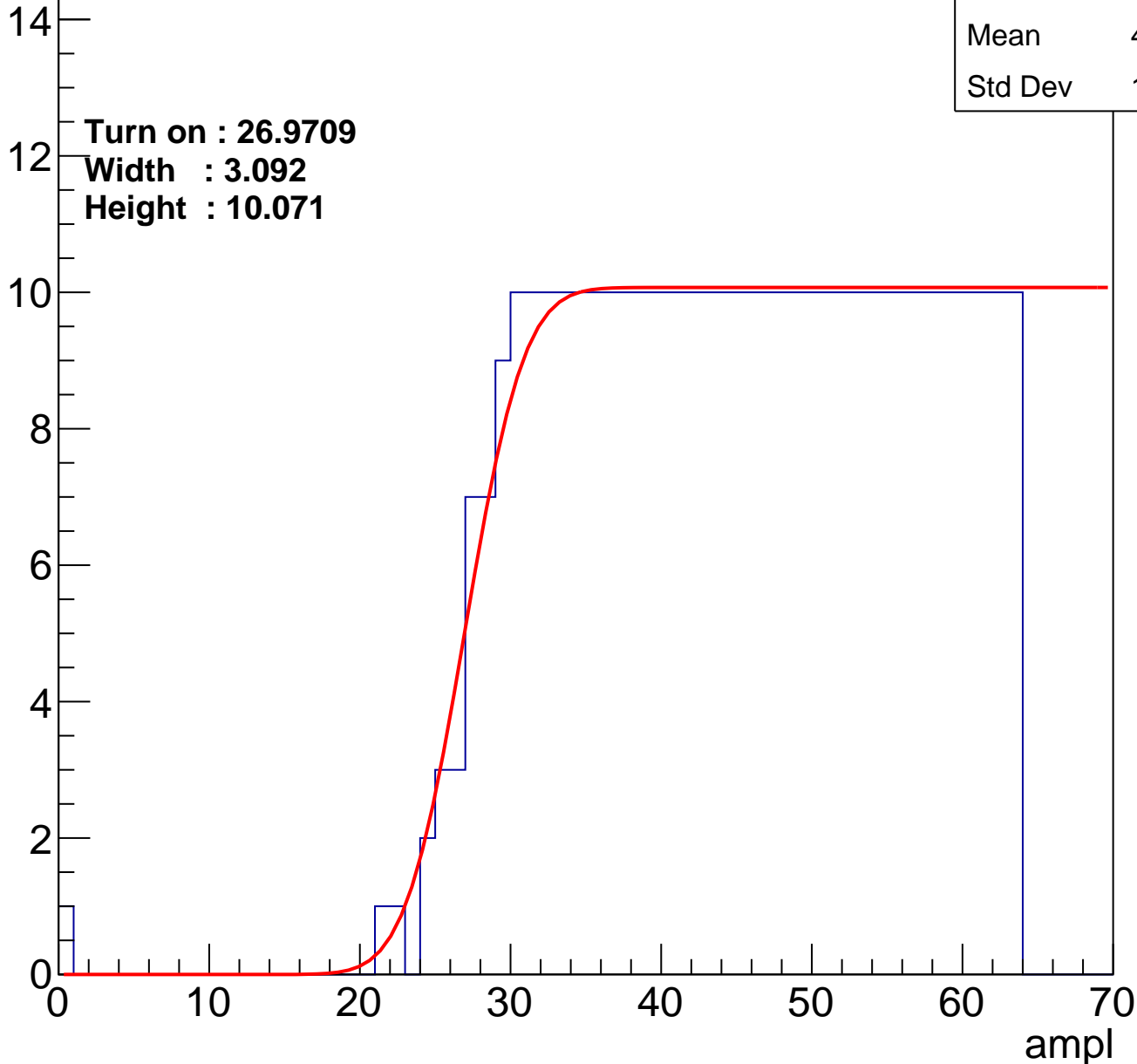
Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.9709

Width : 3.092

Height : 10.071

Entry



B1L000S, U5-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 26.7977

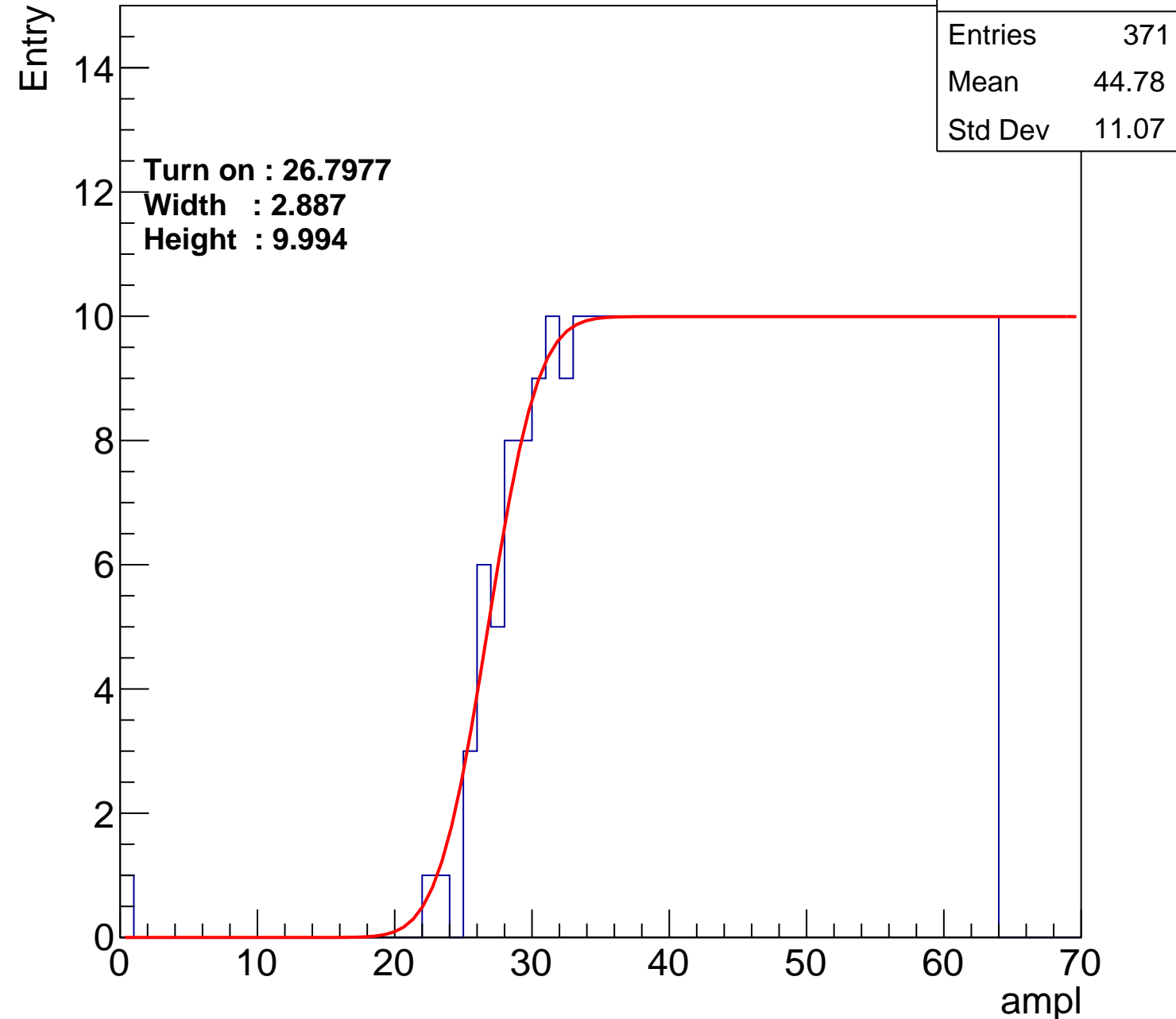
Width : 2.887

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.32
Std Dev	11.3

Turn on : 25.0977

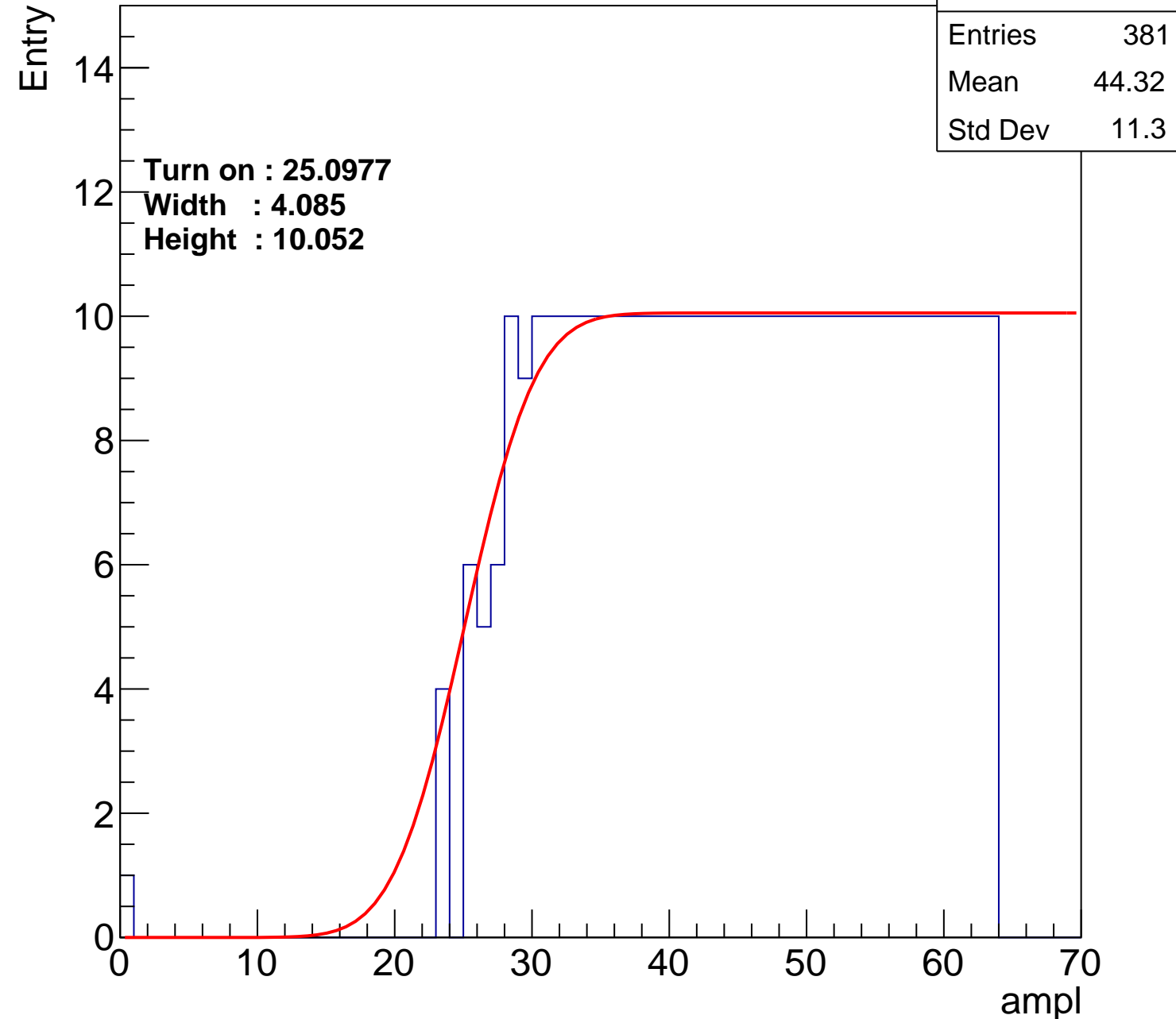
Width : 4.085

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.6586

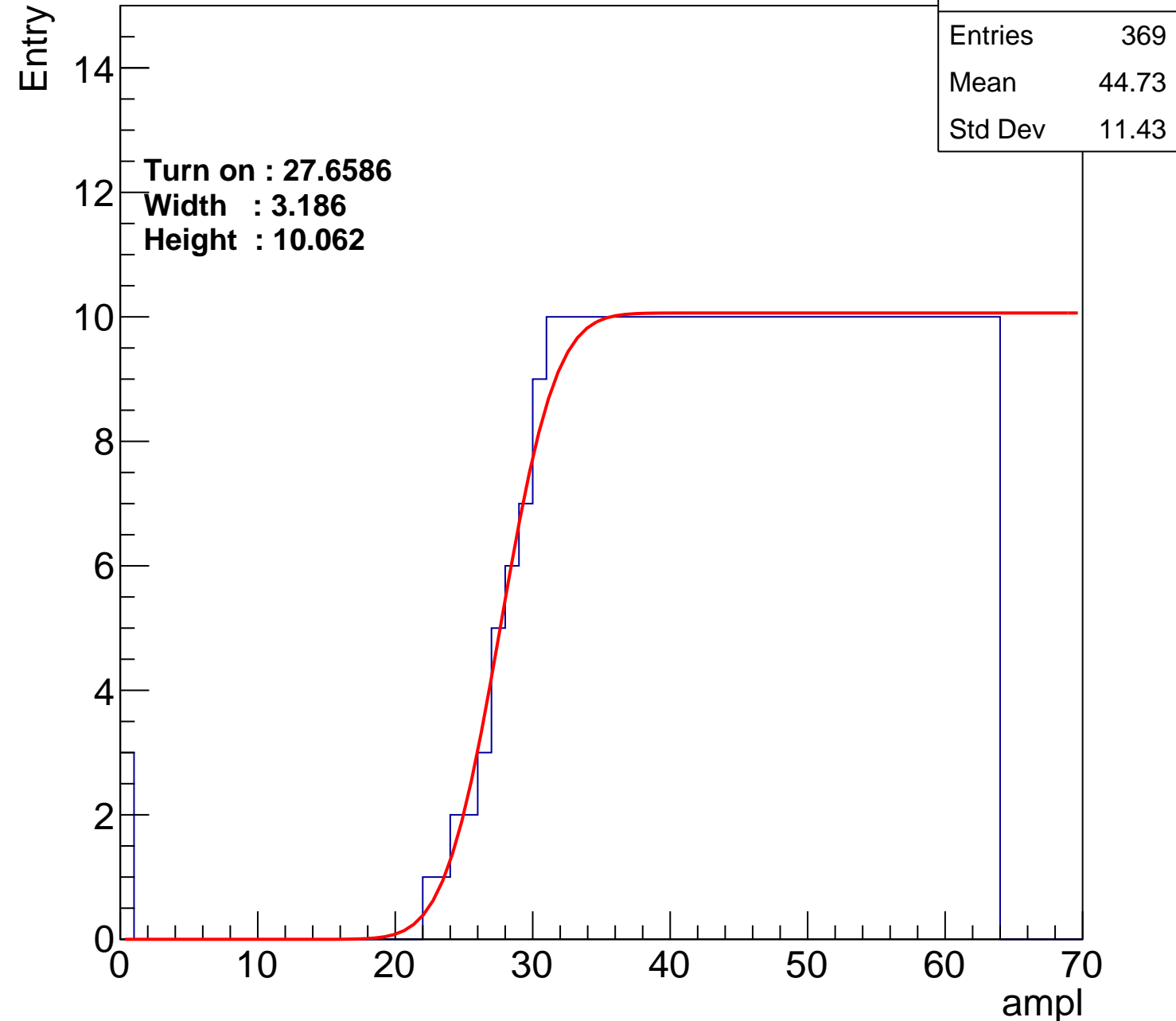
Width : 3.186

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 26.4797

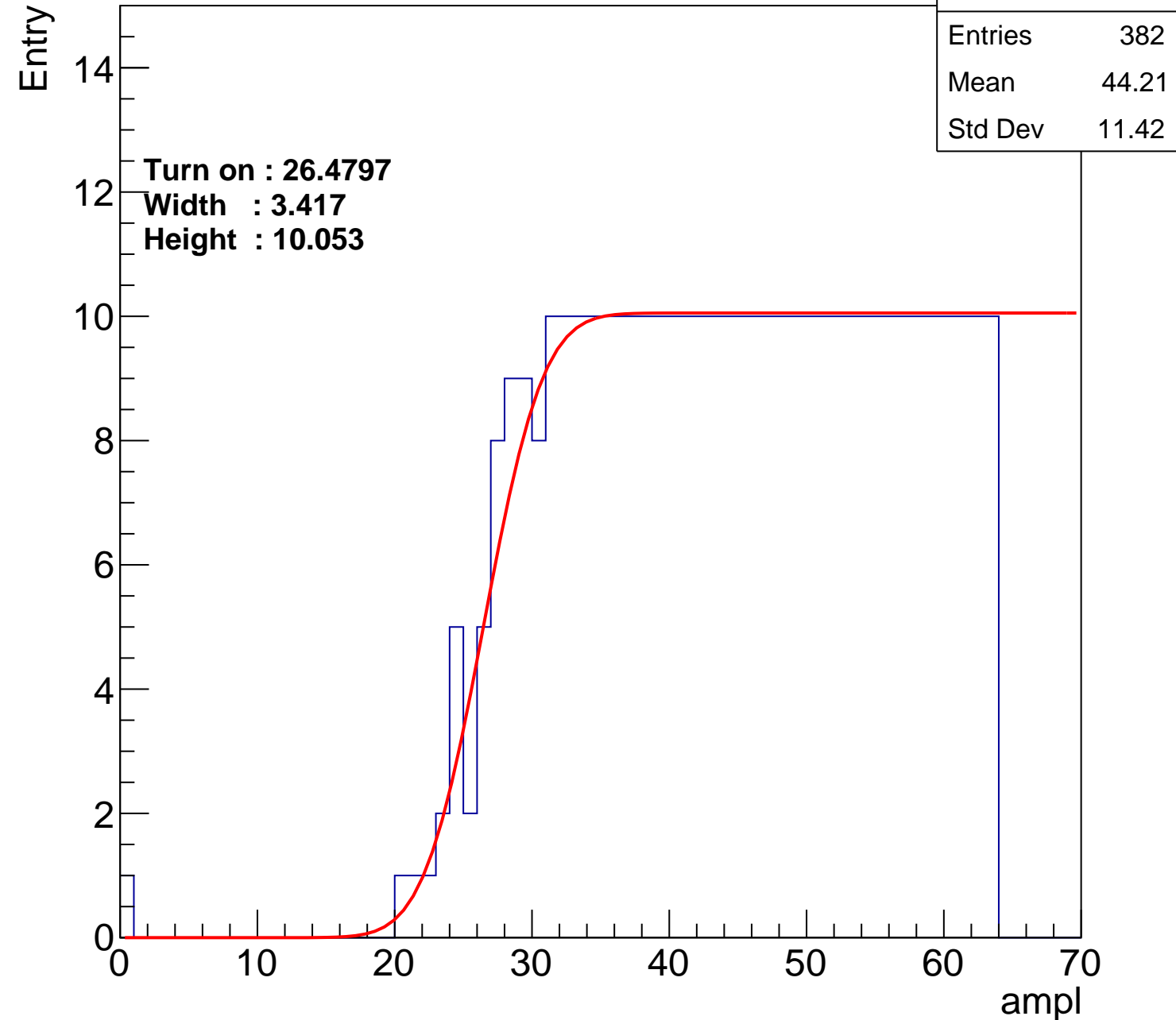
Width : 3.417

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 25.8995

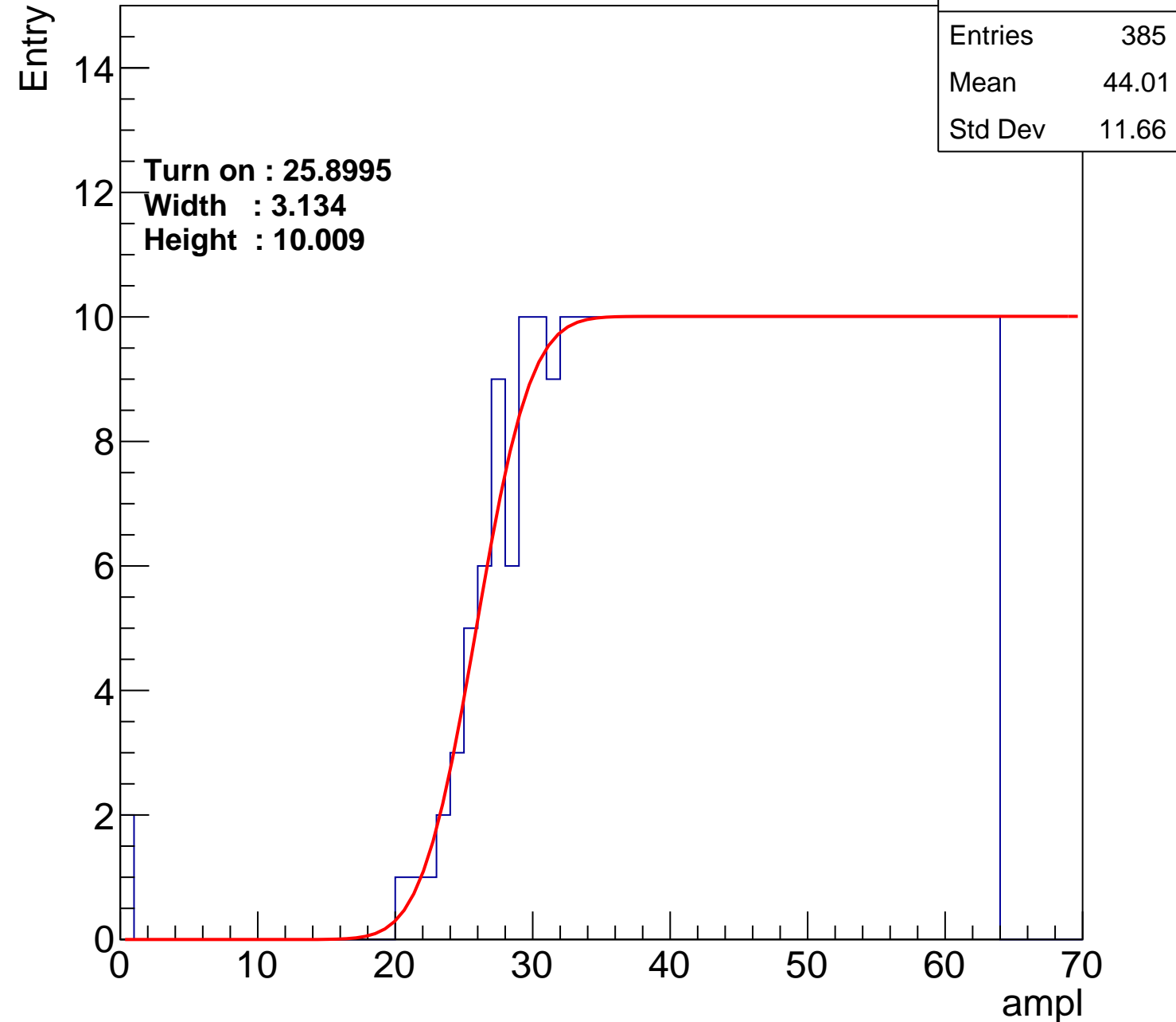
Width : 3.134

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch69

calib_packv5_042523_0143.root, FC#3, port B3

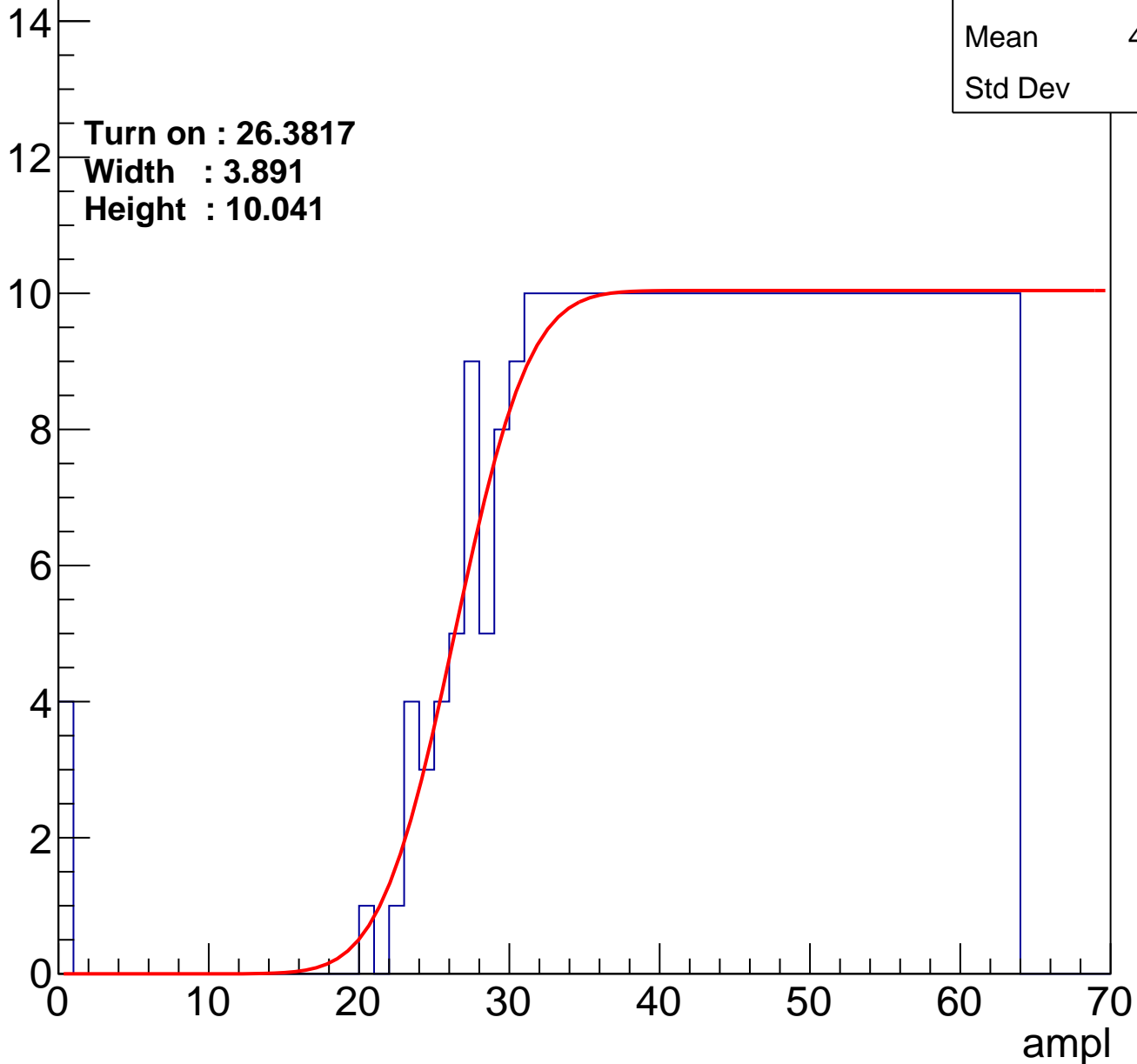
Entries	383
Mean	43.95
Std Dev	12

Turn on : 26.3817

Width : 3.891

Height : 10.041

Entry



B1L000S, U5-ch70

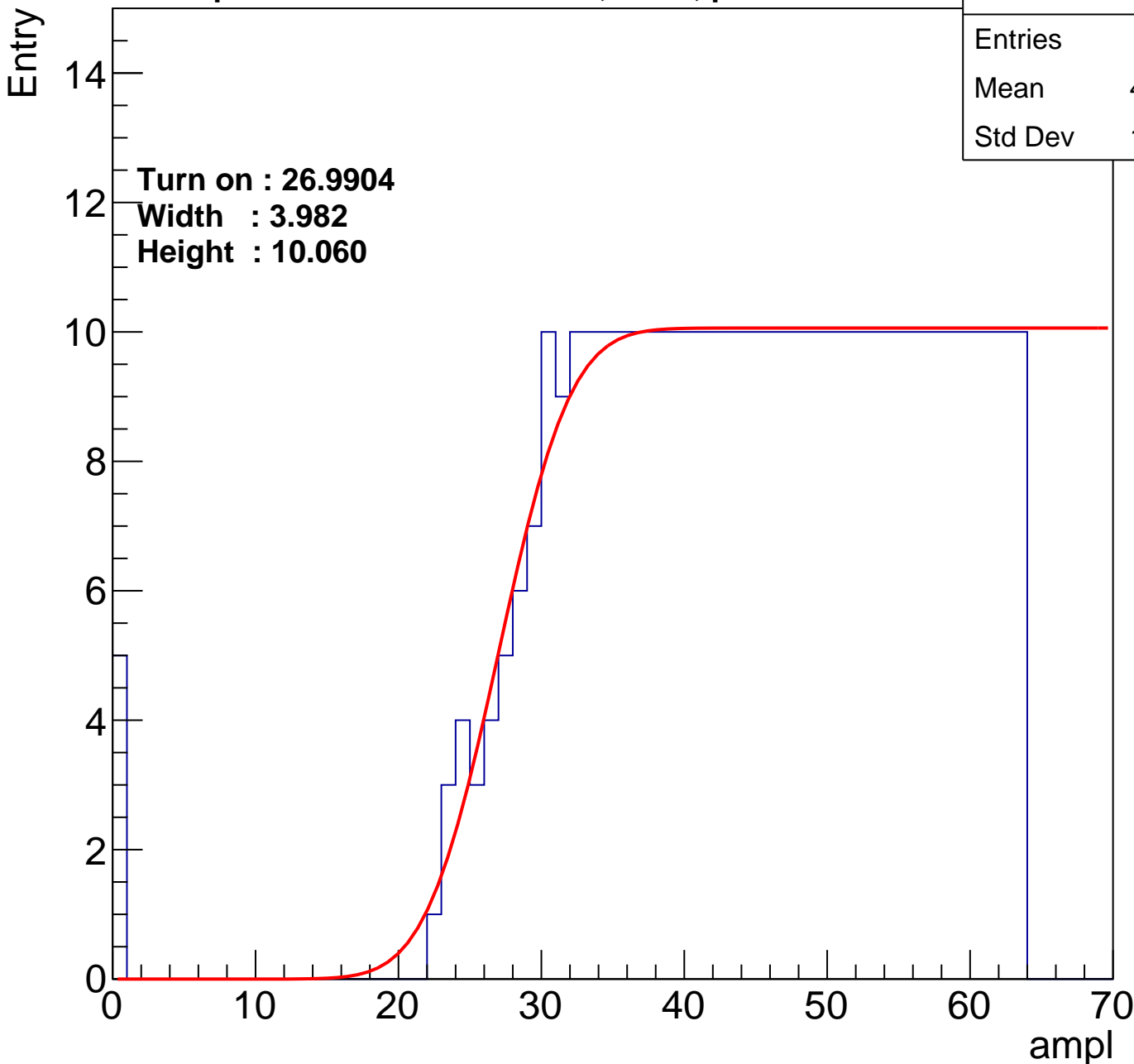
calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.17
Std Dev	12.04

Turn on : 26.9904

Width : 3.982

Height : 10.060



B1L000S, U5-ch71

calib_packv5_042523_0143.root, FC#3, port B3

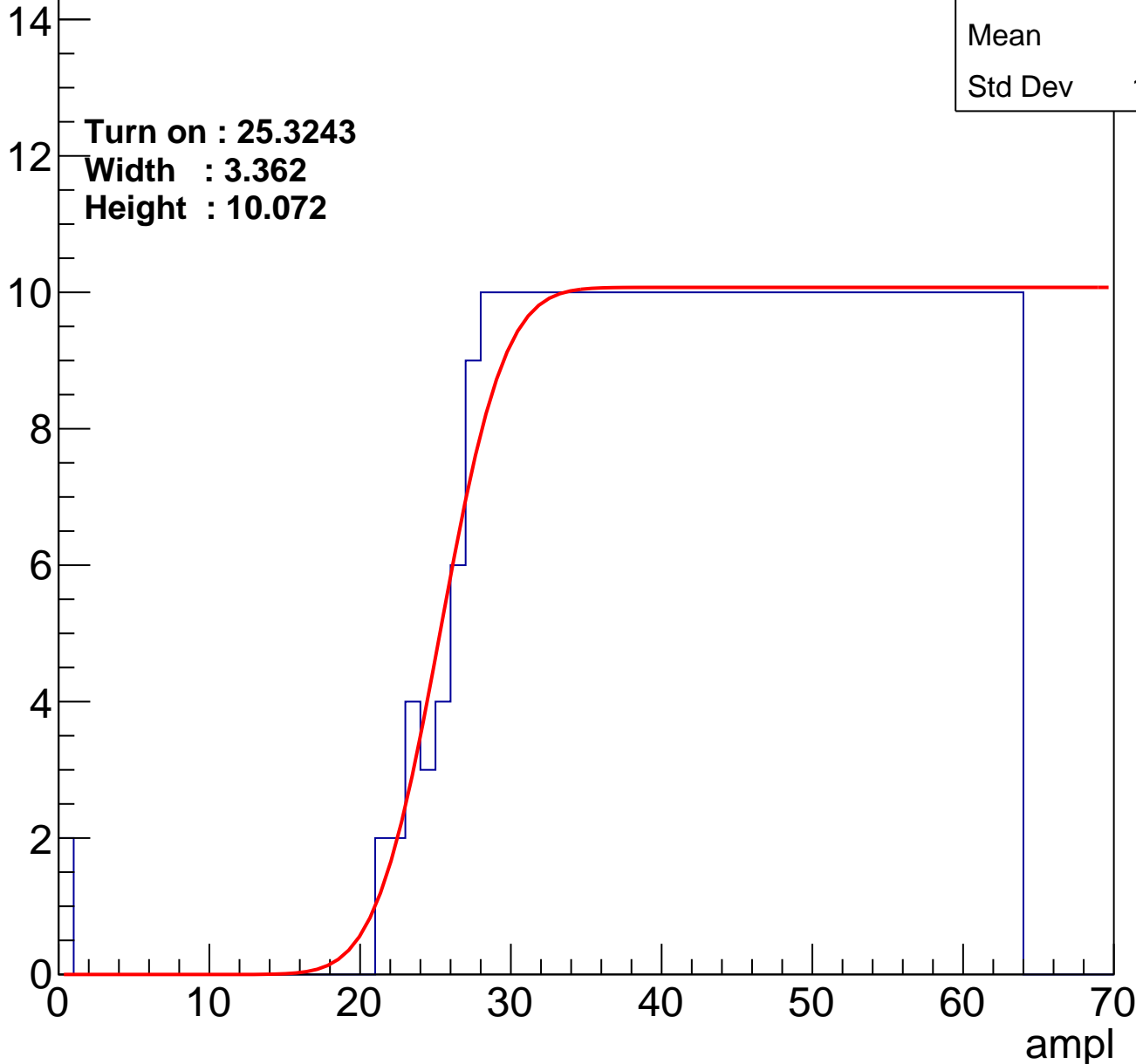
Entries	392
Mean	43.7
Std Dev	11.79

Turn on : 25.3243

Width : 3.362

Height : 10.072

Entry



B1L000S, U5-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	407
Mean	42.8
Std Dev	12.53

Turn on : 24.1976

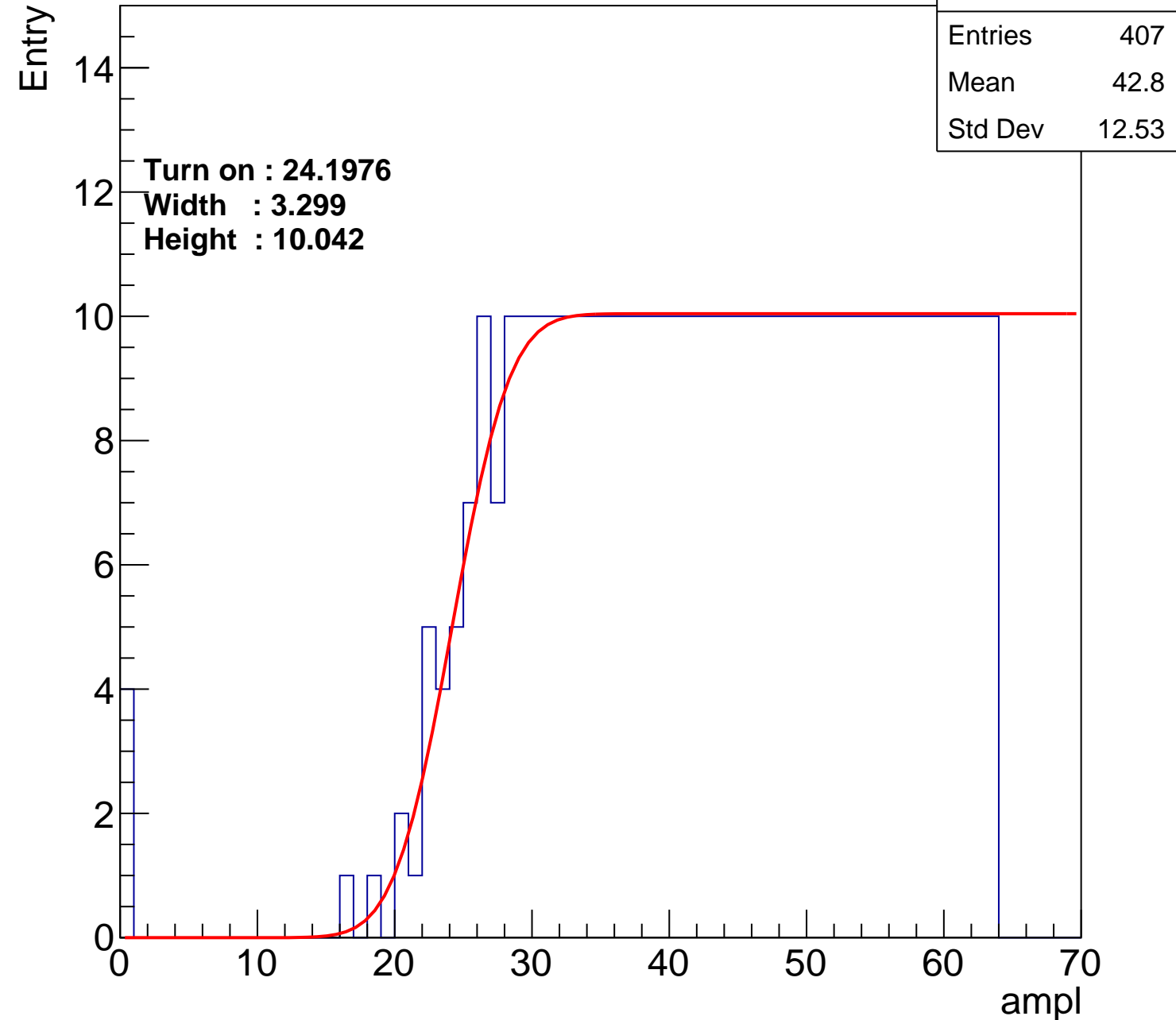
Width : 3.299

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.31
Std Dev	11.5

Turn on : 26.5660

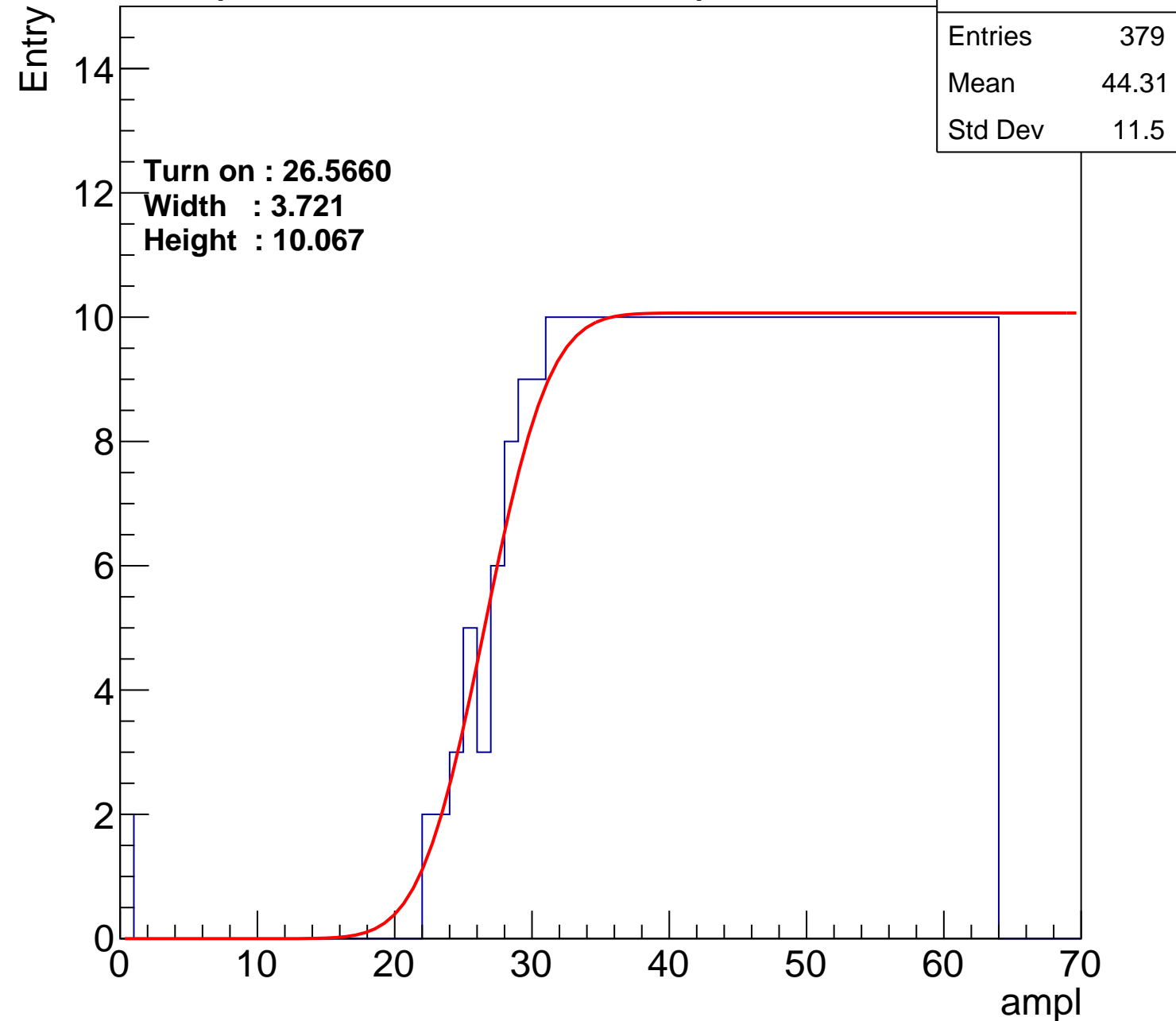
Width : 3.721

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 26.1194

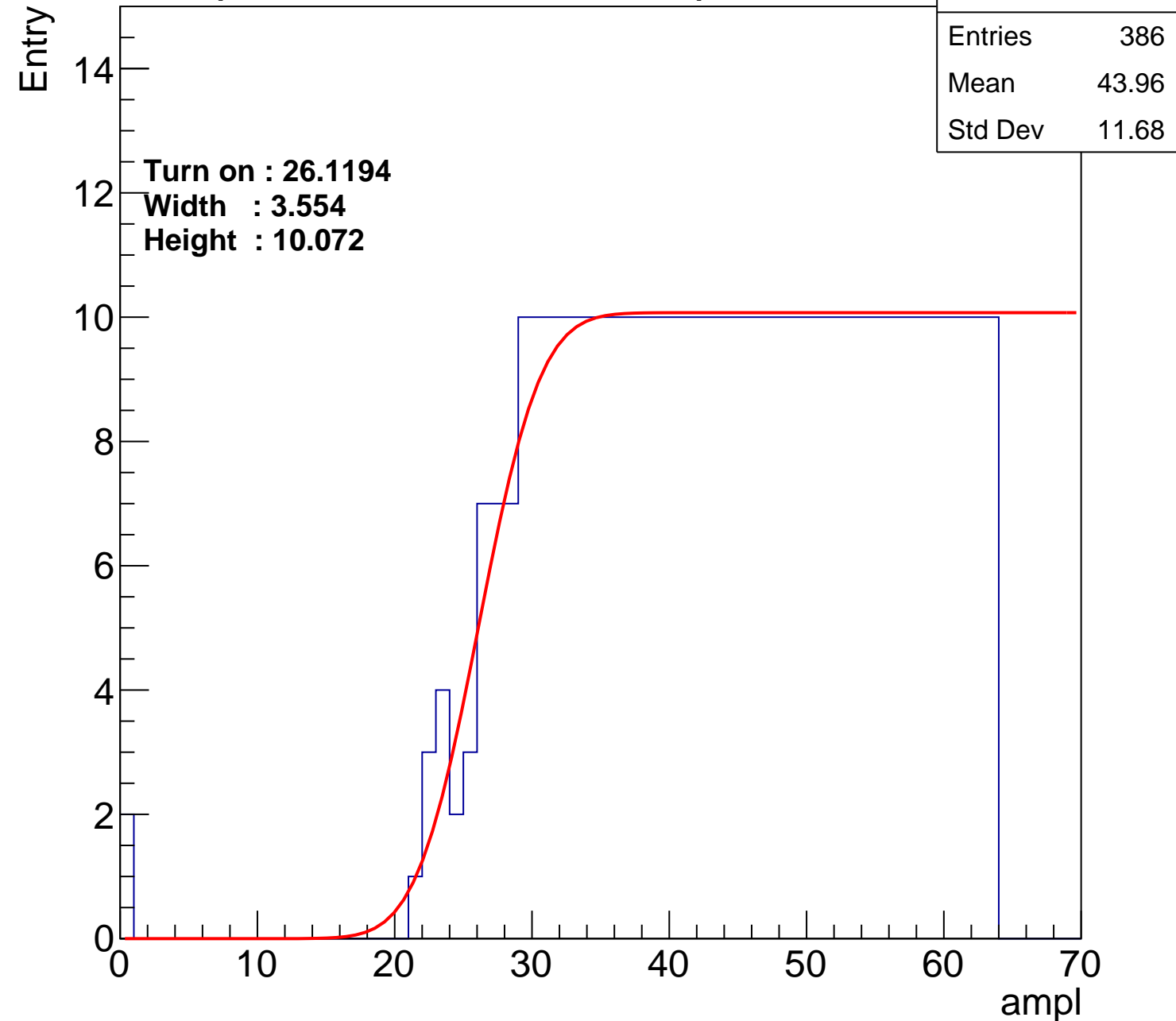
Width : 3.554

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.85
Std Dev	11.86

Turn on : 25.5828

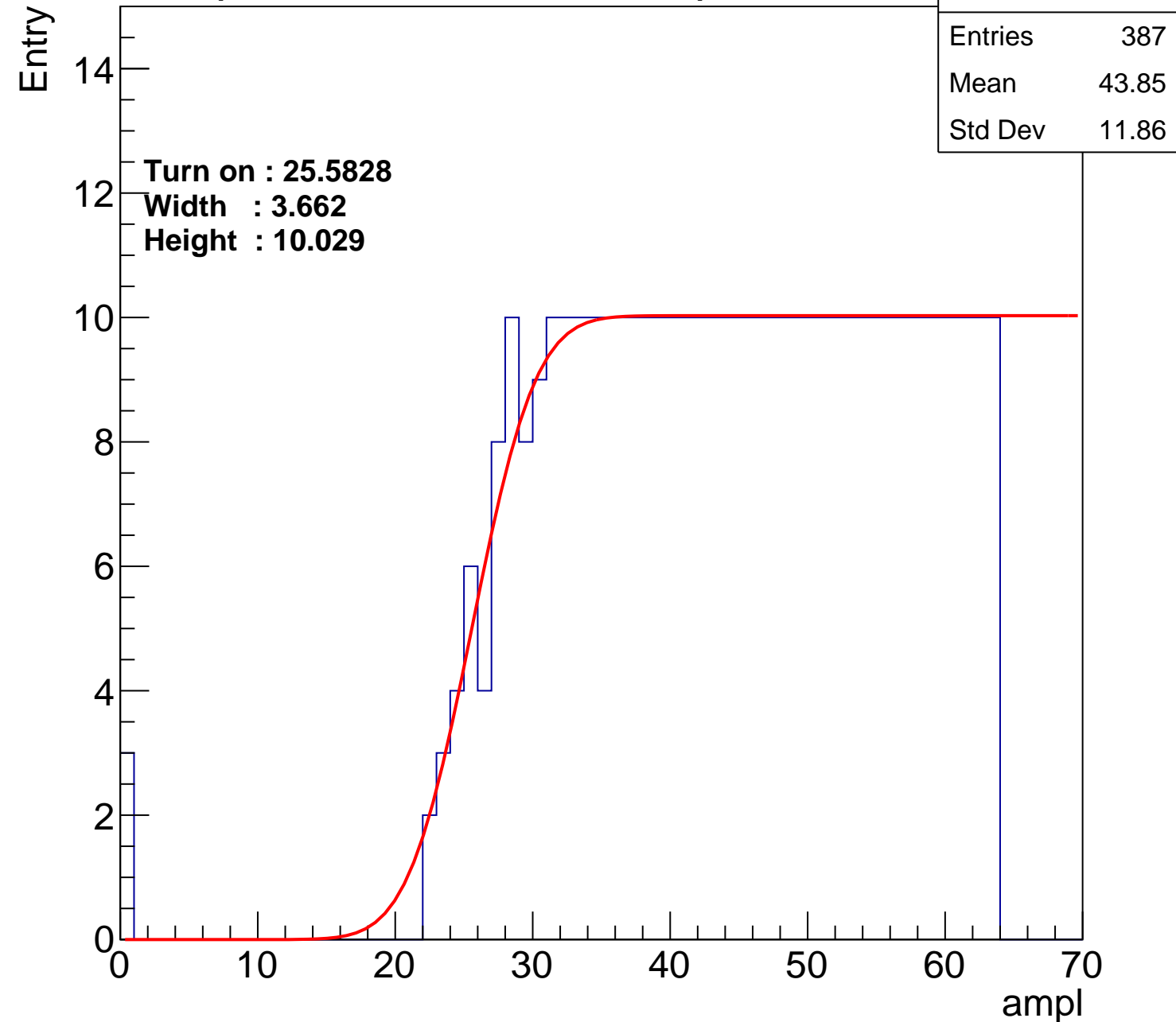
Width : 3.662

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch76

calib_packv5_042523_0143.root, FC#3, port B3

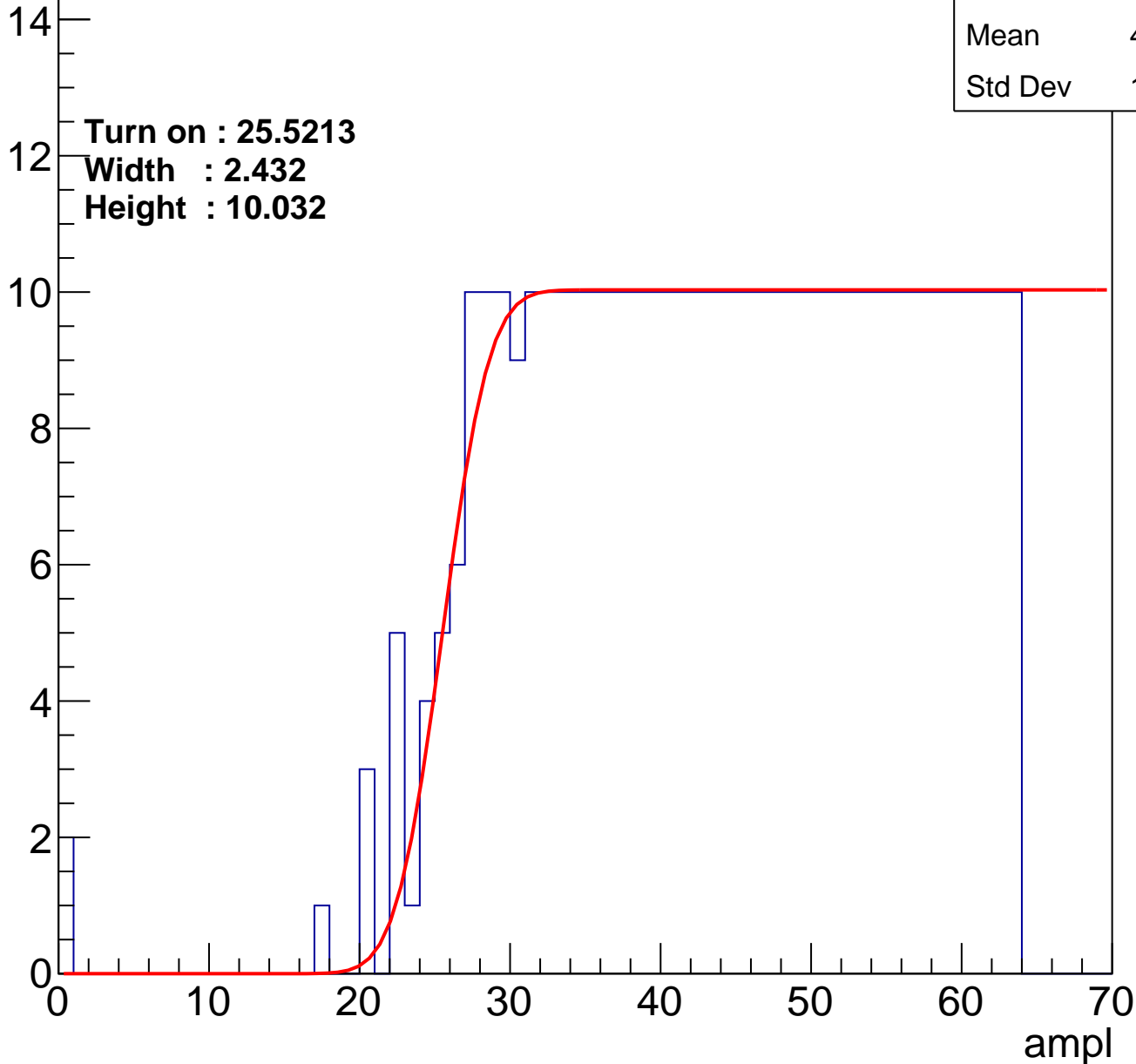
Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 25.5213

Width : 2.432

Height : 10.032

Entry



B1L000S, U5-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.52
Std Dev	12.26

Turn on : 25.7118

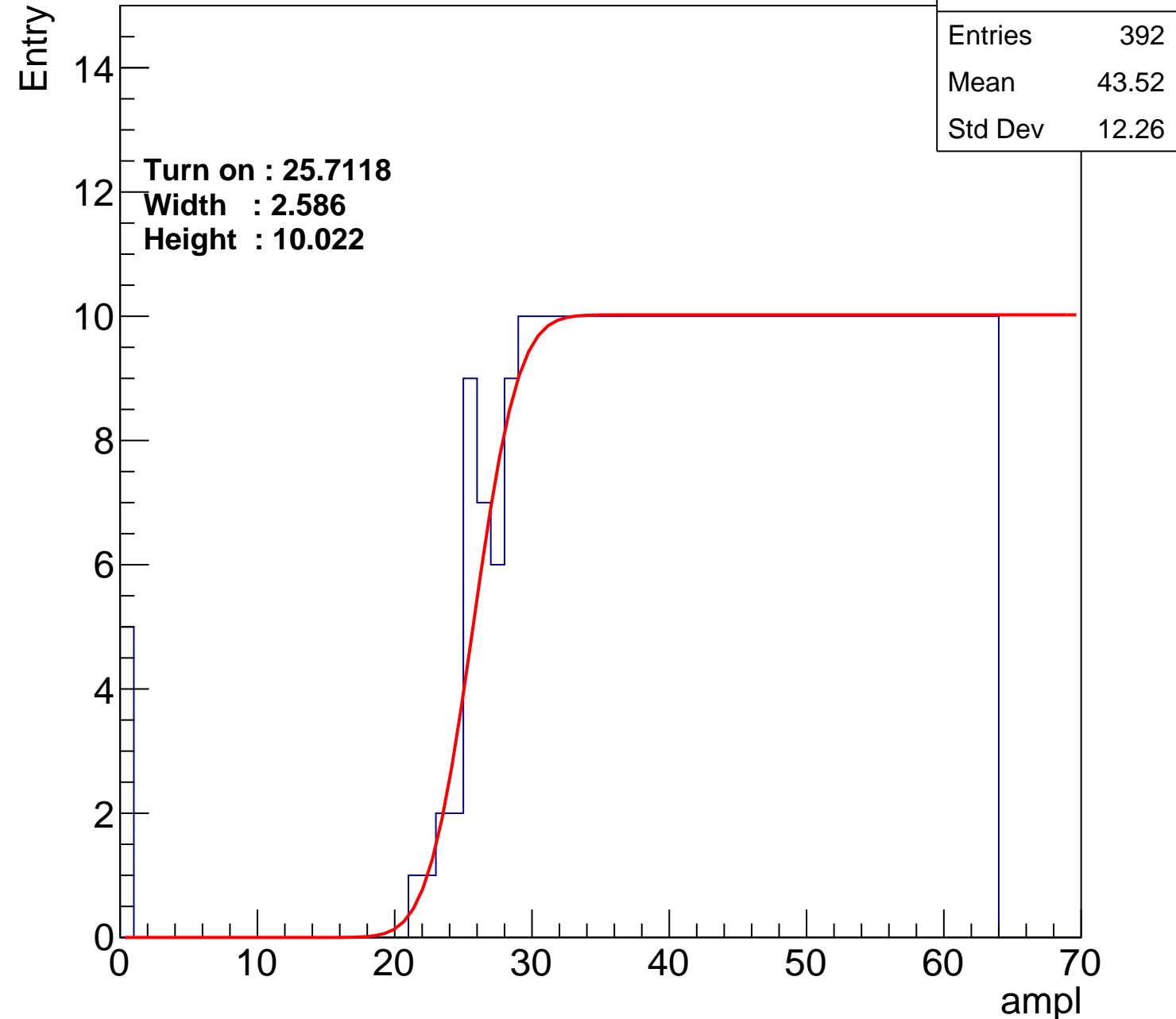
Width : 2.586

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.56
Std Dev	12.18

Turn on : 25.4542

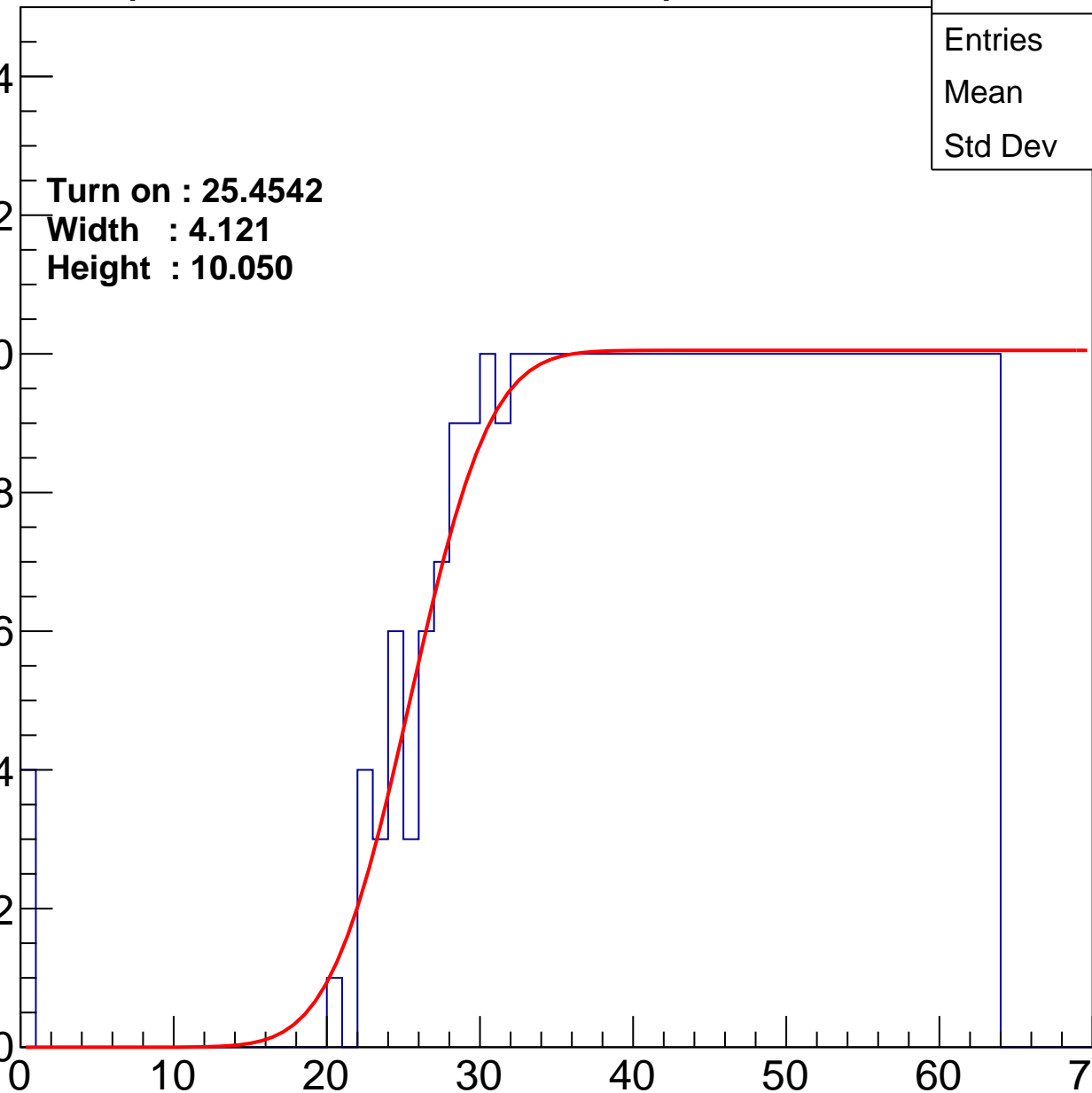
Width : 4.121

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.32

Turn on : 26.9049

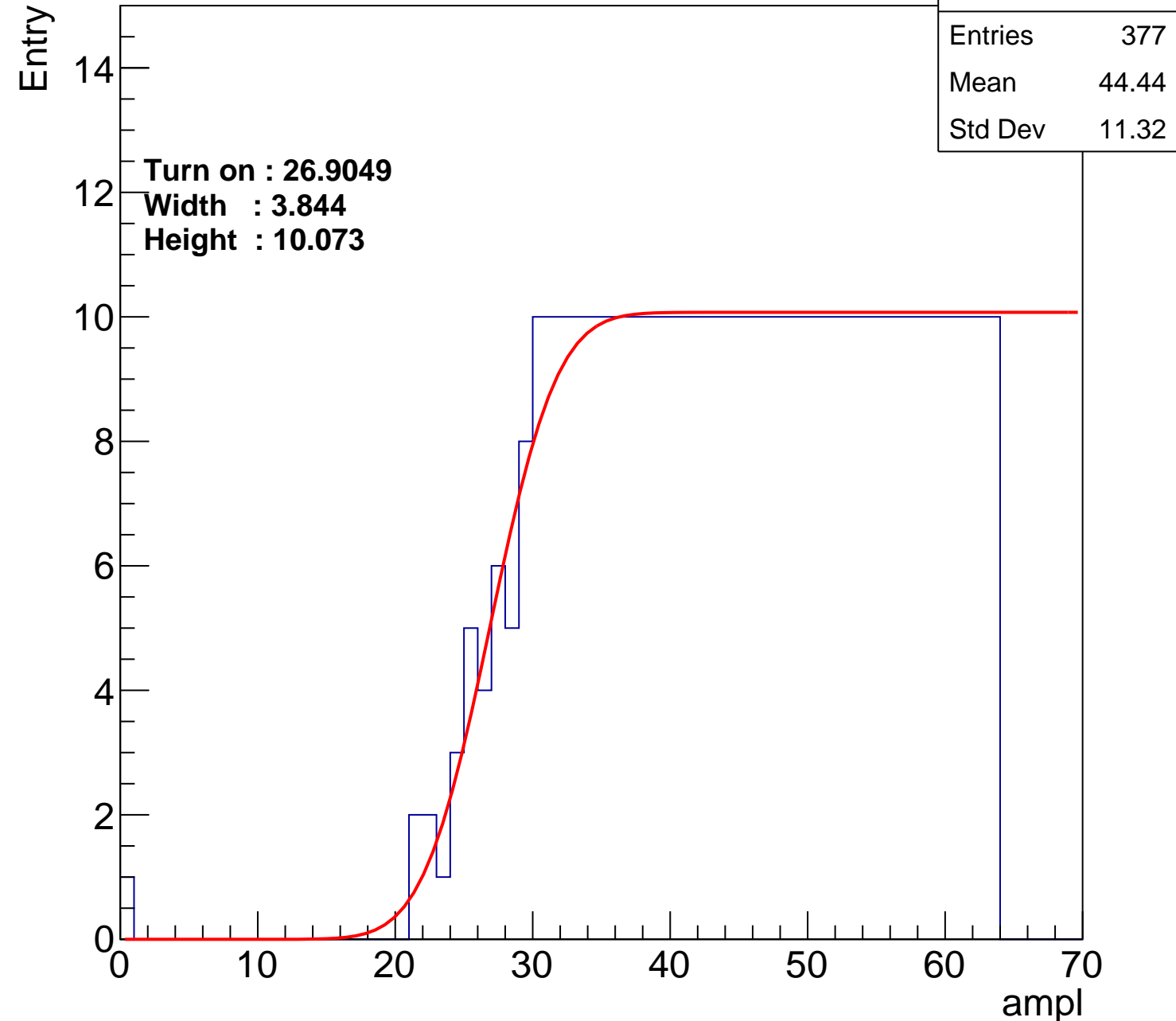
Width : 3.844

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.8683

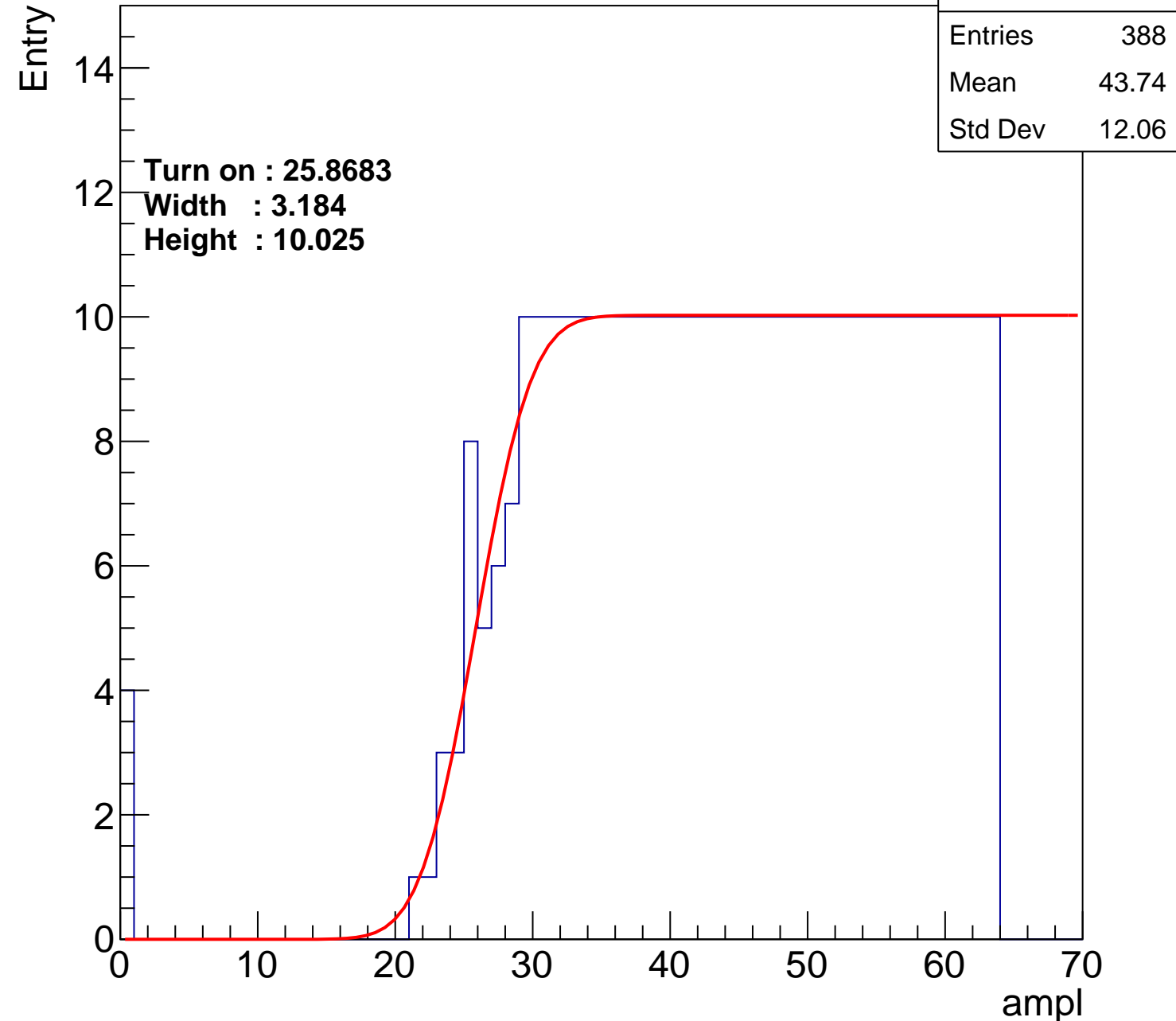
Width : 3.184

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.61
Std Dev	12.55

Turn on : 26.3818

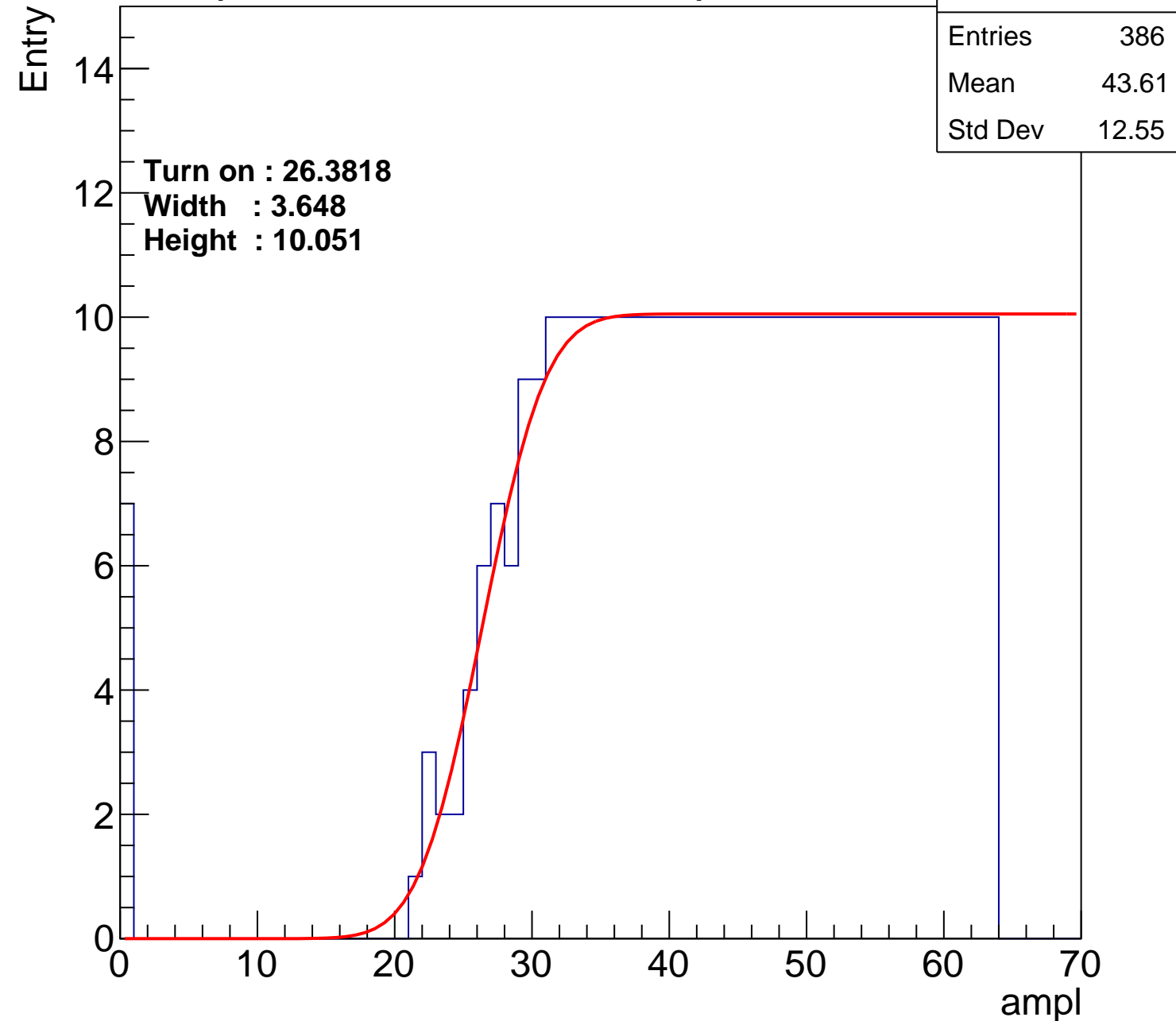
Width : 3.648

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch82

calib_packv5_042523_0143.root, FC#3, port B3

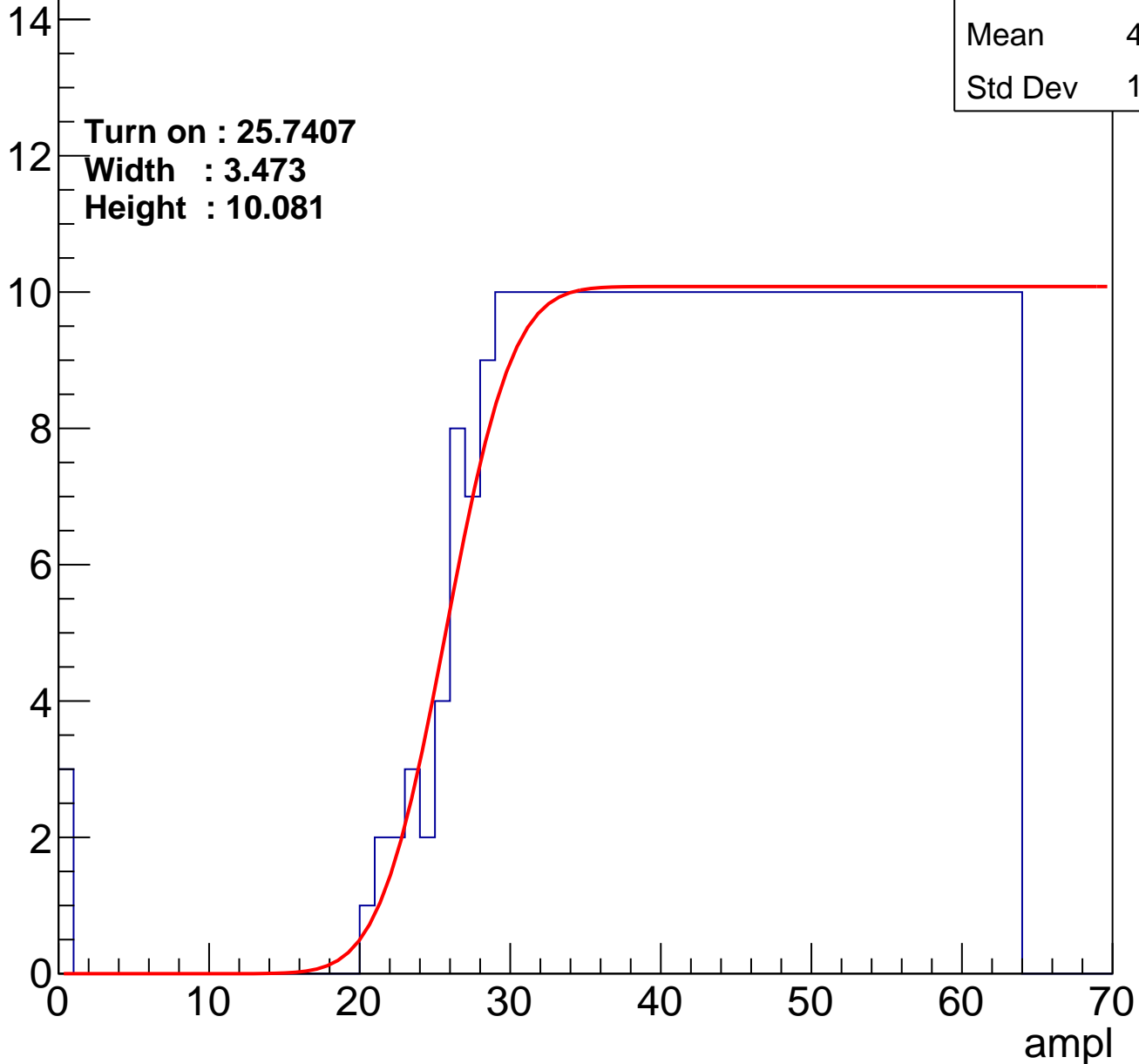
Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.7407

Width : 3.473

Height : 10.081

Entry



B1L000S, U5-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.1194

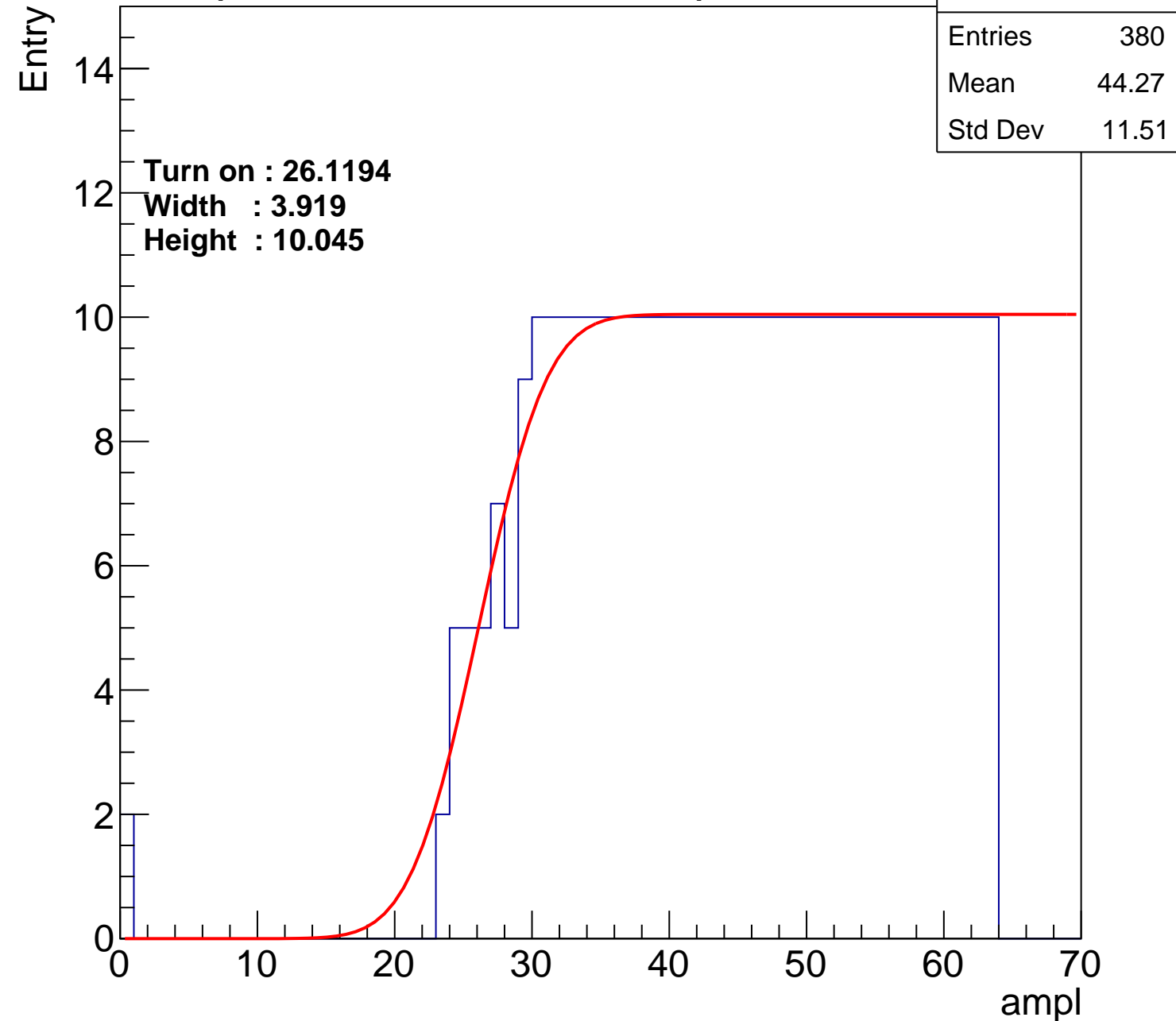
Width : 3.919

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	418
Mean	42.3
Std Dev	12.75

Turn on : 23.1839

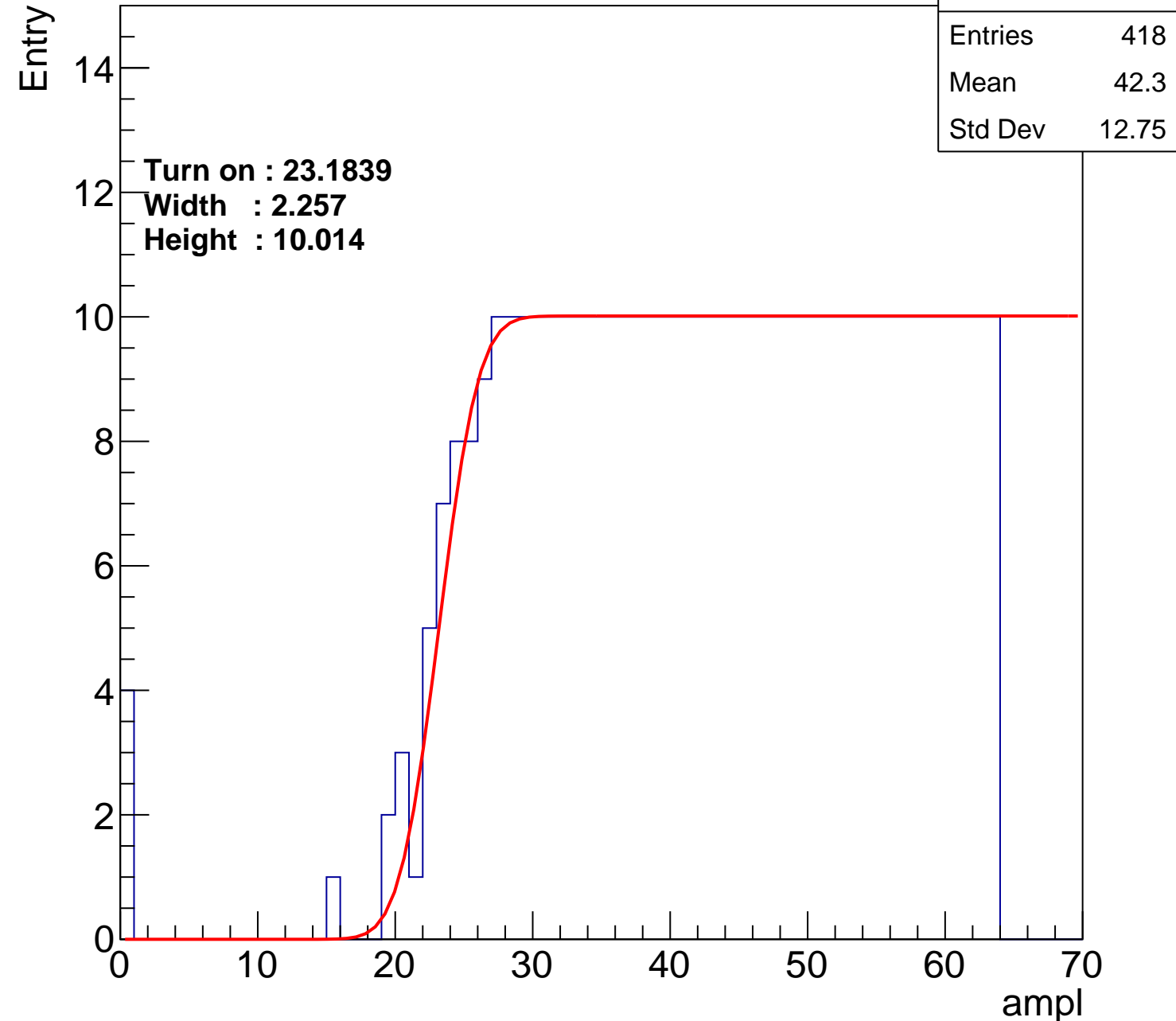
Width : 2.257

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.55
Std Dev	11.2

Turn on : 26.7508

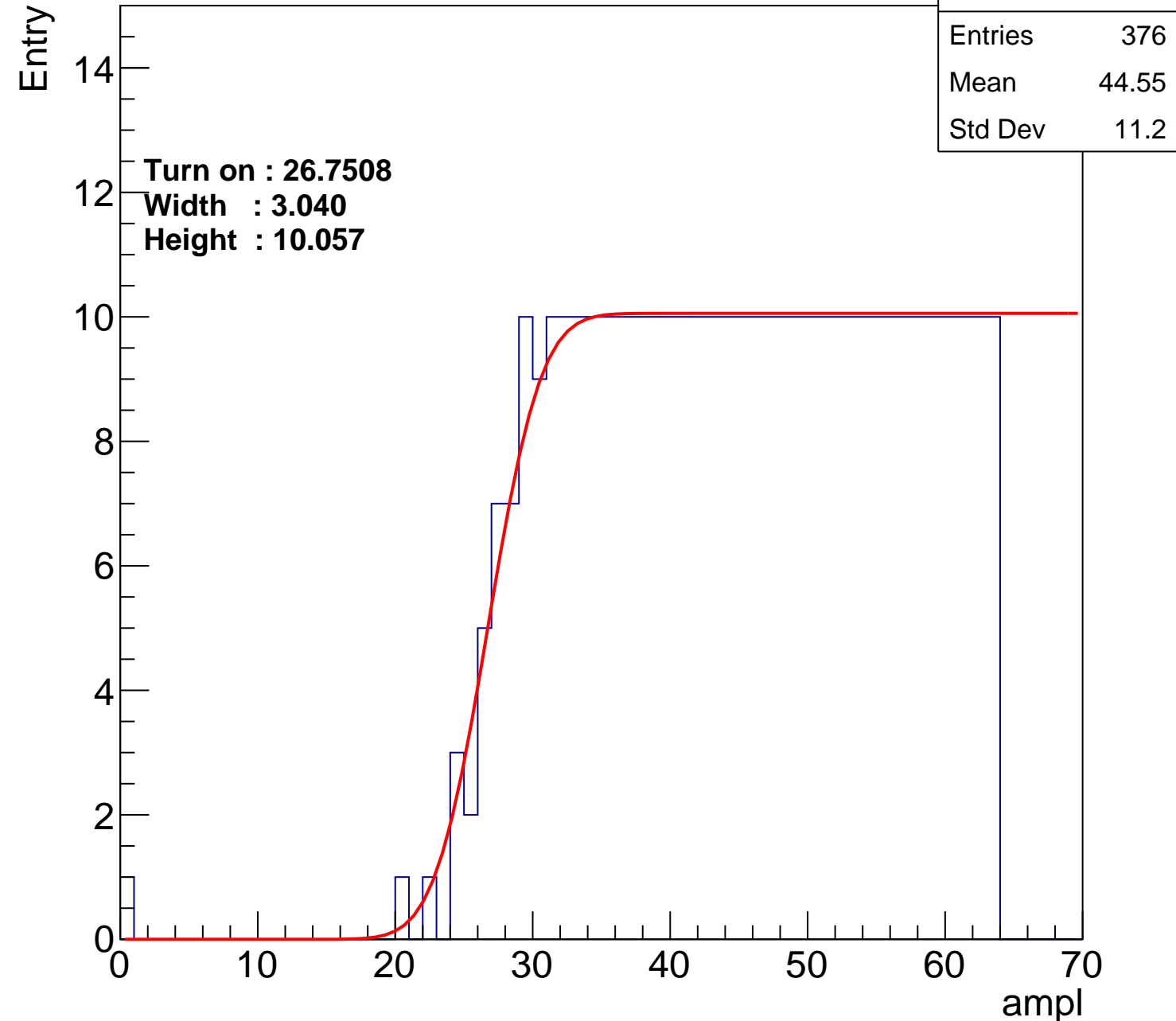
Width : 3.040

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.54
Std Dev	12.12

Turn on : 25.4796

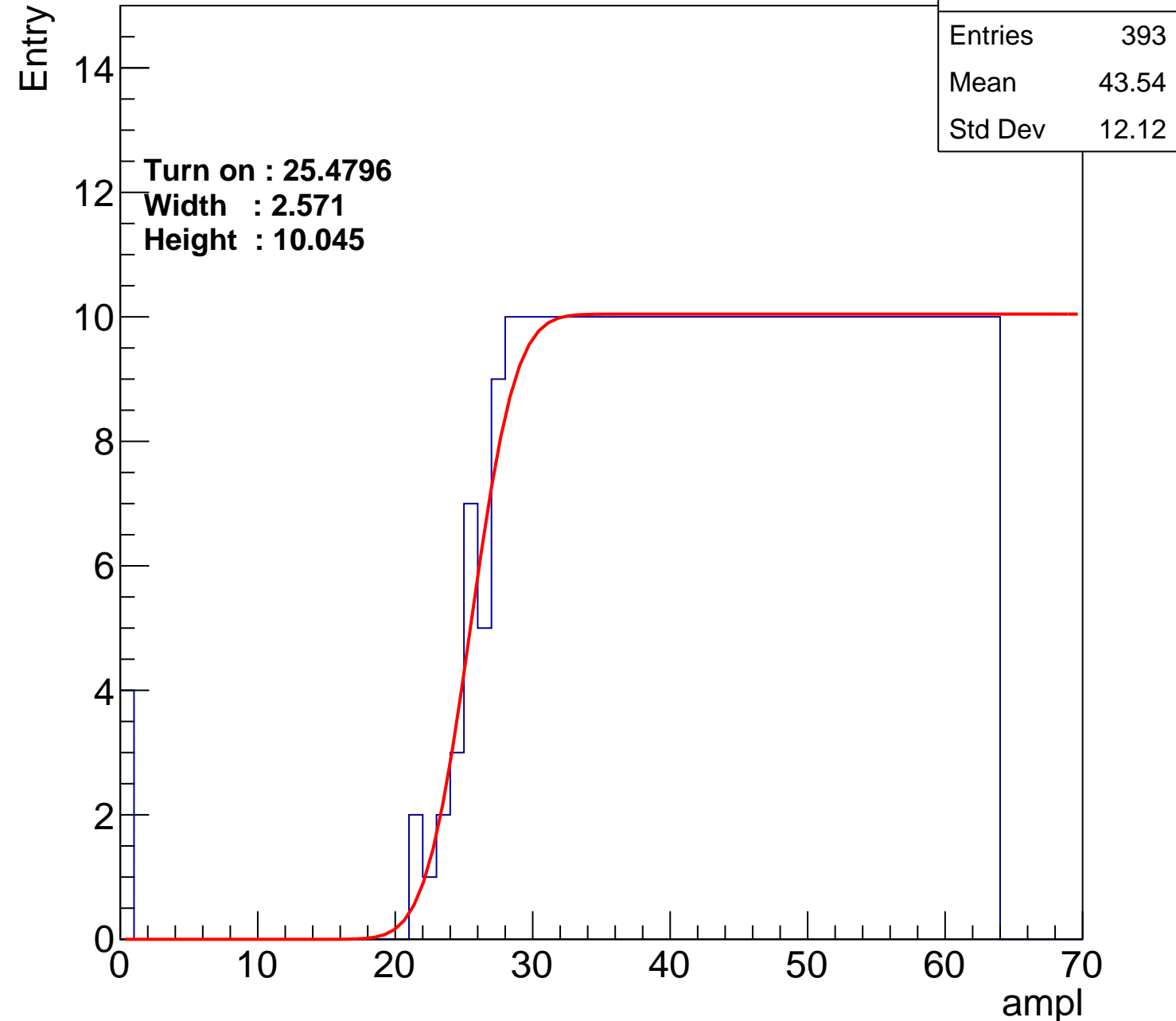
Width : 2.571

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.97
Std Dev	10.99

Turn on : 27.5548

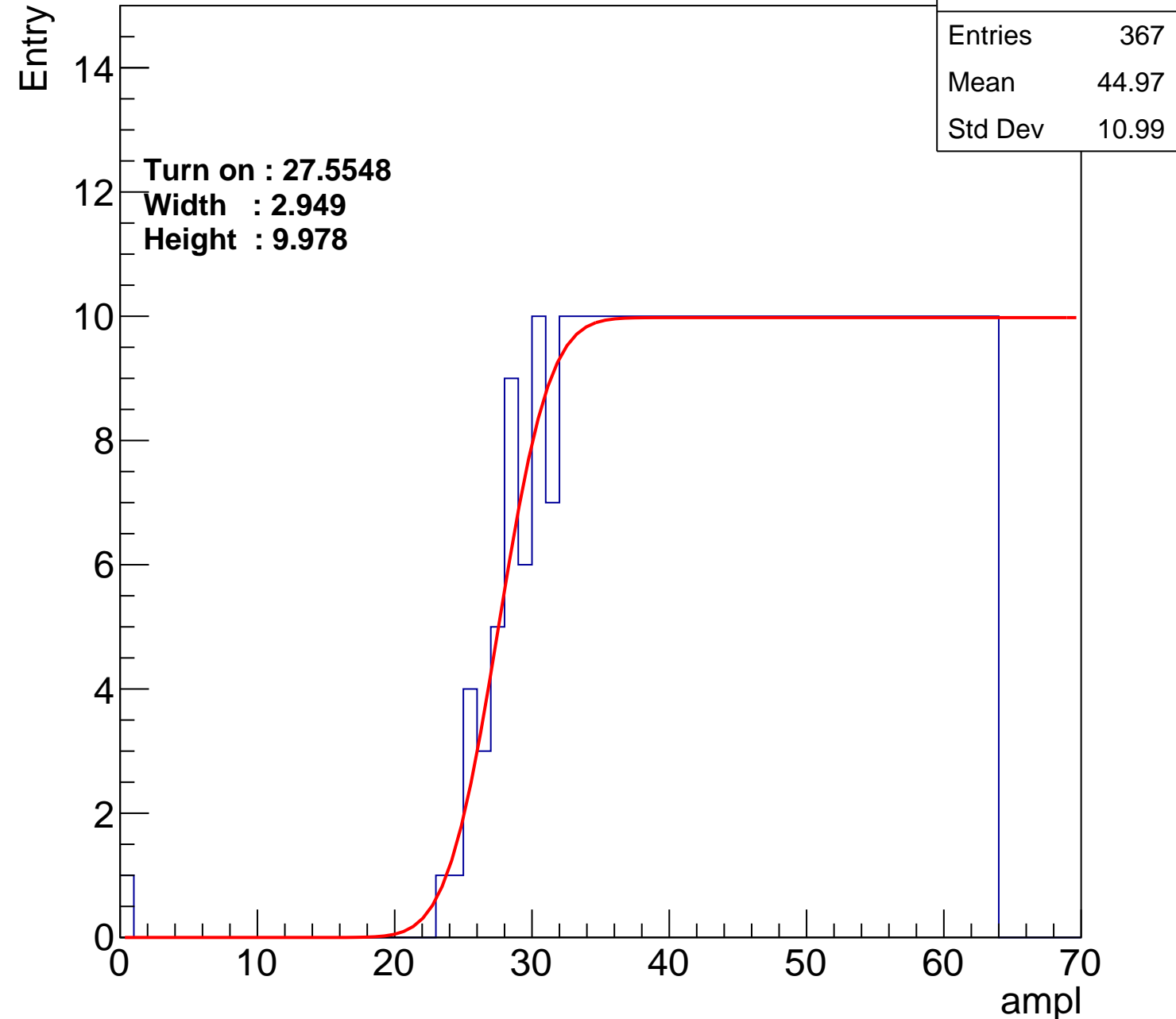
Width : 2.949

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.9137

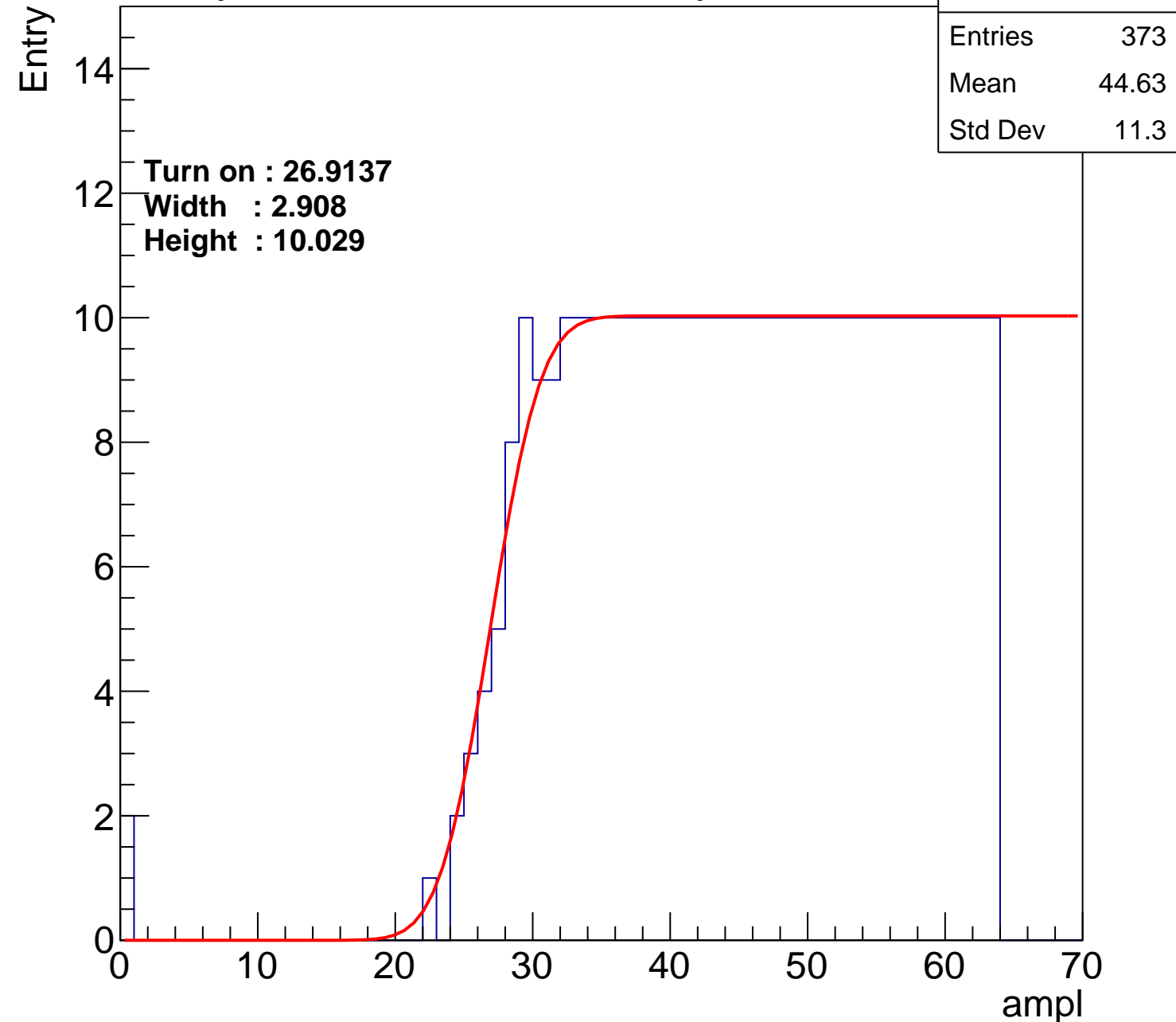
Width : 2.908

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.9035

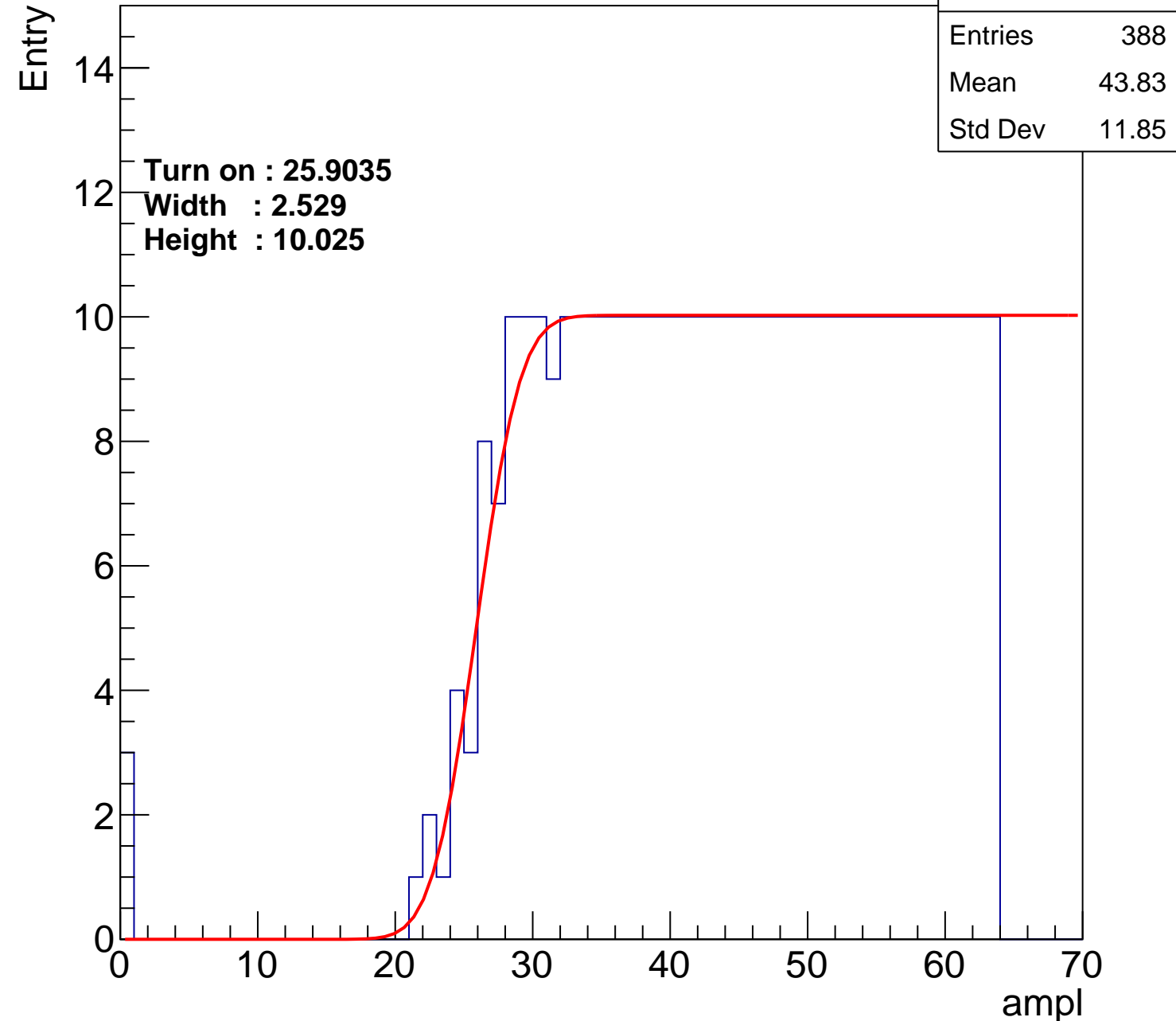
Width : 2.529

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.34
Std Dev	11.95

Turn on : 27.7428

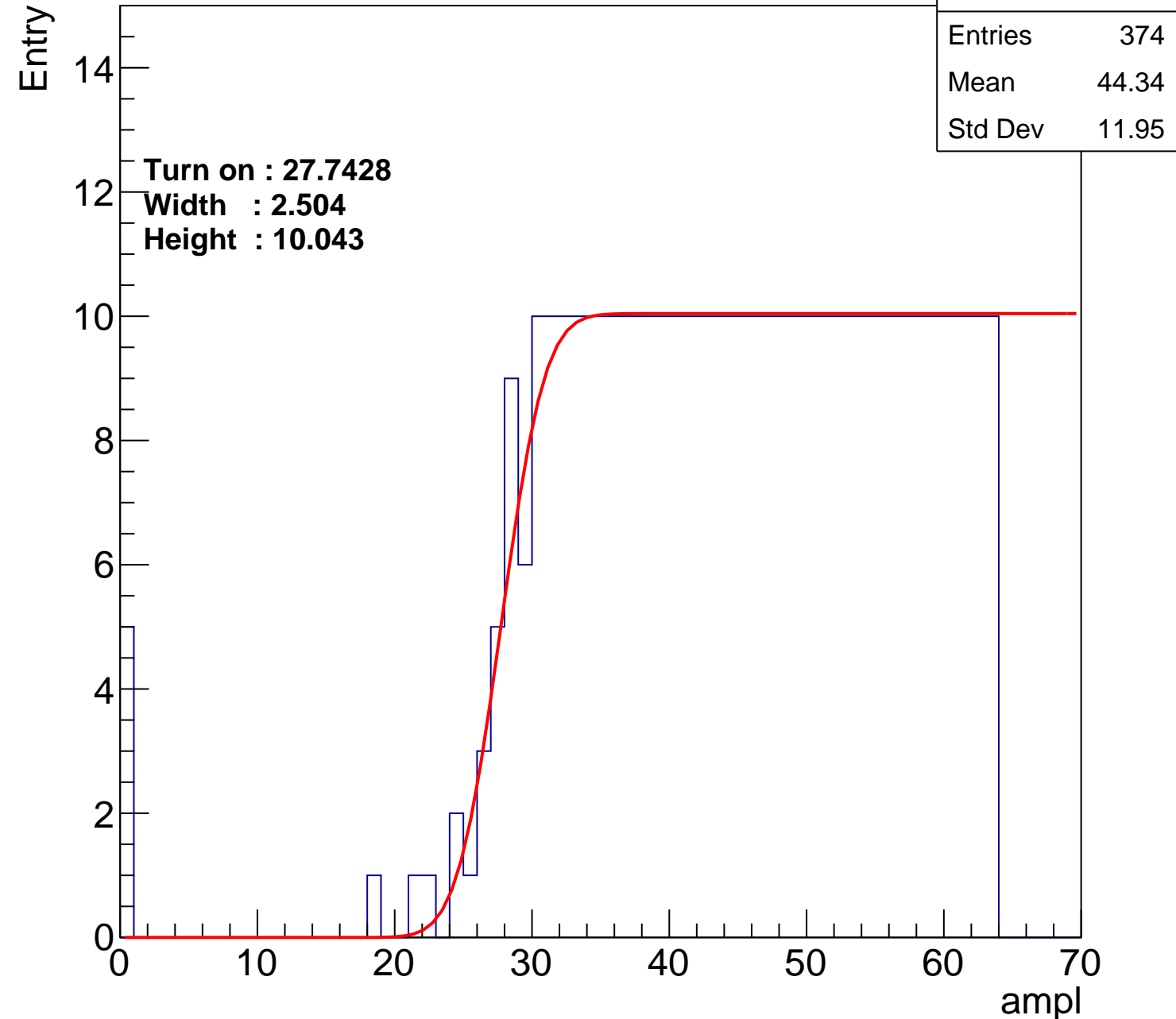
Width : 2.504

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.0364

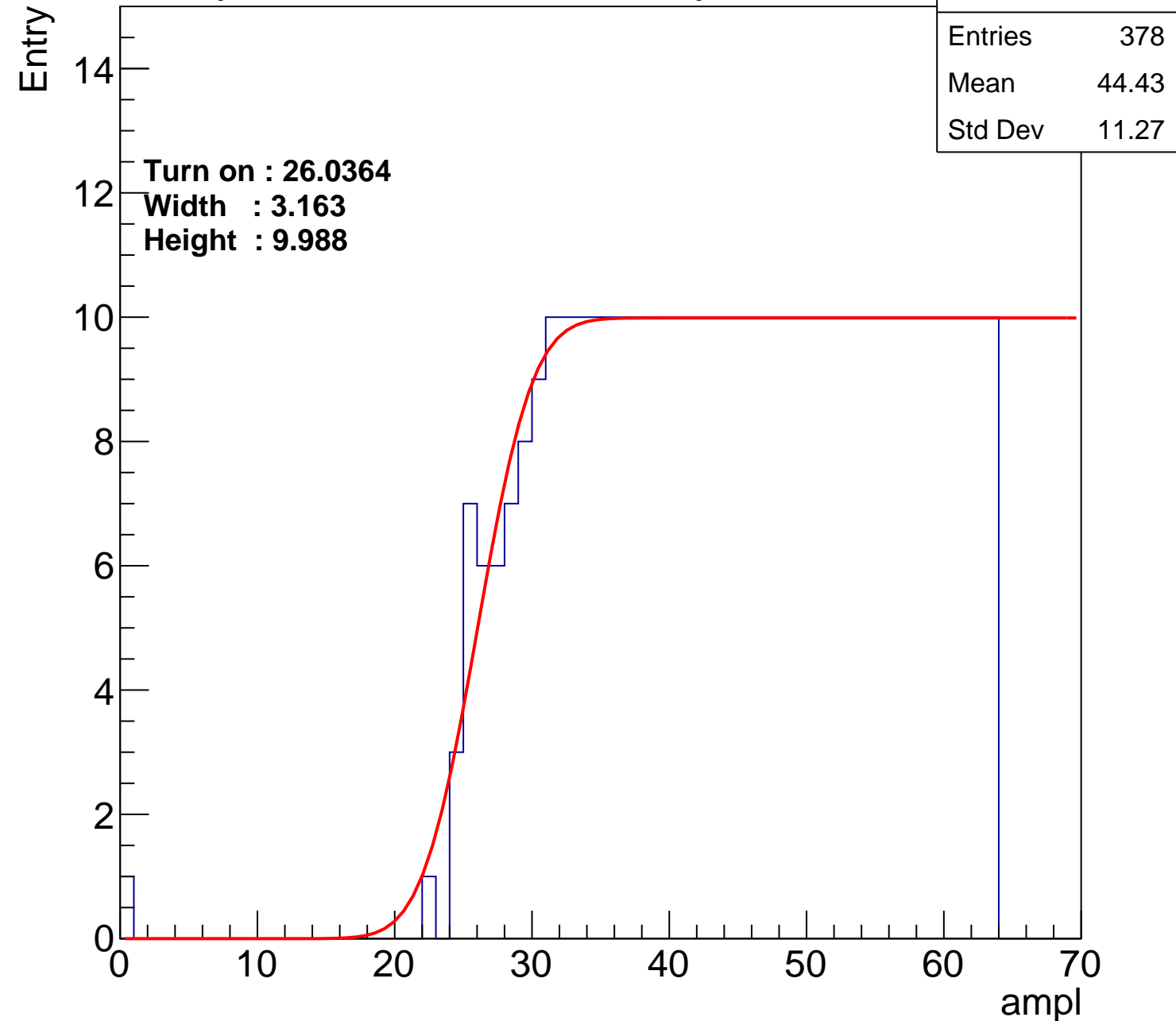
Width : 3.163

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	44.82
Std Dev	11.73

Turn on : 28.3387

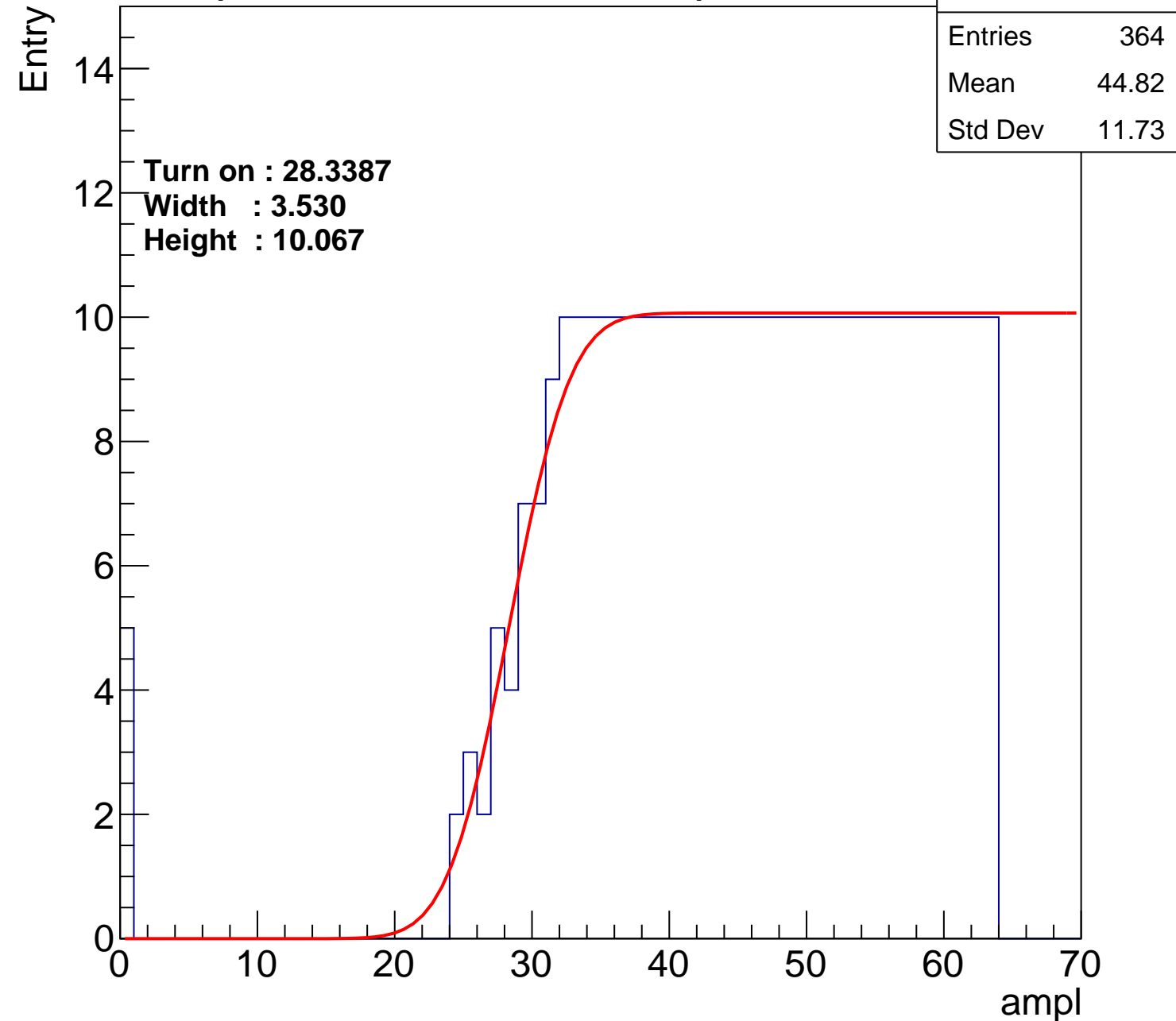
Width : 3.530

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch93

calib_packv5_042523_0143.root, FC#3, port B3

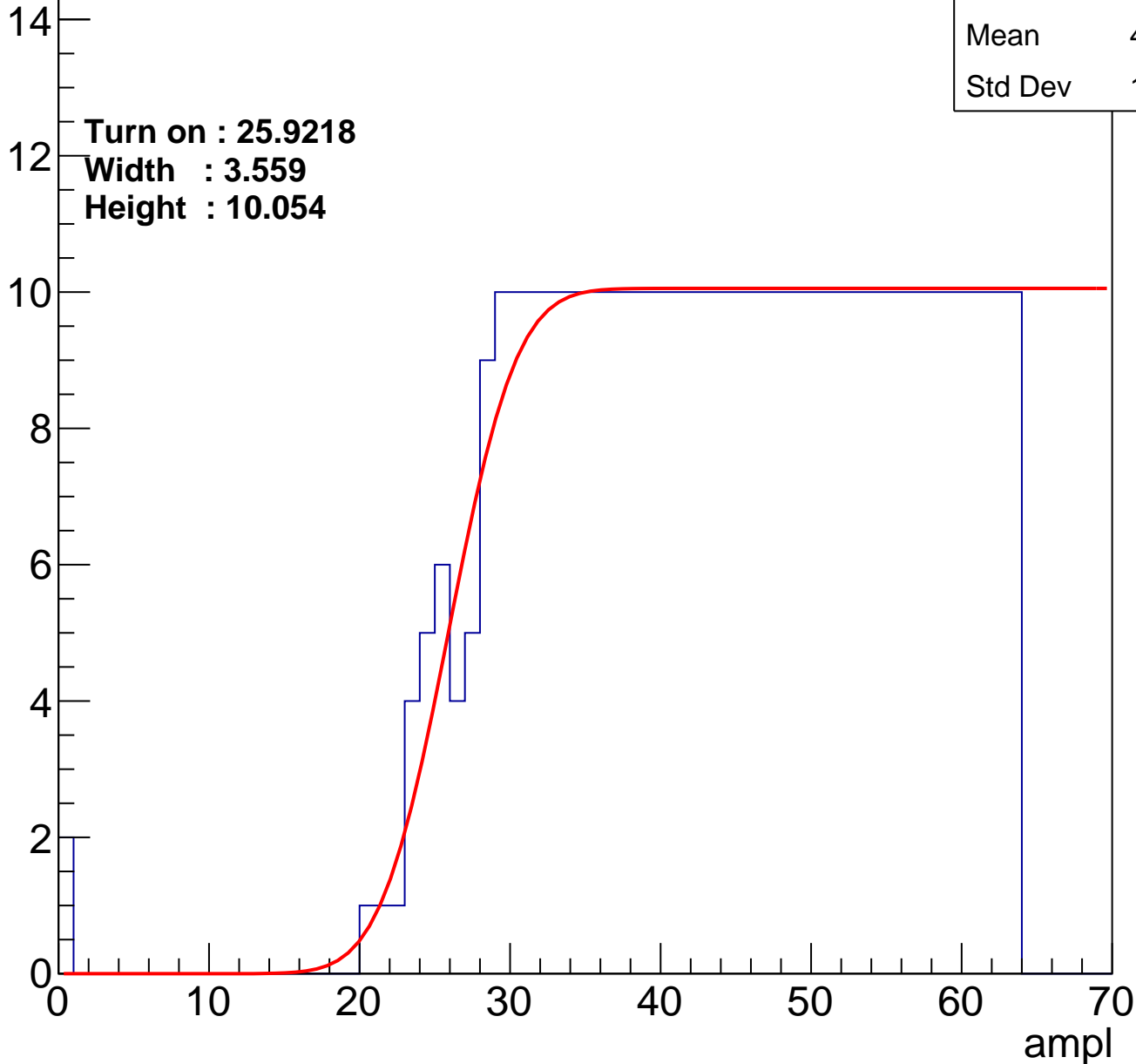
Entries	388
Mean	43.86
Std Dev	11.75

Turn on : 25.9218

Width : 3.559

Height : 10.054

Entry



B1L000S, U5-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.22
Std Dev	11.39

Turn on : 25.2977

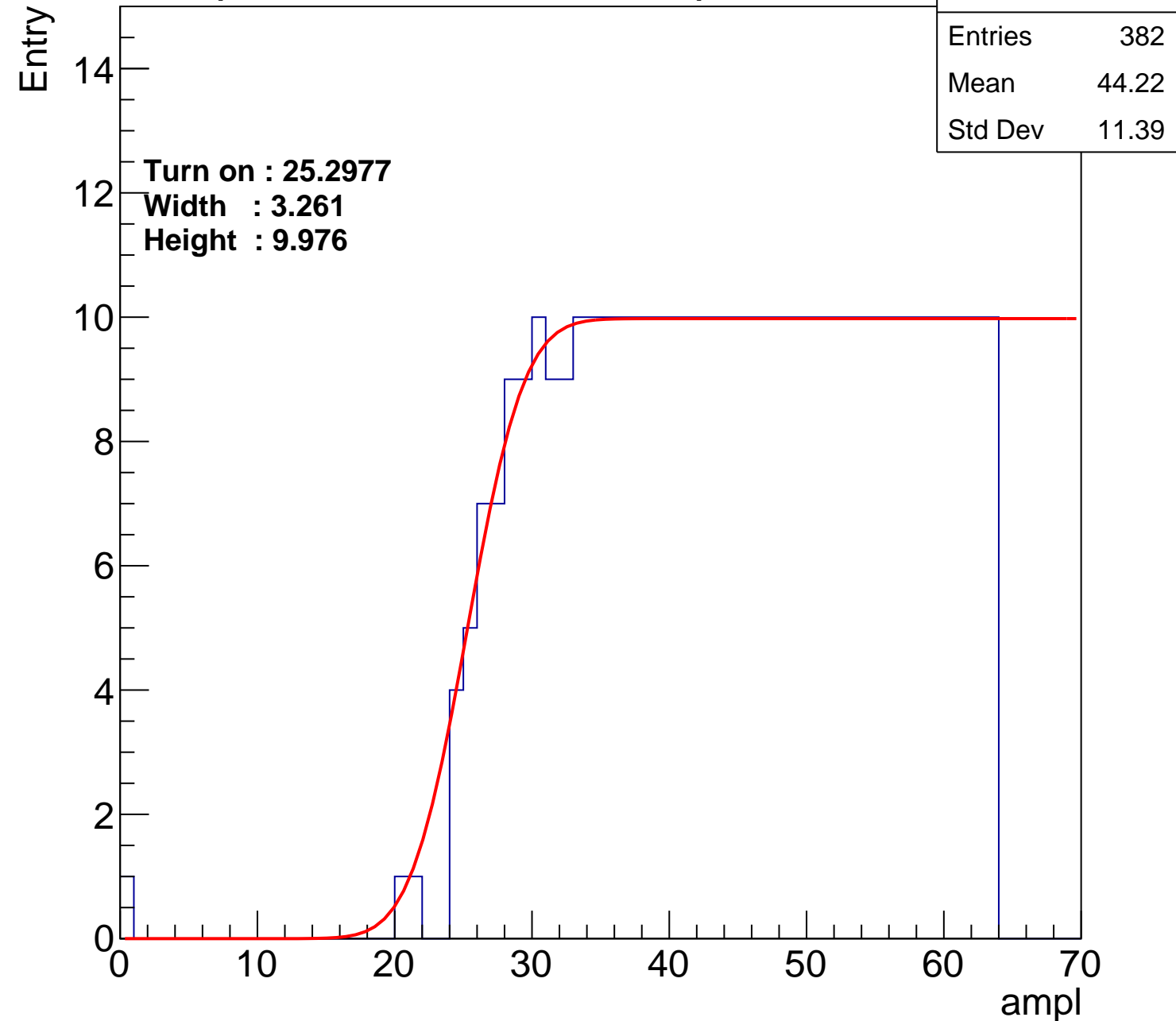
Width : 3.261

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch95

calib_packv5_042523_0143.root, FC#3, port B3

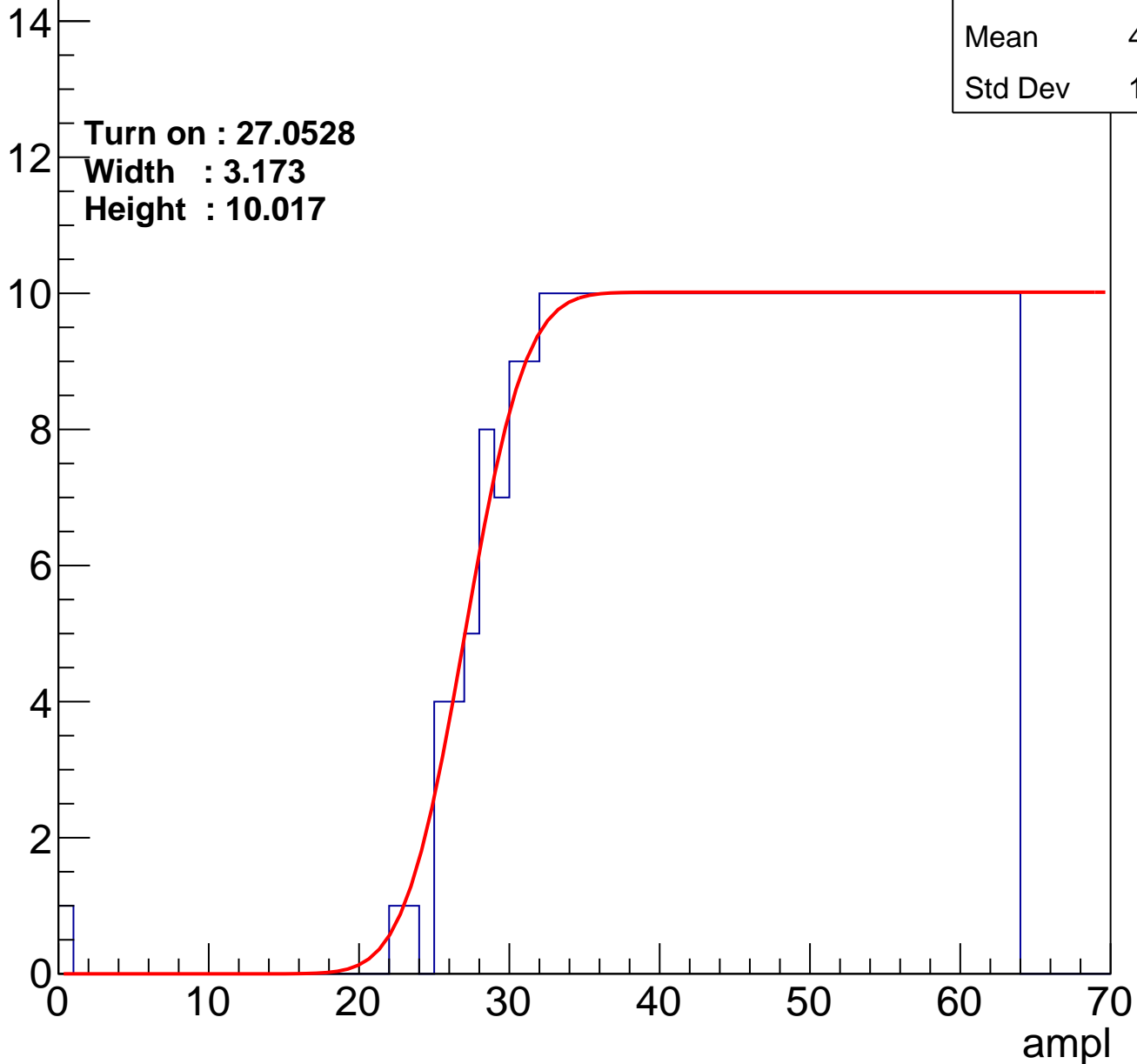
Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.0528

Width : 3.173

Height : 10.017

Entry



B1L000S, U5-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.49
Std Dev	12.02

Turn on : 25.0410

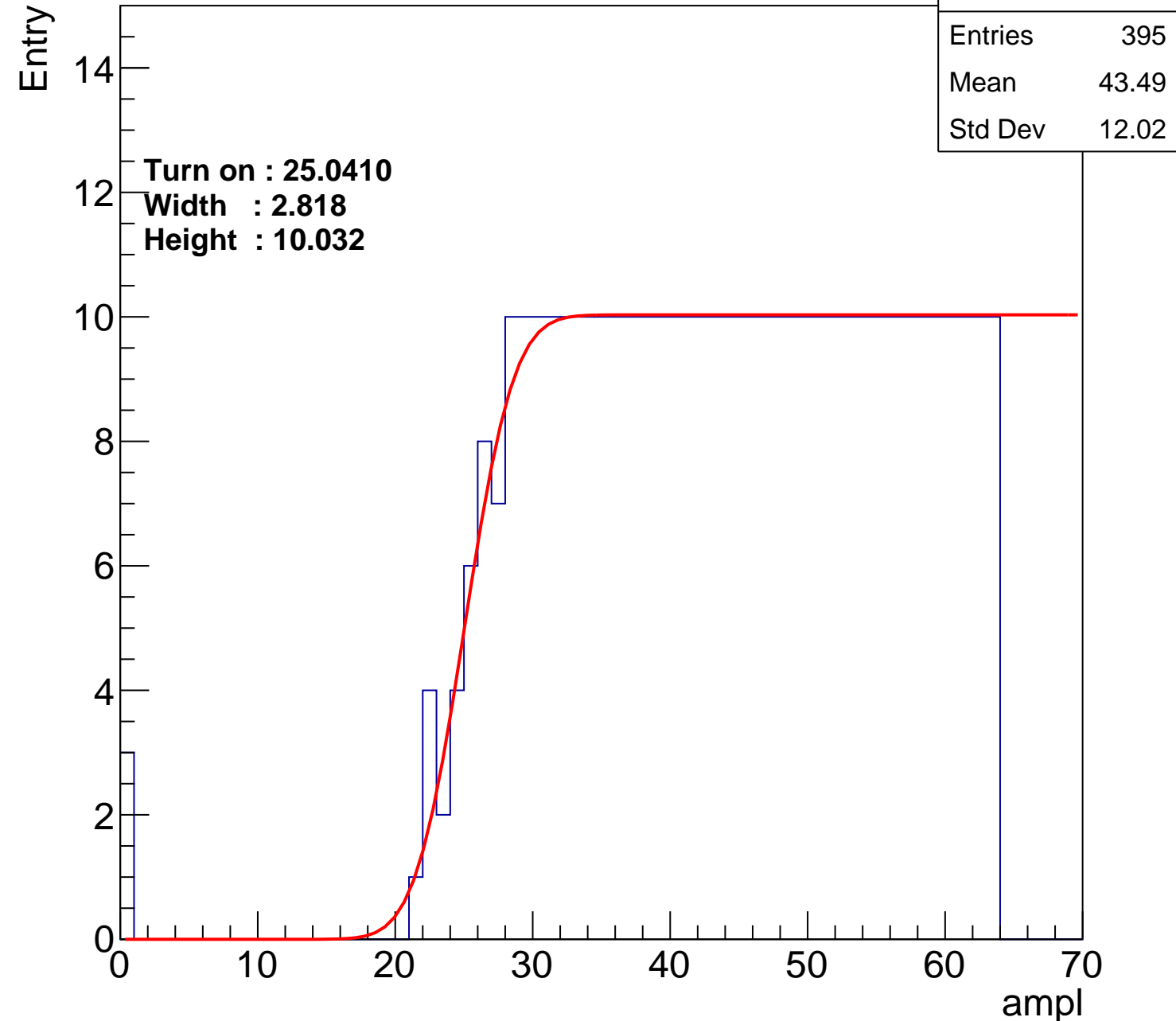
Width : 2.818

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.76
Std Dev	11.25

Turn on : 27.3464

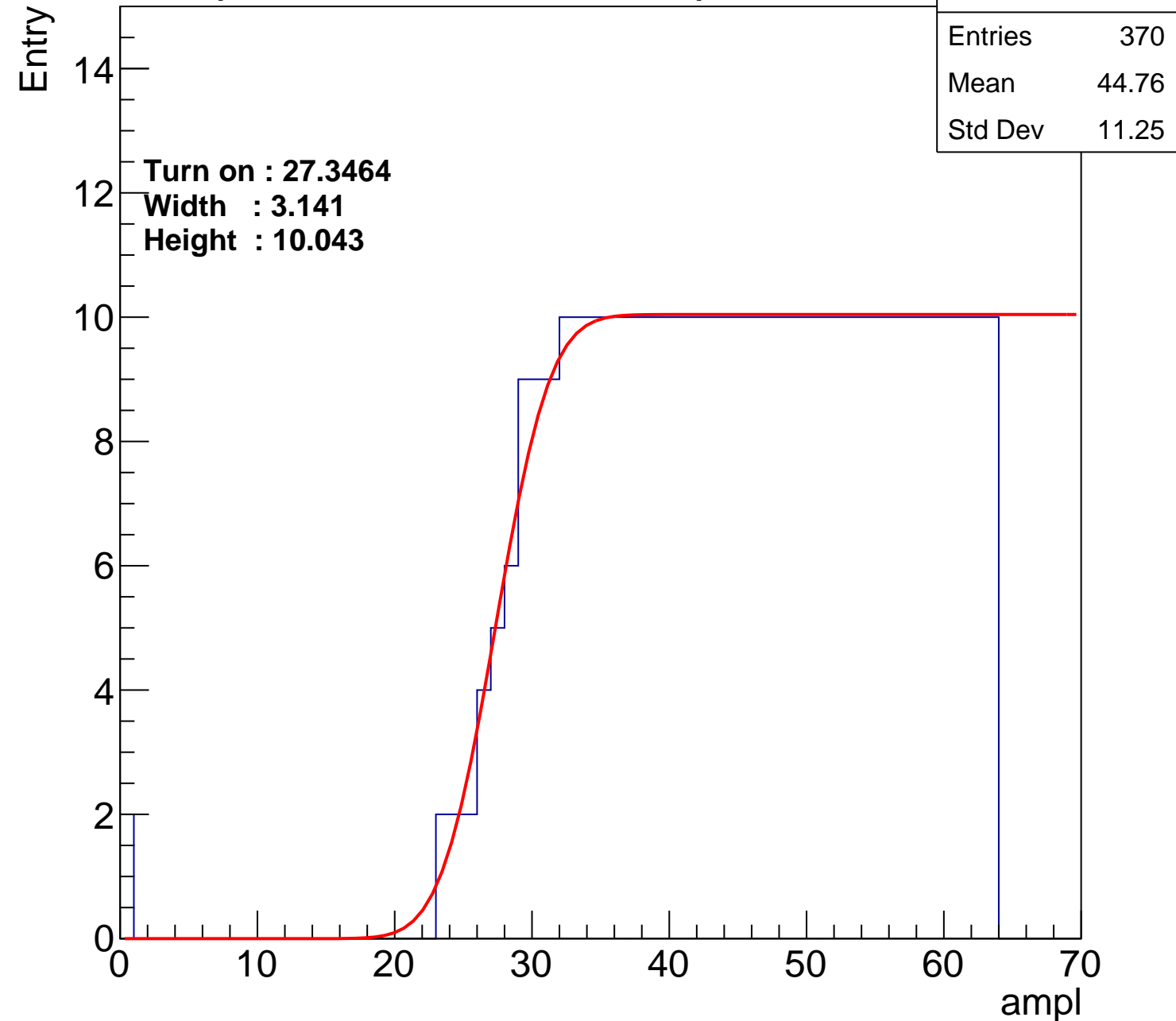
Width : 3.141

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	416
Mean	42.47
Std Dev	12.53

Turn on : 22.7645

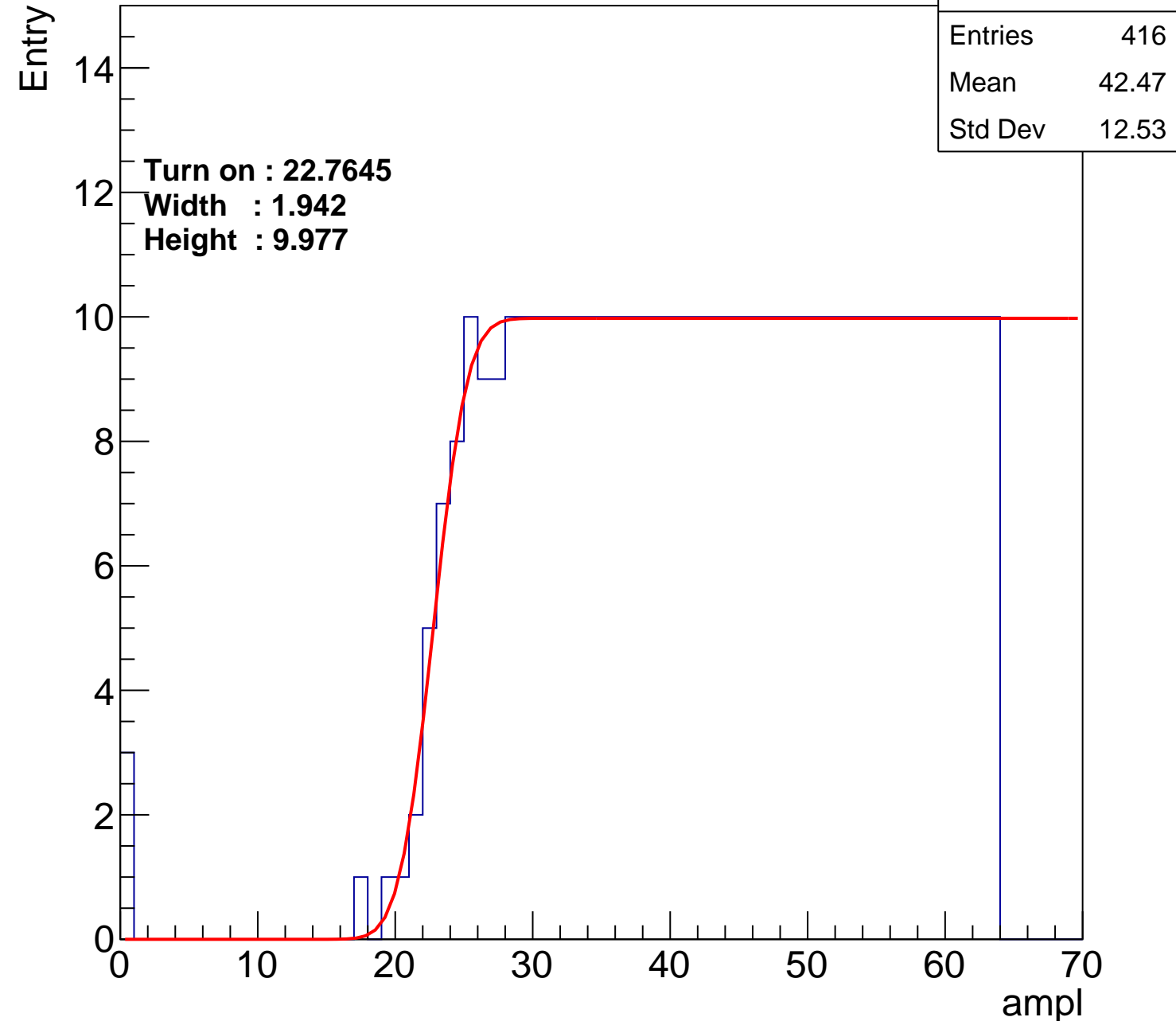
Width : 1.942

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch99

calib_packv5_042523_0143.root, FC#3, port B3

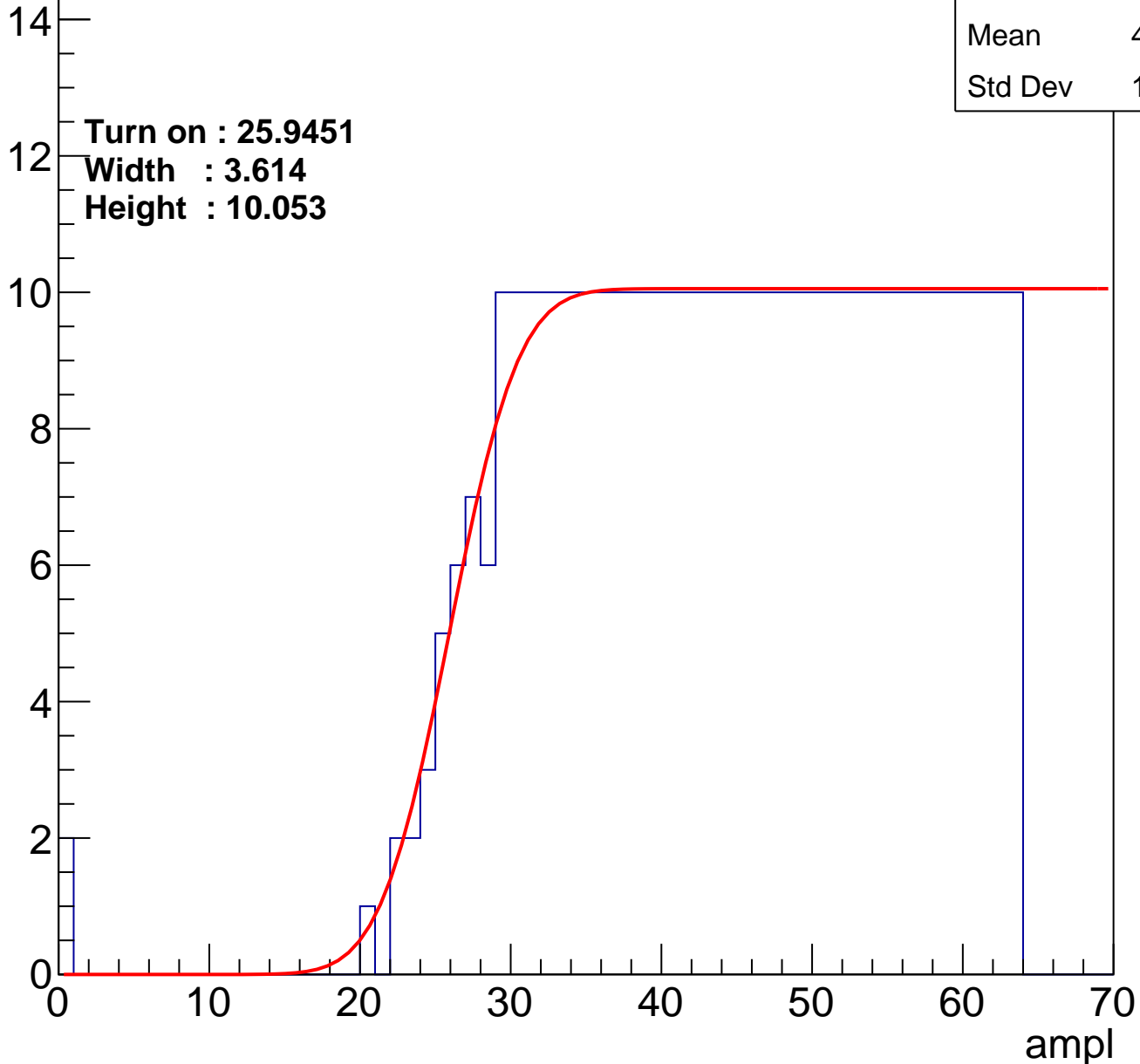
Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 25.9451

Width : 3.614

Height : 10.053

Entry



B1L000S, U5-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.8868

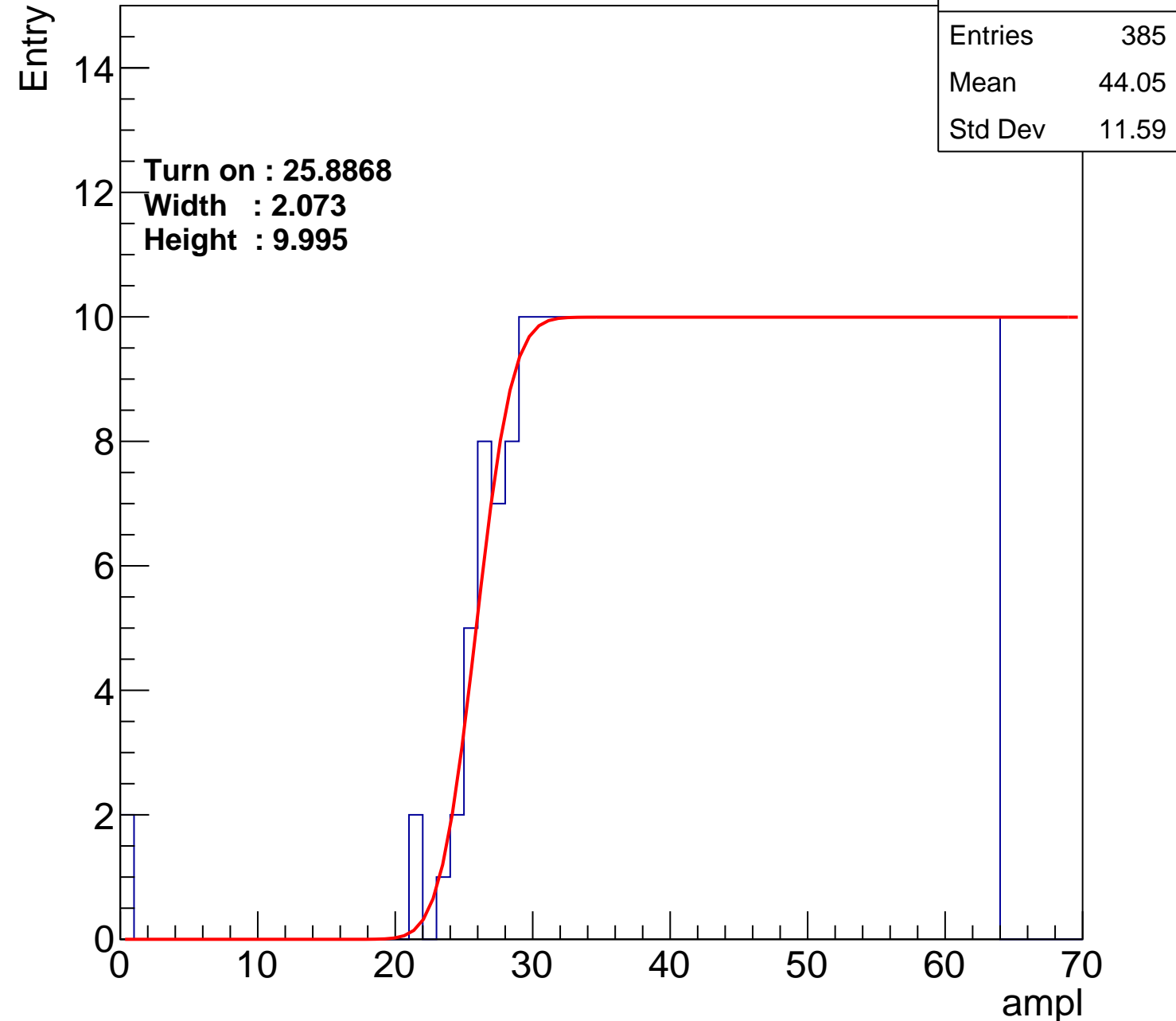
Width : 2.073

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch101

calib_packv5_042523_0143.root, FC#3, port B3

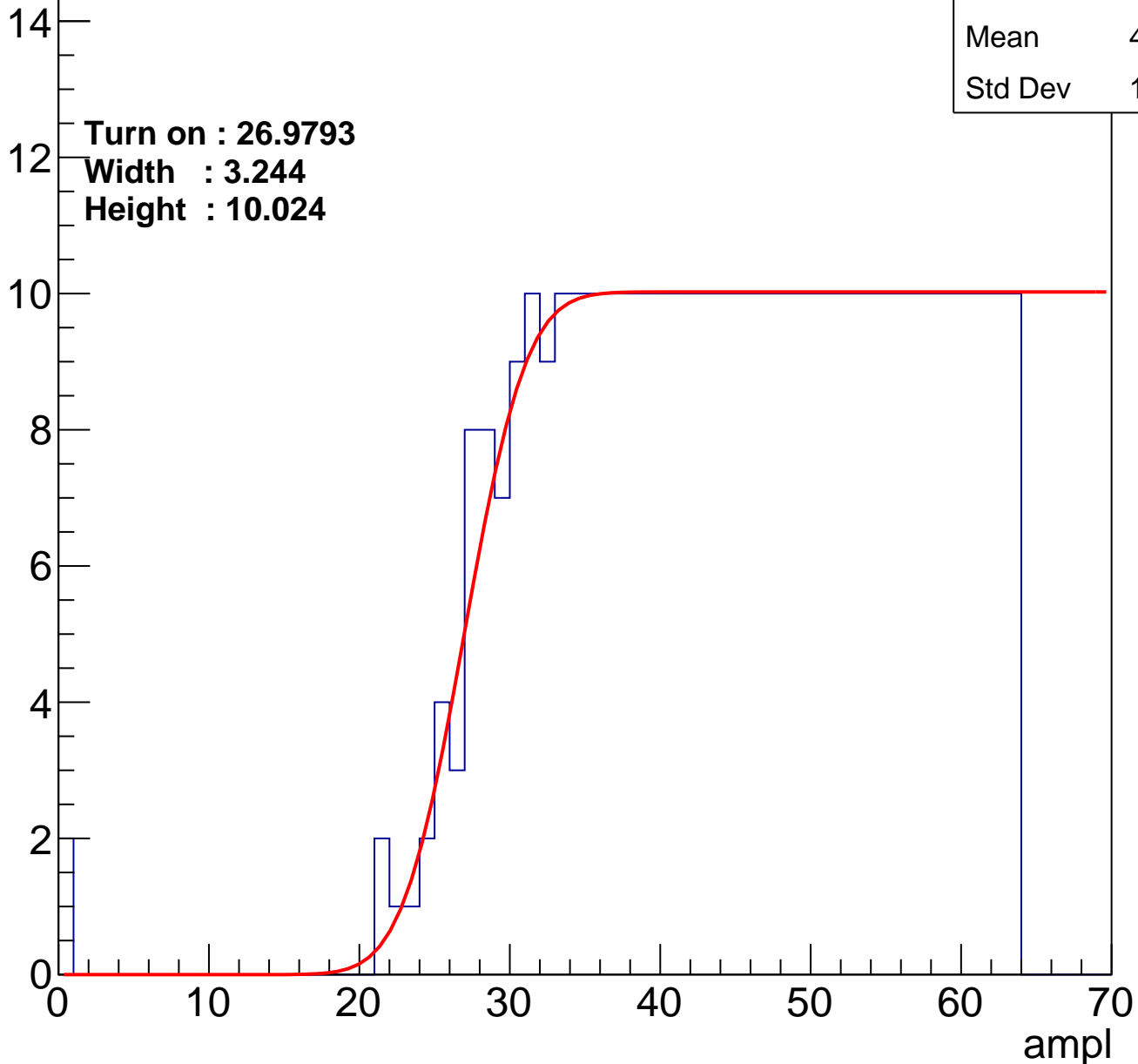
Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 26.9793

Width : 3.244

Height : 10.024

Entry



B1L000S, U5-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.99
Std Dev	11.57

Turn on : 26.2475

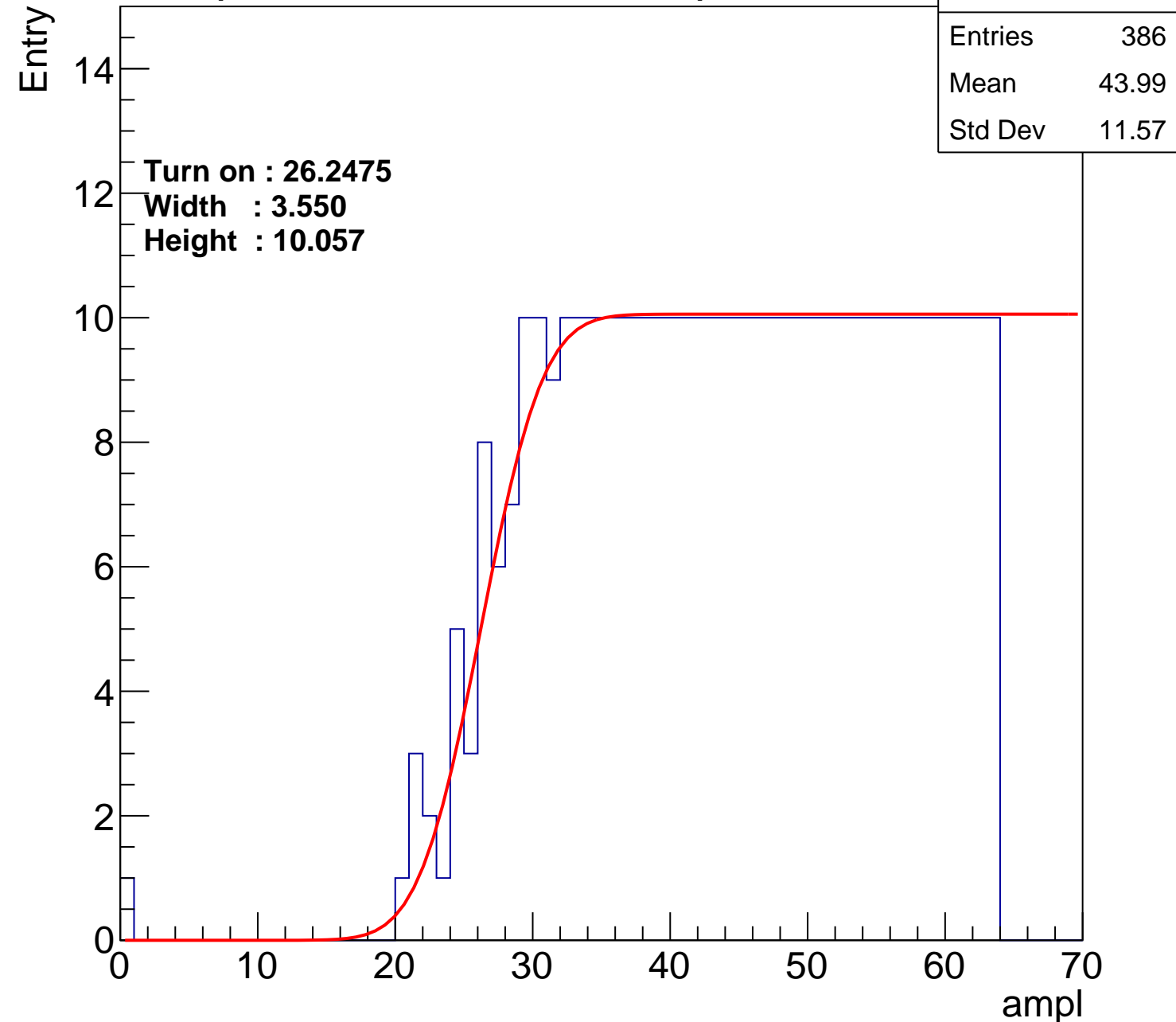
Width : 3.550

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch103

calib_packv5_042523_0143.root, FC#3, port B3

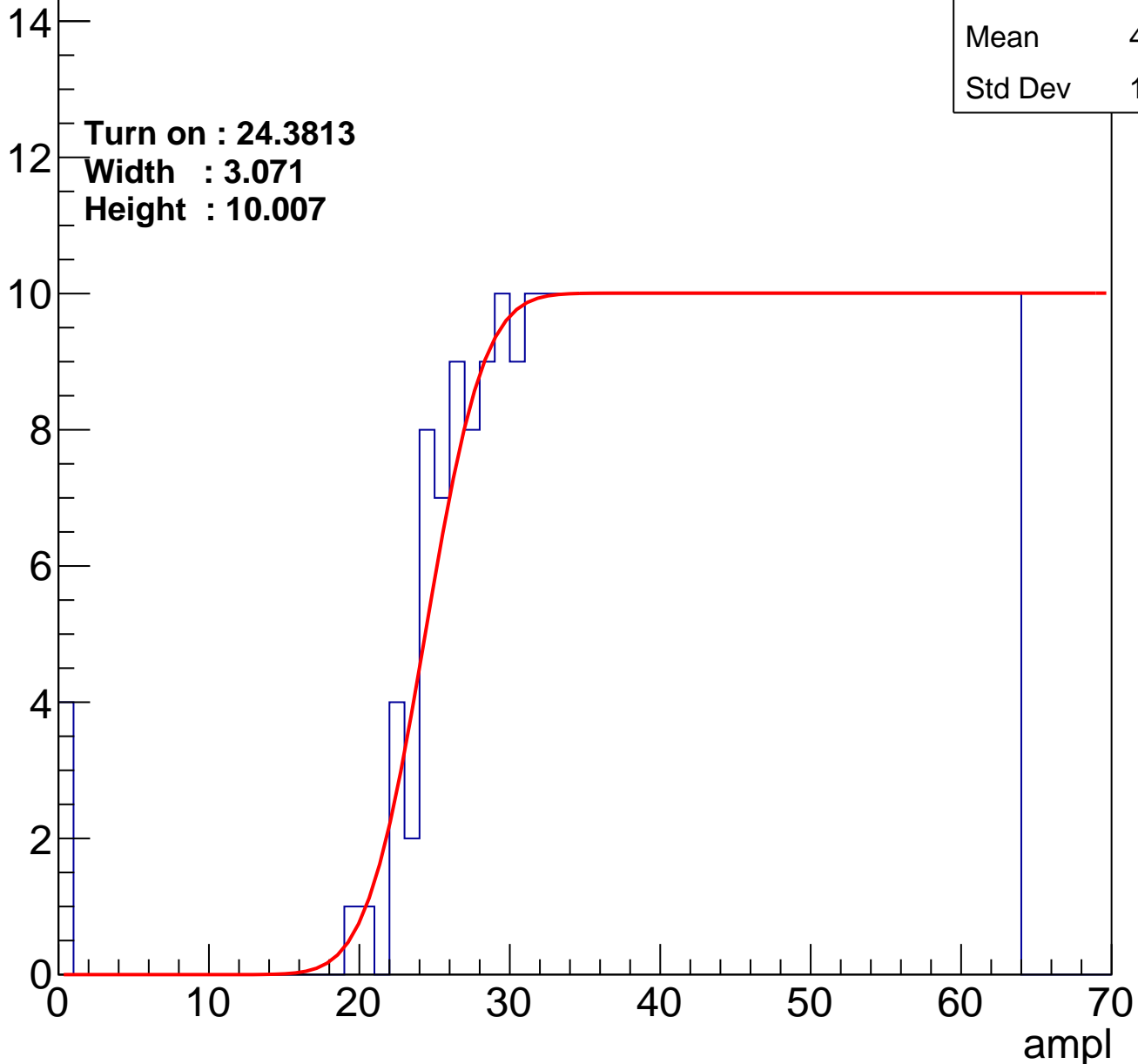
Entries	402
Mean	43.06
Std Dev	12.38

Turn on : 24.3813

Width : 3.071

Height : 10.007

Entry



B1L000S, U5-ch104

calib_packv5_042523_0143.root, FC#3, port B3

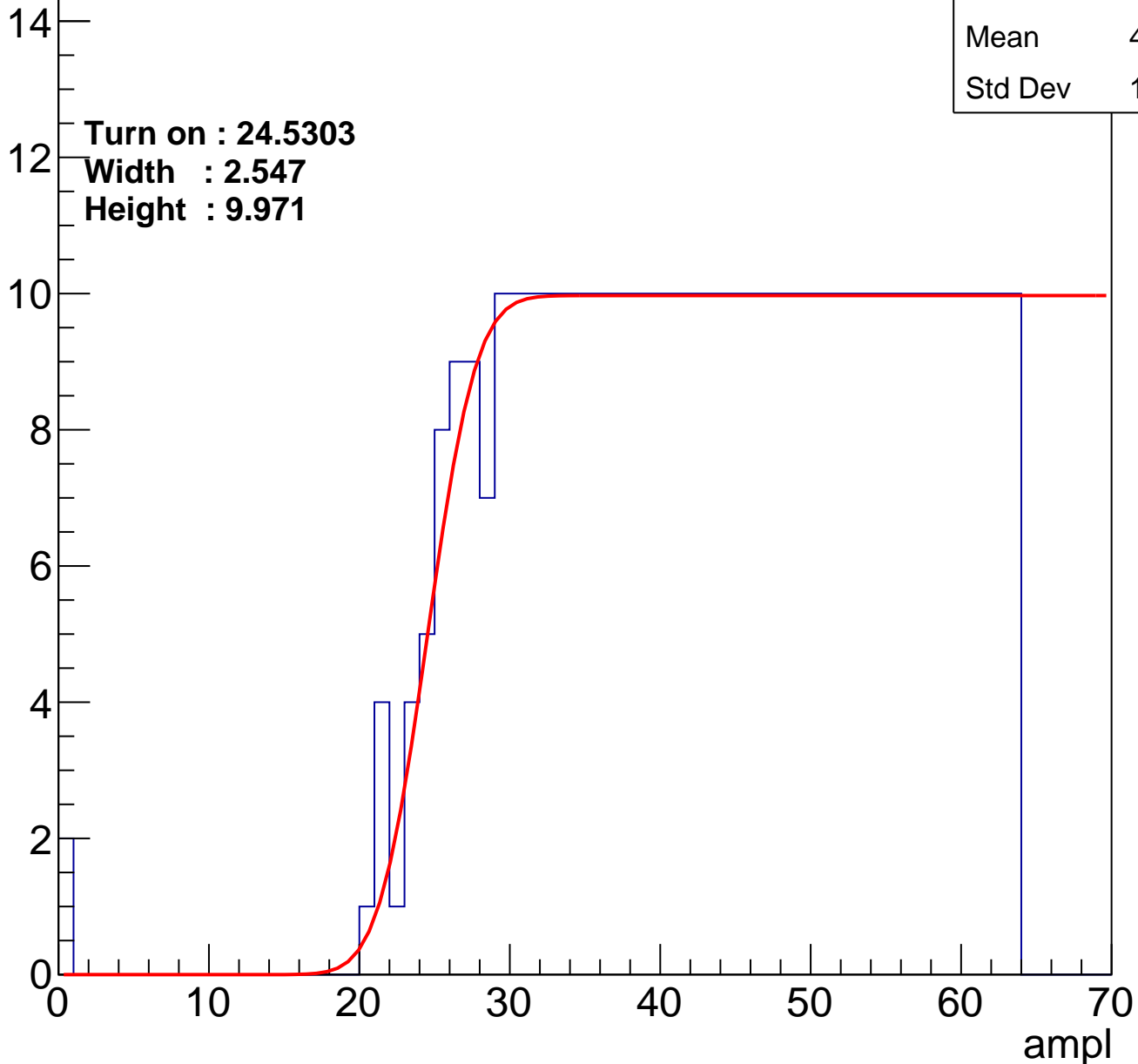
Entries	400
Mean	43.28
Std Dev	12.03

Turn on : 24.5303

Width : 2.547

Height : 9.971

Entry



B1L000S, U5-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.6467

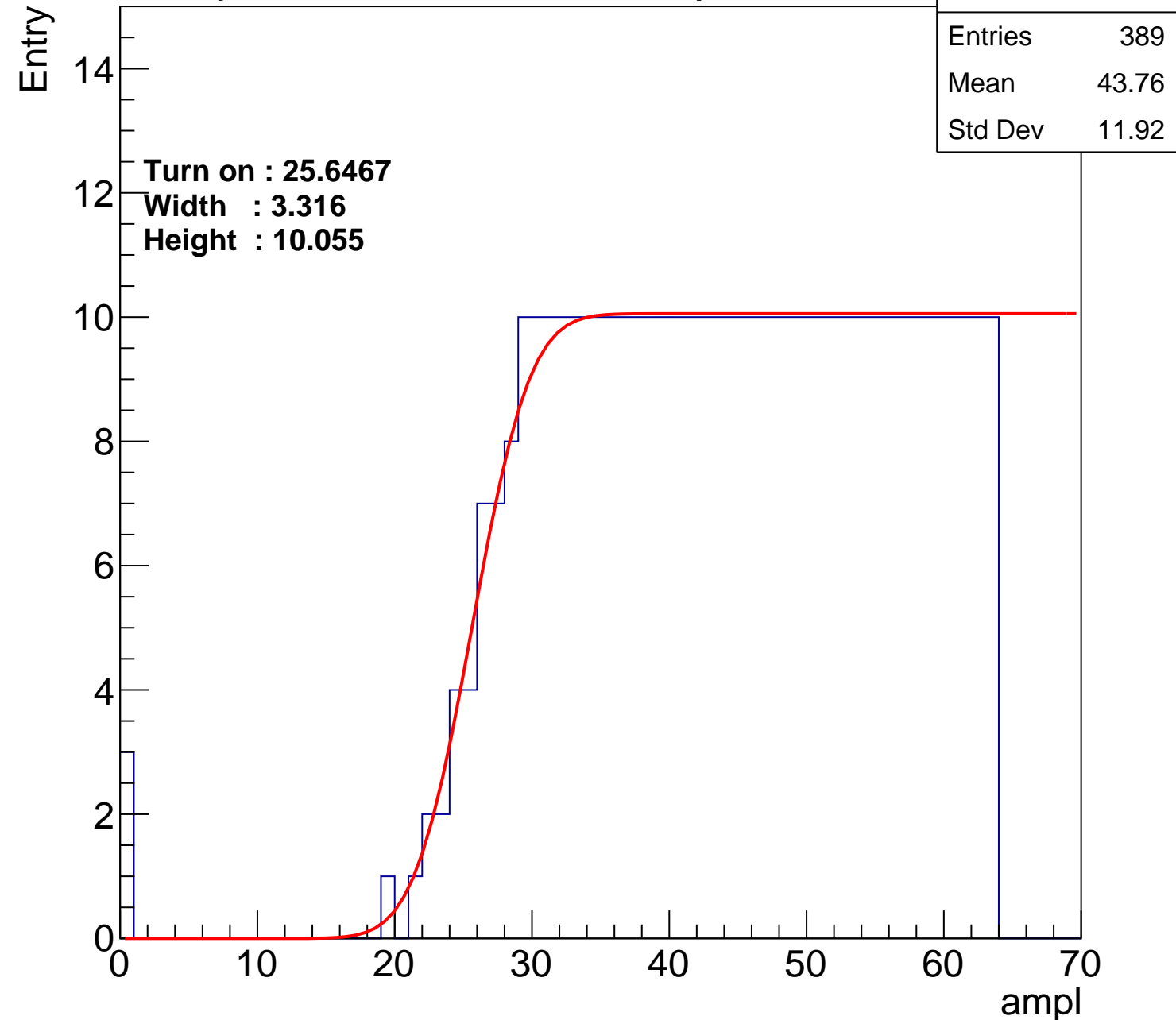
Width : 3.316

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch106

calib_packv5_042523_0143.root, FC#3, port B3

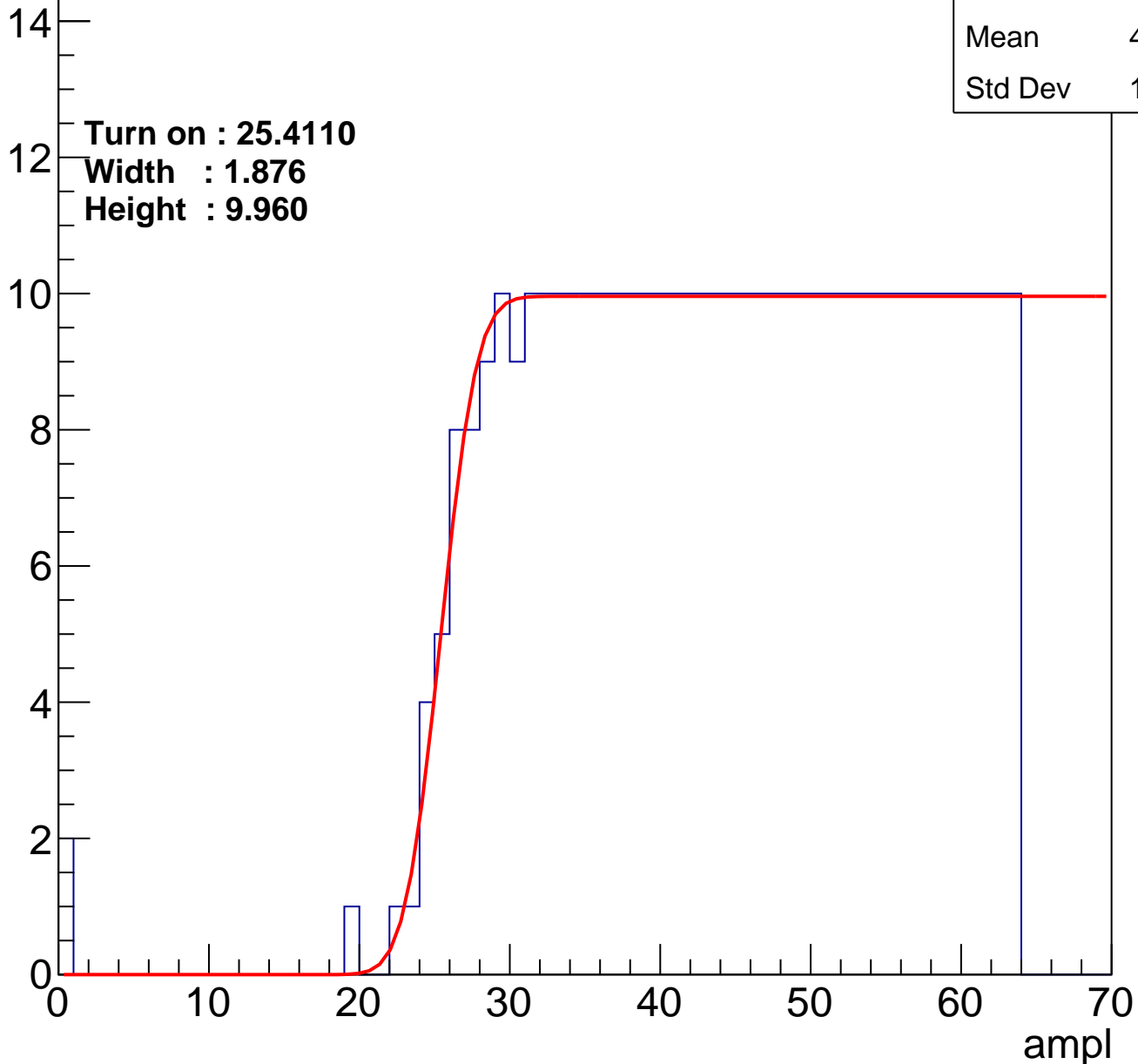
Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.4110

Width : 1.876

Height : 9.960

Entry



B1L000S, U5-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.7707

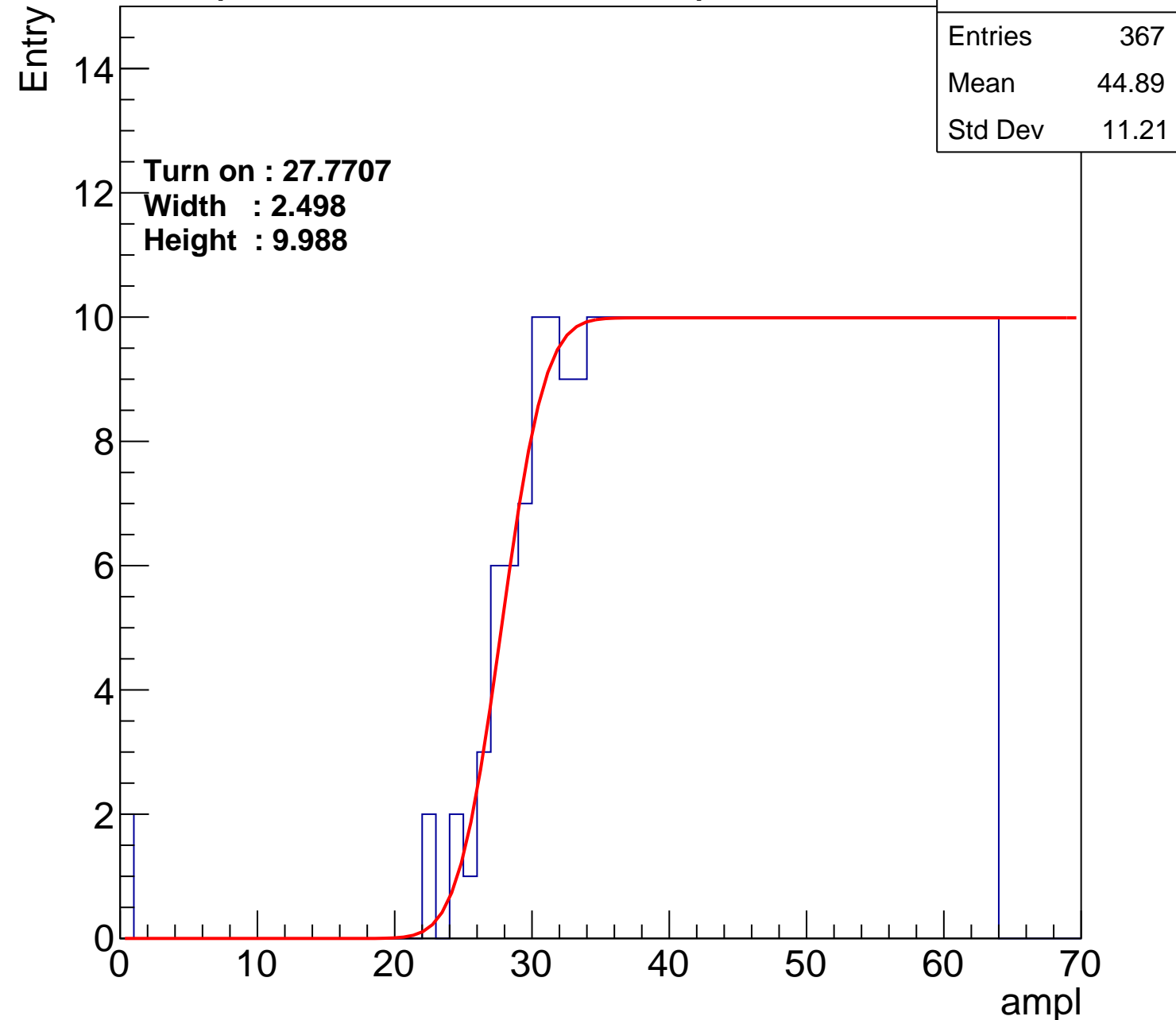
Width : 2.498

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch108

calib_packv5_042523_0143.root, FC#3, port B3

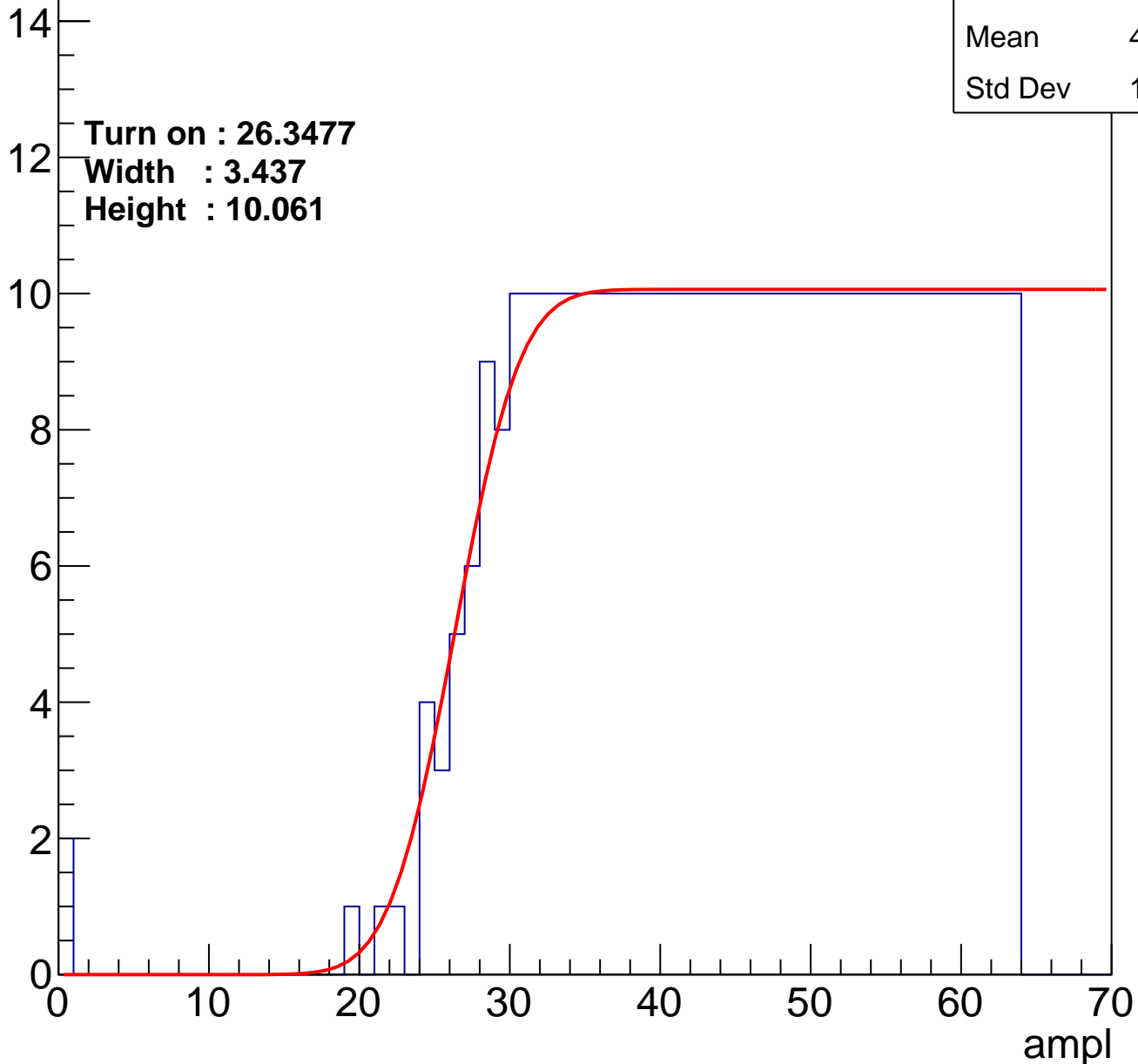
Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.3477

Width : 3.437

Height : 10.061

Entry



B1L000S, U5-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.54
Std Dev	12.05

Turn on : 25.1121

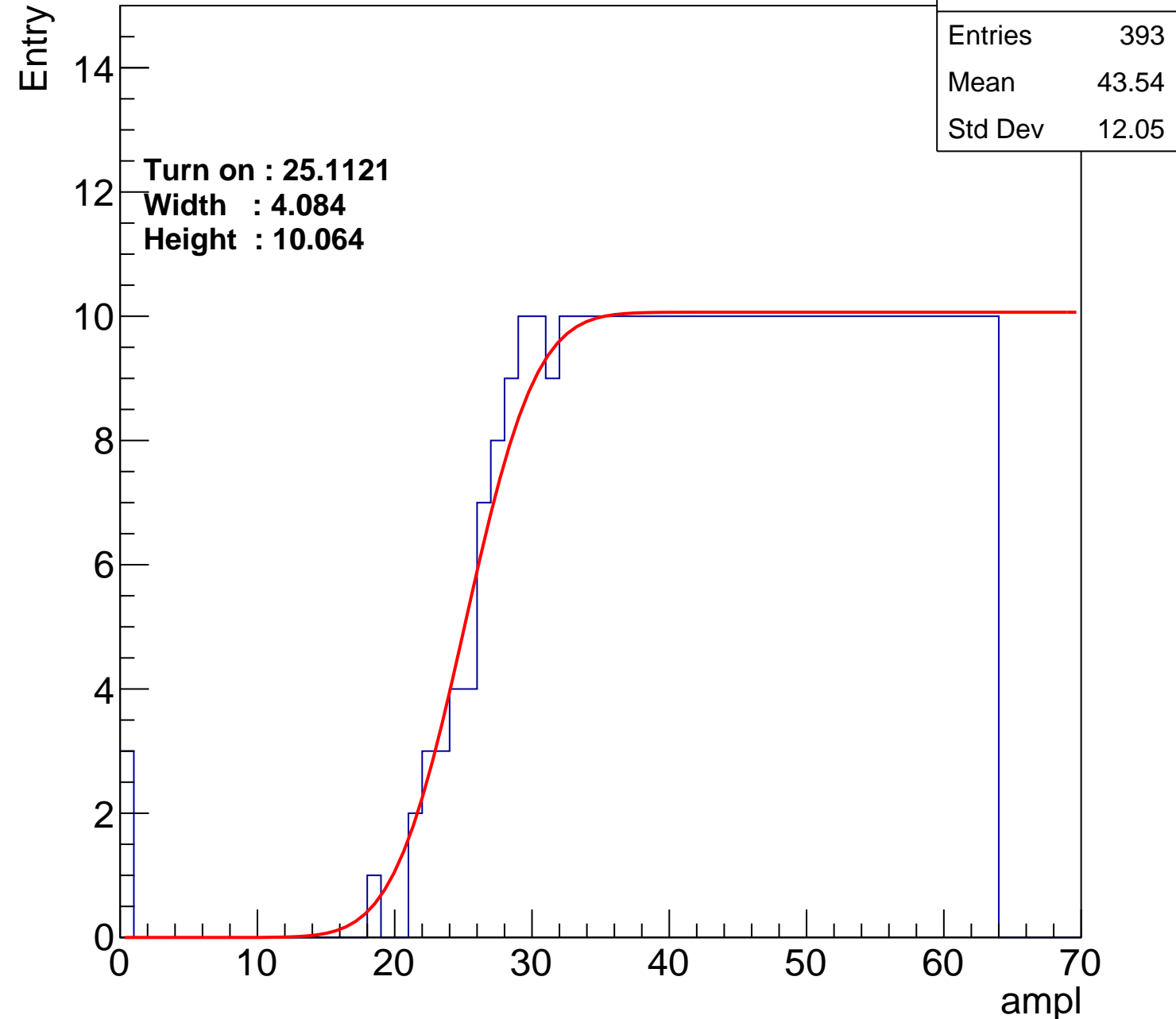
Width : 4.084

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch110

calib_packv5_042523_0143.root, FC#3, port B3

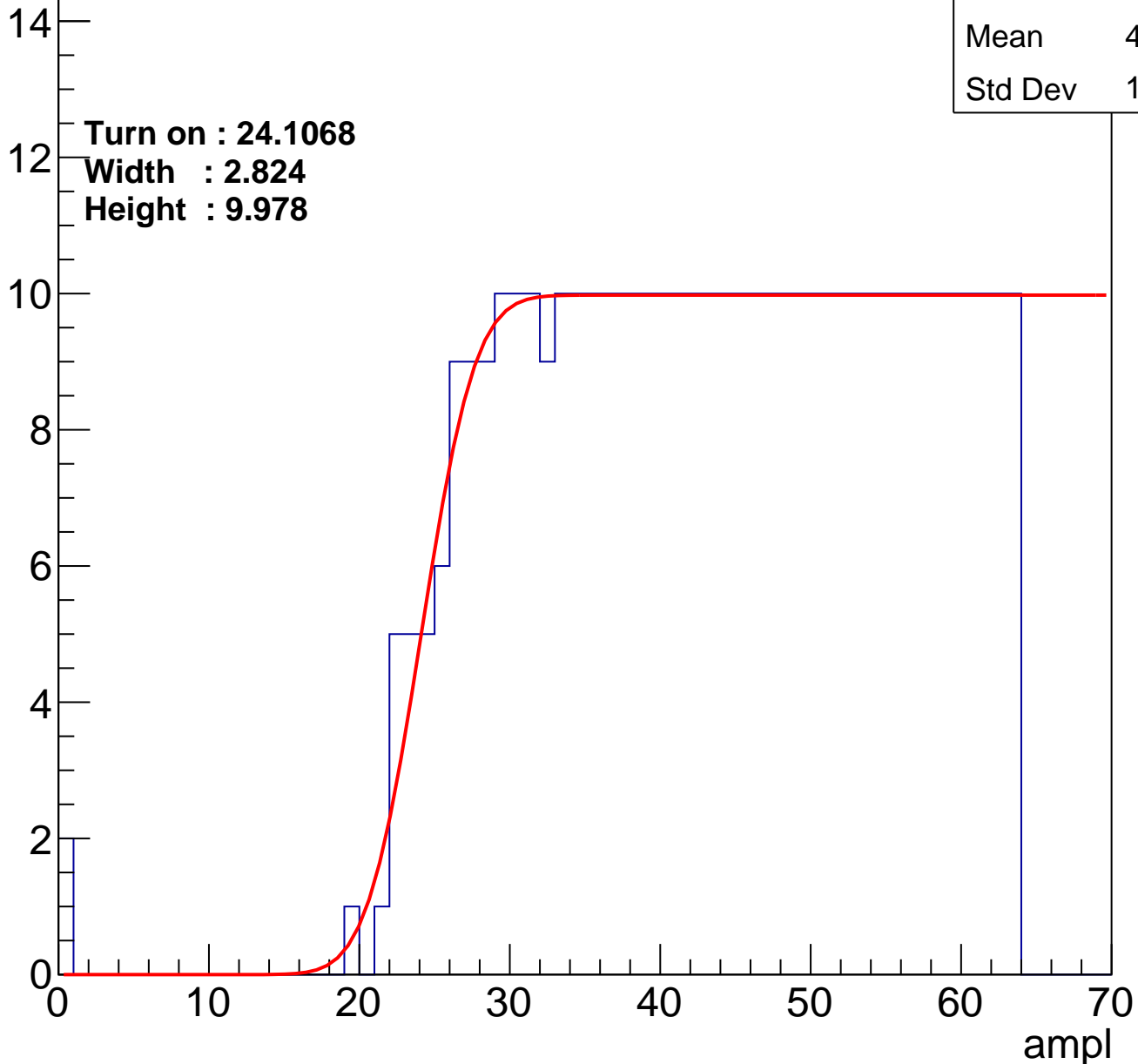
Entries	401
Mean	43.22
Std Dev	12.06

Turn on : 24.1068

Width : 2.824

Height : 9.978

Entry



B1L000S, U5-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.3436

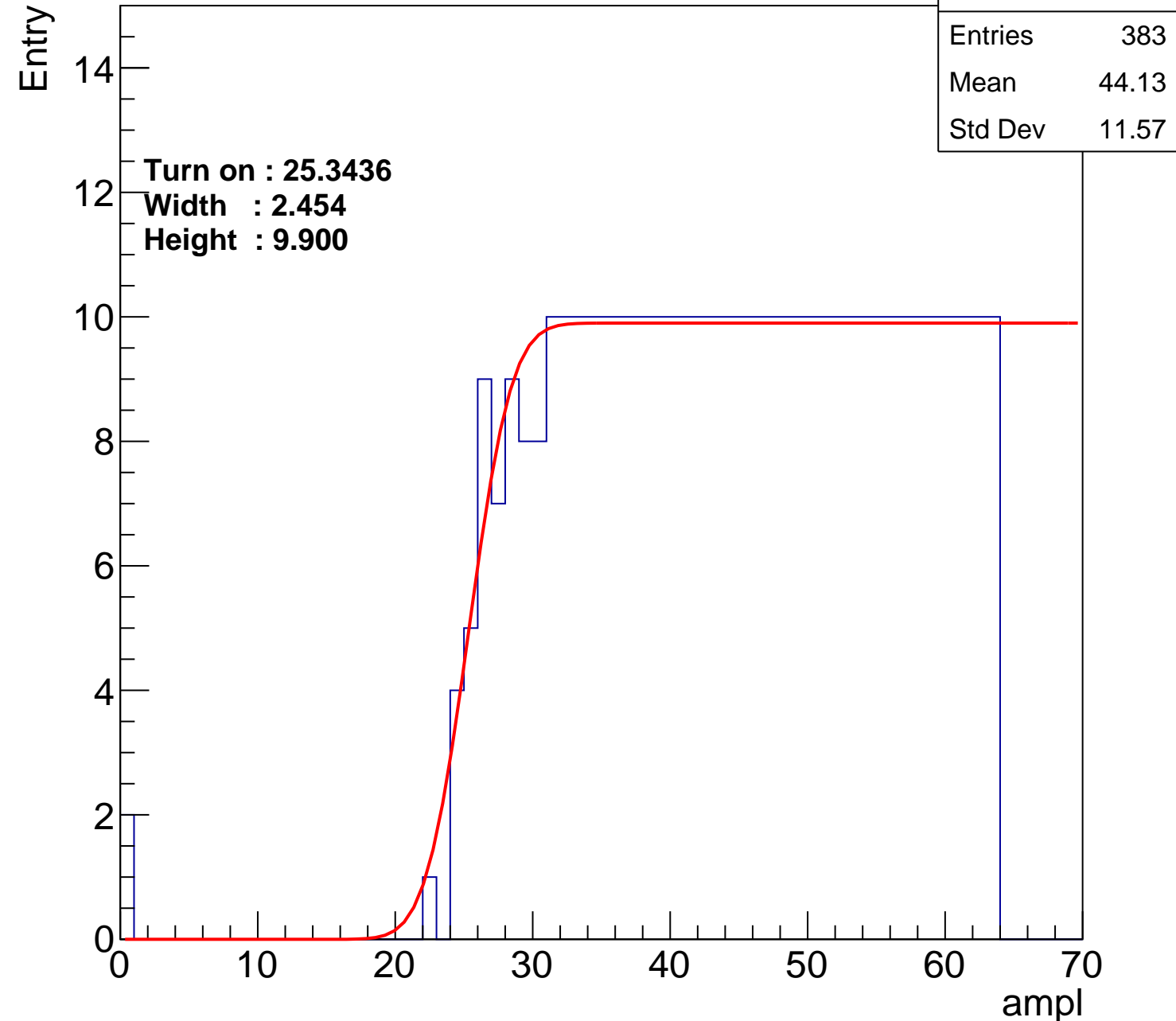
Width : 2.454

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch112

calib_packv5_042523_0143.root, FC#3, port B3

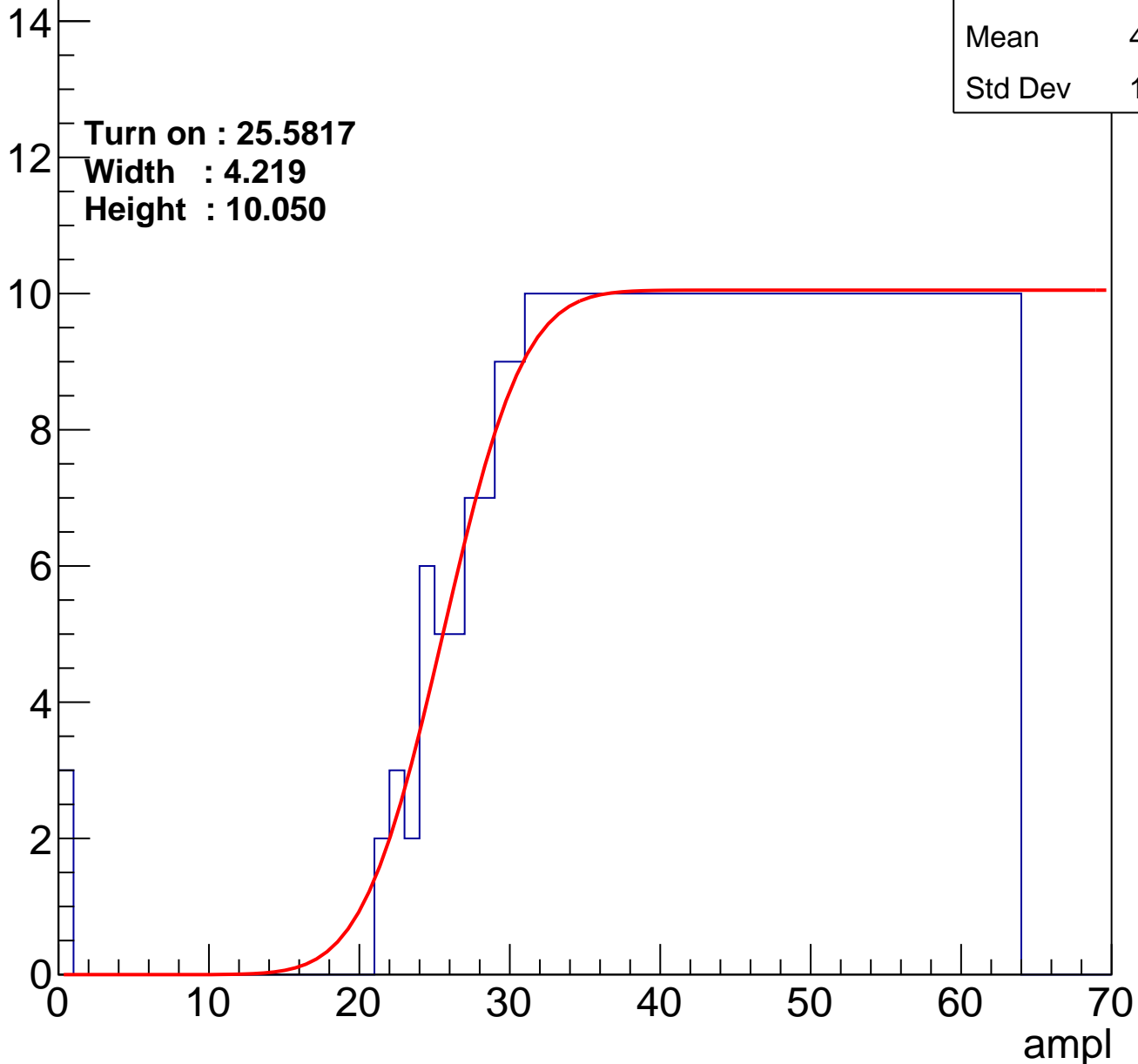
Entries	388
Mean	43.76
Std Dev	11.96

Turn on : 25.5817

Width : 4.219

Height : 10.050

Entry



B1L000S, U5-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.55
Std Dev	12.03

Turn on : 24.9624

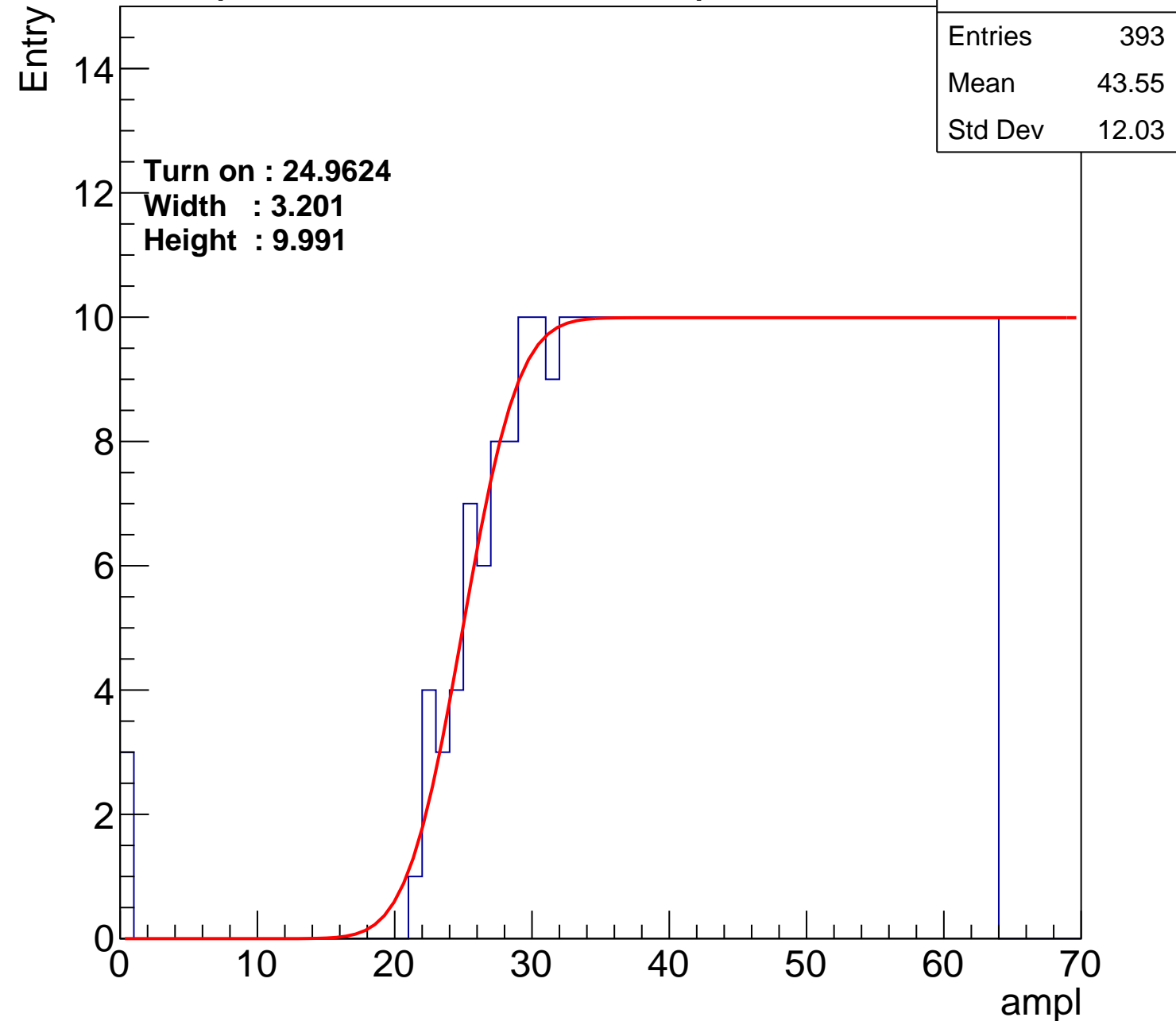
Width : 3.201

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

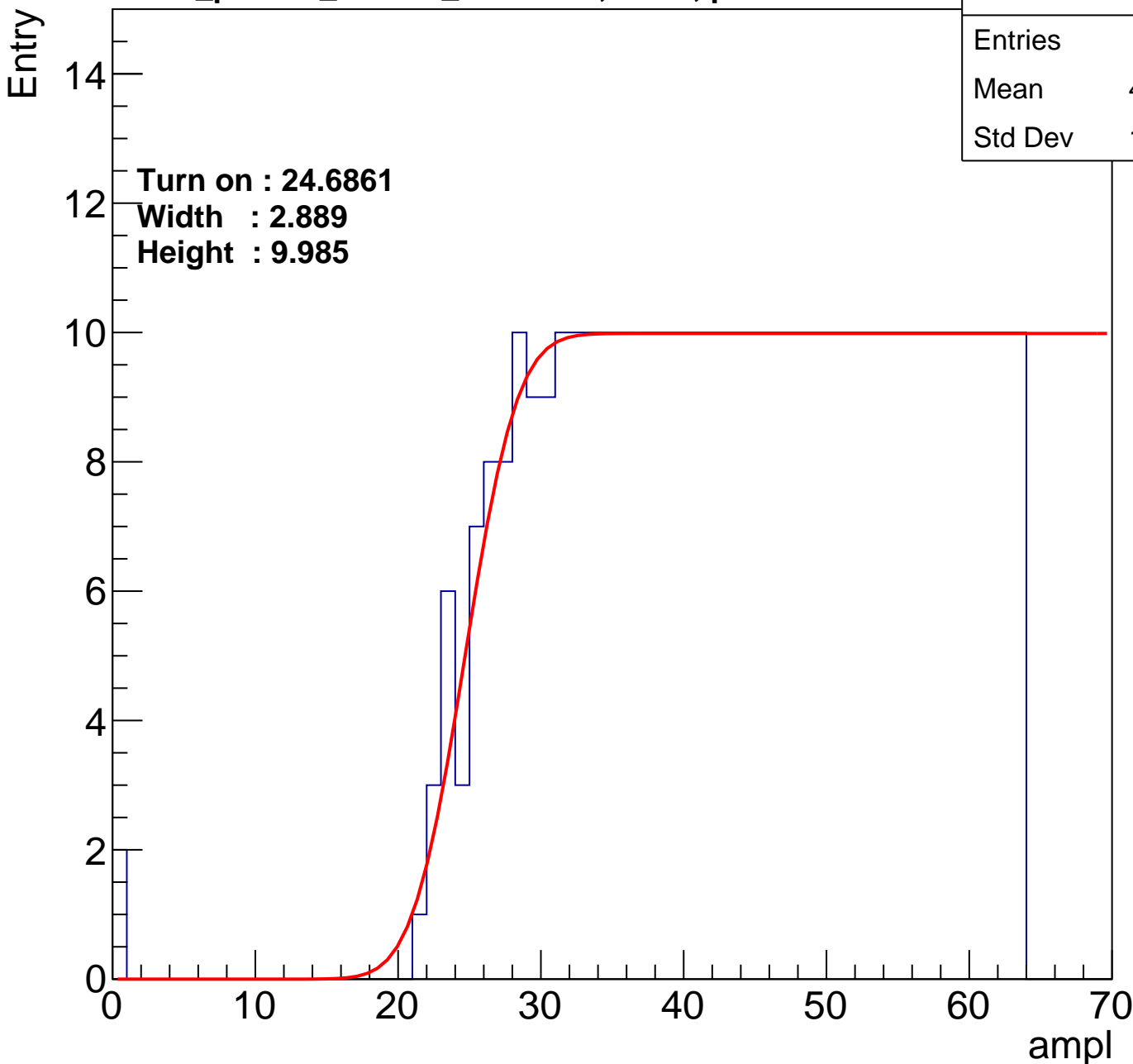
Entries	396
Mean	43.48
Std Dev	11.92

Mean	43.48
------	-------

Std Dev	11.92
---------	-------

Width : 2.889

Height : 9.985



B1L000S, U5-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 24.6585

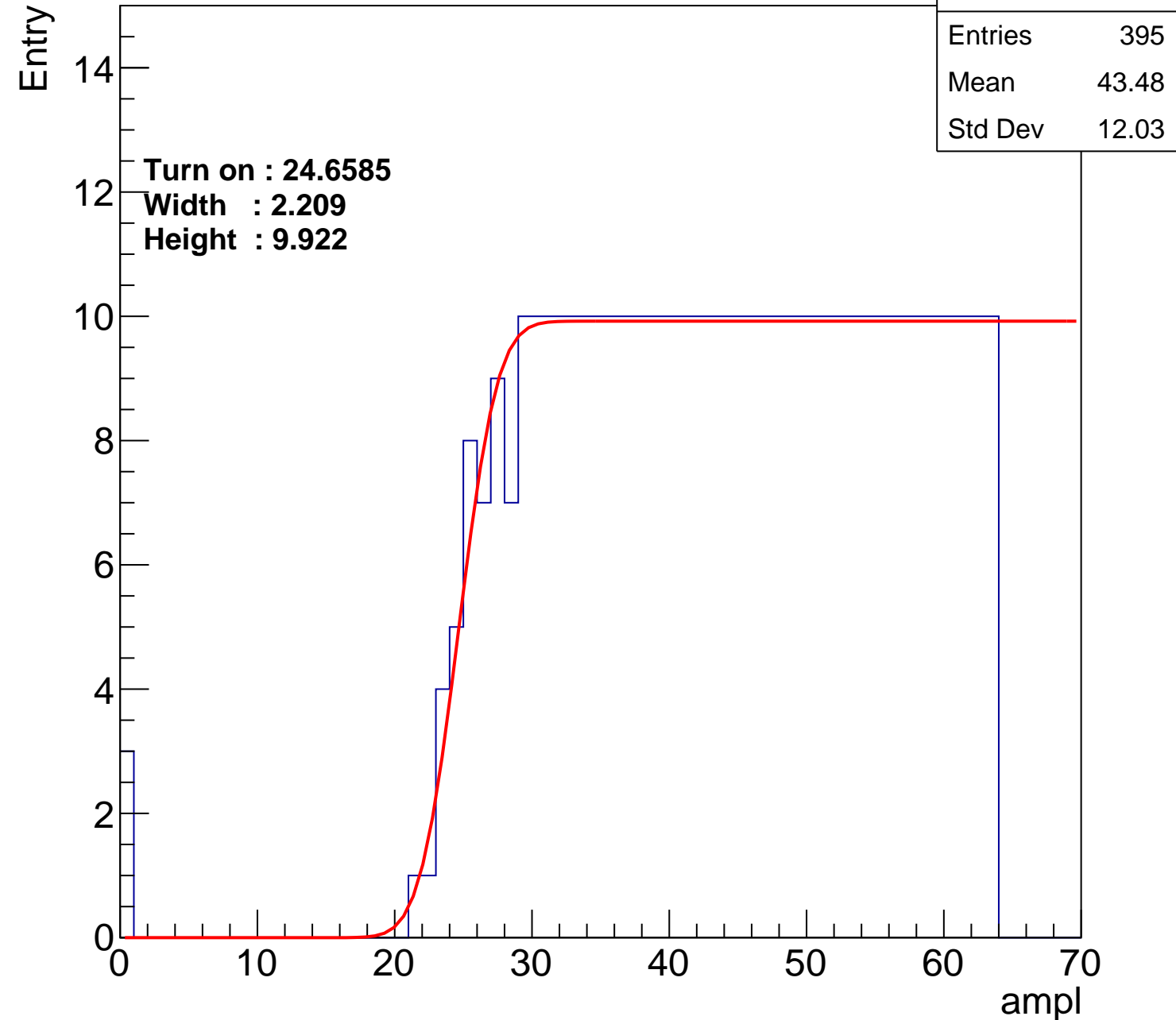
Width : 2.209

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.1
Std Dev	11.91

Turn on : 26.2870

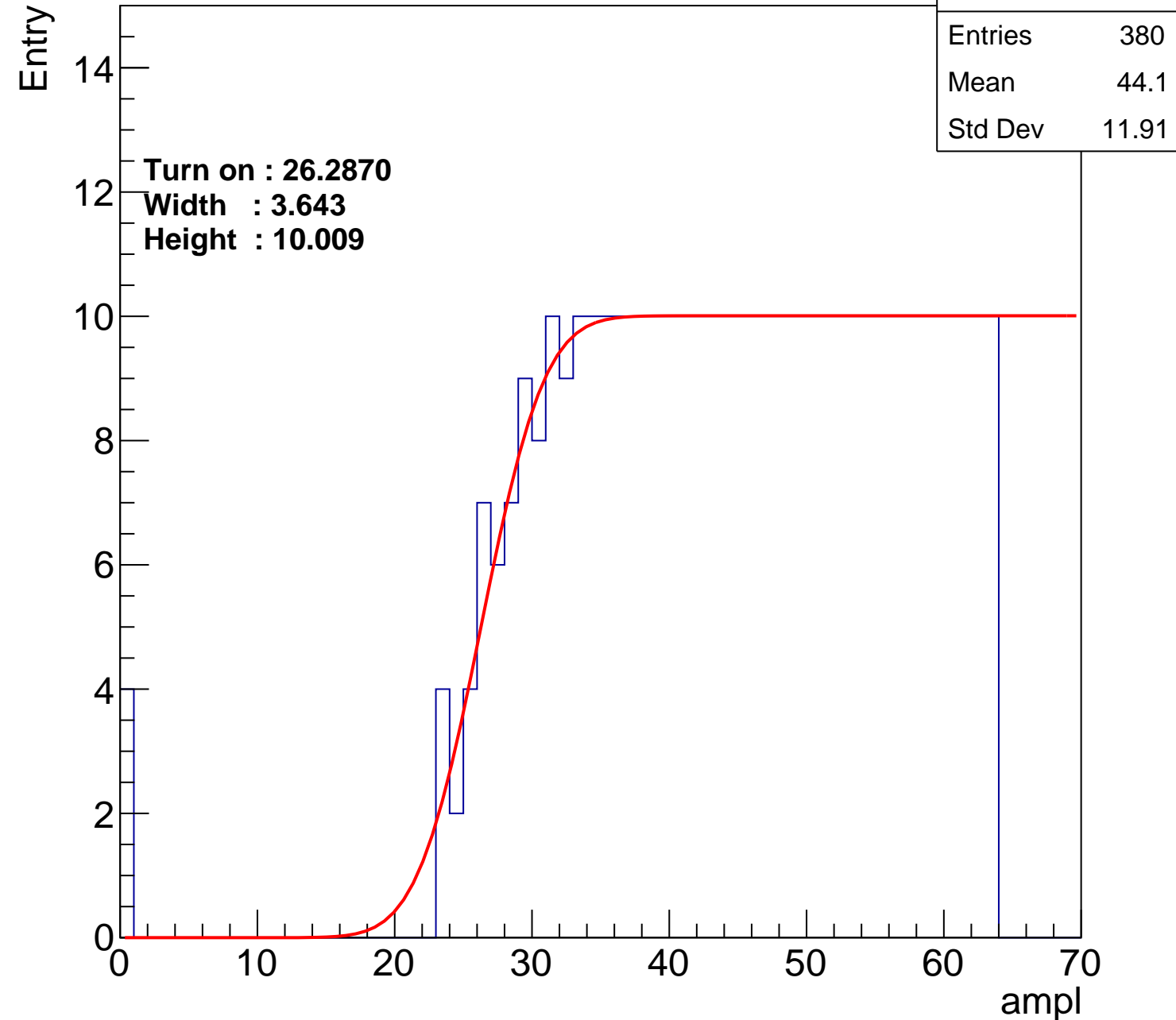
Width : 3.643

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.19
Std Dev	11.76

Turn on : 26.1270

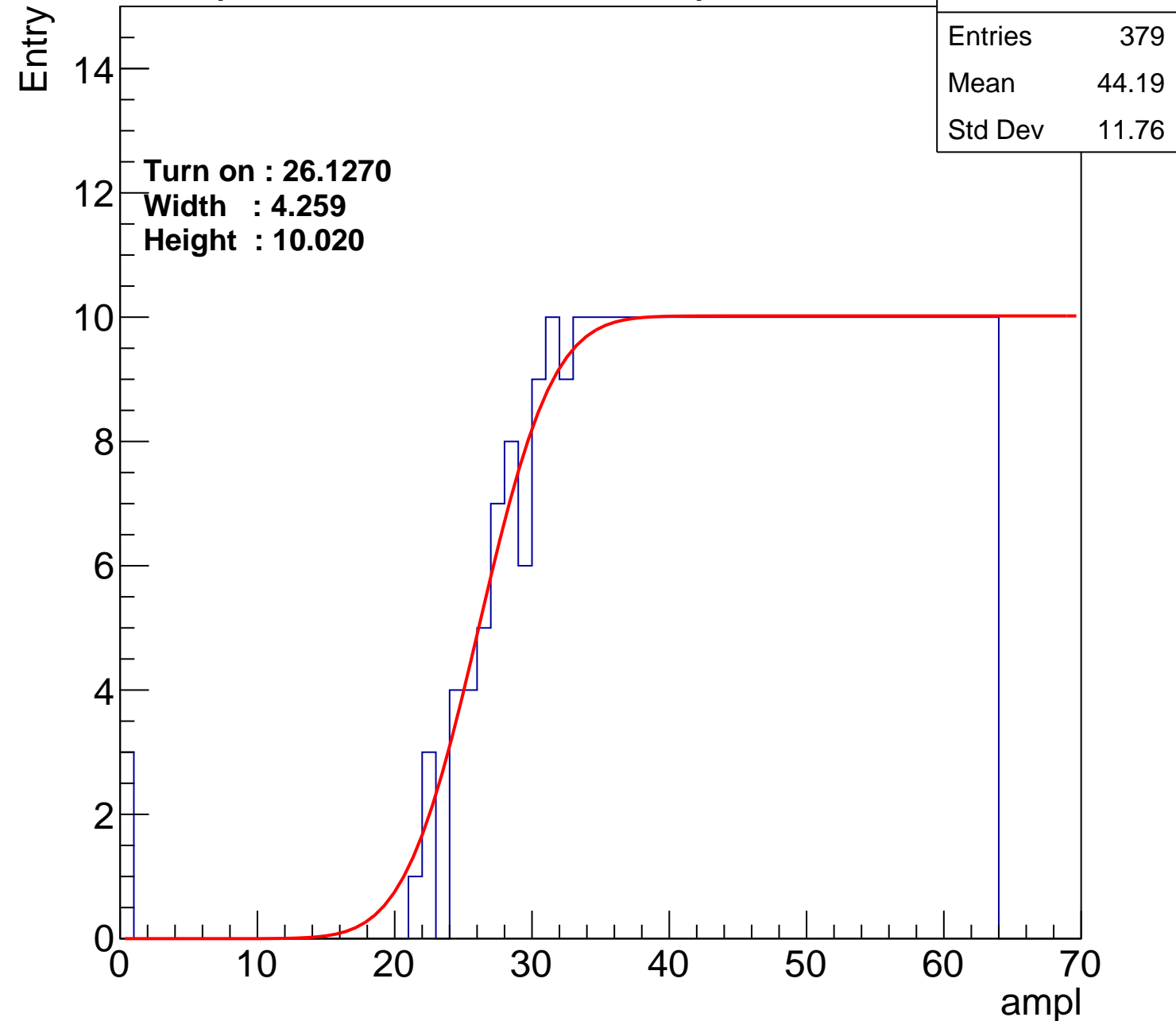
Width : 4.259

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch118

calib_packv5_042523_0143.root, FC#3, port B3

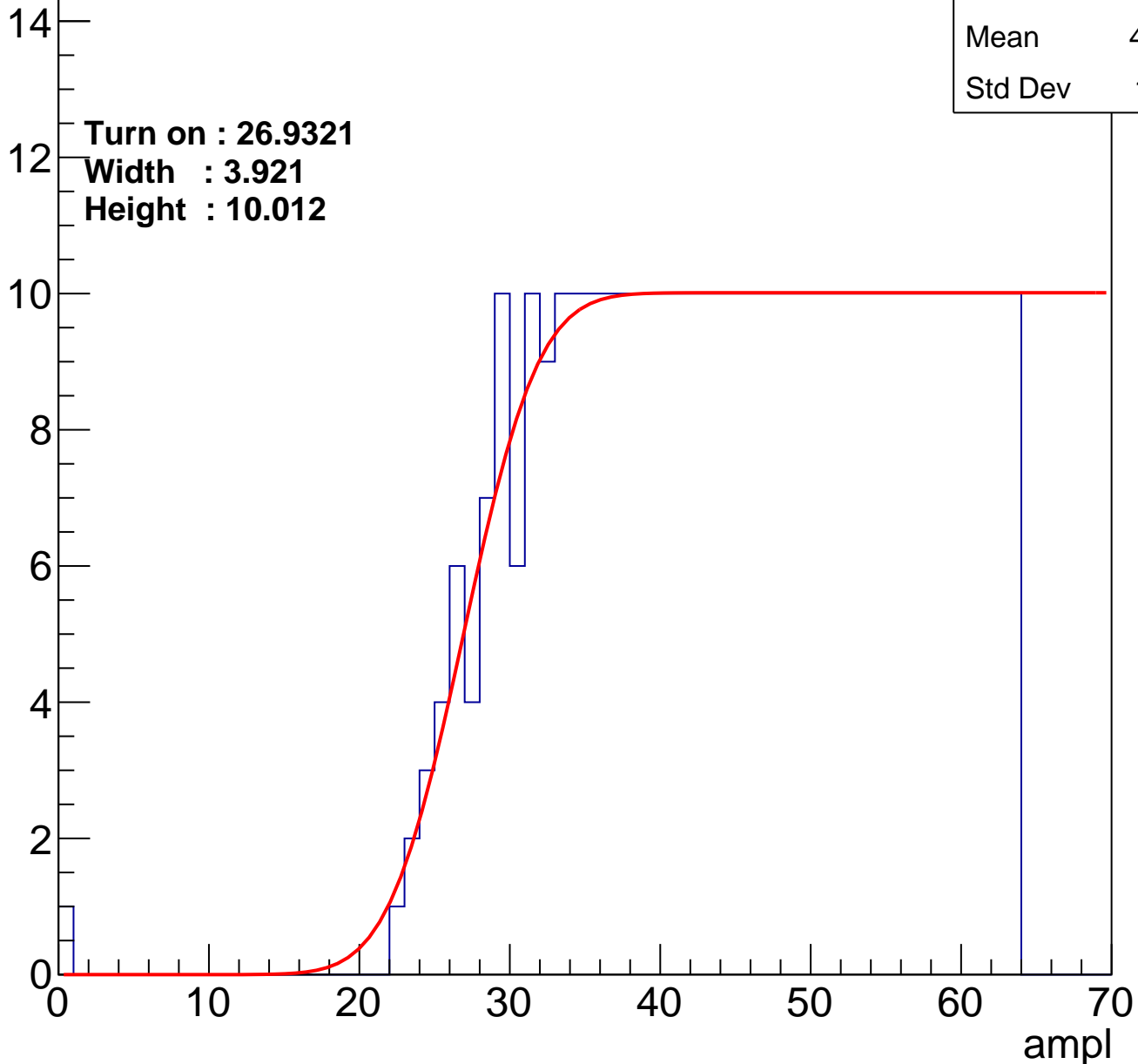
Entries	373
Mean	44.63
Std Dev	11.21

Turn on : 26.9321

Width : 3.921

Height : 10.012

Entry



B1L000S, U5-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.4634

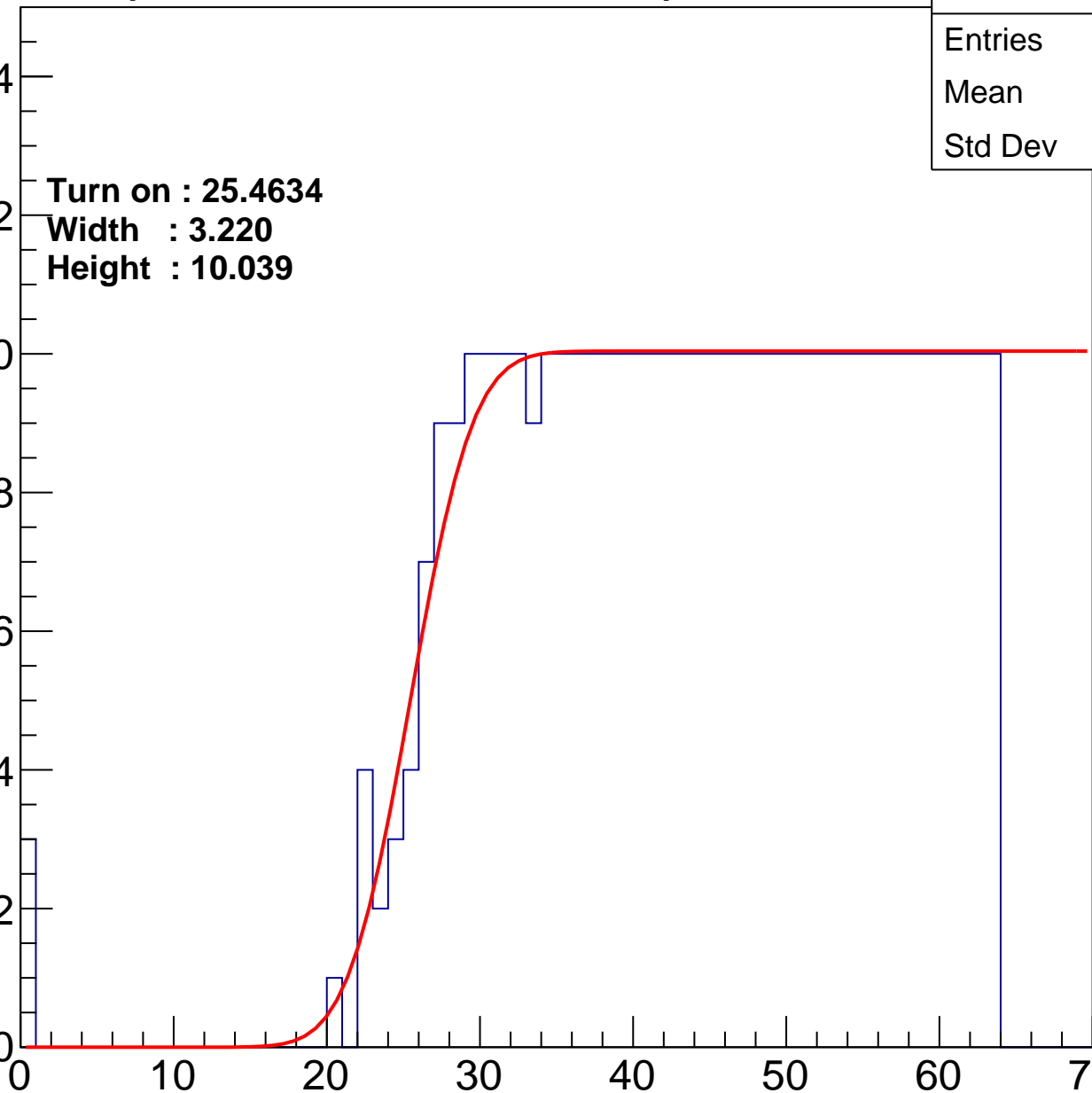
Width : 3.220

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch120

calib_packv5_042523_0143.root, FC#3, port B3

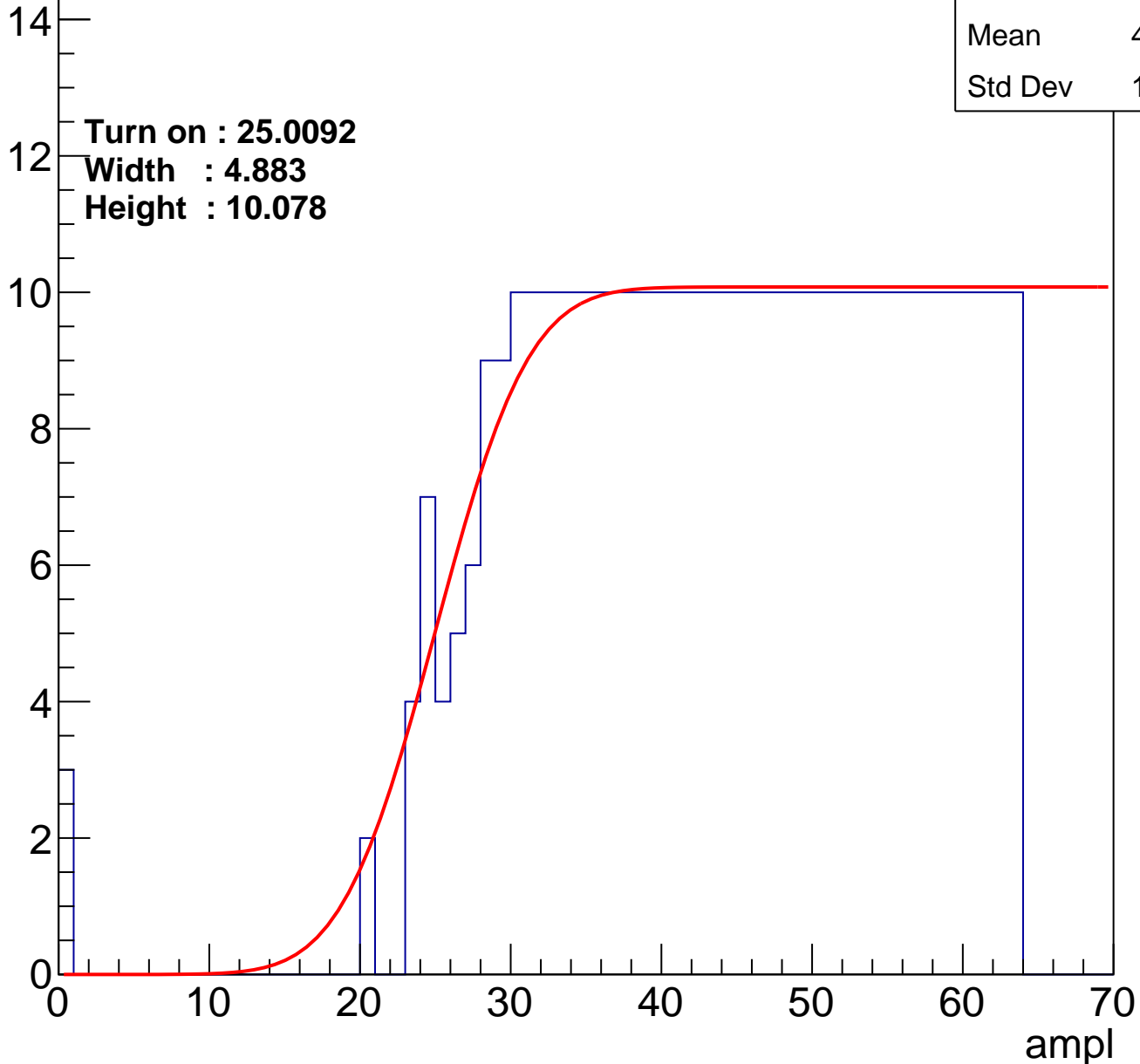
Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 25.0092

Width : 4.883

Height : 10.078

Entry



B1L000S, U5-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.52
Std Dev	12.03

Turn on : 25.1162

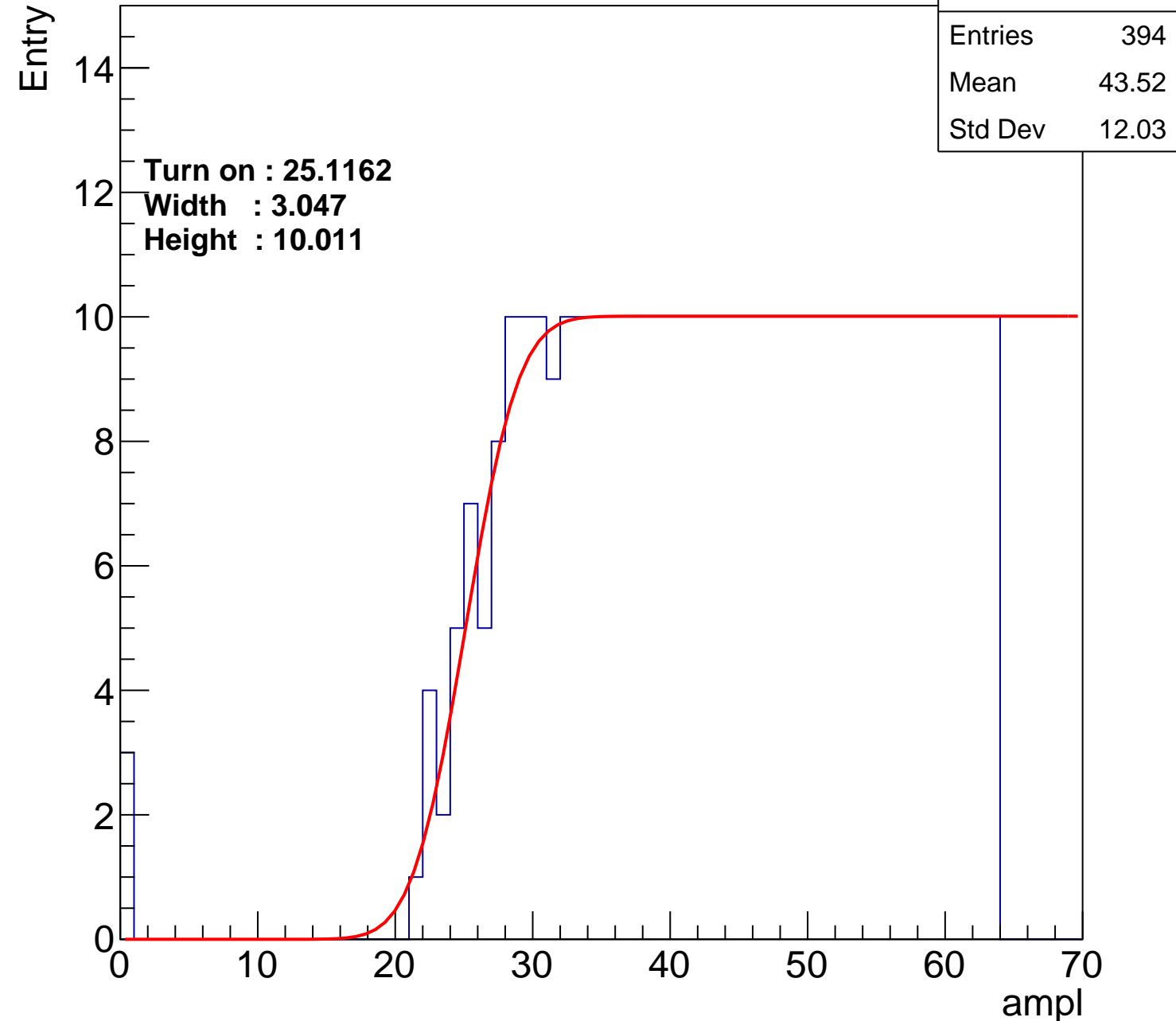
Width : 3.047

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.46
Std Dev	12.58

Turn on : 25.9995

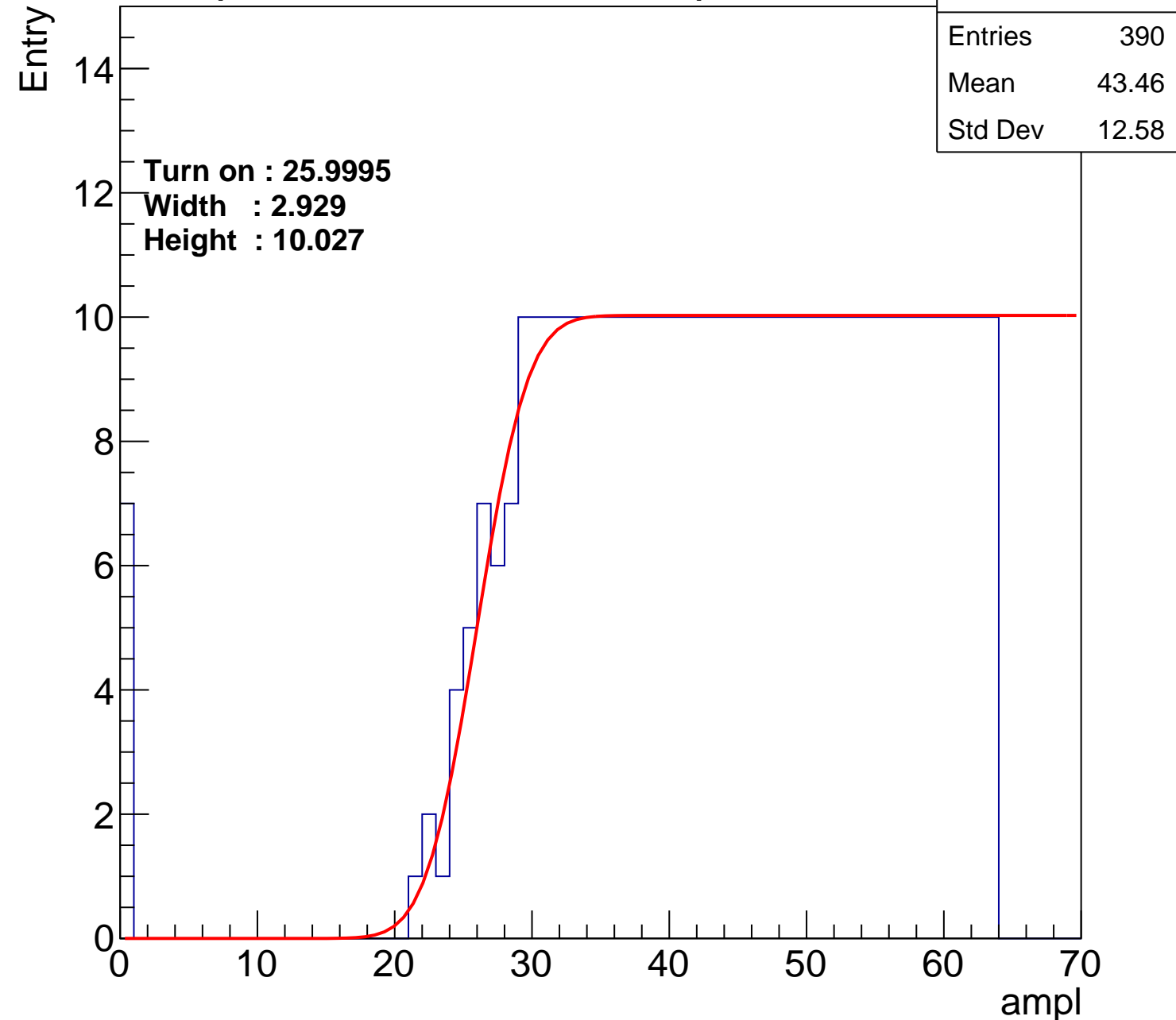
Width : 2.929

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U5-ch123

calib_packv5_042523_0143.root, FC#3, port B3

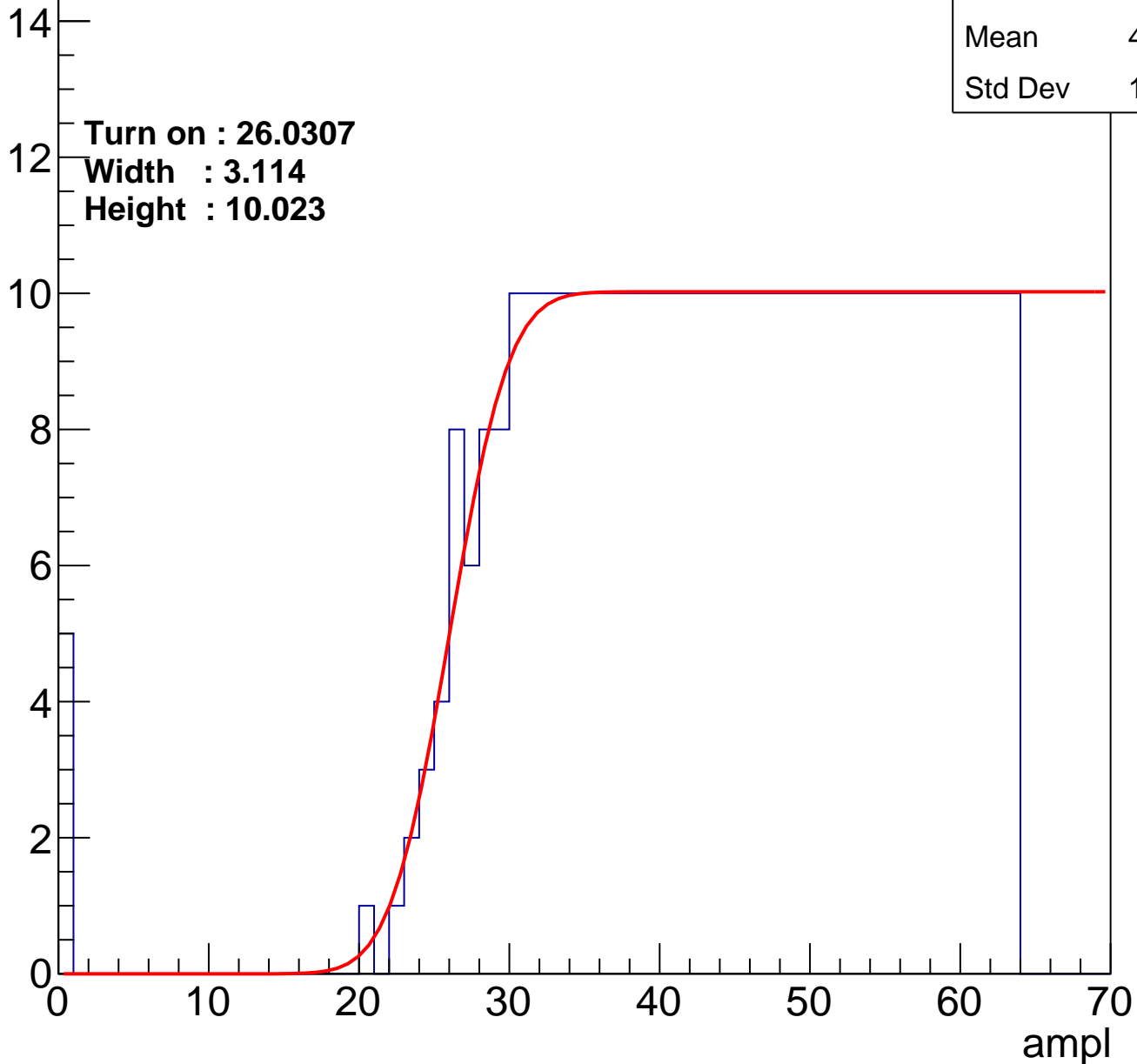
Entries	386
Mean	43.77
Std Dev	12.18

Turn on : 26.0307

Width : 3.114

Height : 10.023

Entry



B1L000S, U5-ch124

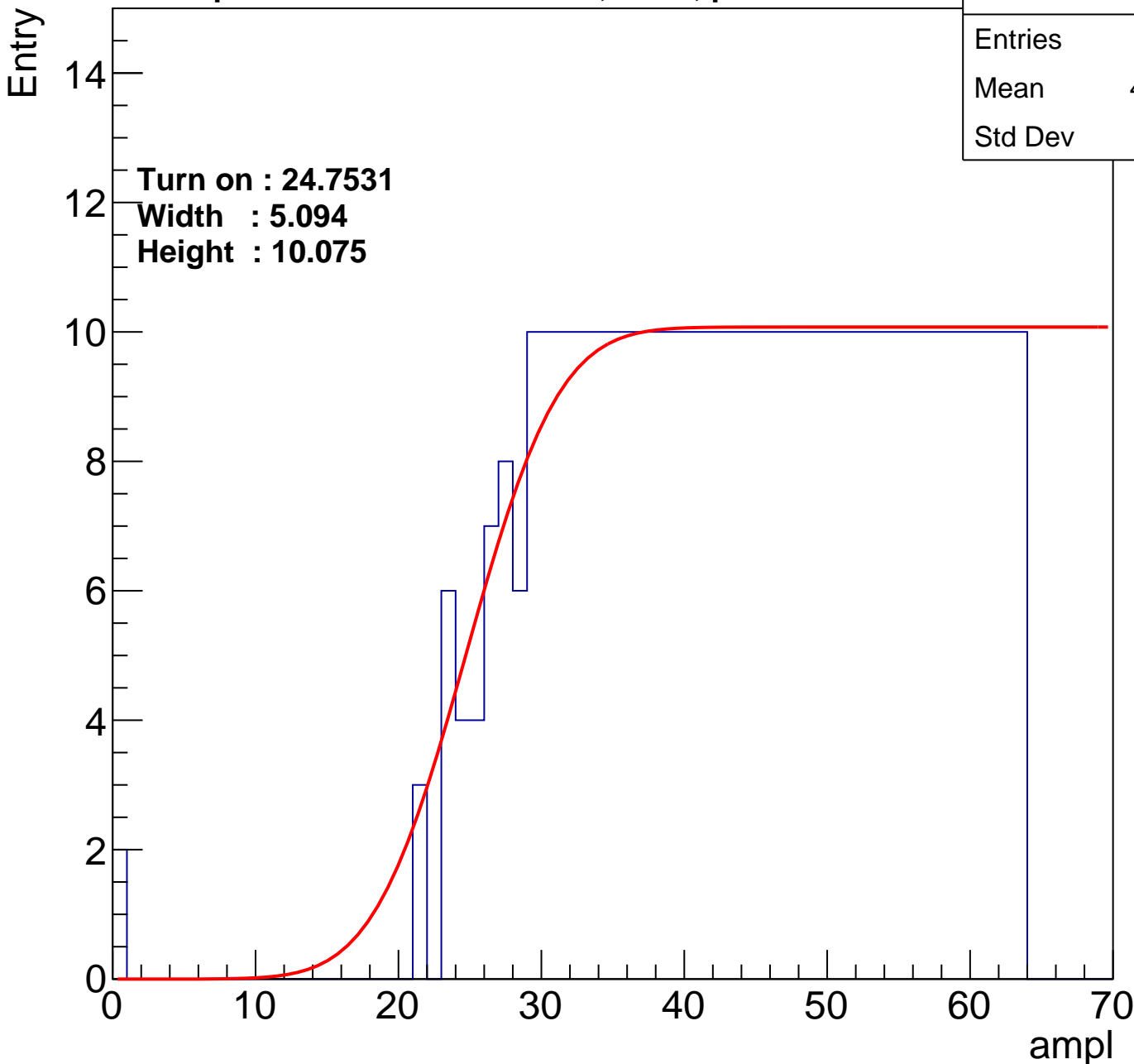
calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.75
Std Dev	11.8

Turn on : 24.7531

Width : 5.094

Height : 10.075



B1L000S, U5-ch125

calib_packv5_042523_0143.root, FC#3, port B3

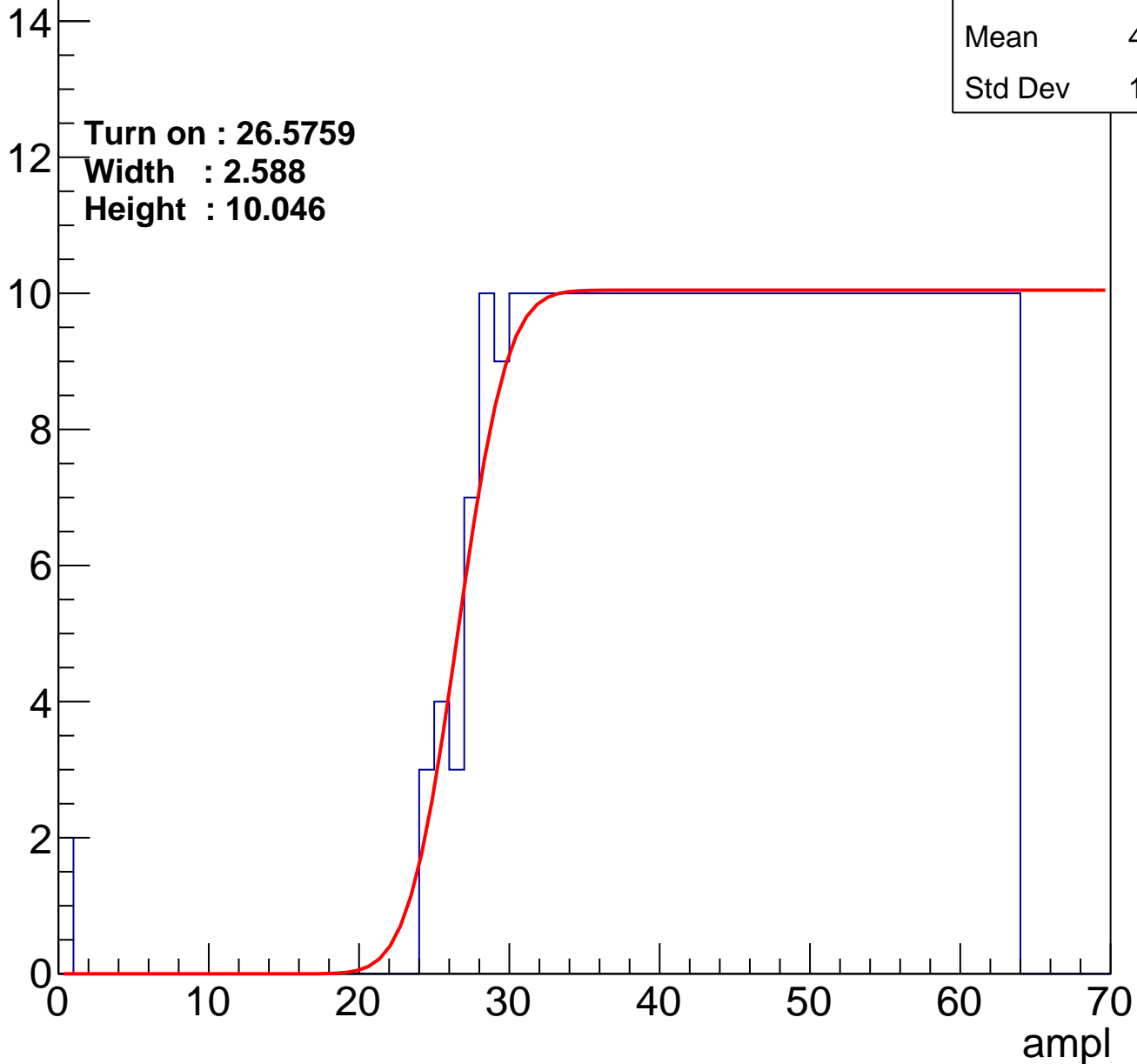
Entries	378
Mean	44.42
Std Dev	11.37

Turn on : 26.5759

Width : 2.588

Height : 10.046

Entry



B1L000S, U5-ch126

calib_packv5_042523_0143.root, FC#3, port B3

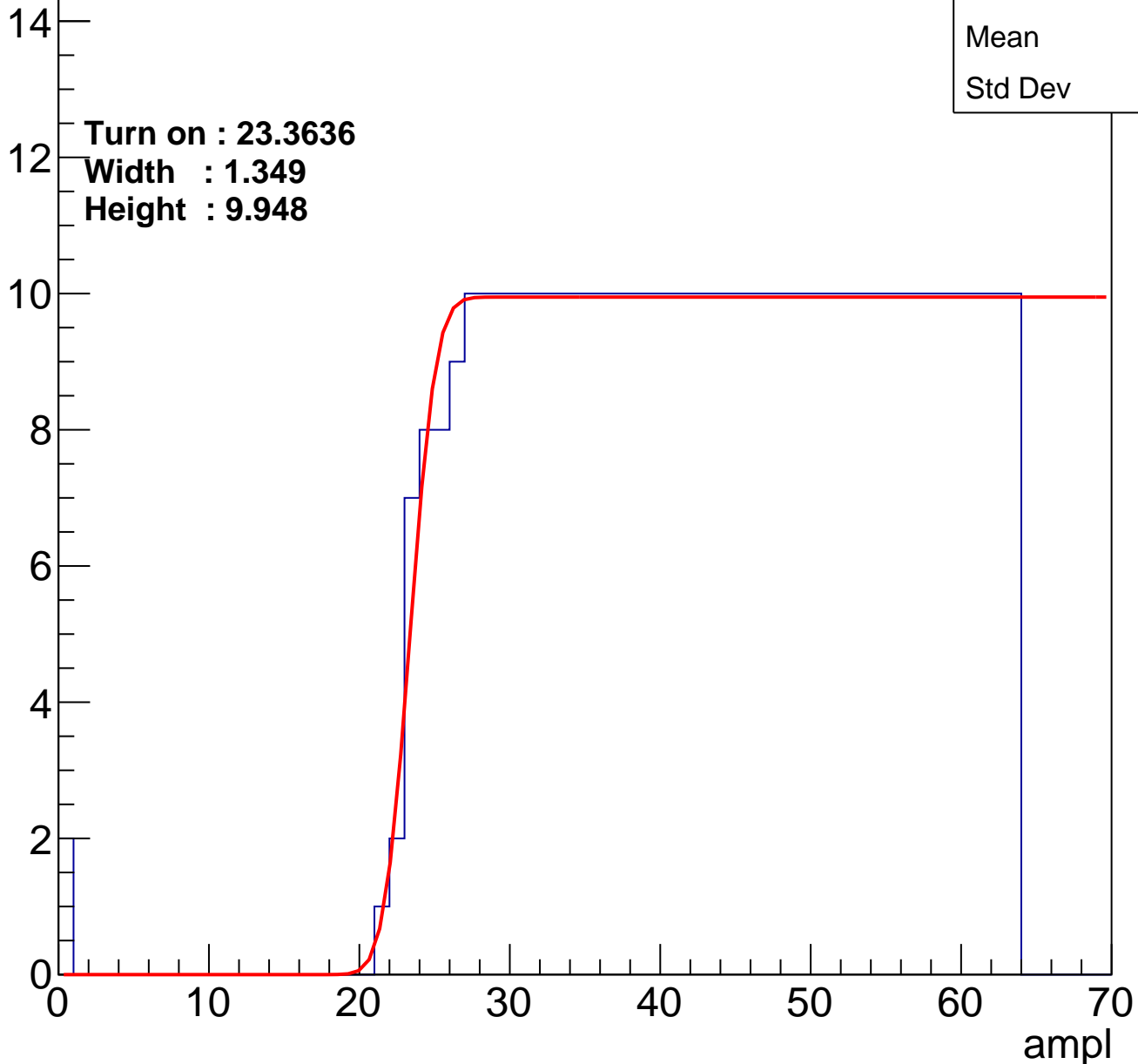
Entries	407
Mean	43
Std Dev	12.1

Turn on : 23.3636

Width : 1.349

Height : 9.948

Entry



B1L000S, U5-ch127

calib_packv5_042523_0143.root, FC#3, port B3

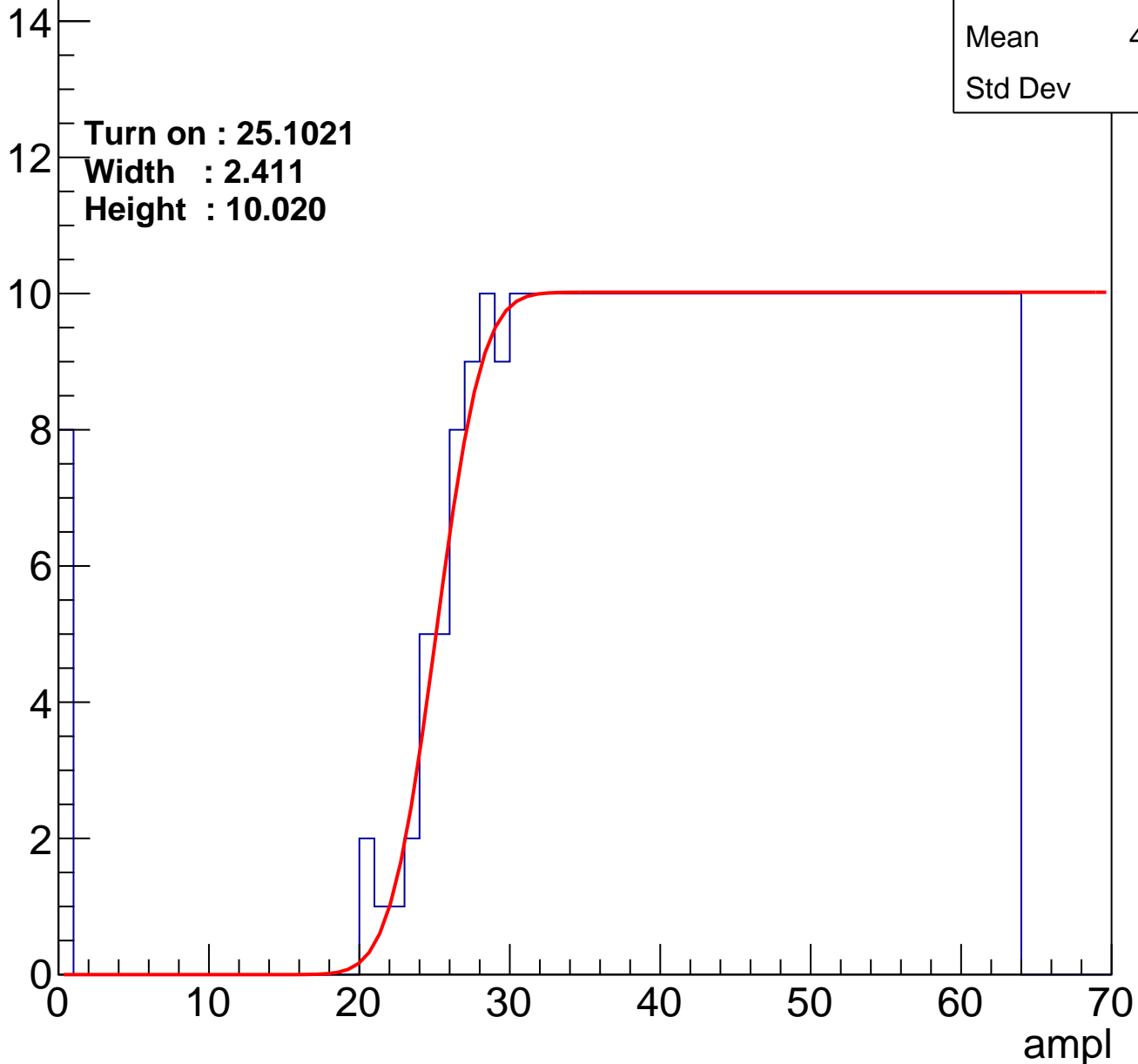
Entries	400
Mean	42.94
Std Dev	12.9

Turn on : 25.1021

Width : 2.411

Height : 10.020

Entry



B1L000S, U5-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	42.94
Std Dev	12.9

Turn on : 25.1021

Width : 2.411

Height : 10.020

Entry

