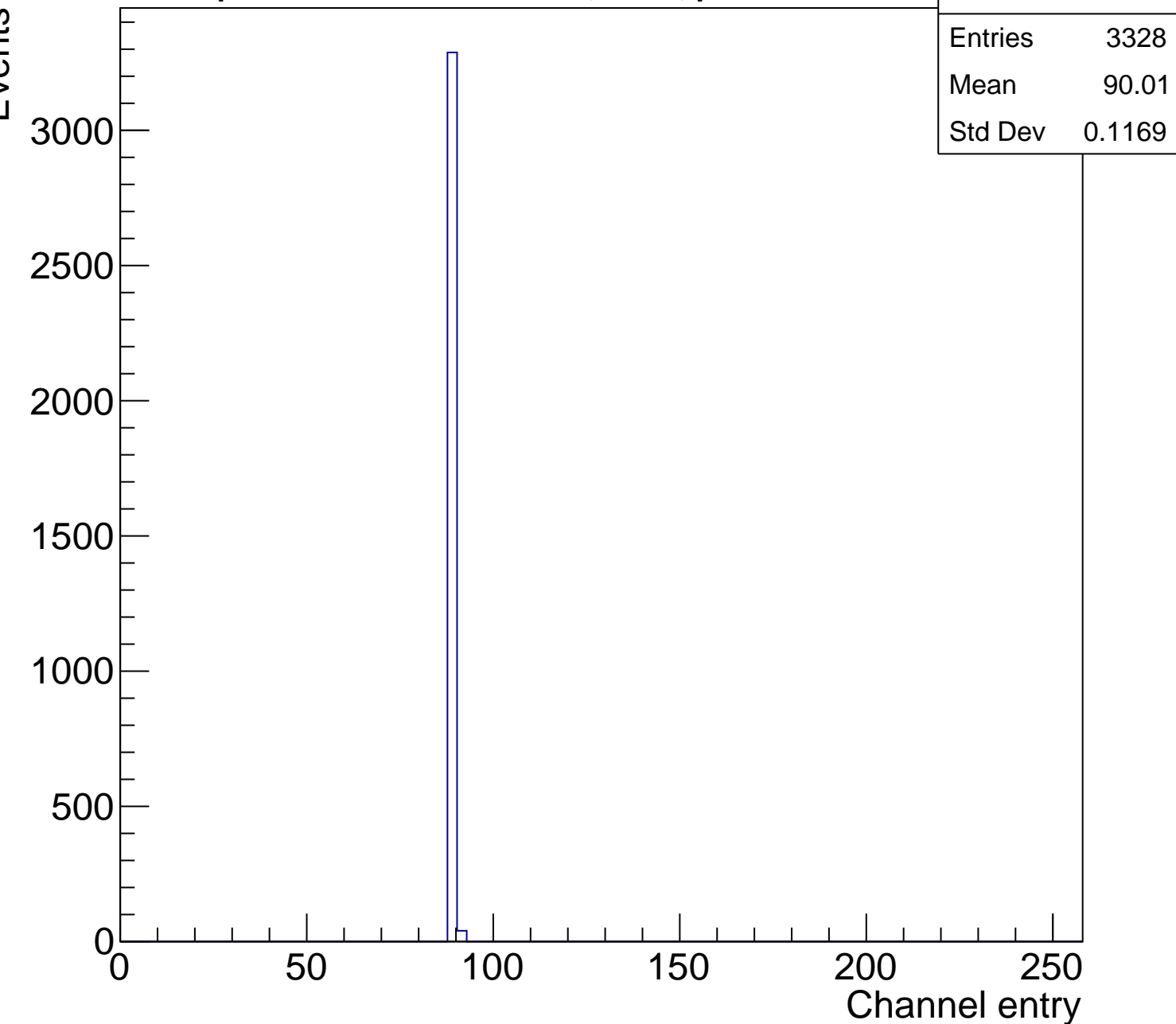


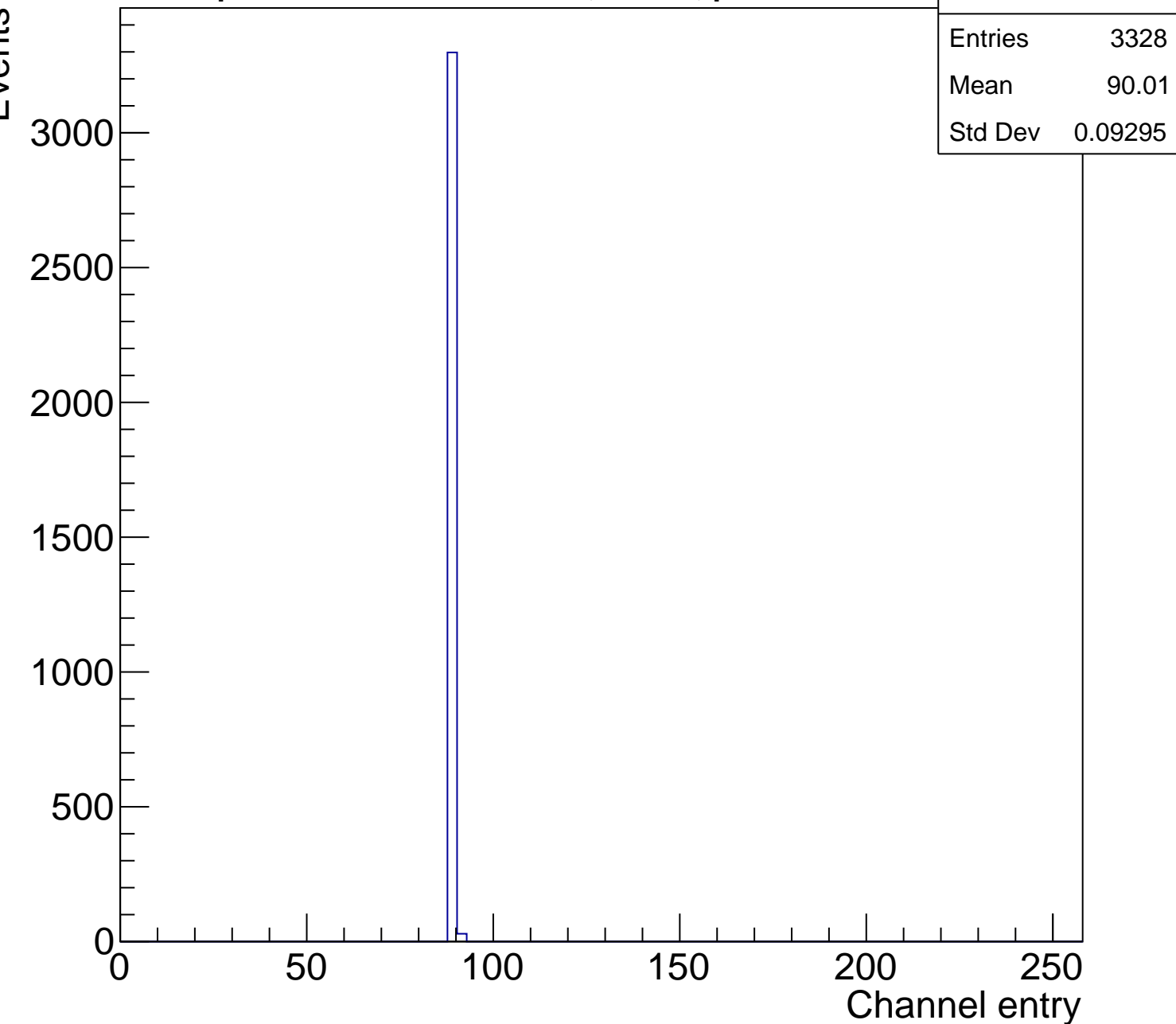
B1L101S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#0, port D2



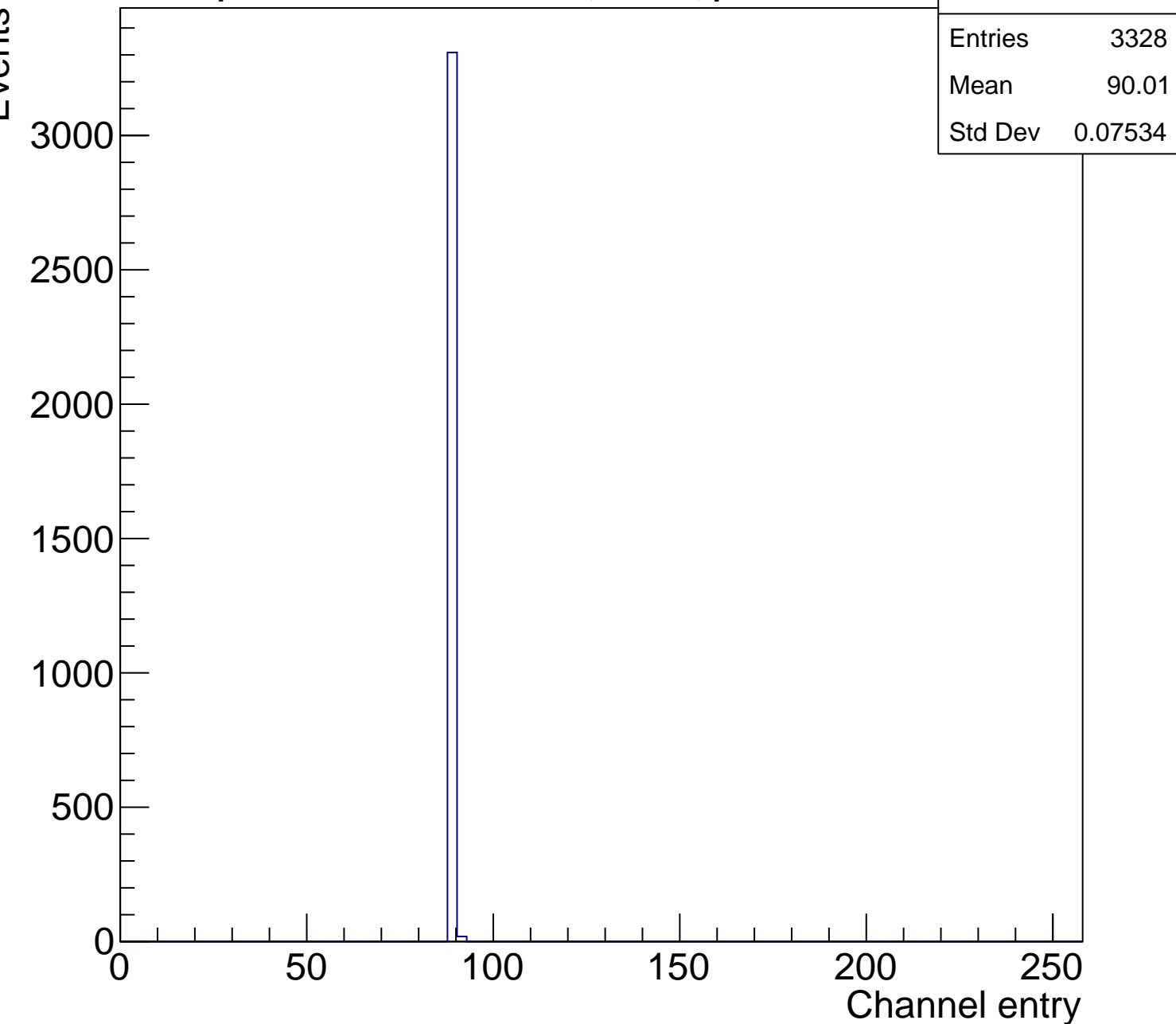
B1L002S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#10, port B3



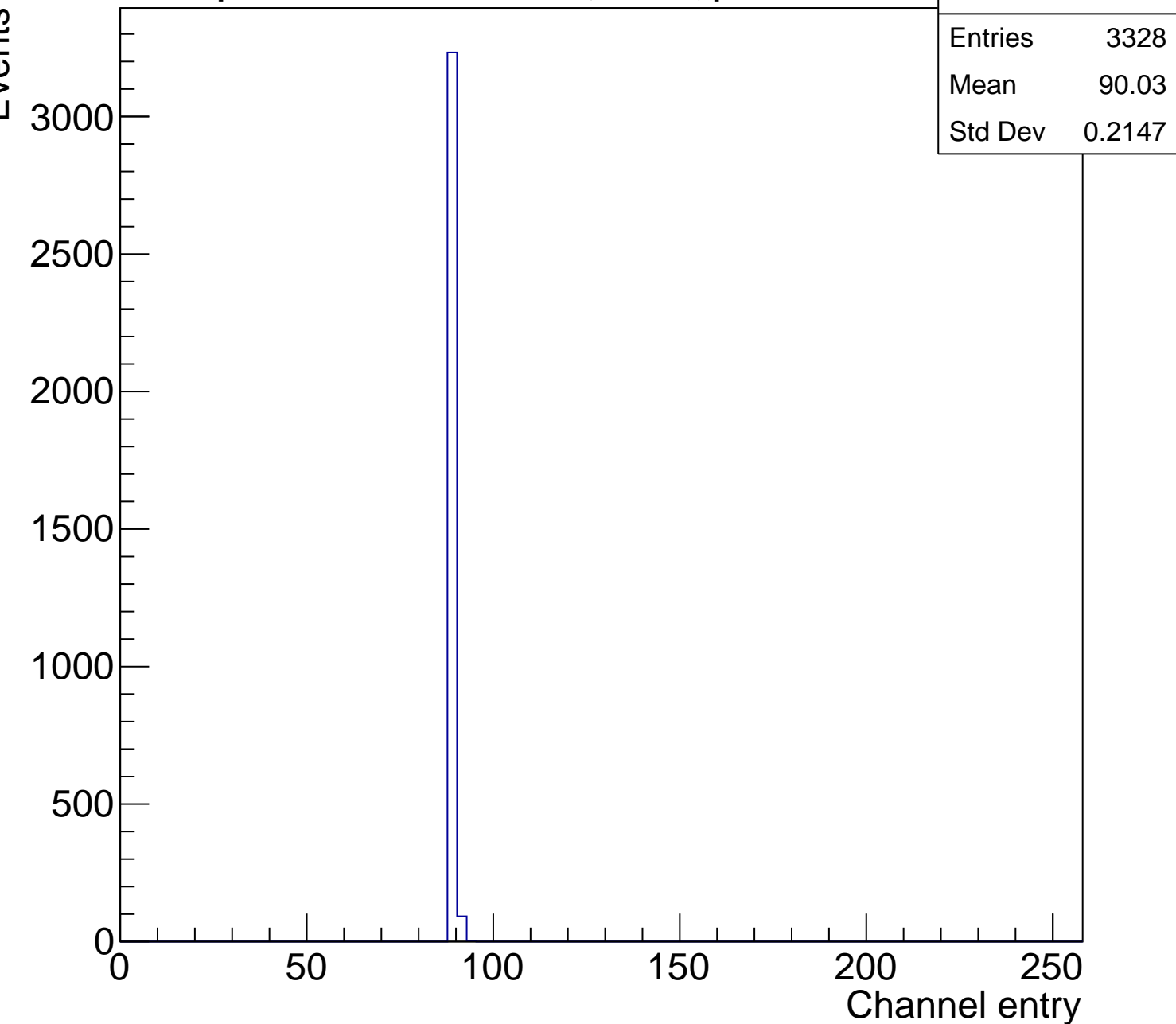
B1L102S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#11, port A2



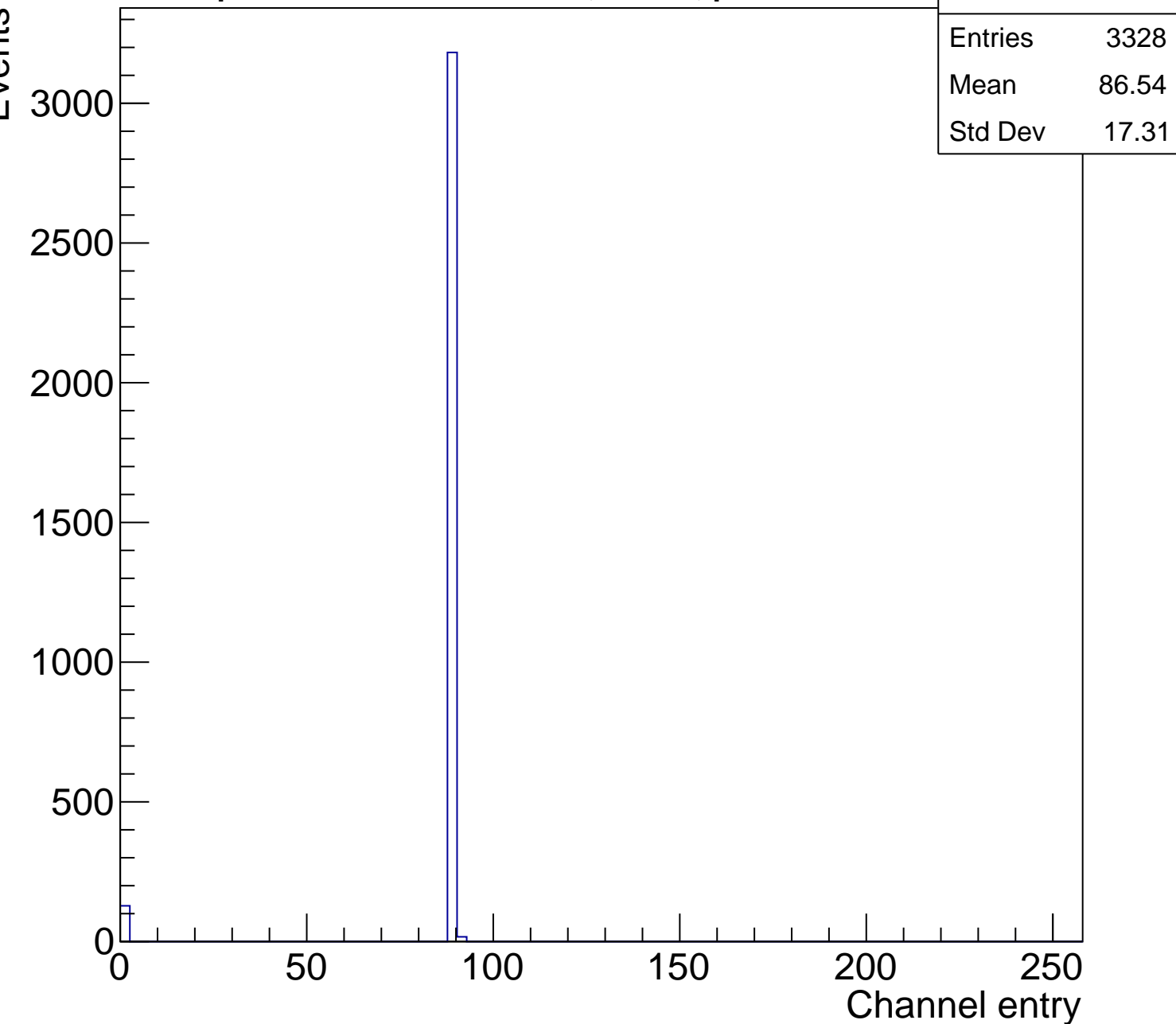
B0L102S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#12, port B1



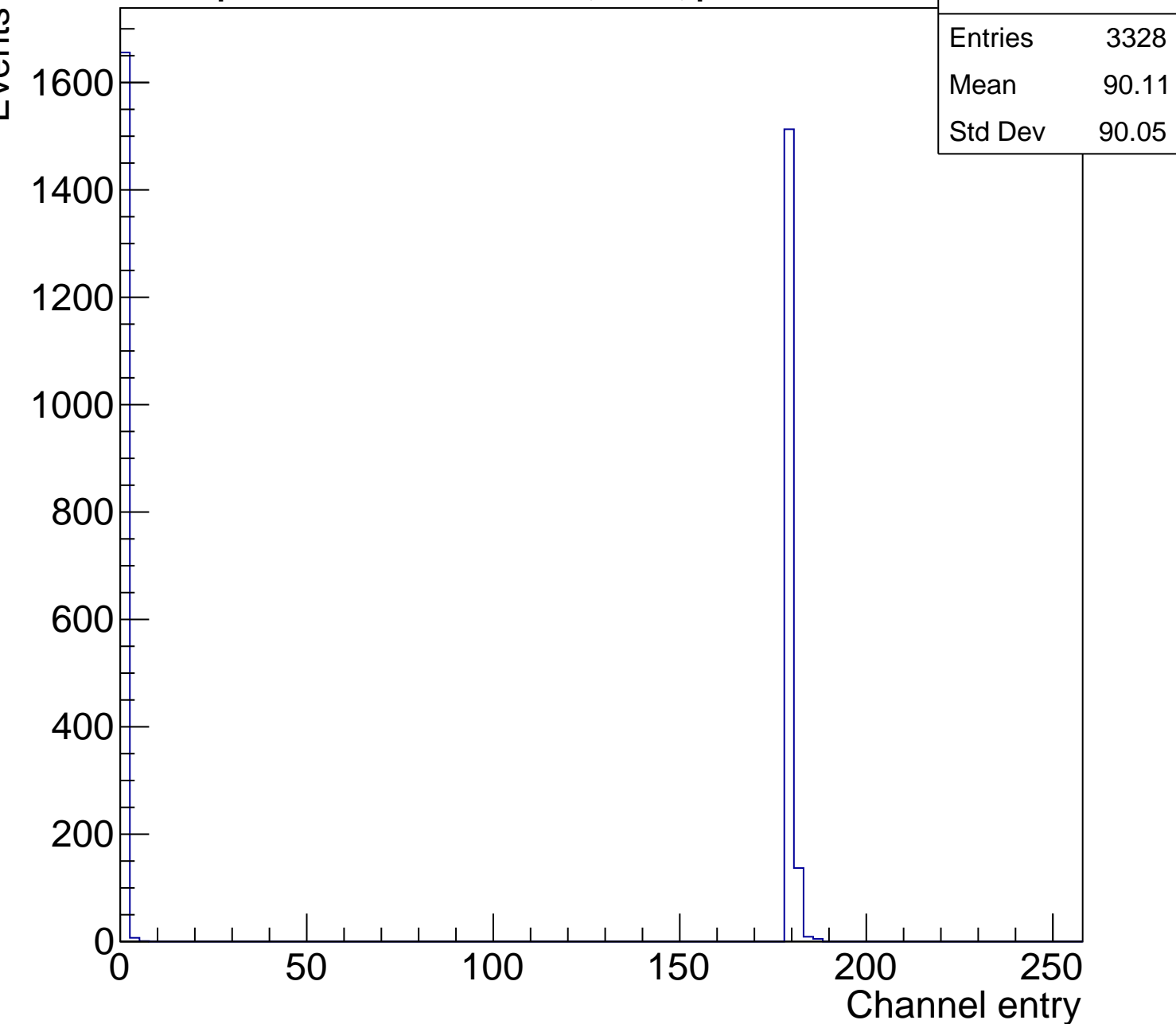
B1L003S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#13, port D2



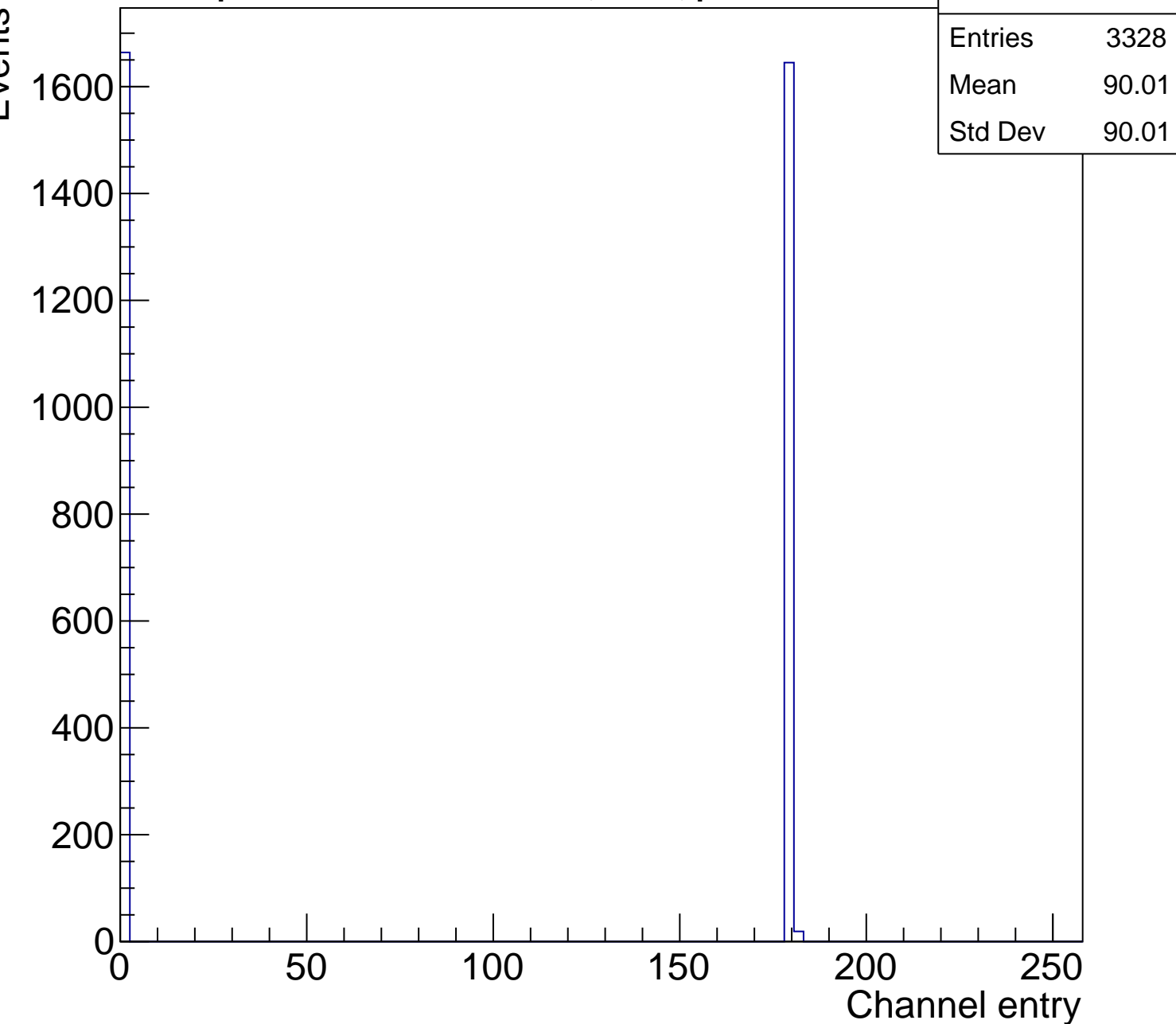
B0L101S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#1, port C1



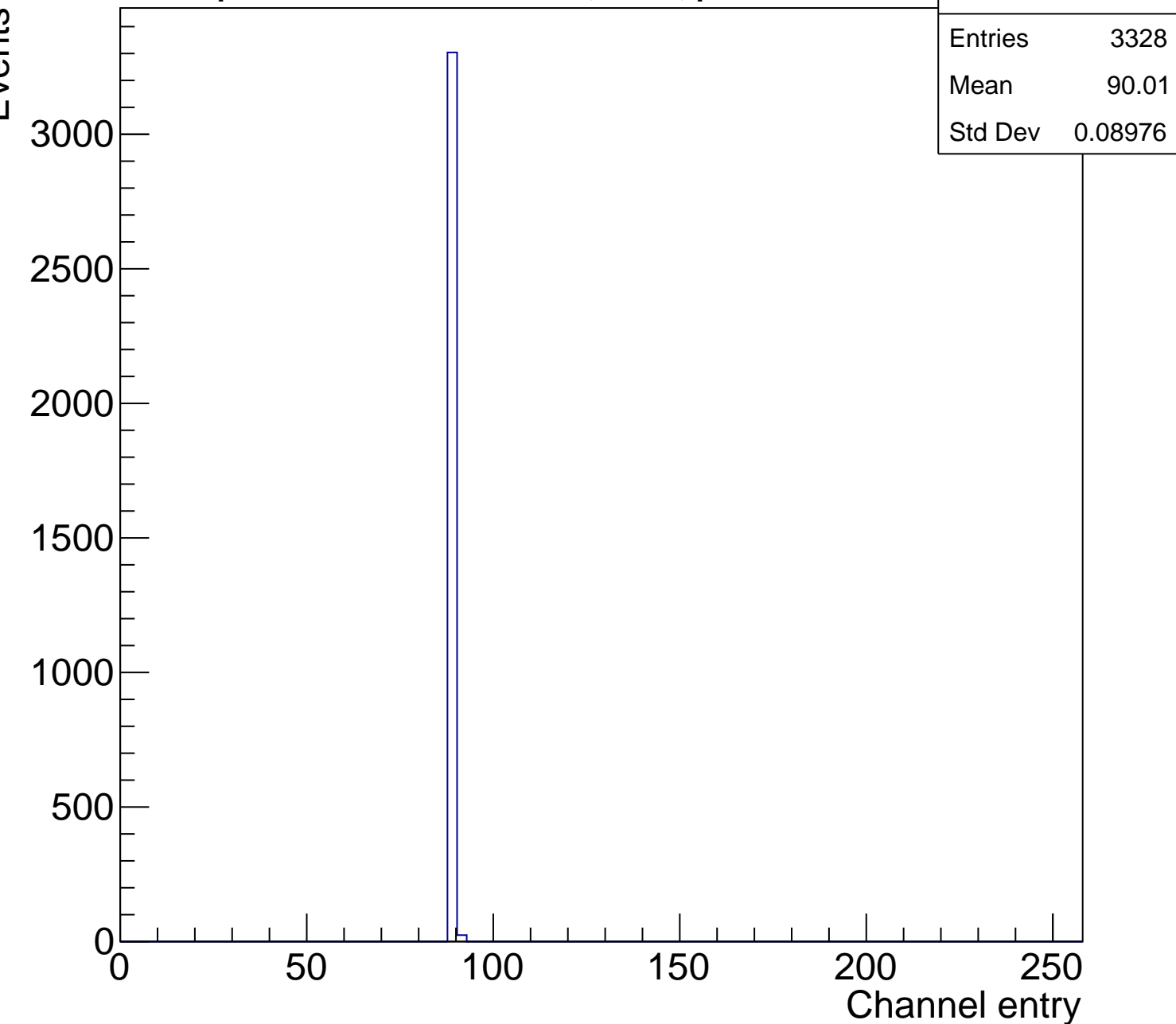
B1L001S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#2, port C2



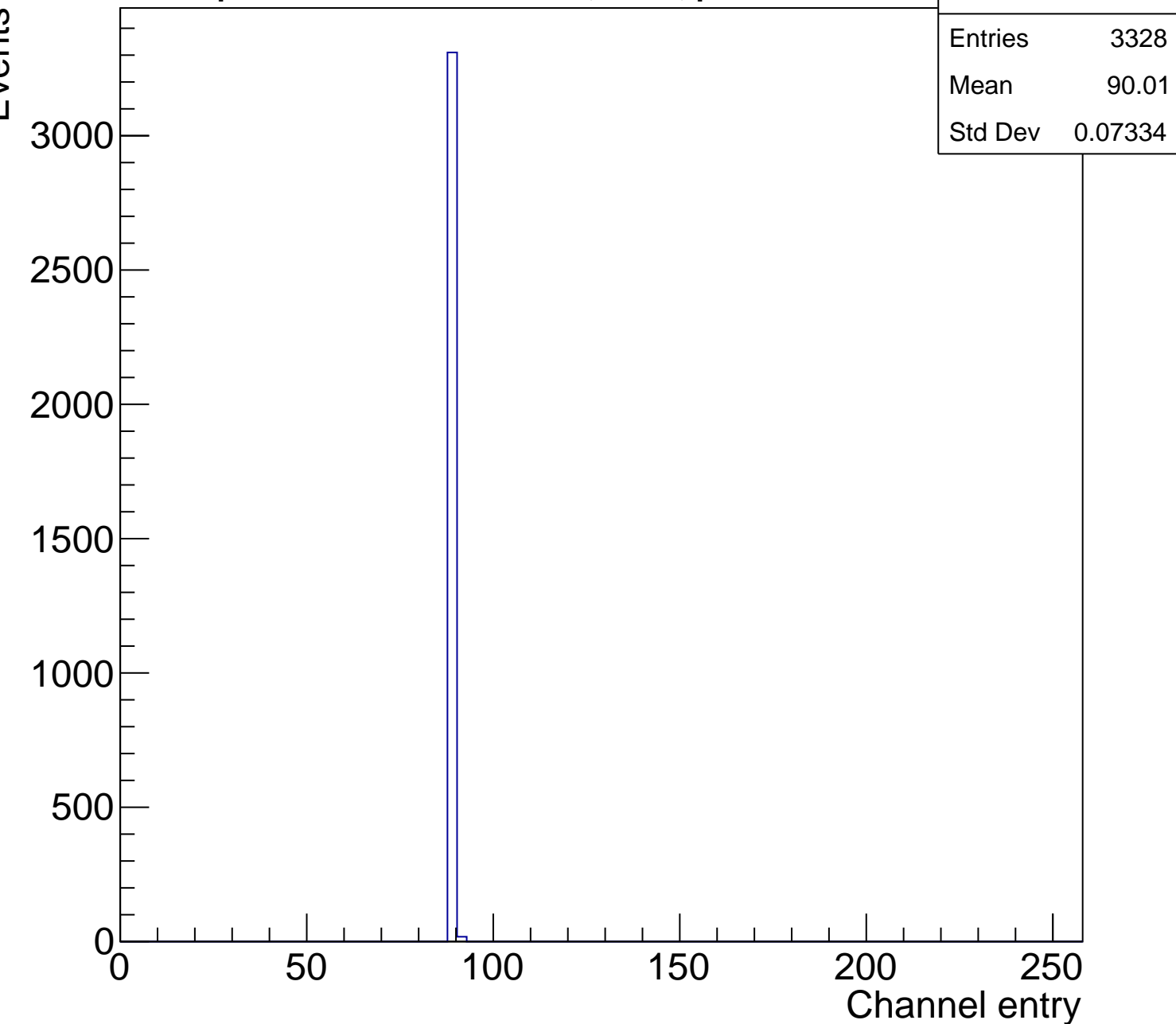
B1L000S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#3, port B3



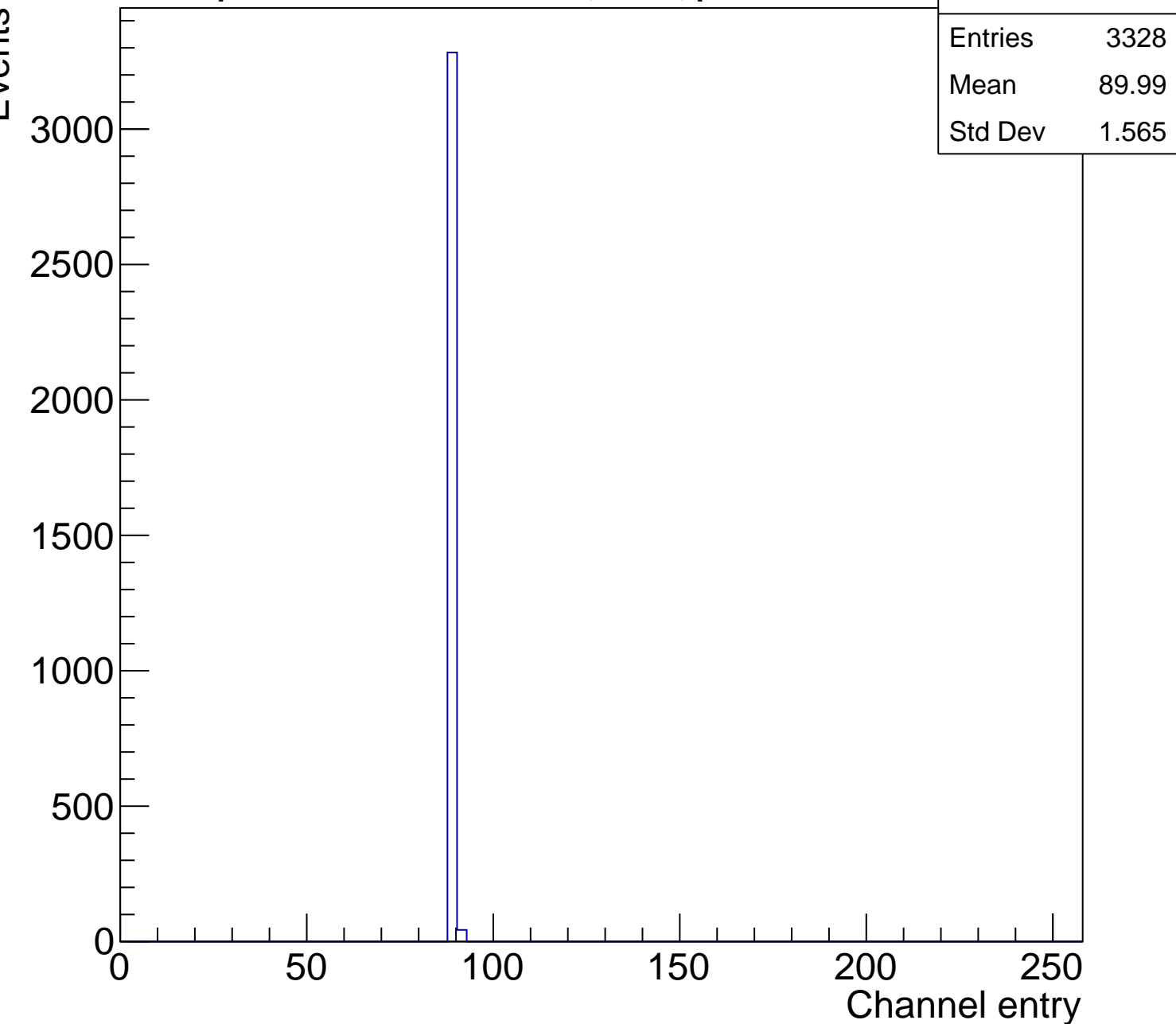
B1L100S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#4, port A2



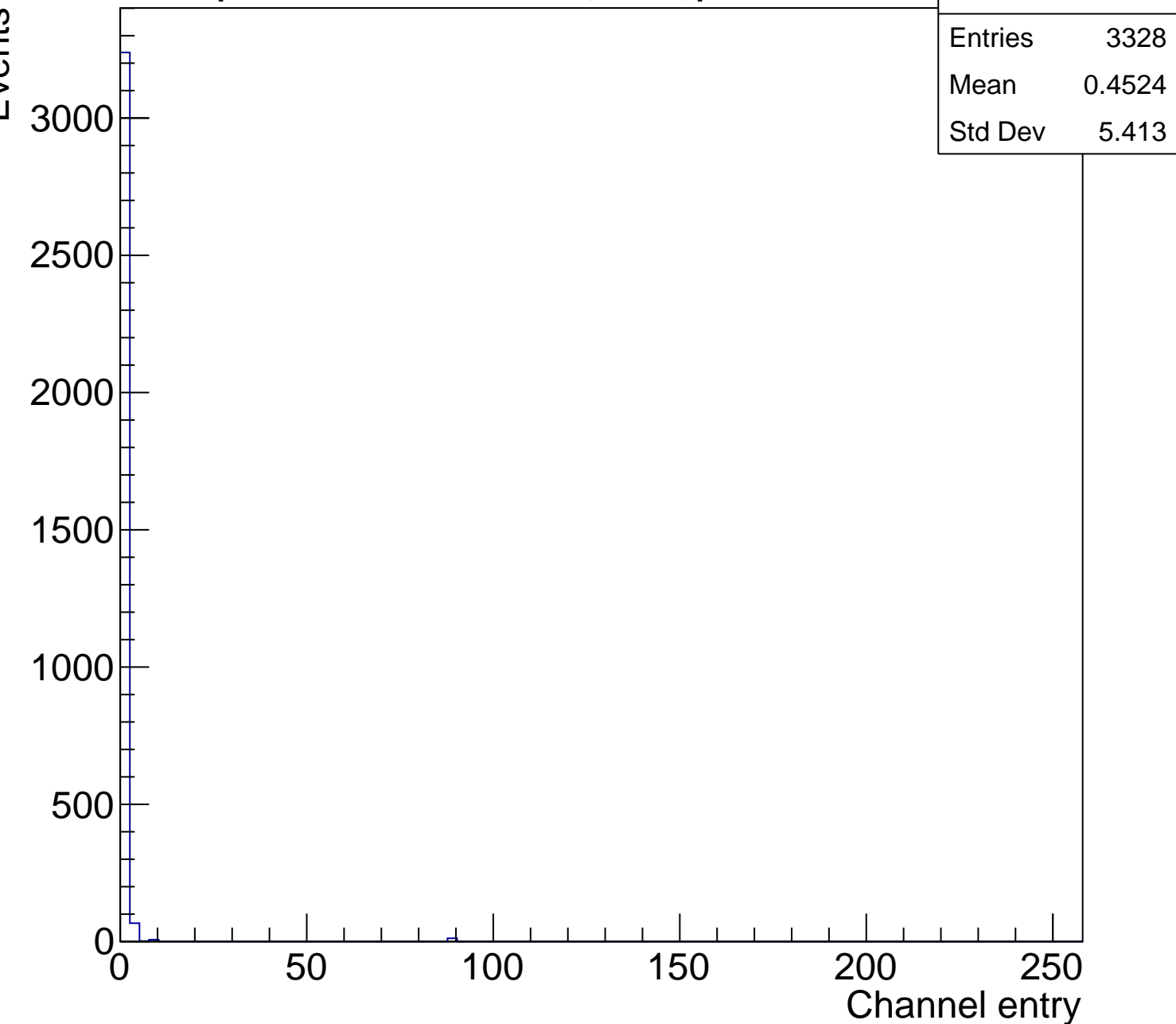
B0L000S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#5, port B1



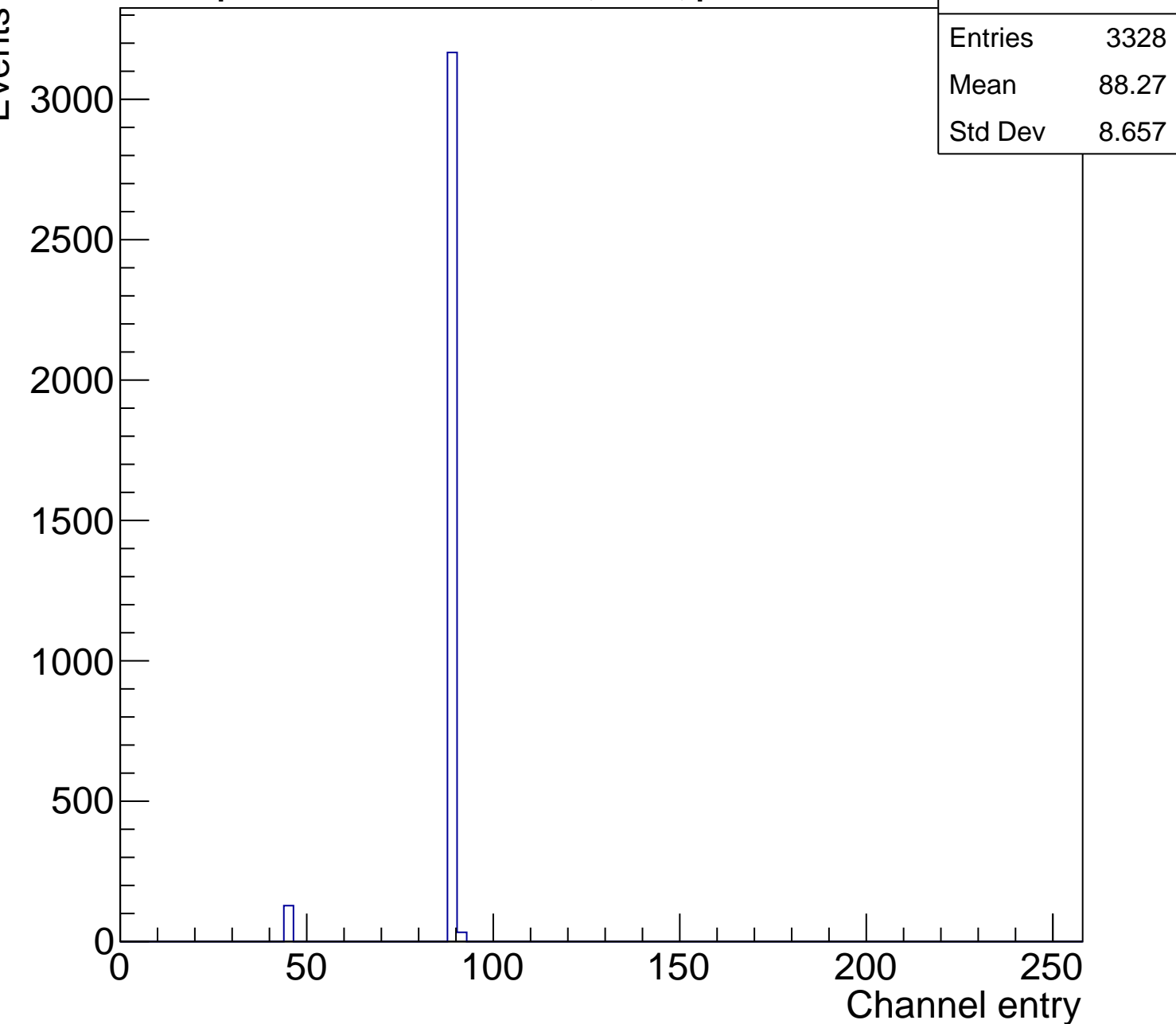
B0L100S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#6, port A1



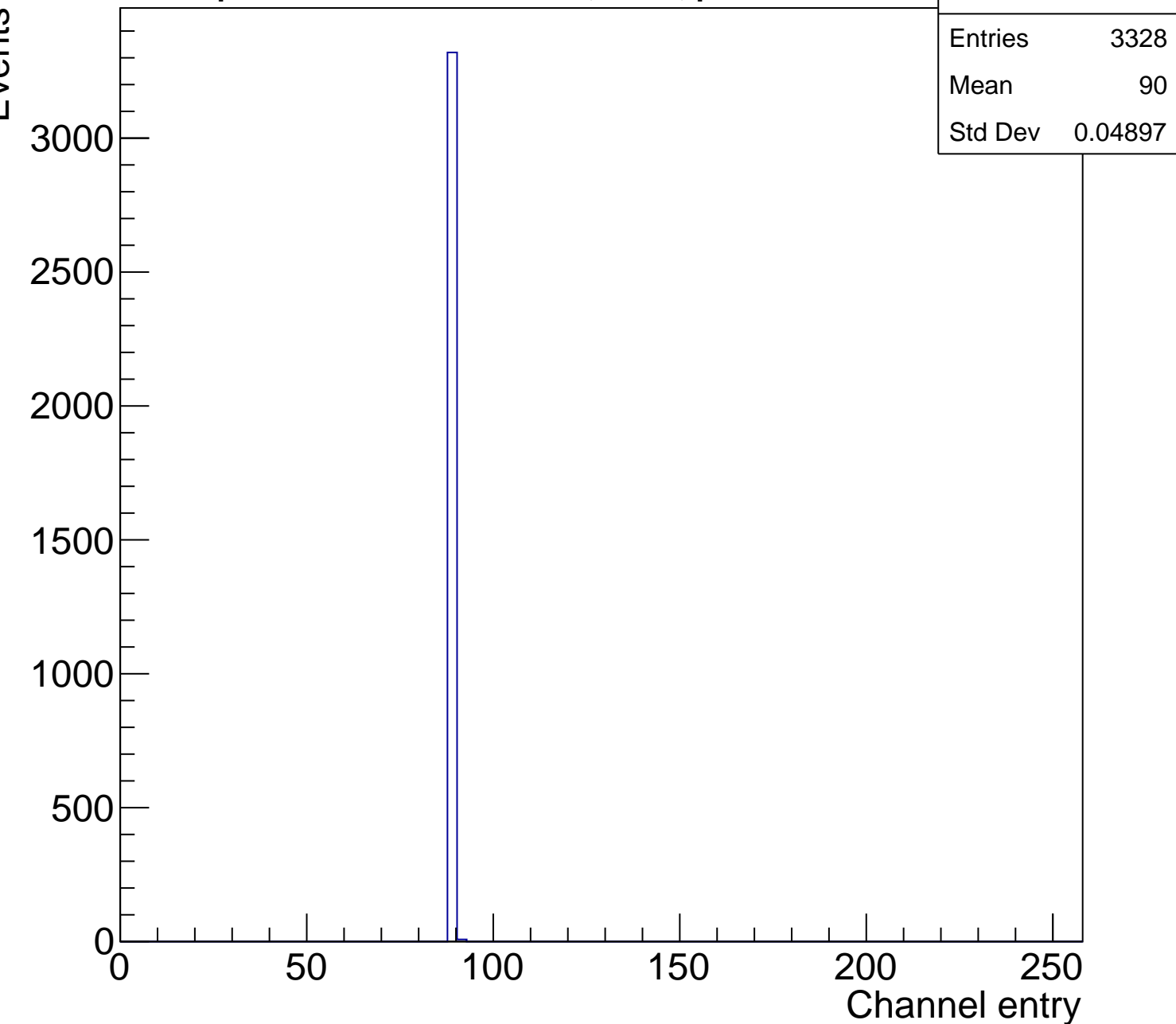
B1L103S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#7, port C2



B0L002S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#8, port C1



B0L001S, channel entry distribution (ampl > 54)

calib_packv5_042523_0143.root, FC#9, port A1

