

B1L002S, U5-ch0

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.44 |
| Std Dev | 11.25 |

Turn on : 26.6271

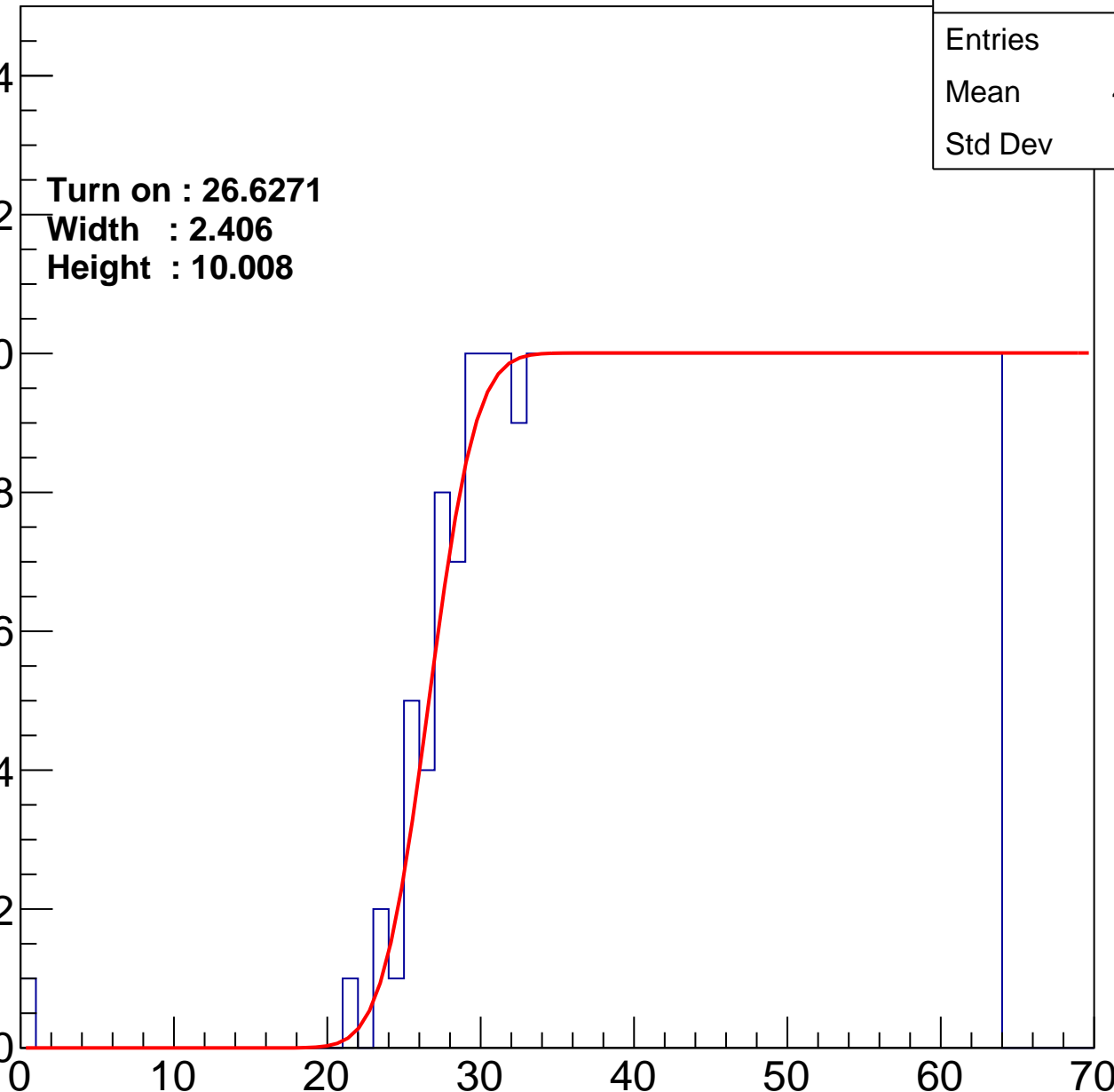
Width : 2.406

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch1

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.32 |
| Std Dev | 12.02 |

Turn on : 24.8023

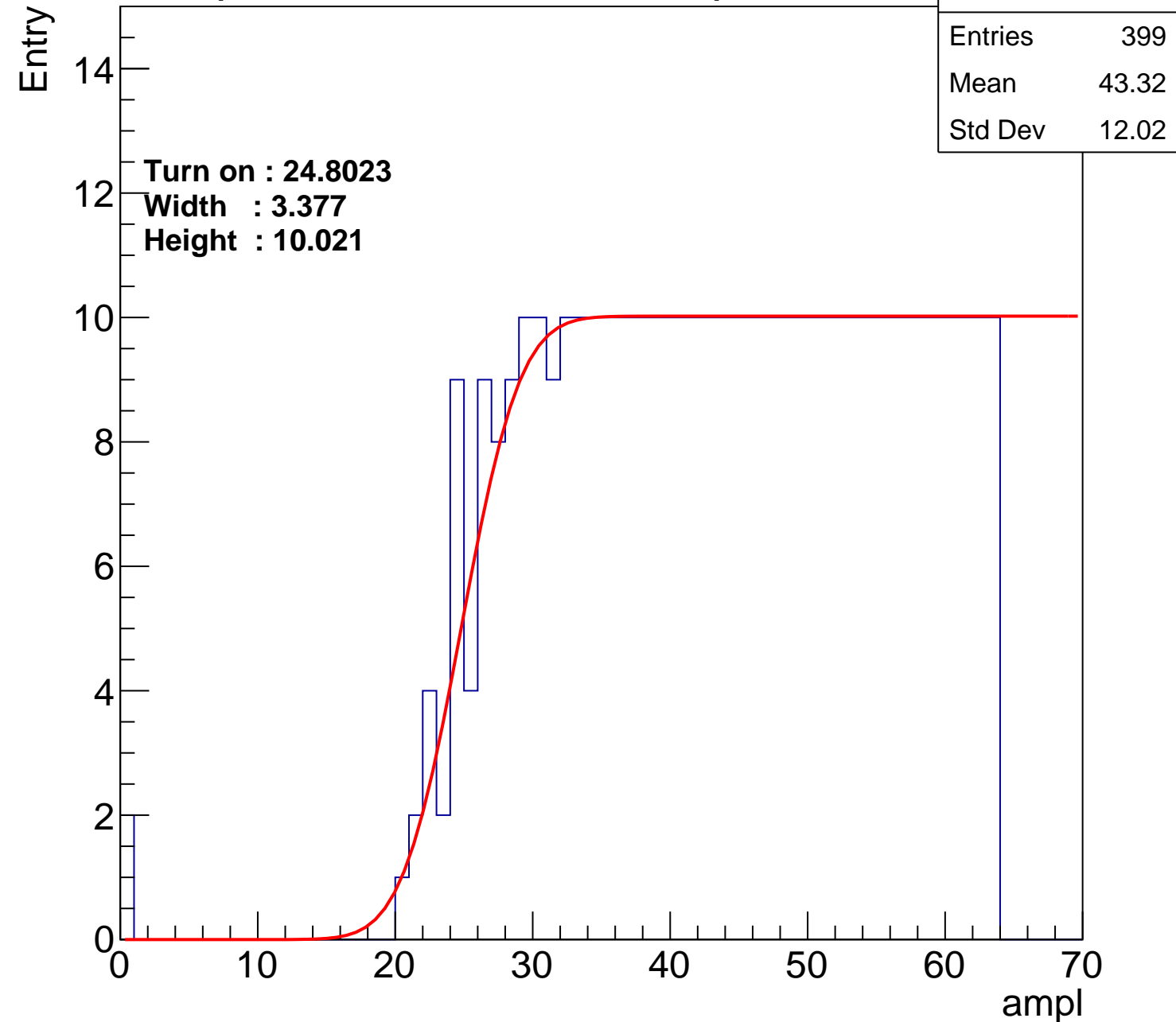
Width : 3.377

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch2

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-----|
| Entries | 403 |
|---------|-----|

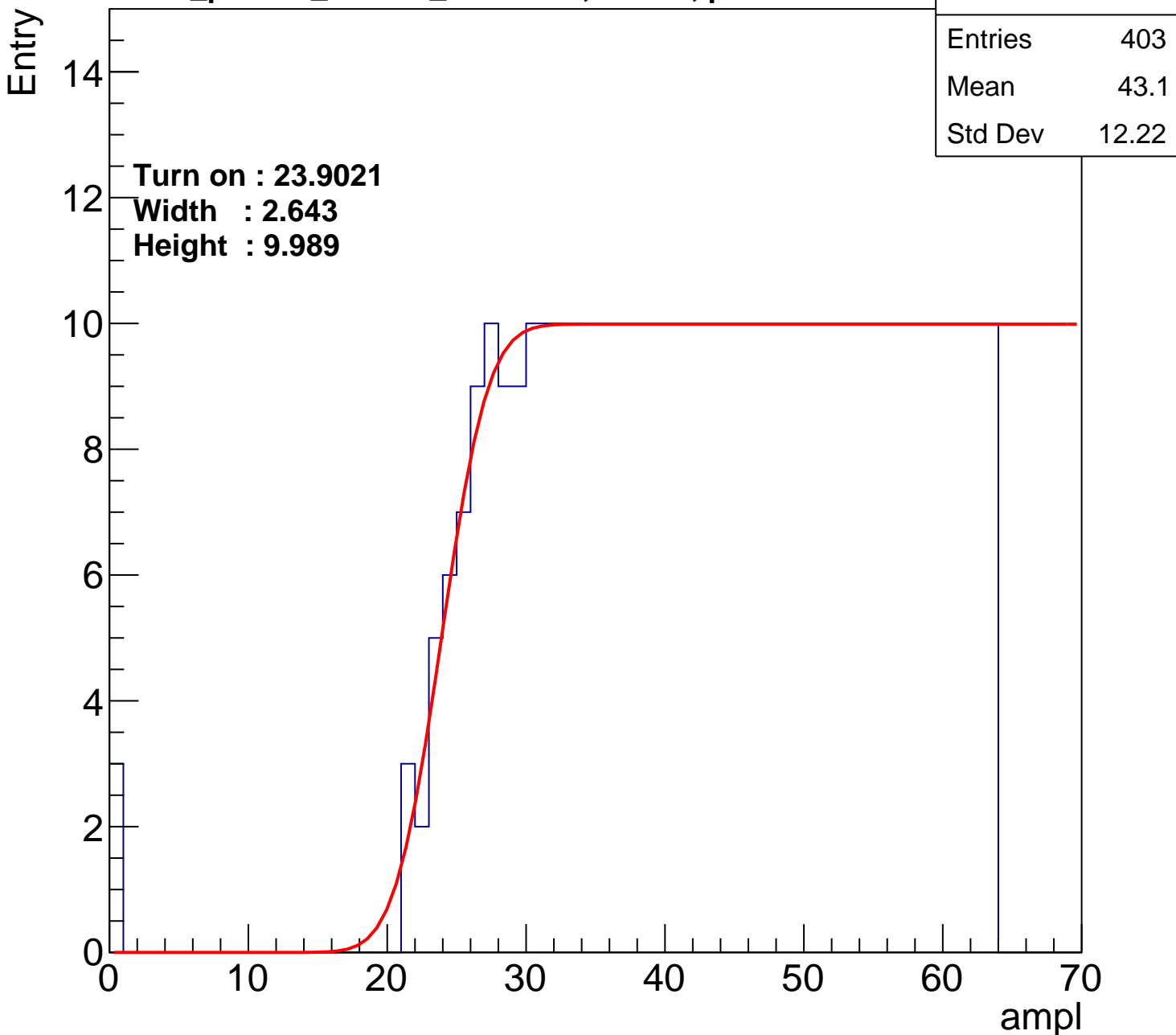
| | |
|------|------|
| Mean | 43.1 |
|------|------|

| | |
|---------|-------|
| Std Dev | 12.22 |
|---------|-------|

Turn on : 23.9021

Width : 2.643

Height : 9.989



B1L002S, U5-ch3

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.26 |
| Std Dev | 12.4 |

Turn on : 24.9034

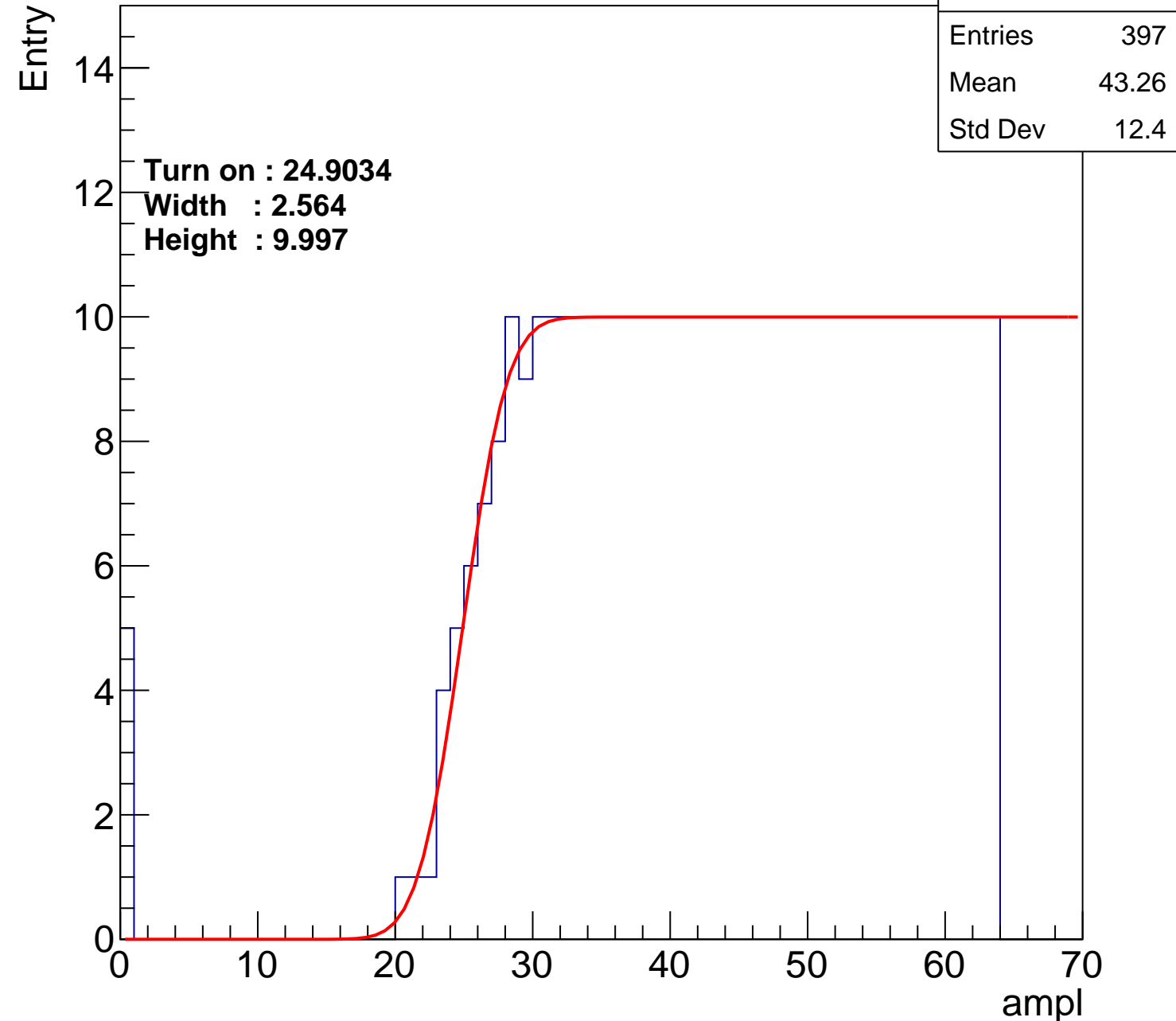
Width : 2.564

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch4

calib_packv5_042523_0143.root, FC#10, port B3

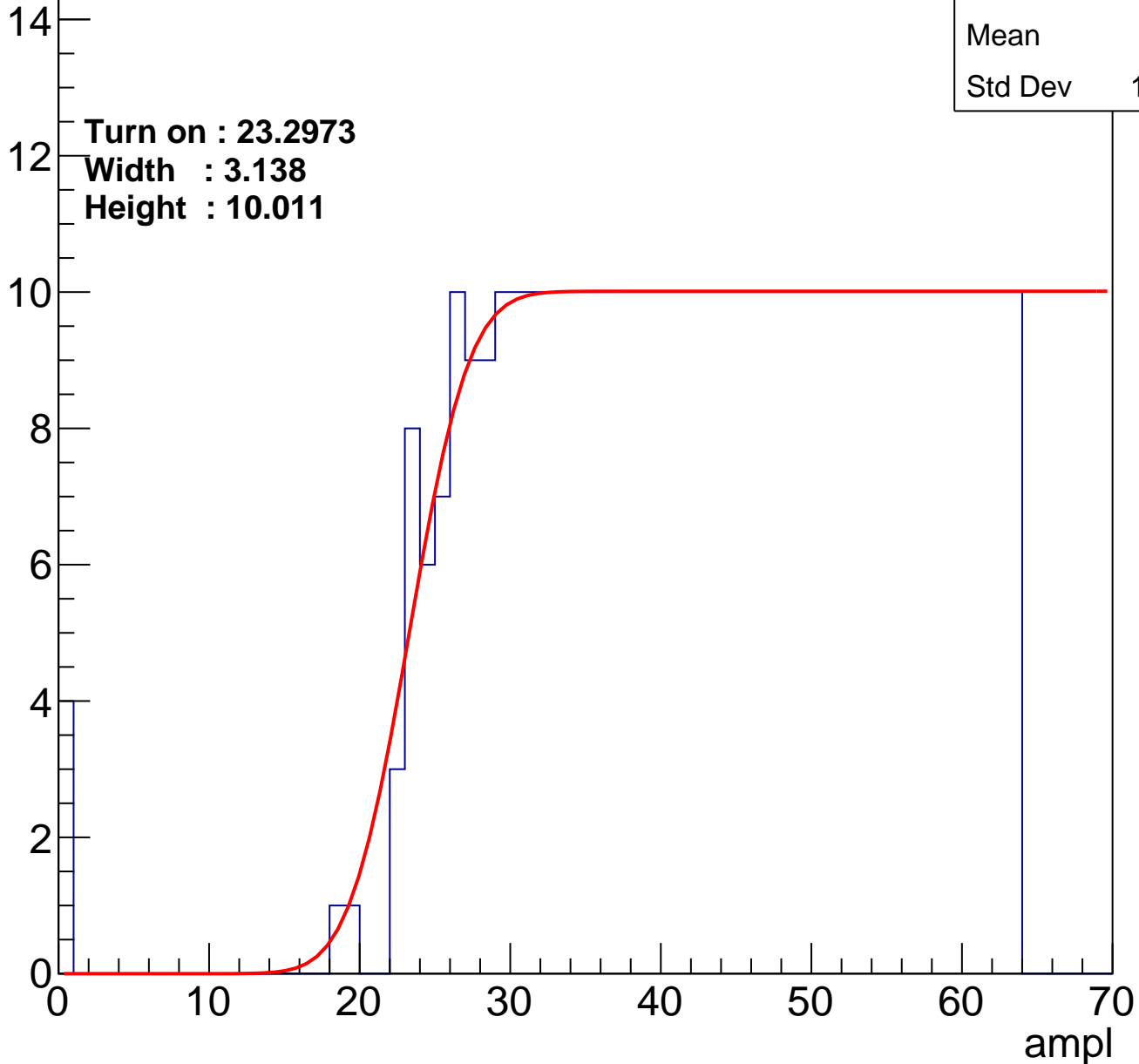
| | |
|---------|-------|
| Entries | 408 |
| Mean | 42.8 |
| Std Dev | 12.49 |

Turn on : 23.2973

Width : 3.138

Height : 10.011

Entry



B1L002S, U5-ch5

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.96 |
| Std Dev | 11.8 |

Turn on : 25.9166

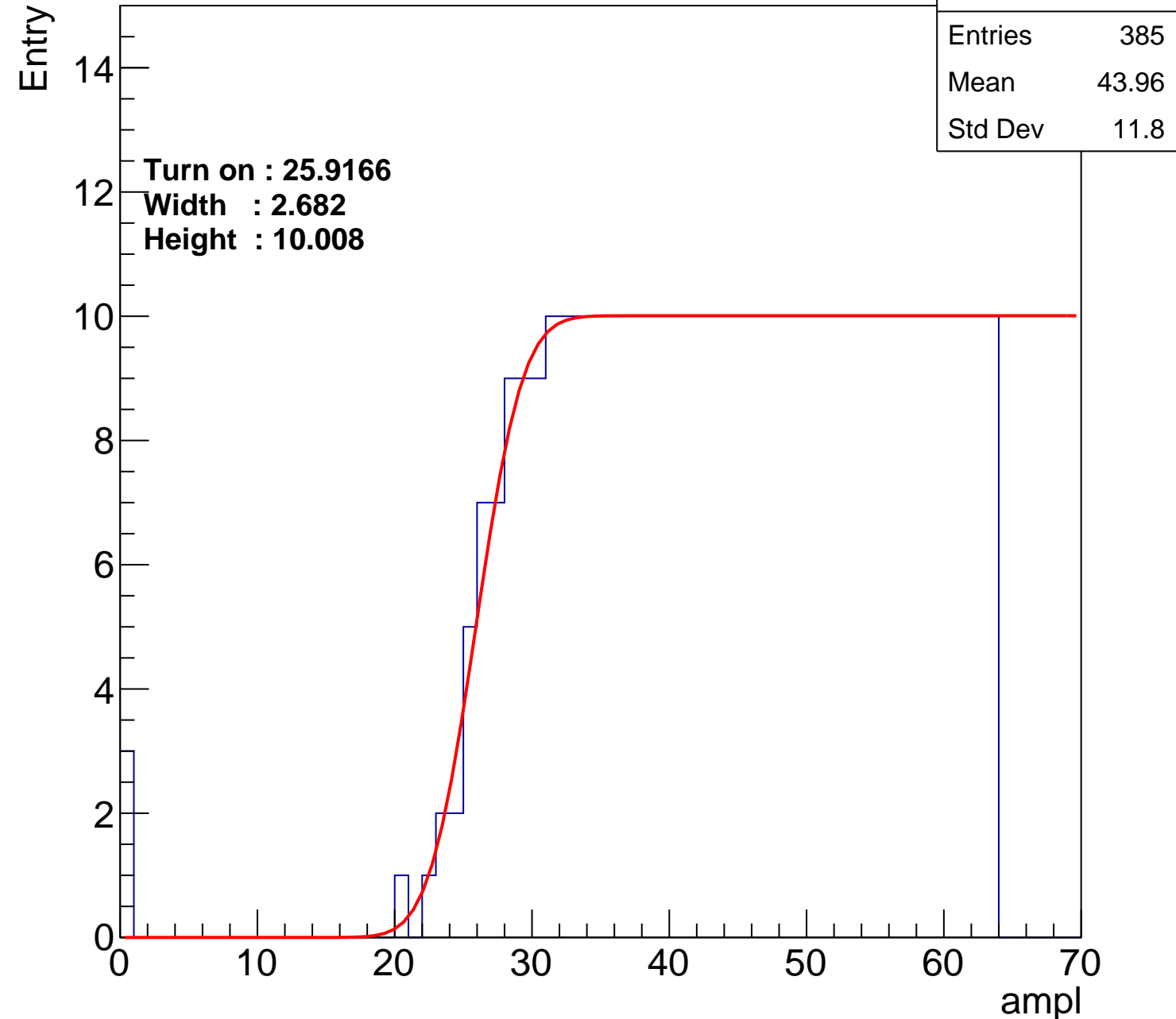
Width : 2.682

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch6

calib_packv5_042523_0143.root, FC#10, port B3

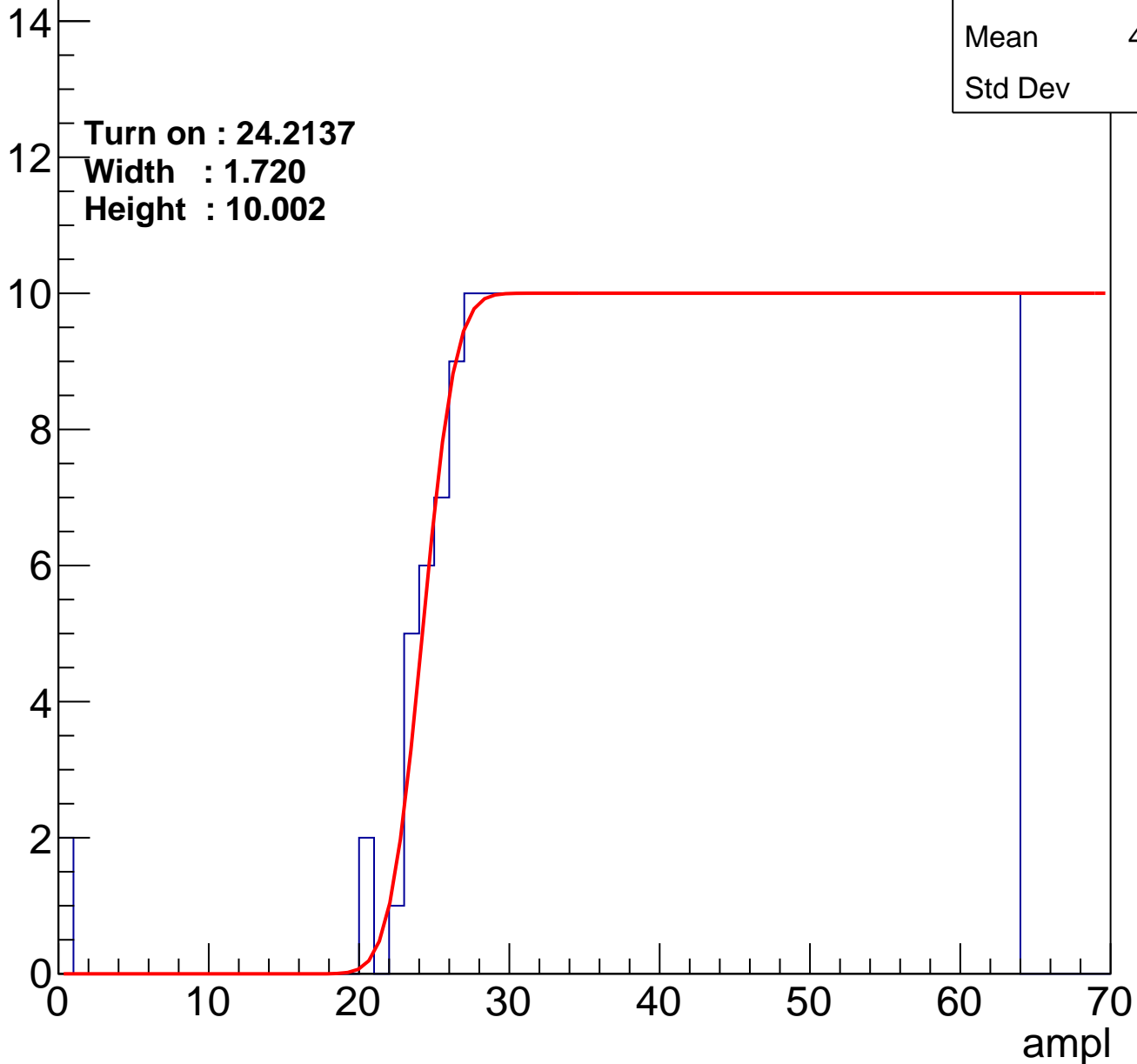
| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.23 |
| Std Dev | 12 |

Turn on : 24.2137

Width : 1.720

Height : 10.002

Entry



B1L002S, U5-ch7

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.41 |
| Std Dev | 12.1 |

Turn on : 24.7224

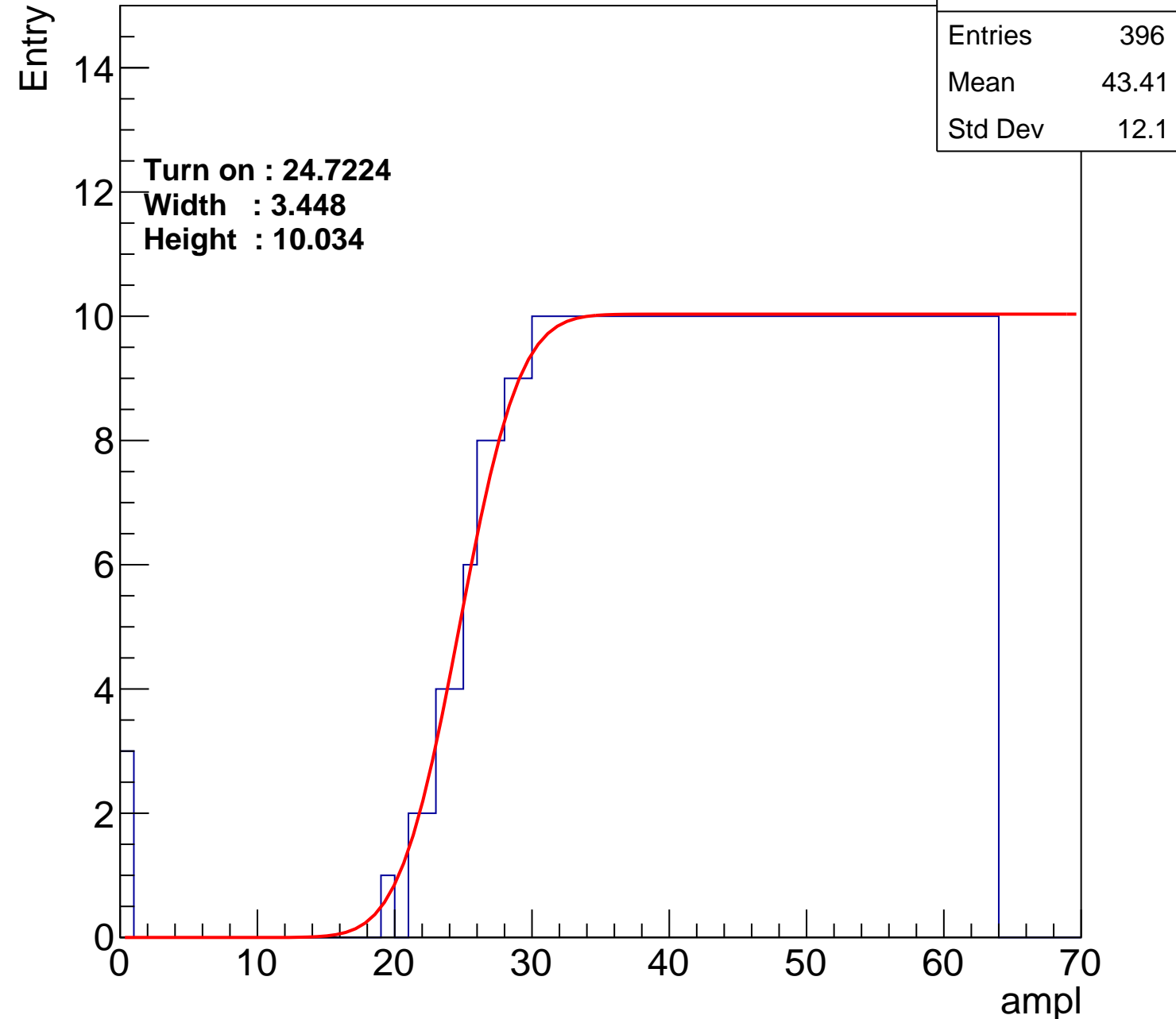
Width : 3.448

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch8

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.7 |
| Std Dev | 11.65 |

Turn on : 24.4216

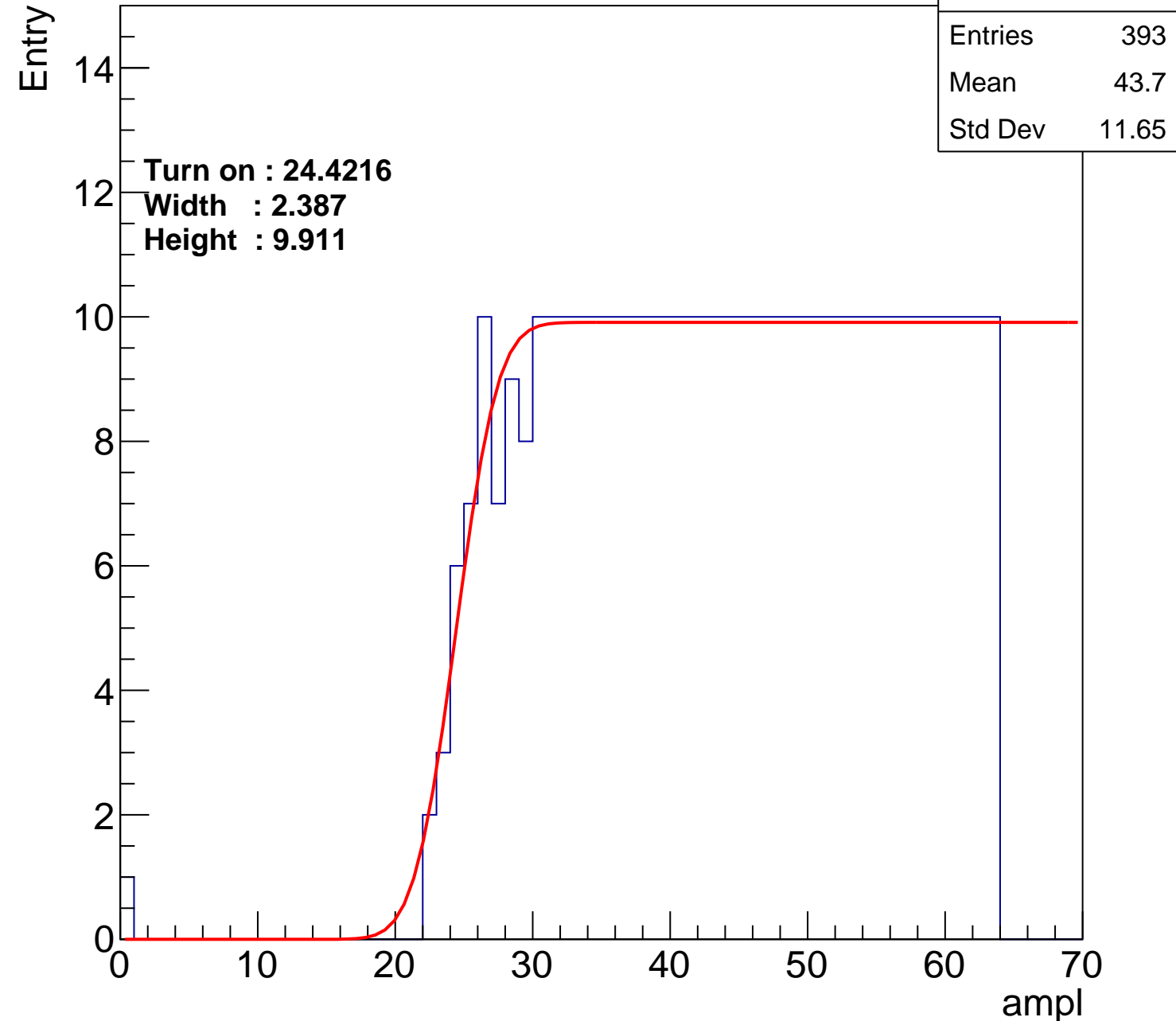
Width : 2.387

Height : 9.911

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch9

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.93 |
| Std Dev | 11.62 |

Turn on : 25.2781

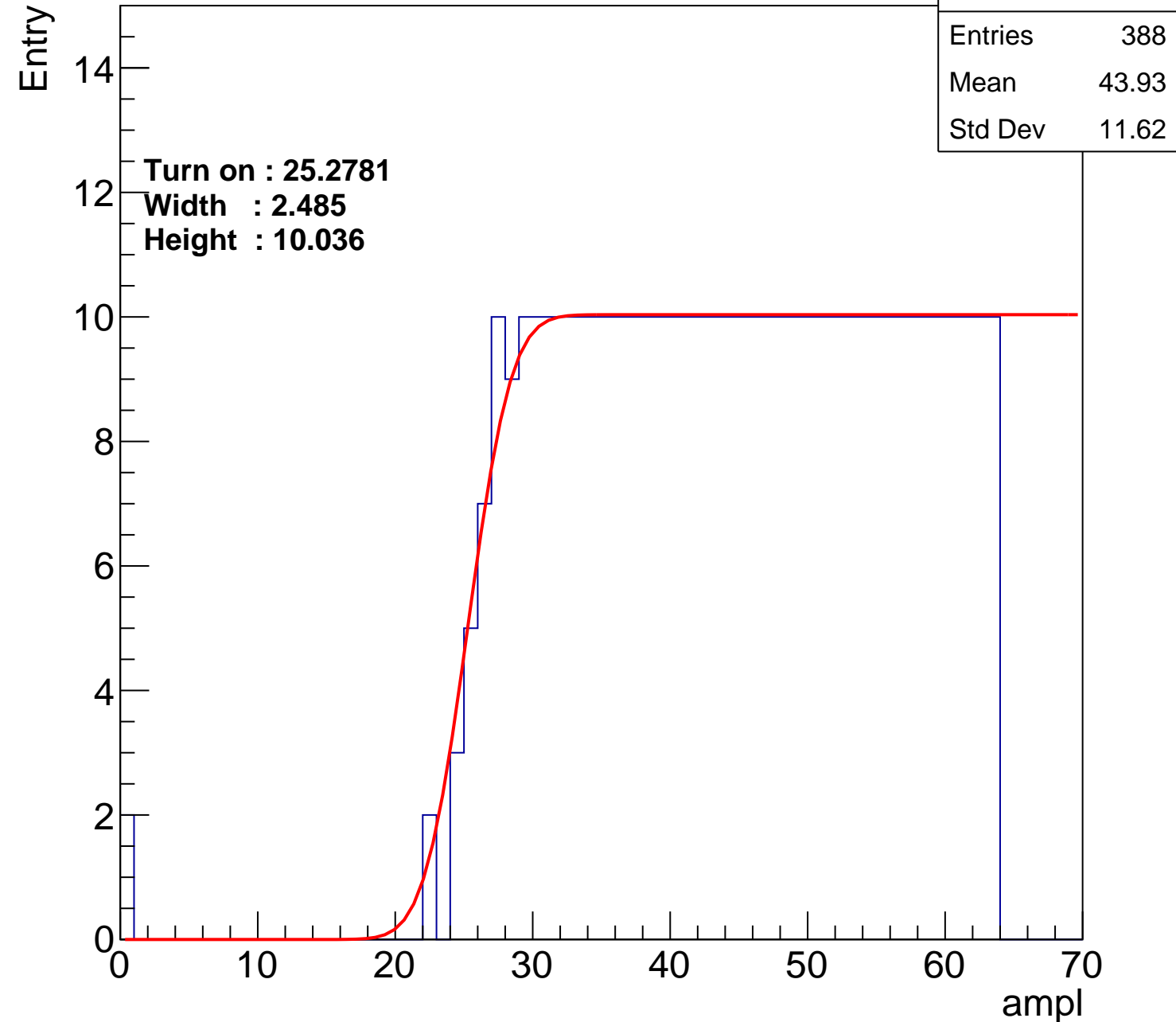
Width : 2.485

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch10

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.87 |
| Std Dev | 11.84 |

Turn on : 25.5627

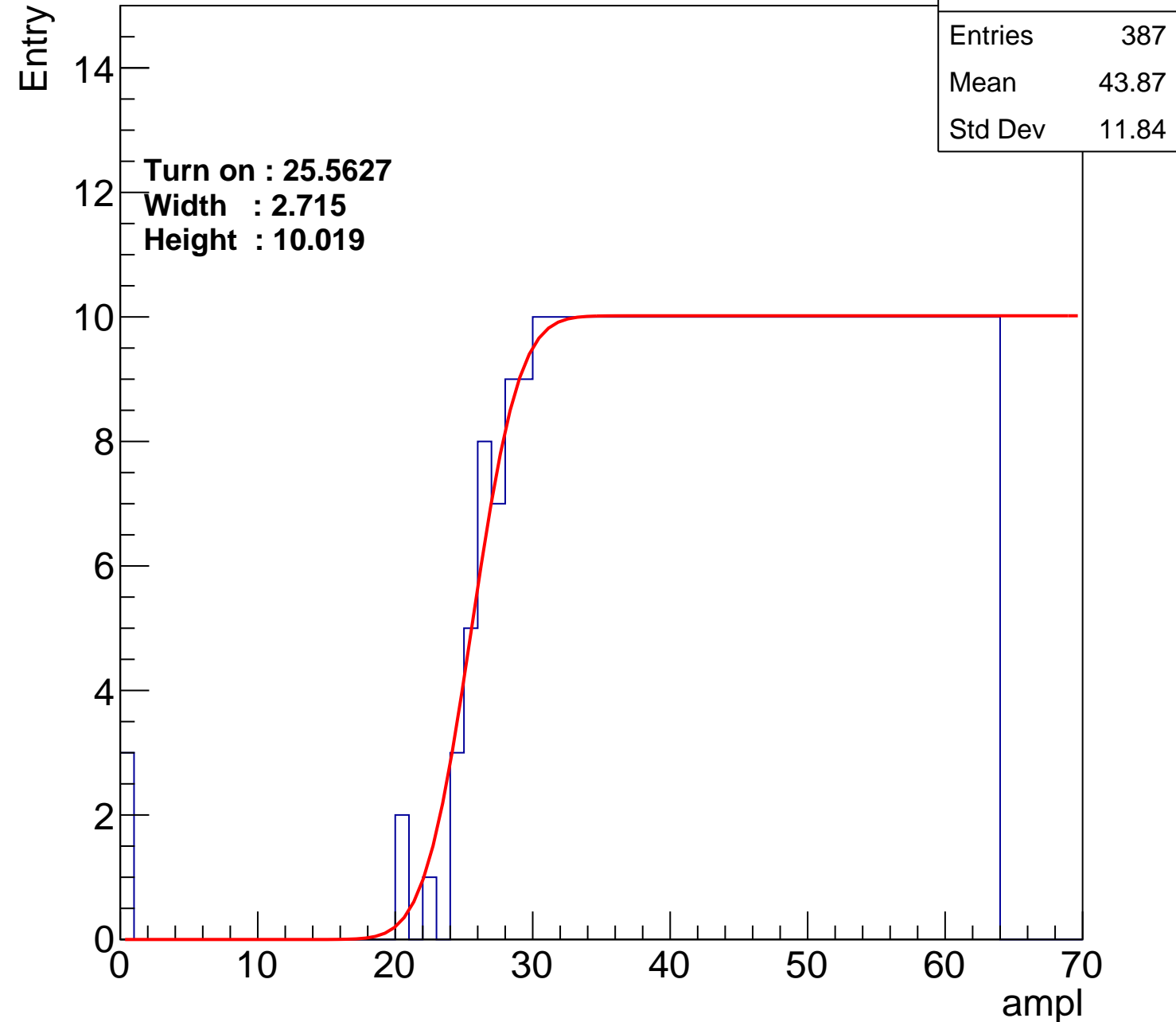
Width : 2.715

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch11

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.54 |
| Std Dev | 11.34 |

Turn on : 26.7413

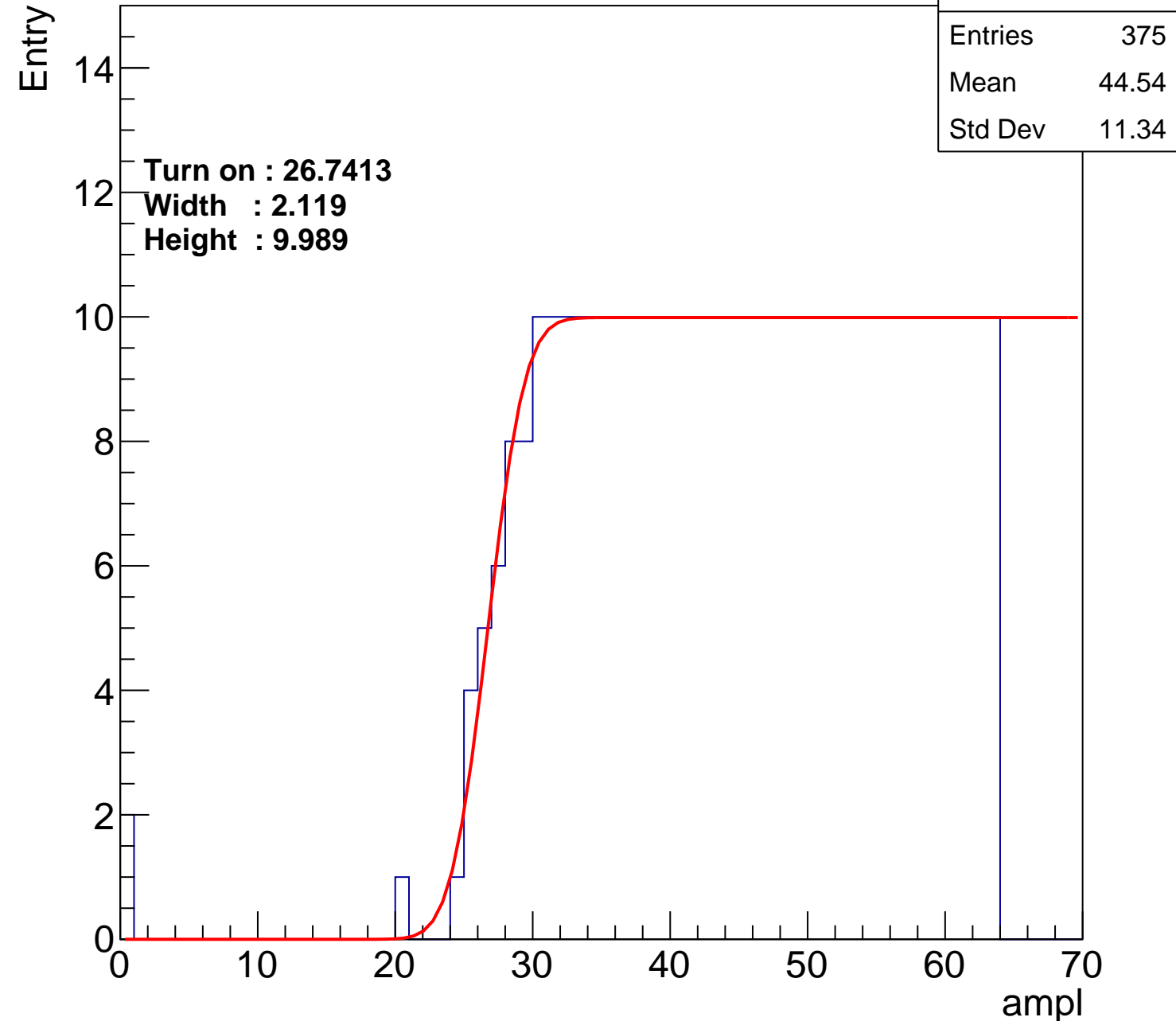
Width : 2.119

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch12

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.19 |
| Std Dev | 12.07 |

Turn on : 24.3163

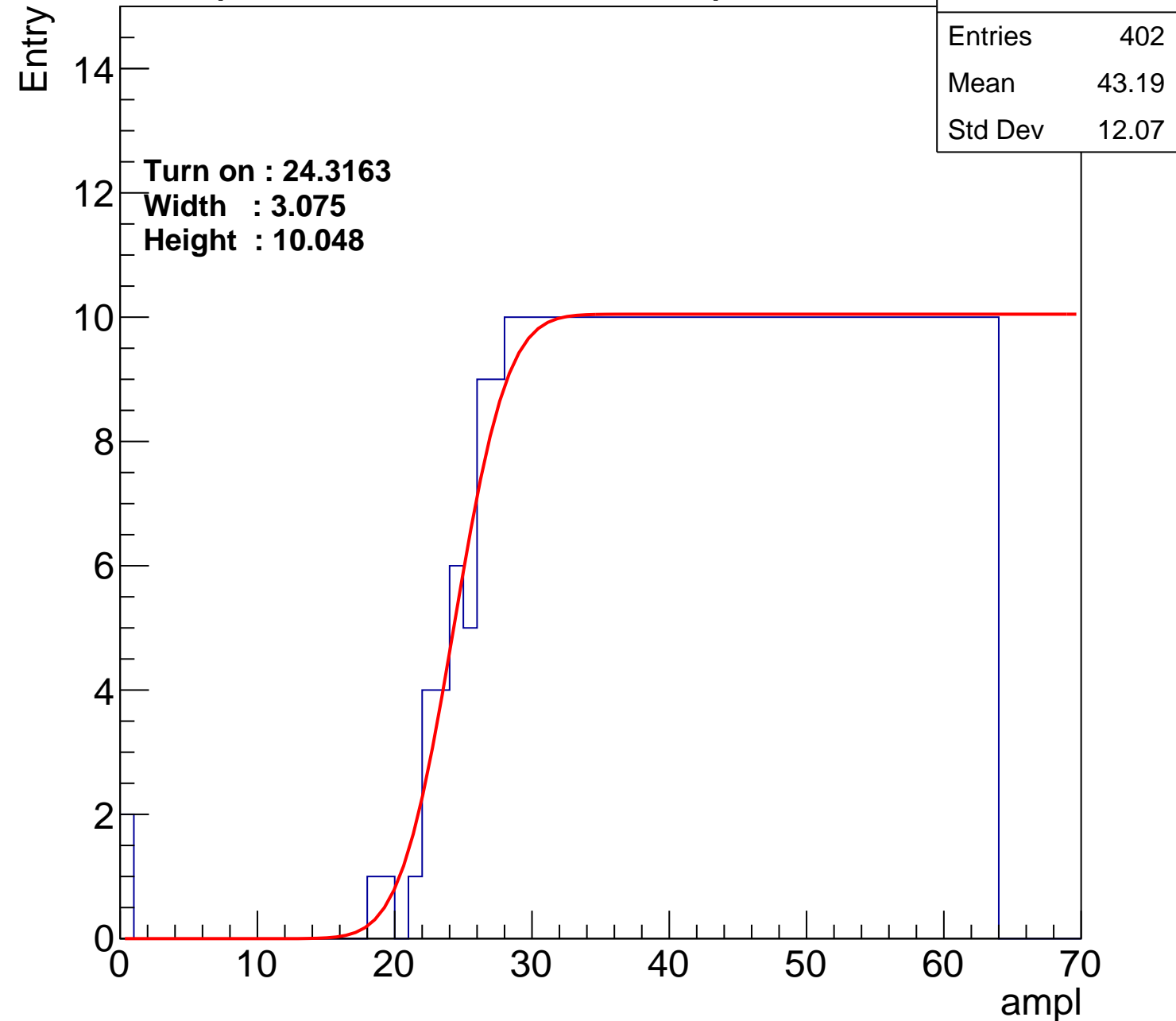
Width : 3.075

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch13

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.42 |
| Std Dev | 12.09 |

Turn on : 24.9656

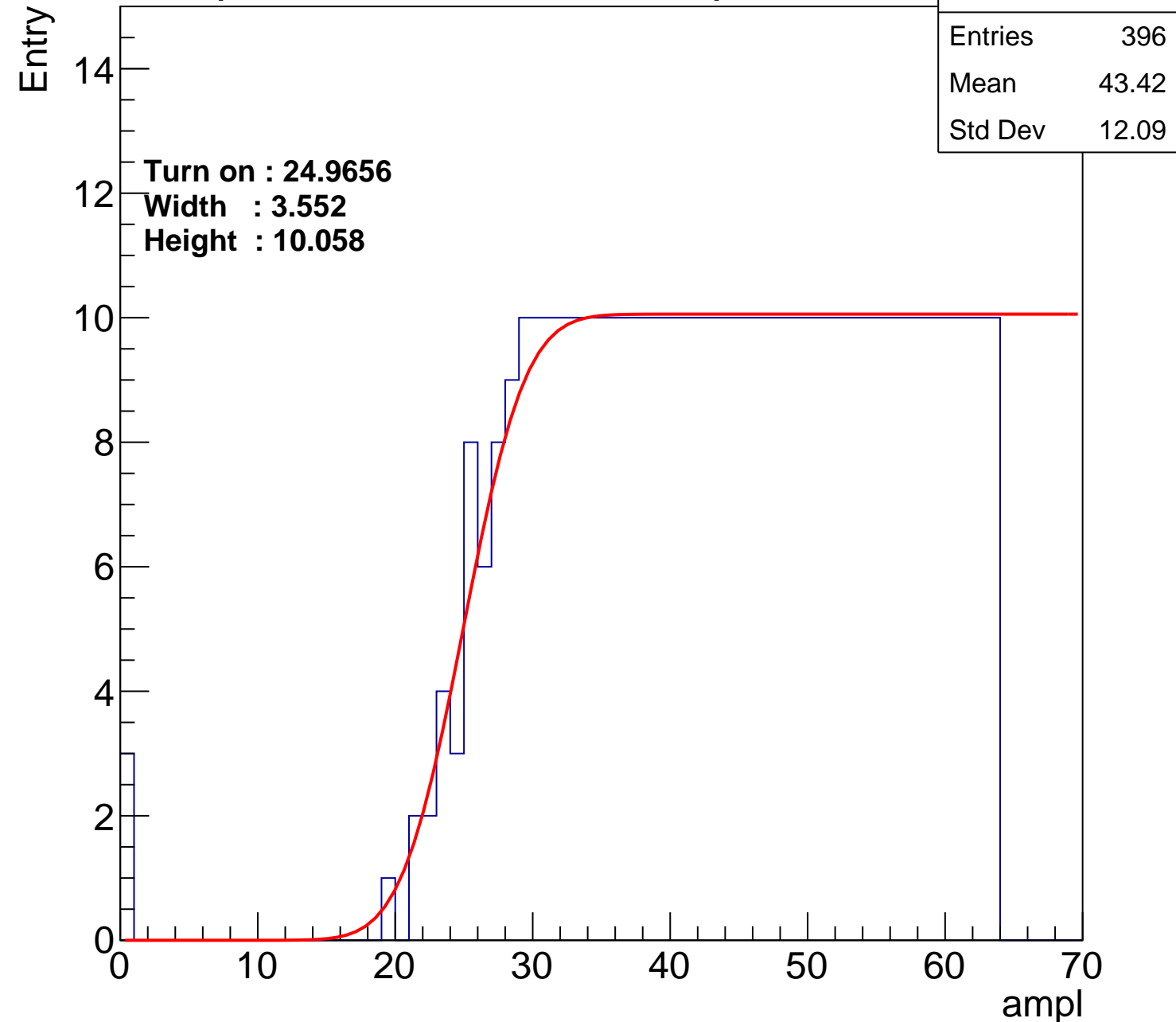
Width : 3.552

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch14

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.51 |
| Std Dev | 11.76 |

Turn on : 24.5434

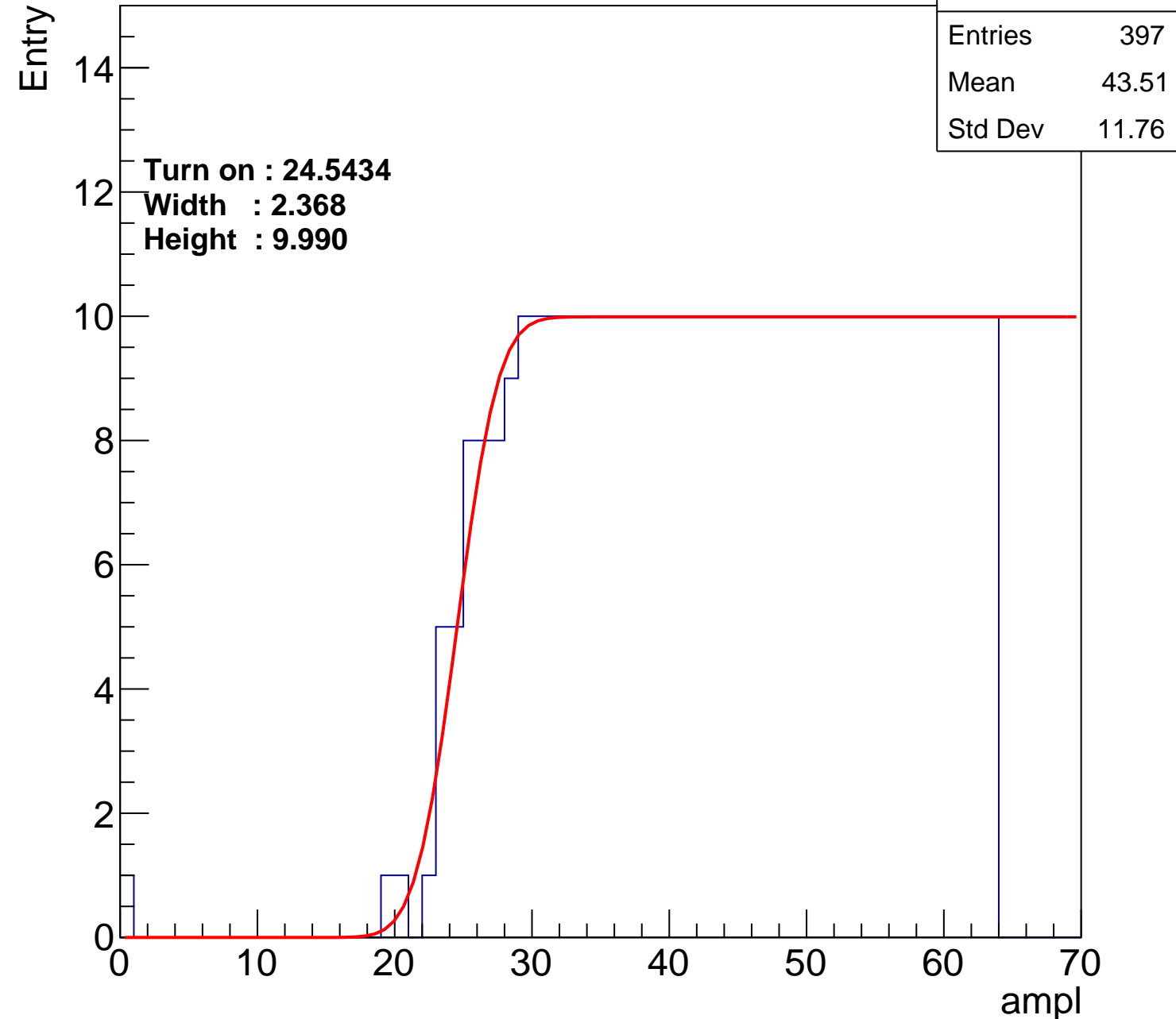
Width : 2.368

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch15

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.85 |
| Std Dev | 11.7 |

Turn on : 25.3817

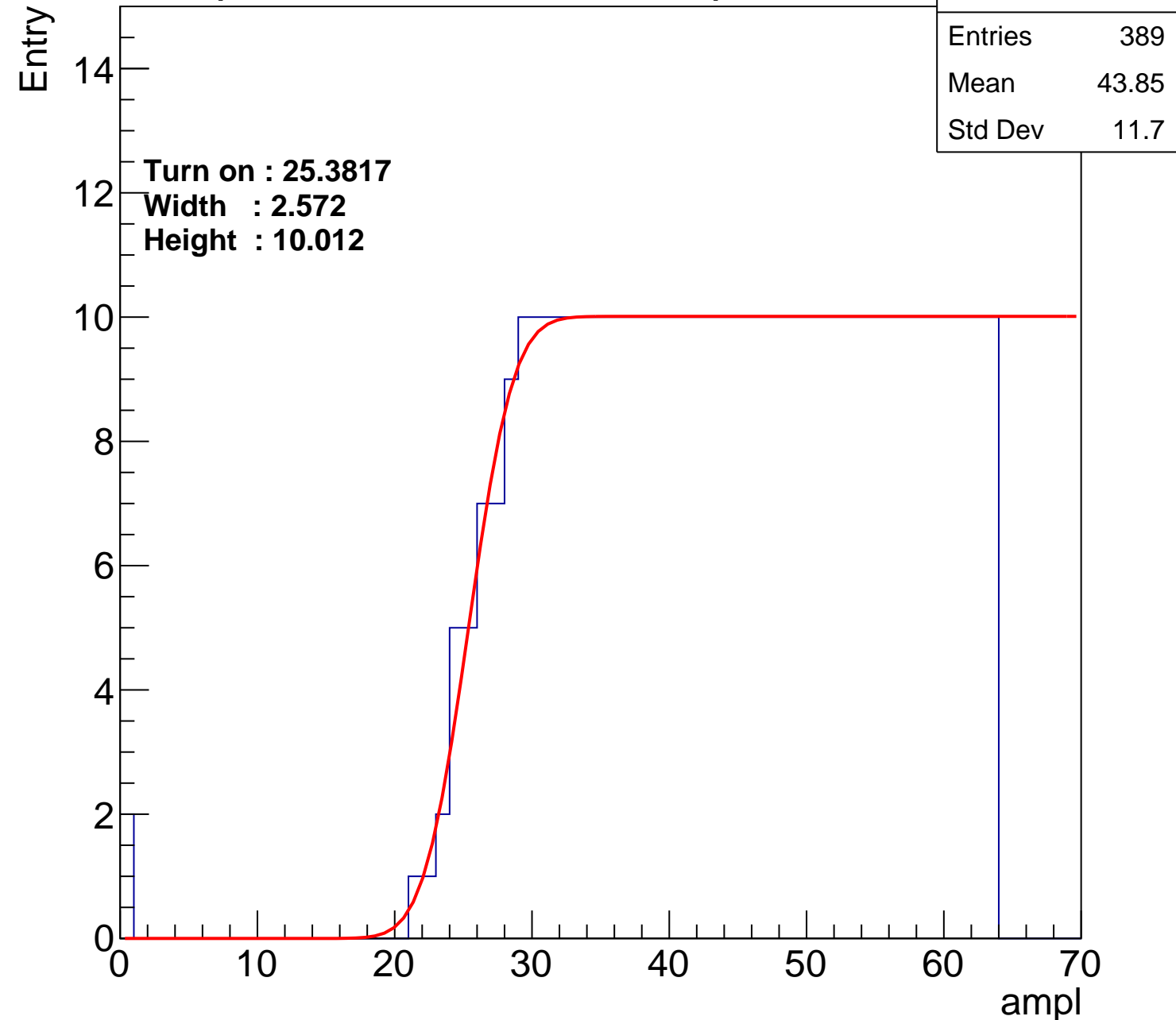
Width : 2.572

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch16

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 44.01 |
| Std Dev | 11.48 |

Turn on : 25.7359

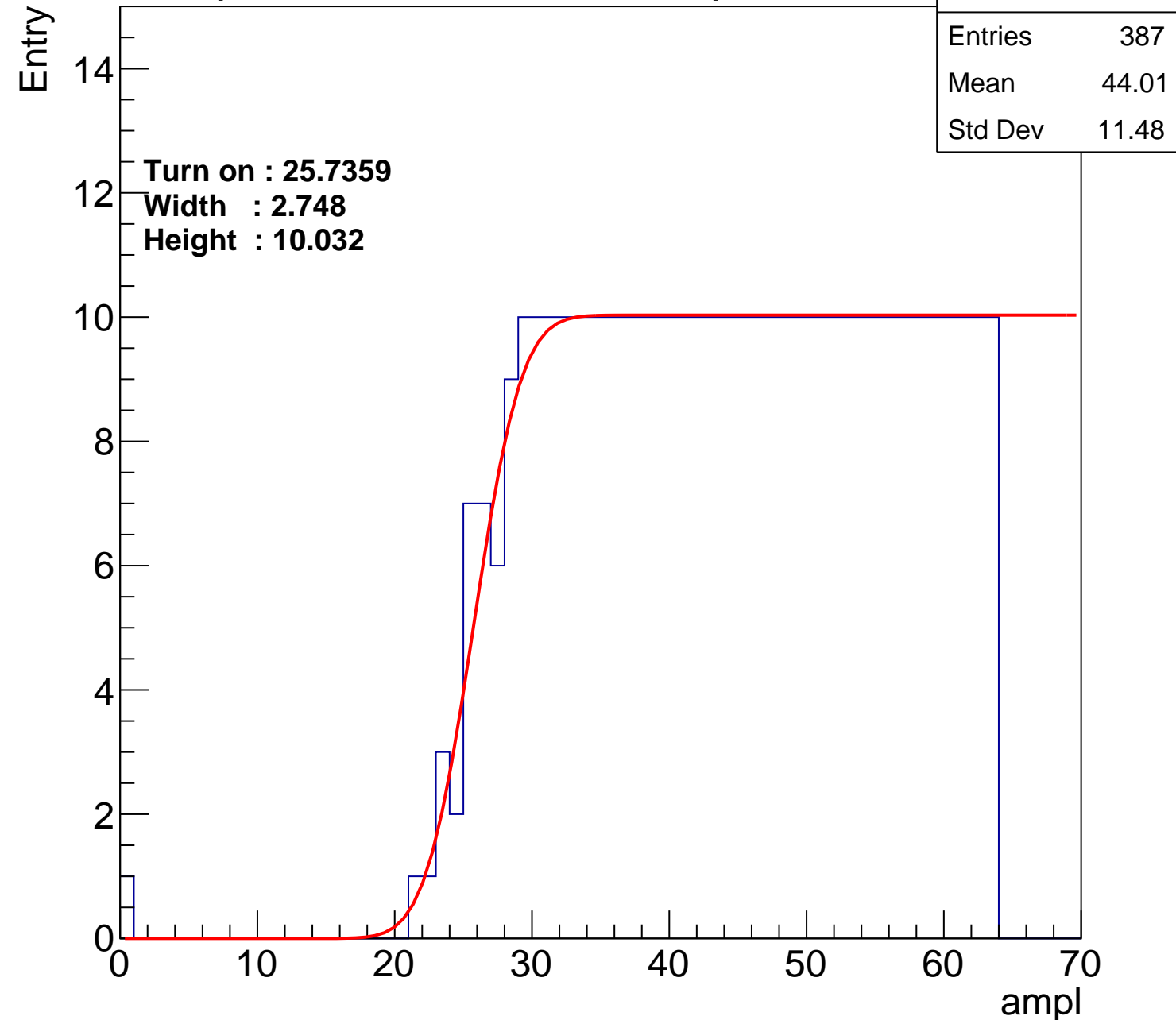
Width : 2.748

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch17

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.84 |
| Std Dev | 11.71 |

Turn on : 25.2178

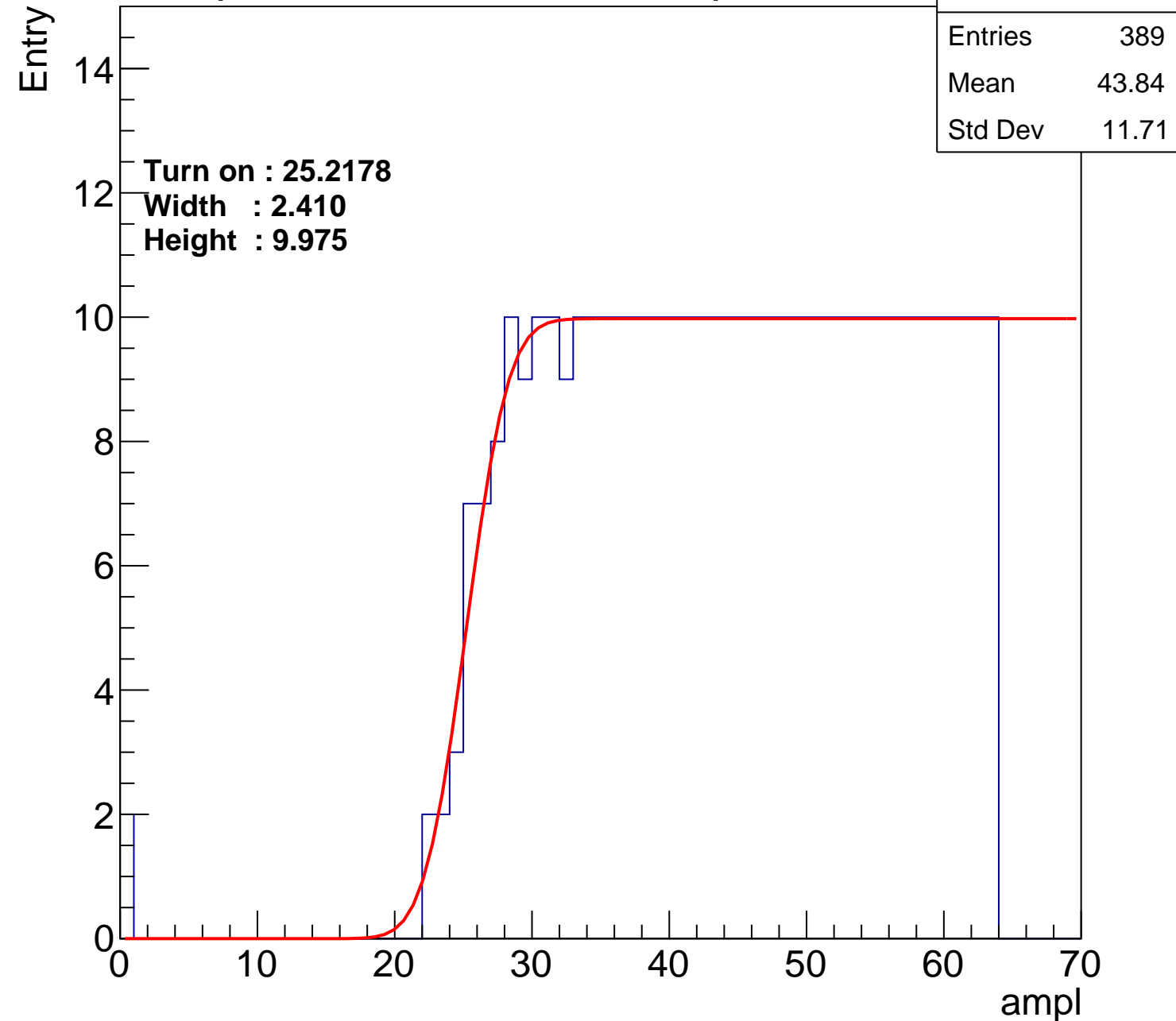
Width : 2.410

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch18

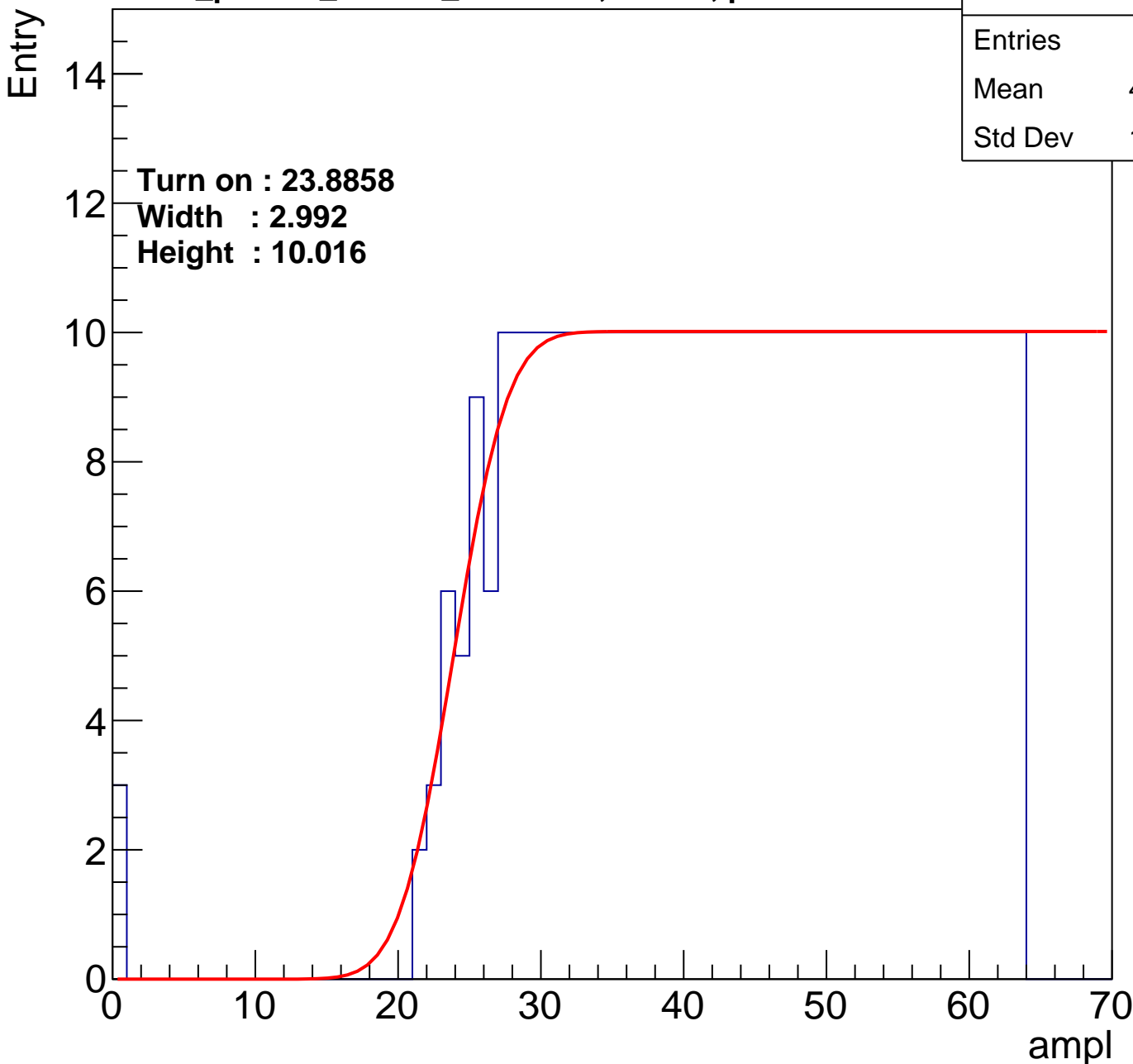
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 23.8858

Width : 2.992

Height : 10.016

| | |
|---------|-------|
| Entries | 404 |
| Mean | 43.06 |
| Std Dev | 12.22 |



B1L002S, U5-ch19

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.38 |
| Std Dev | 11.98 |

Turn on : 24.3903

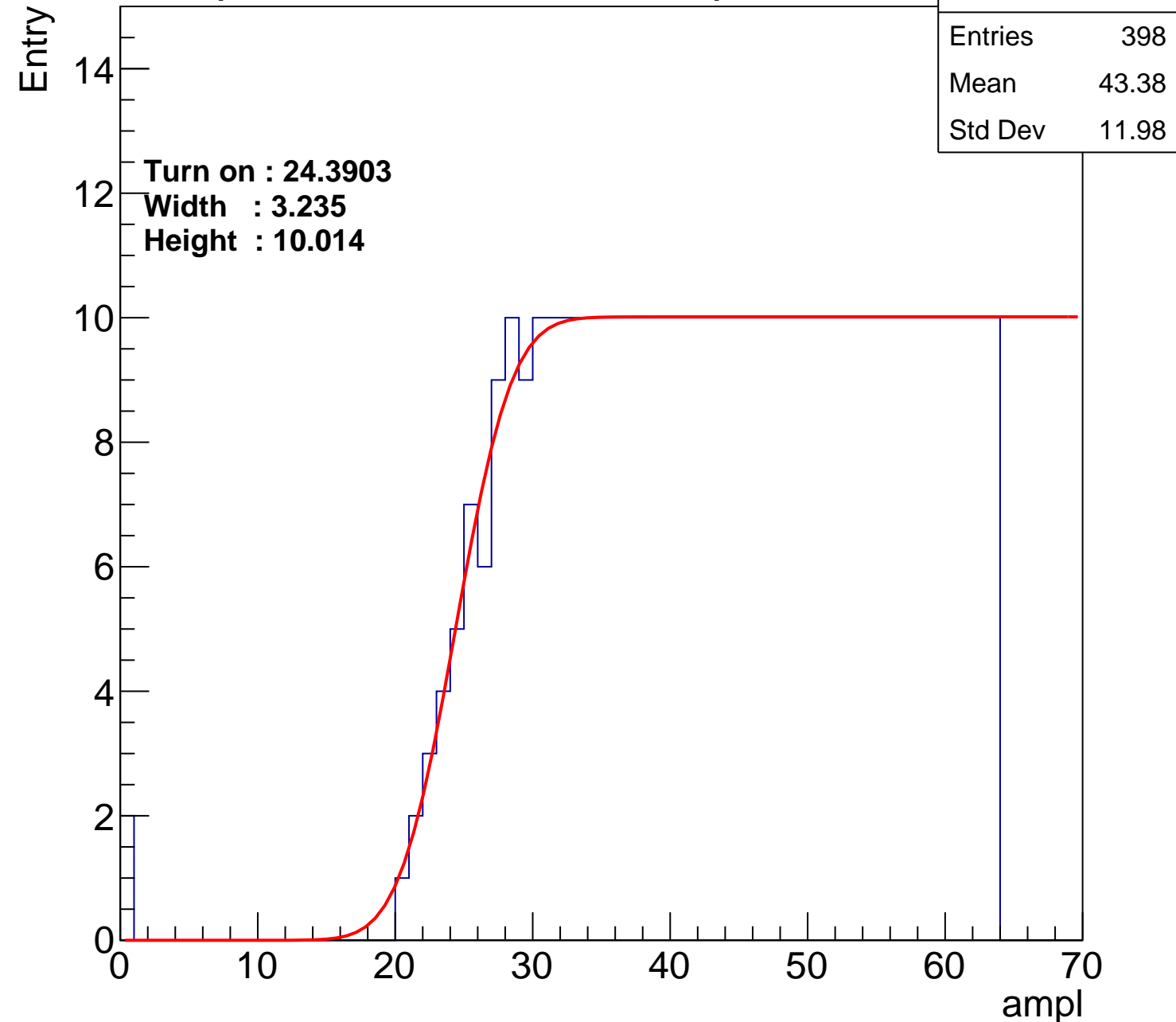
Width : 3.235

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch20

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 405 |
| Mean | 43.07 |
| Std Dev | 12.1 |

Turn on : 23.6398

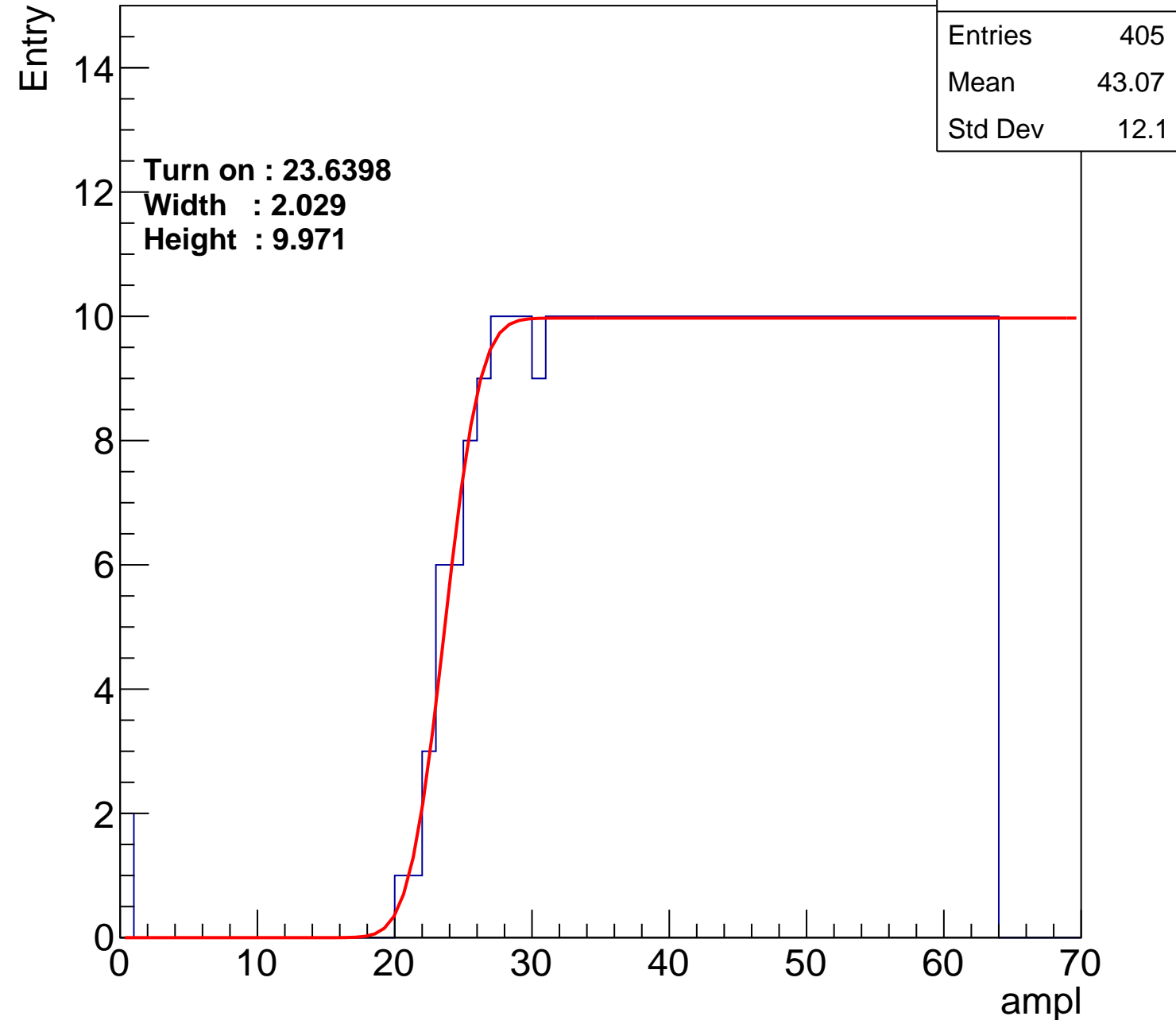
Width : 2.029

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch21

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.67 |
| Std Dev | 11.16 |

Turn on : 27.4170

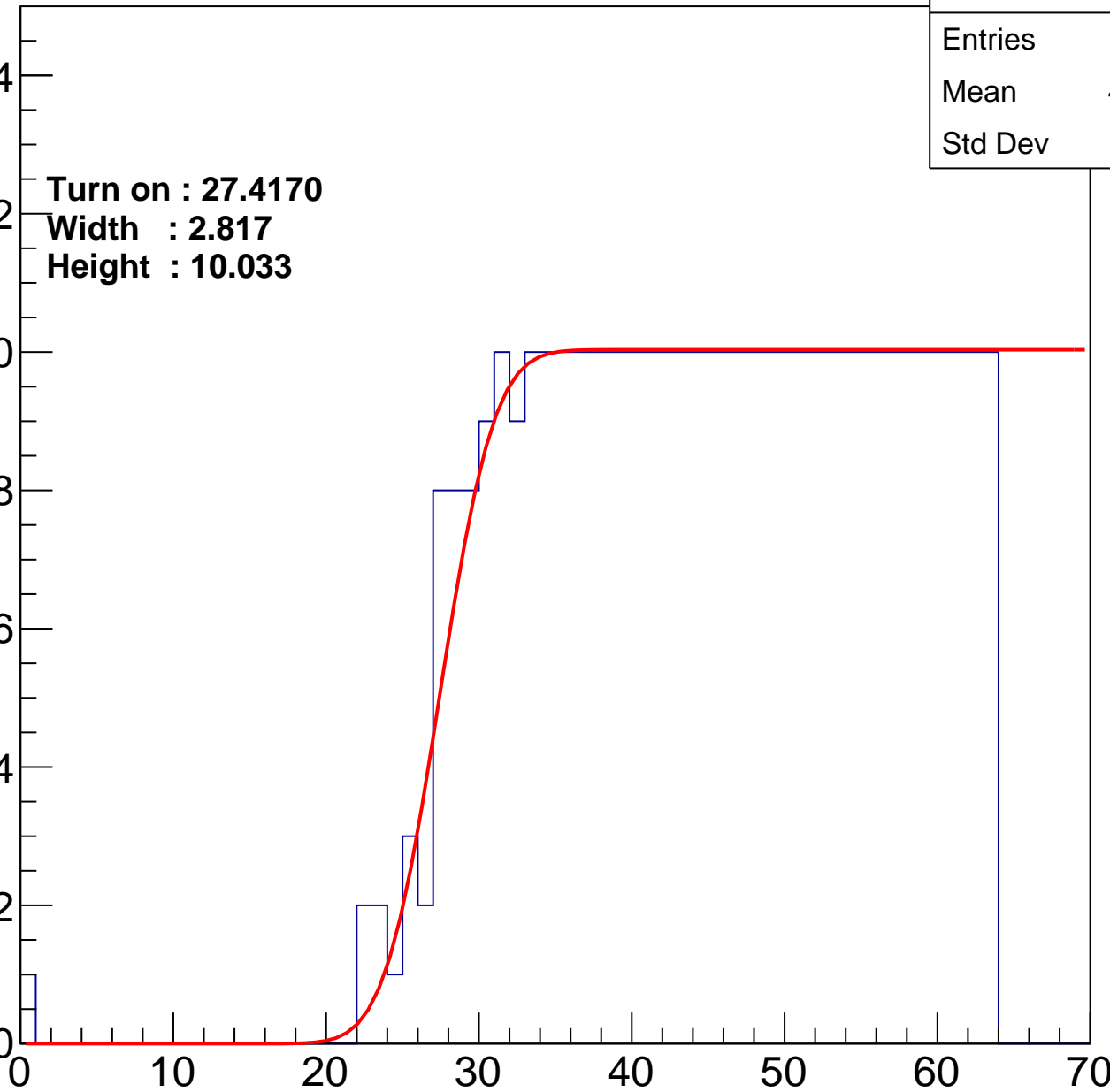
Width : 2.817

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch22

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 408 |
| Mean | 42.84 |
| Std Dev | 12.37 |

Turn on : 23.6209

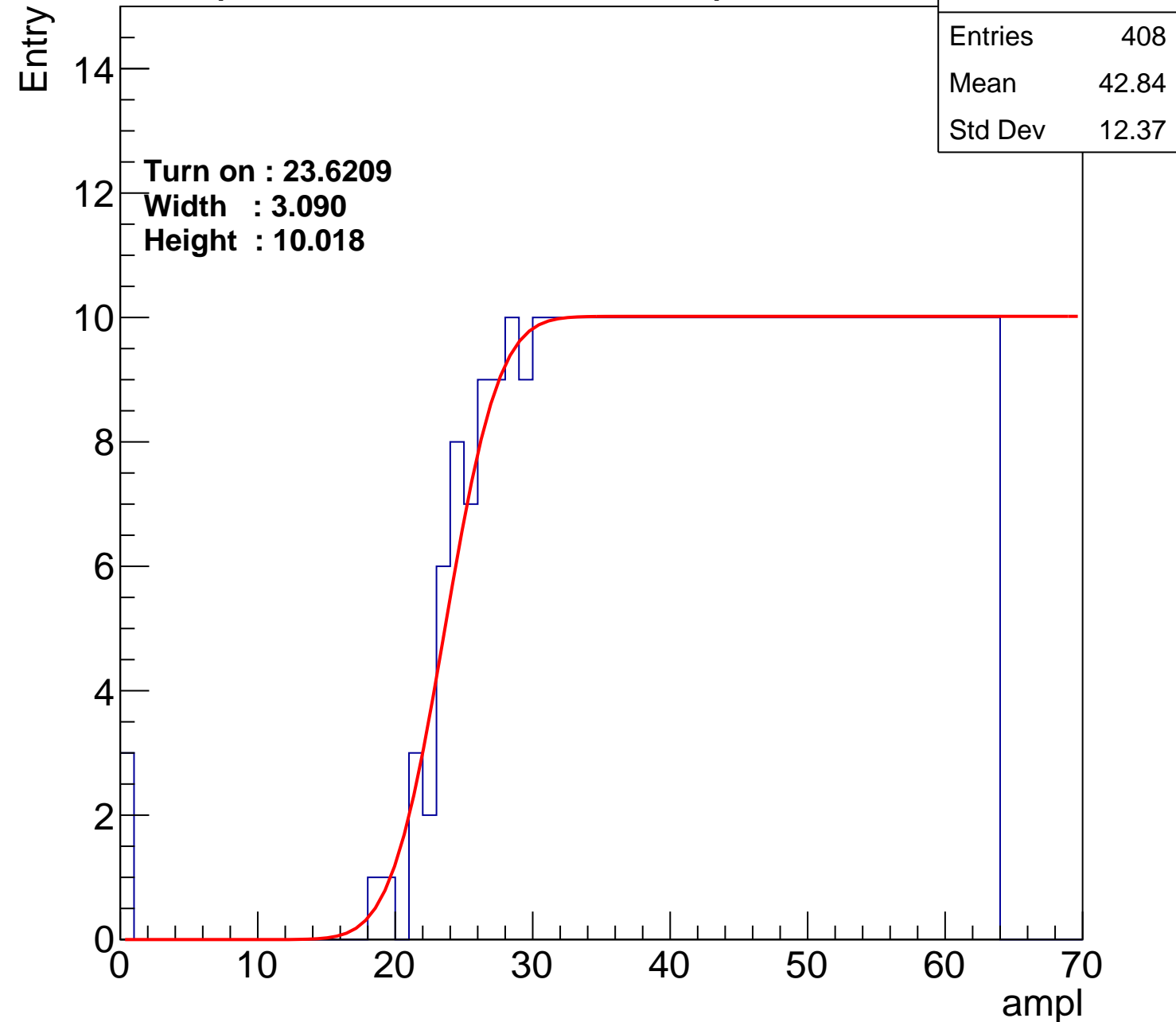
Width : 3.090

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch23

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.49 |
| Std Dev | 11.89 |

Turn on : 24.4653

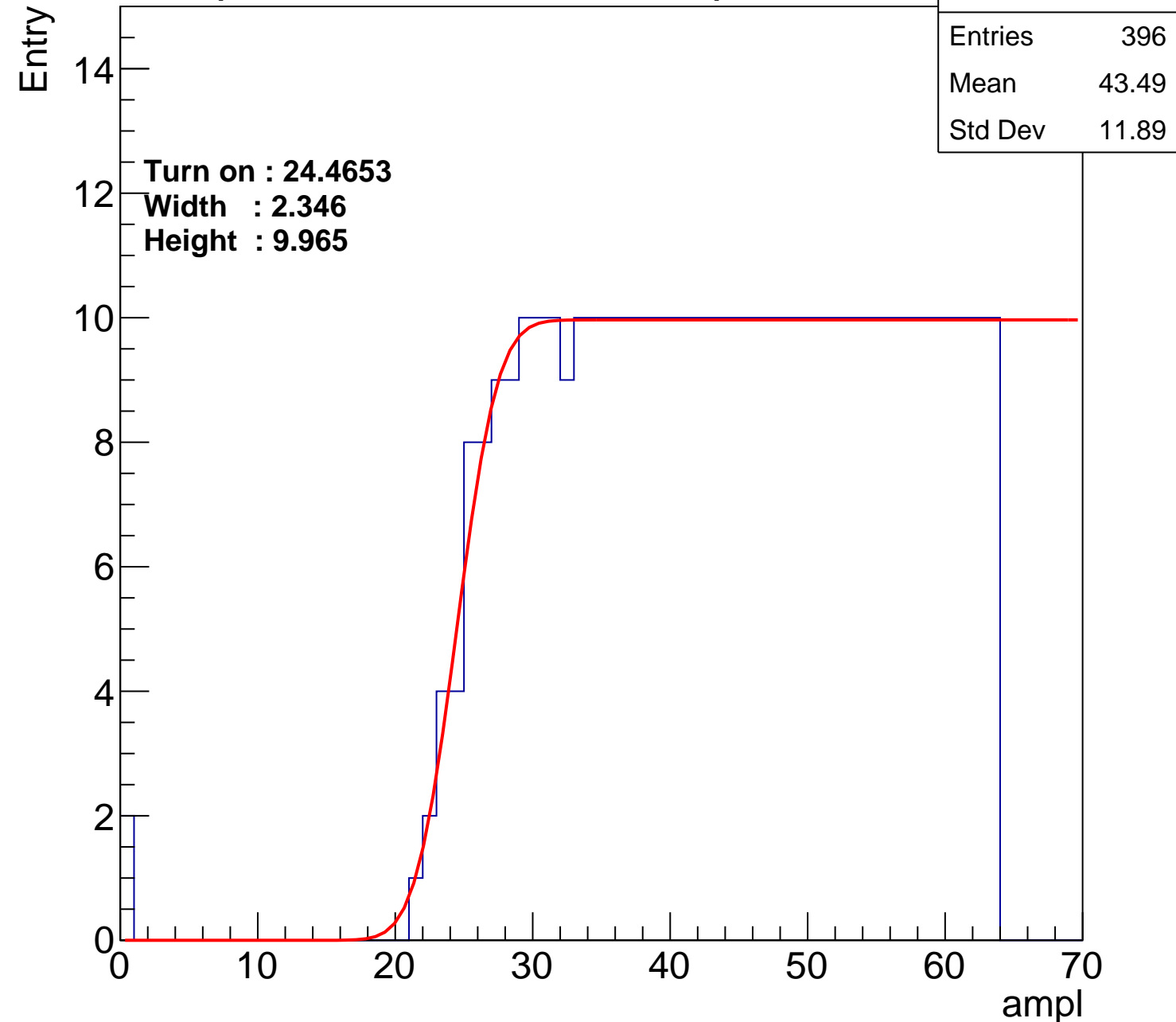
Width : 2.346

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch24

calib_packv5_042523_0143.root, FC#10, port B3

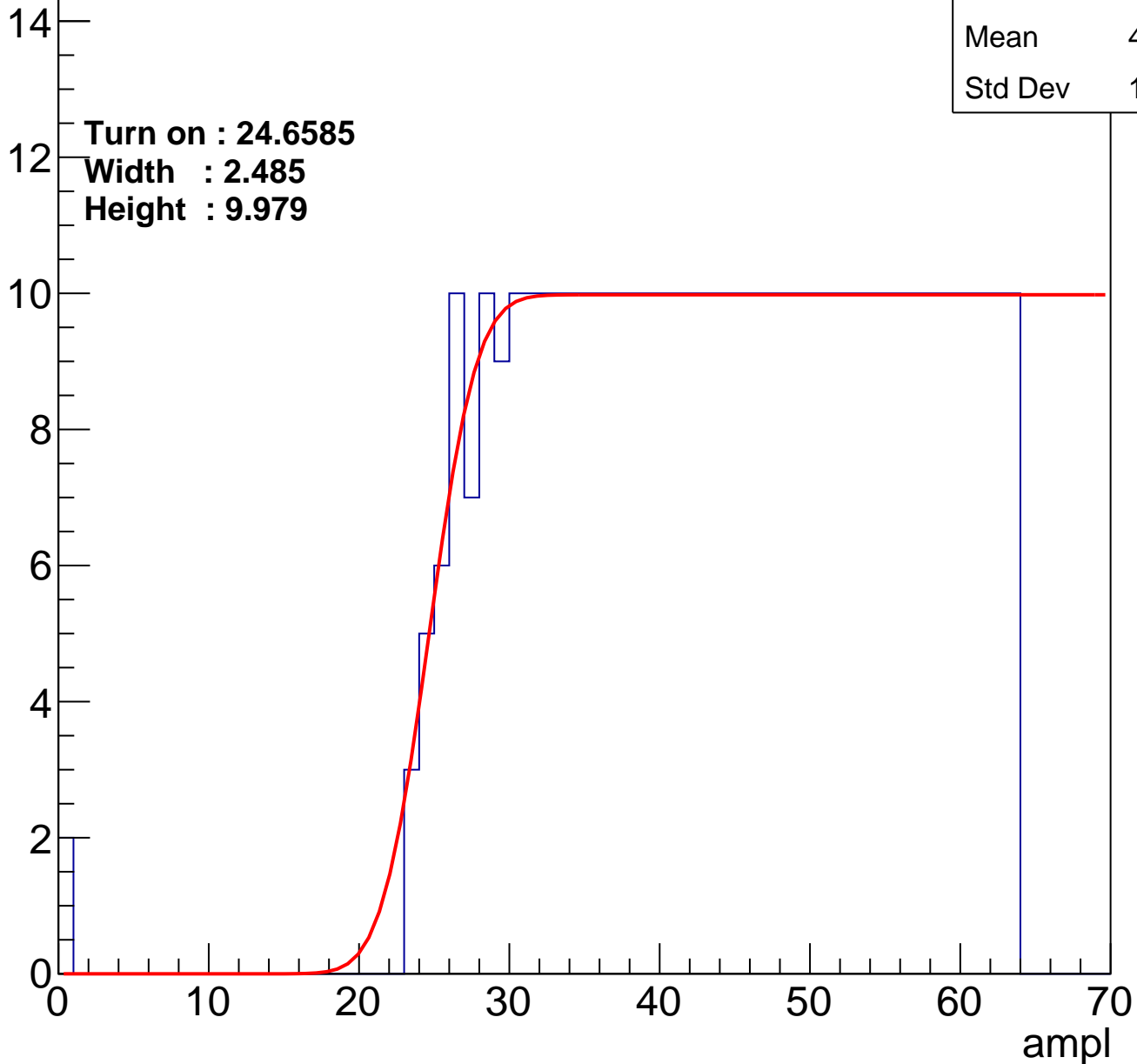
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.72 |
| Std Dev | 11.74 |

Turn on : 24.6585

Width : 2.485

Height : 9.979

Entry



B1L002S, U5-ch25

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.08 |
| Std Dev | 11.59 |

Turn on : 25.8351

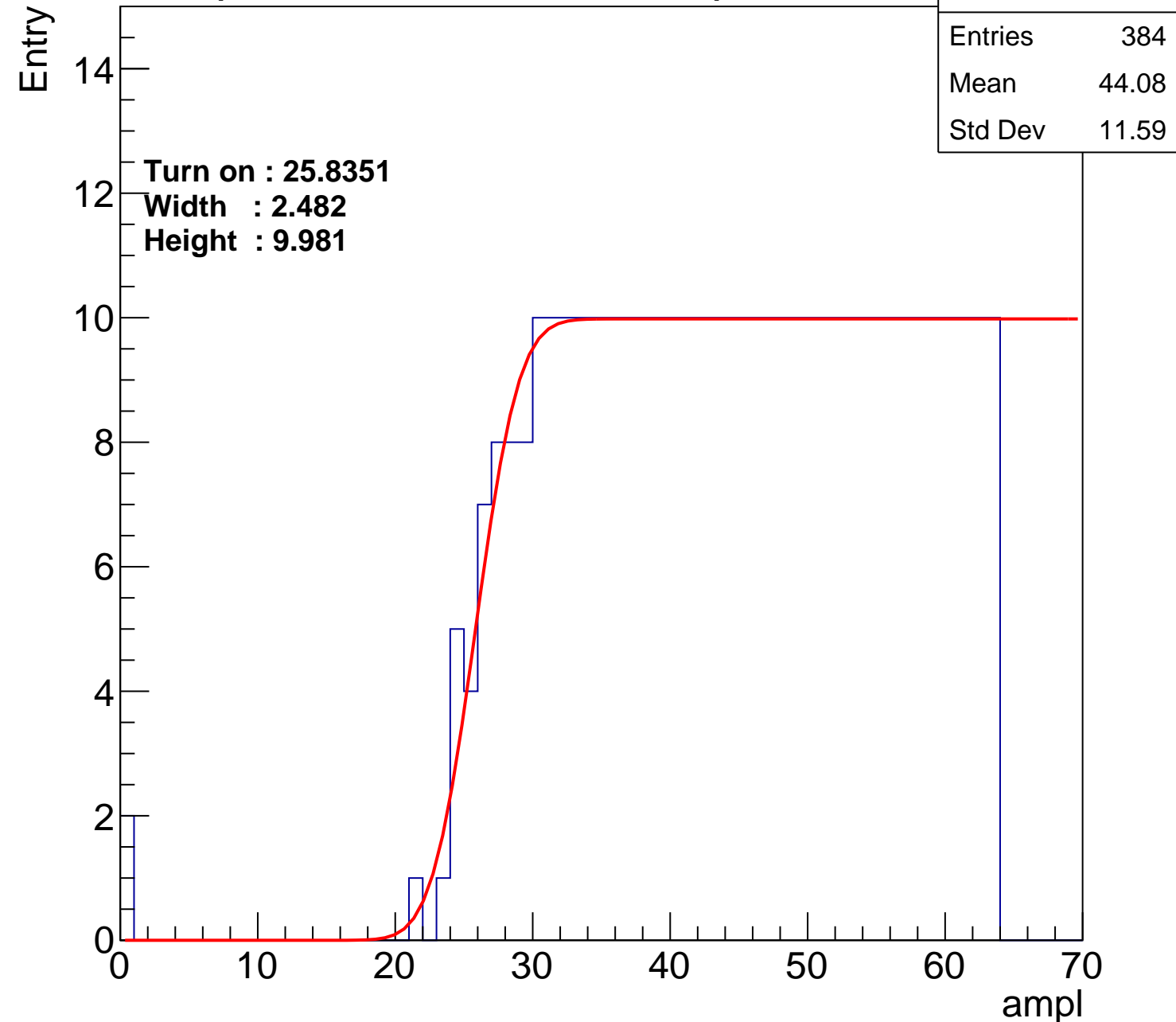
Width : 2.482

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch26

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.72 |
| Std Dev | 11.81 |

Turn on : 25.5679

Width : 3.089

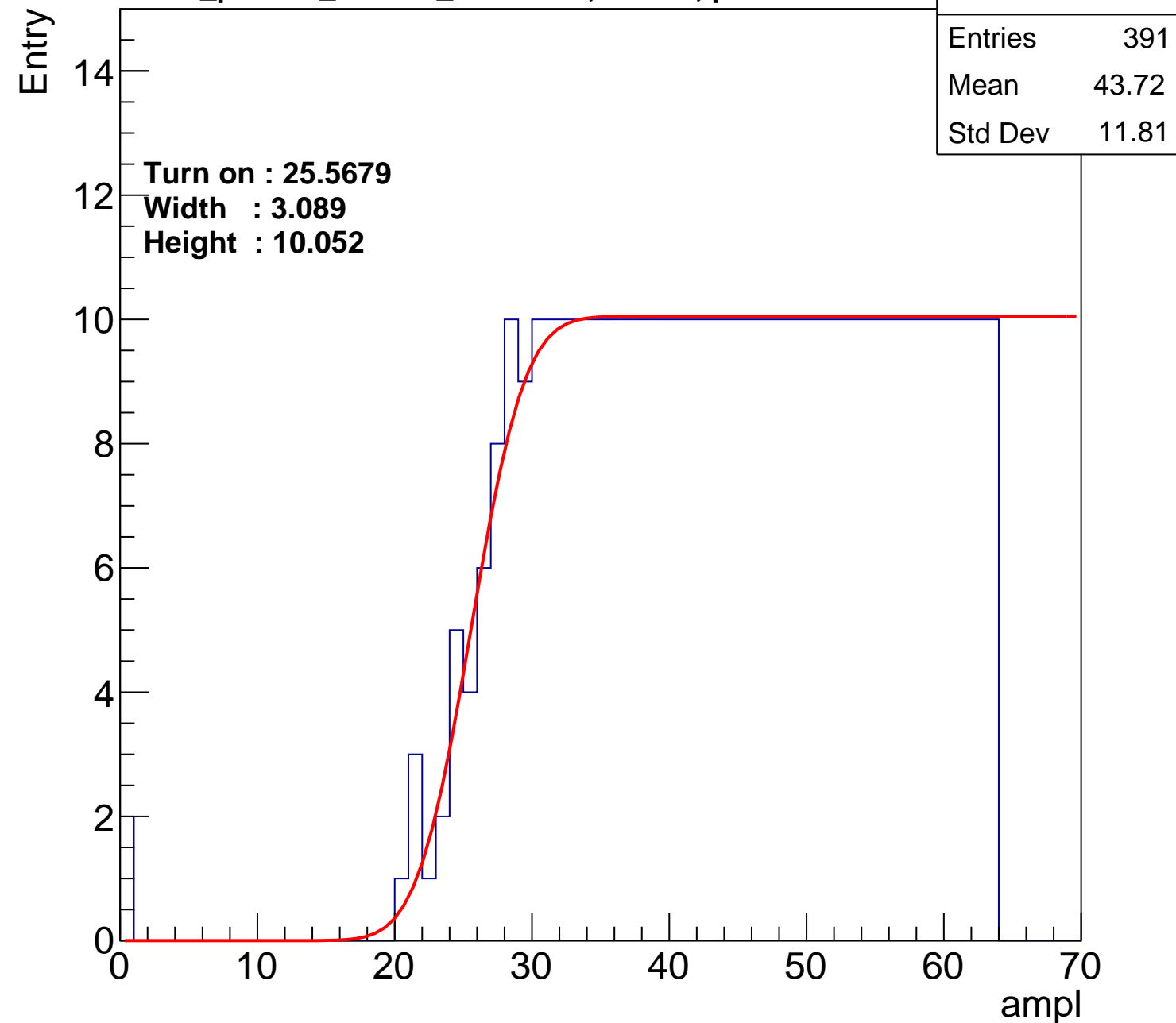
Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U5-ch27

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.65 |
| Std Dev | 11.87 |

Turn on : 25.4190

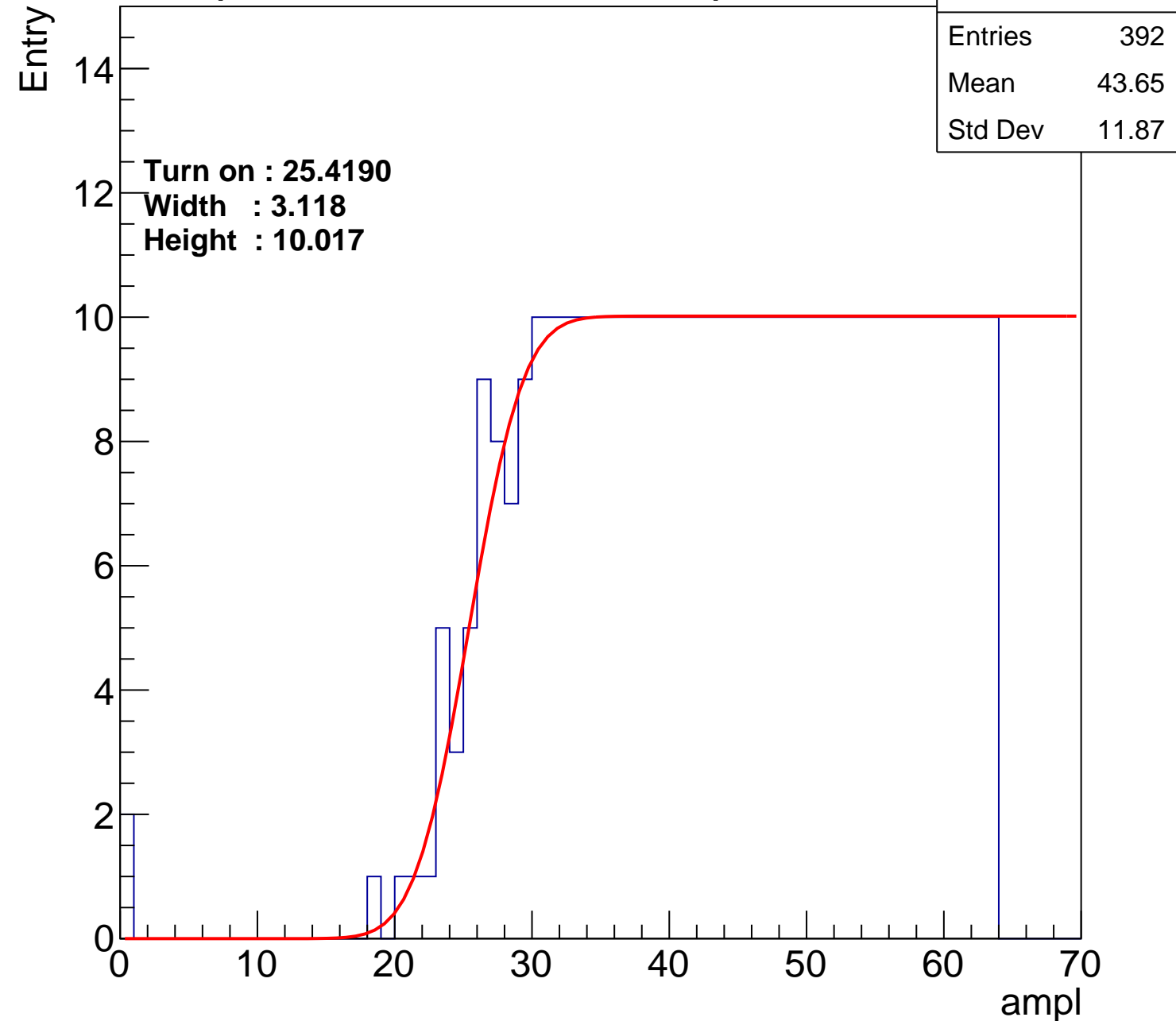
Width : 3.118

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch28

calib_packv5_042523_0143.root, FC#10, port B3

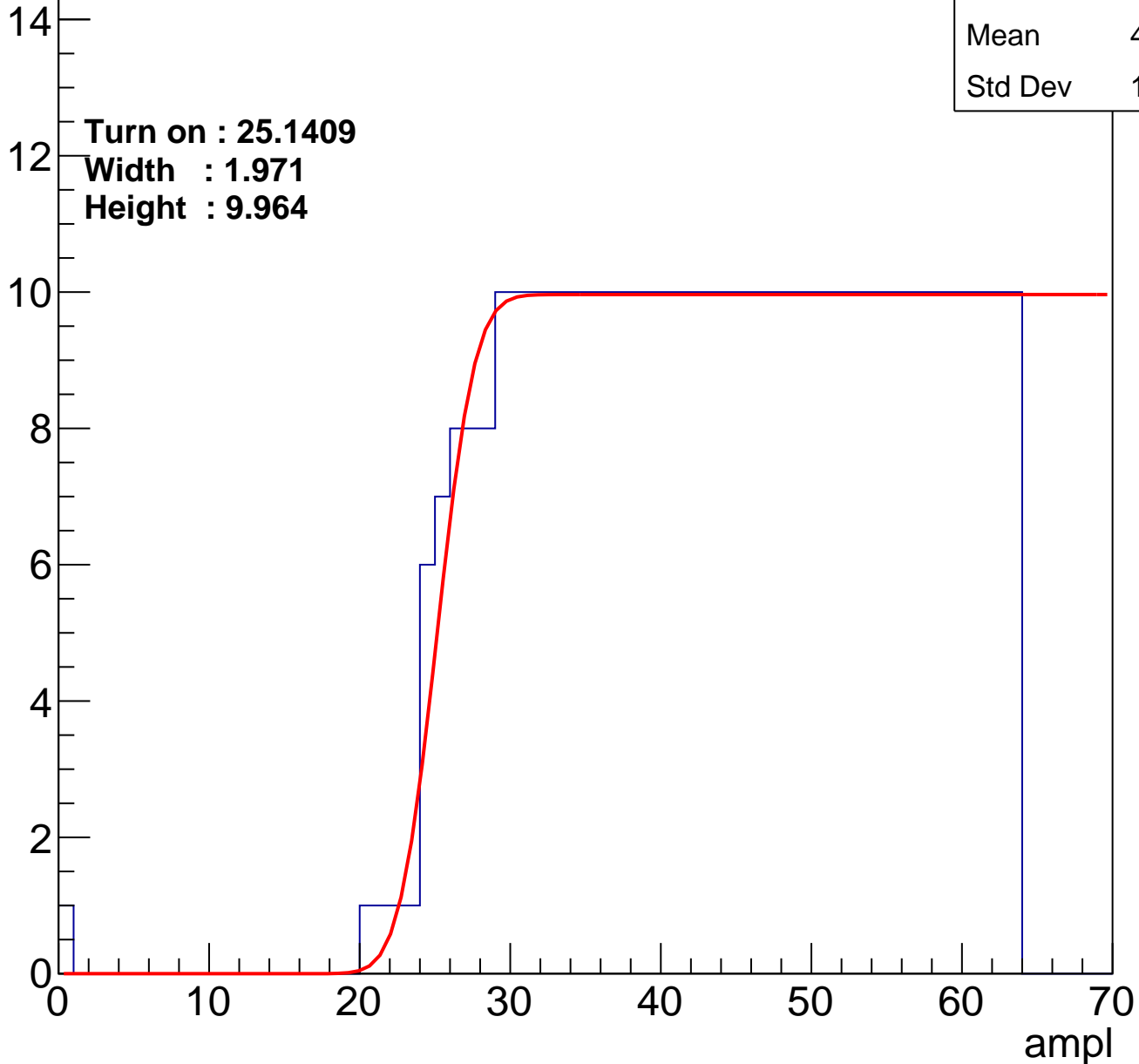
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.76 |
| Std Dev | 11.62 |

Turn on : 25.1409

Width : 1.971

Height : 9.964

Entry



B1L002S, U5-ch29

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.1 |
| Std Dev | 12.05 |

Turn on : 26.8705

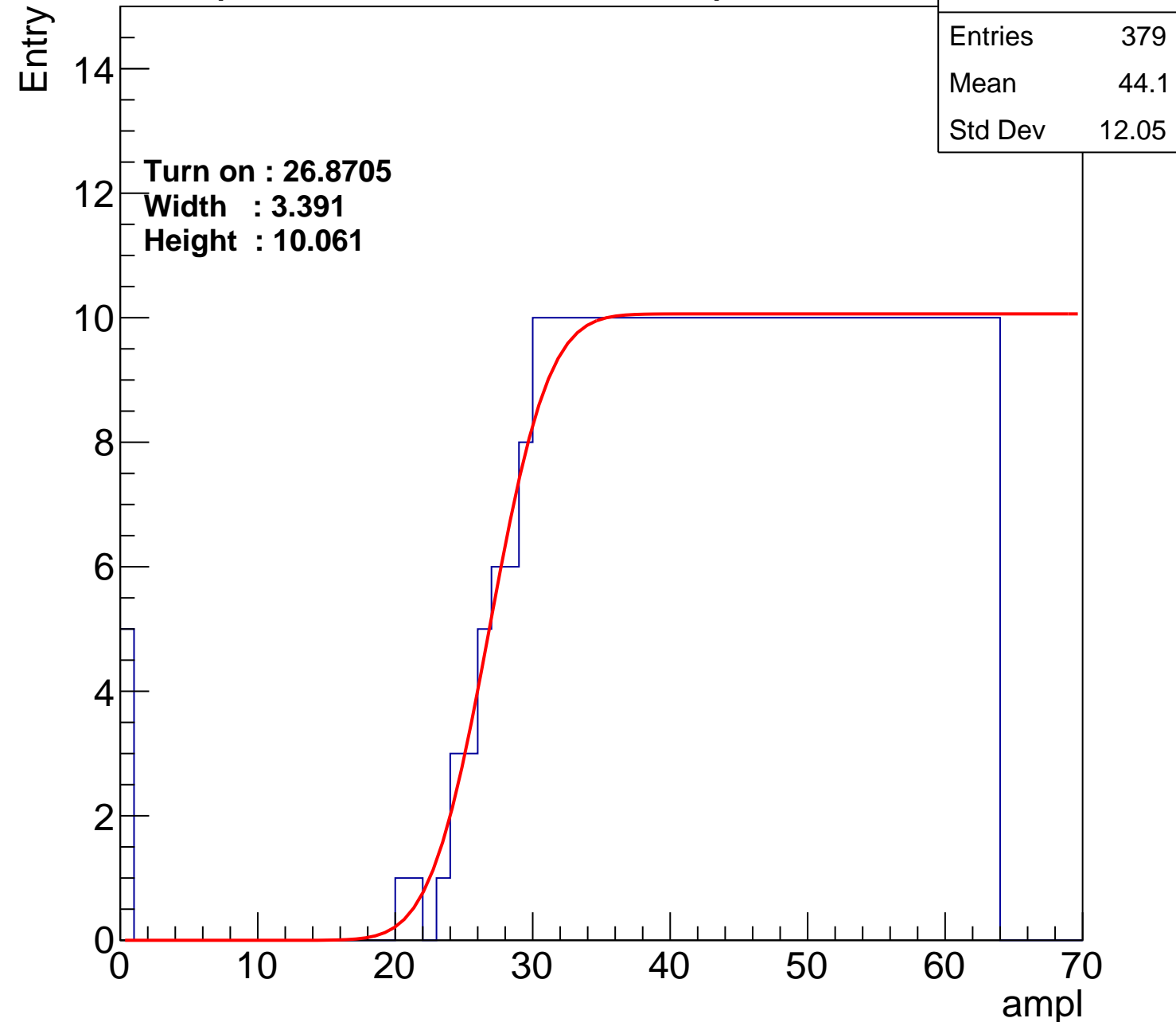
Width : 3.391

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch30

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.62 |
| Std Dev | 11.9 |

Turn on : 25.1998

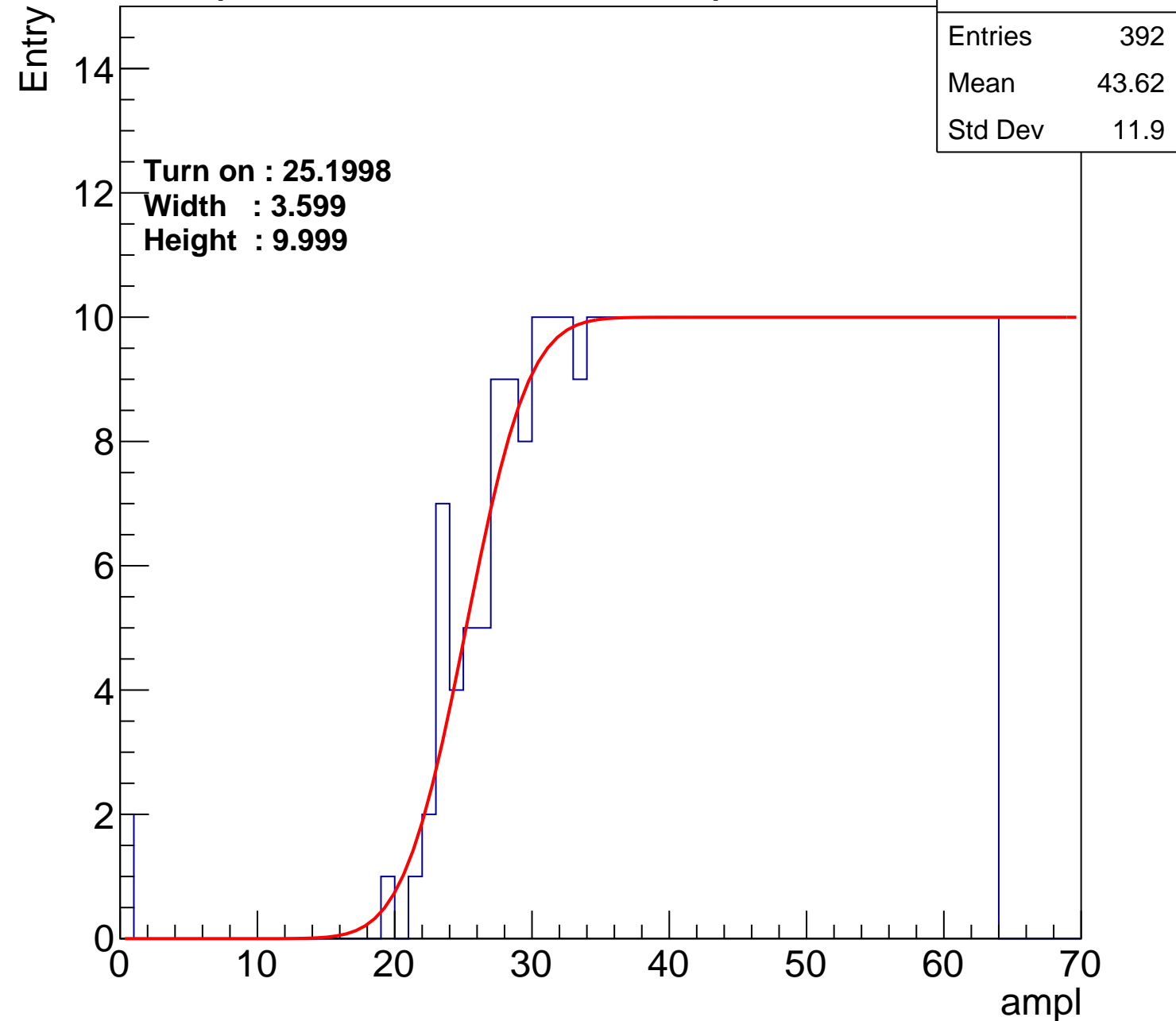
Width : 3.599

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch31

calib_packv5_042523_0143.root, FC#10, port B3

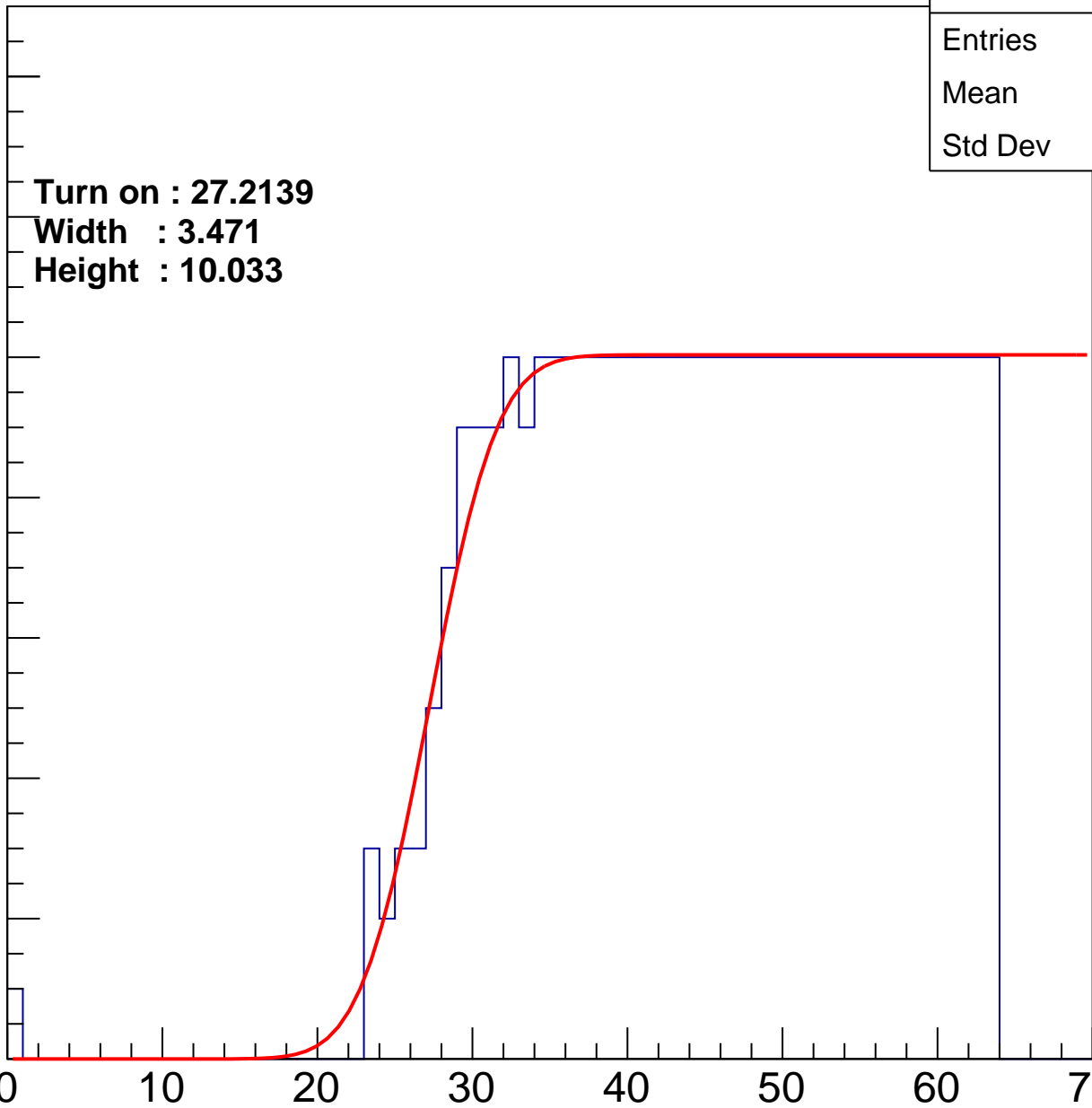
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2139
Width : 3.471
Height : 10.033

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.81 |
| Std Dev | 11.09 |

ampl



B1L002S, U5-ch32

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.52 |
| Std Dev | 11.18 |

Turn on : 26.8651

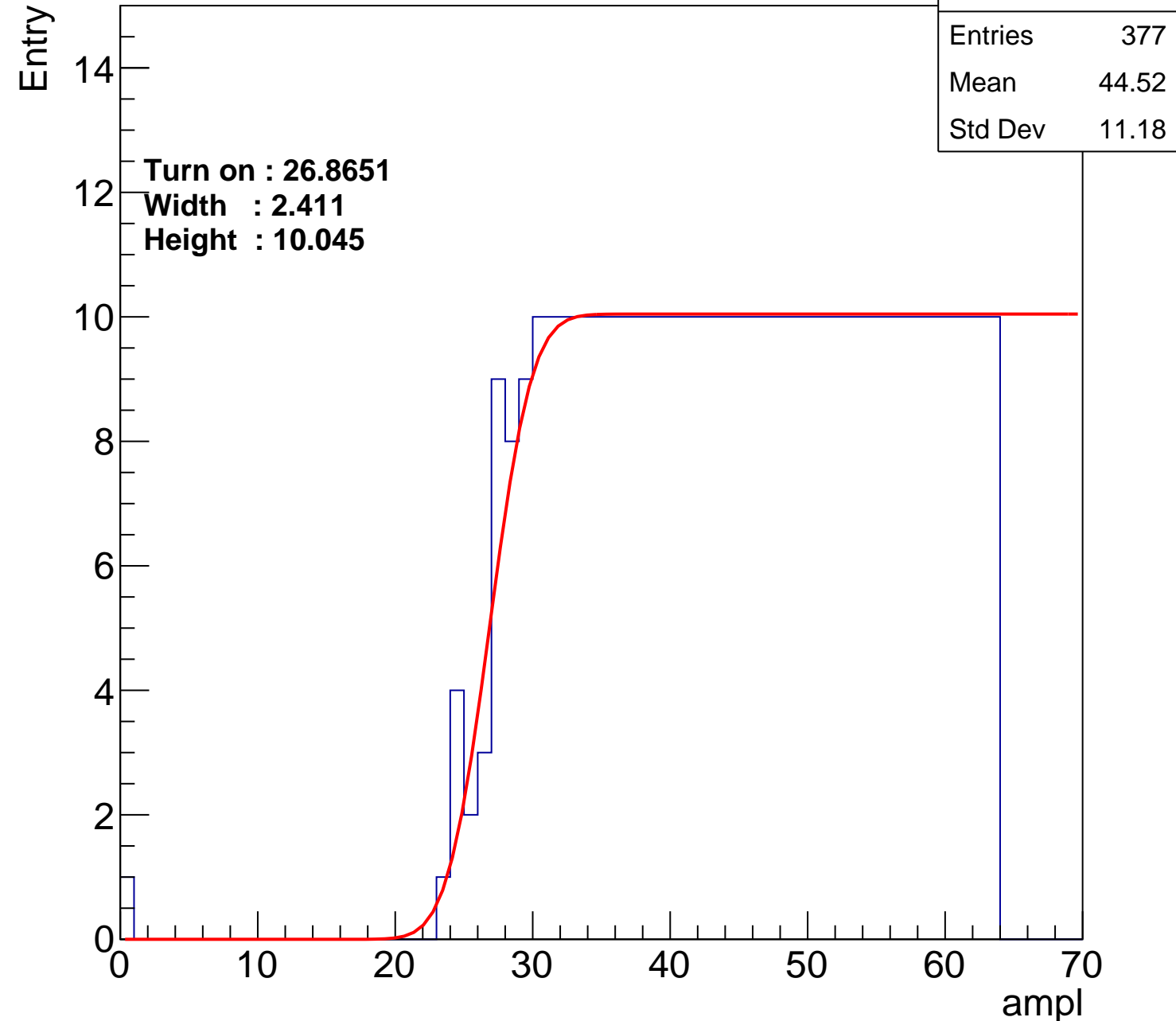
Width : 2.411

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch33

calib_packv5_042523_0143.root, FC#10, port B3

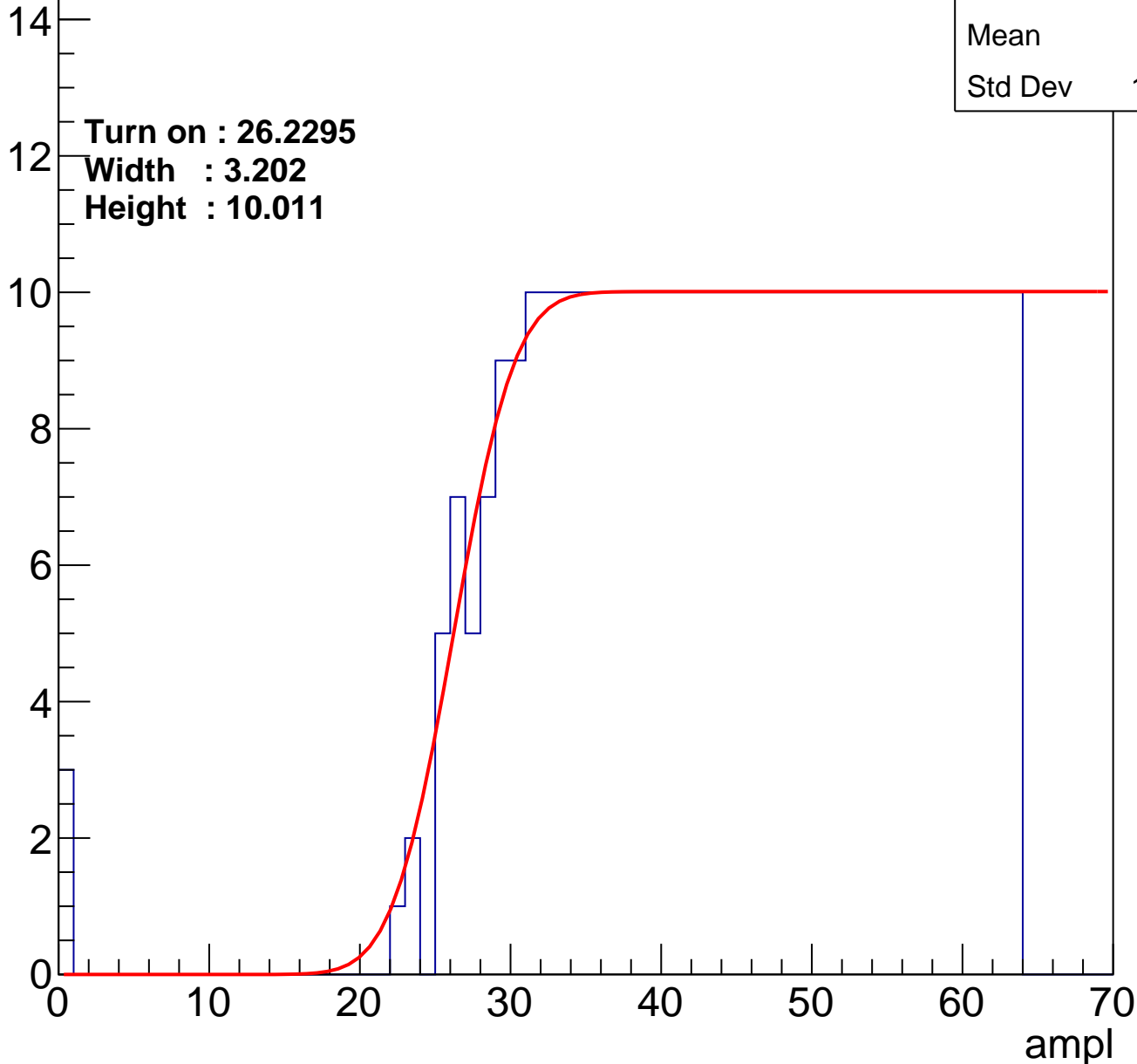
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.3 |
| Std Dev | 11.63 |

Turn on : 26.2295

Width : 3.202

Height : 10.011

Entry



B1L002S, U5-ch34

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.95 |
| Std Dev | 11.77 |

Turn on : 25.7436

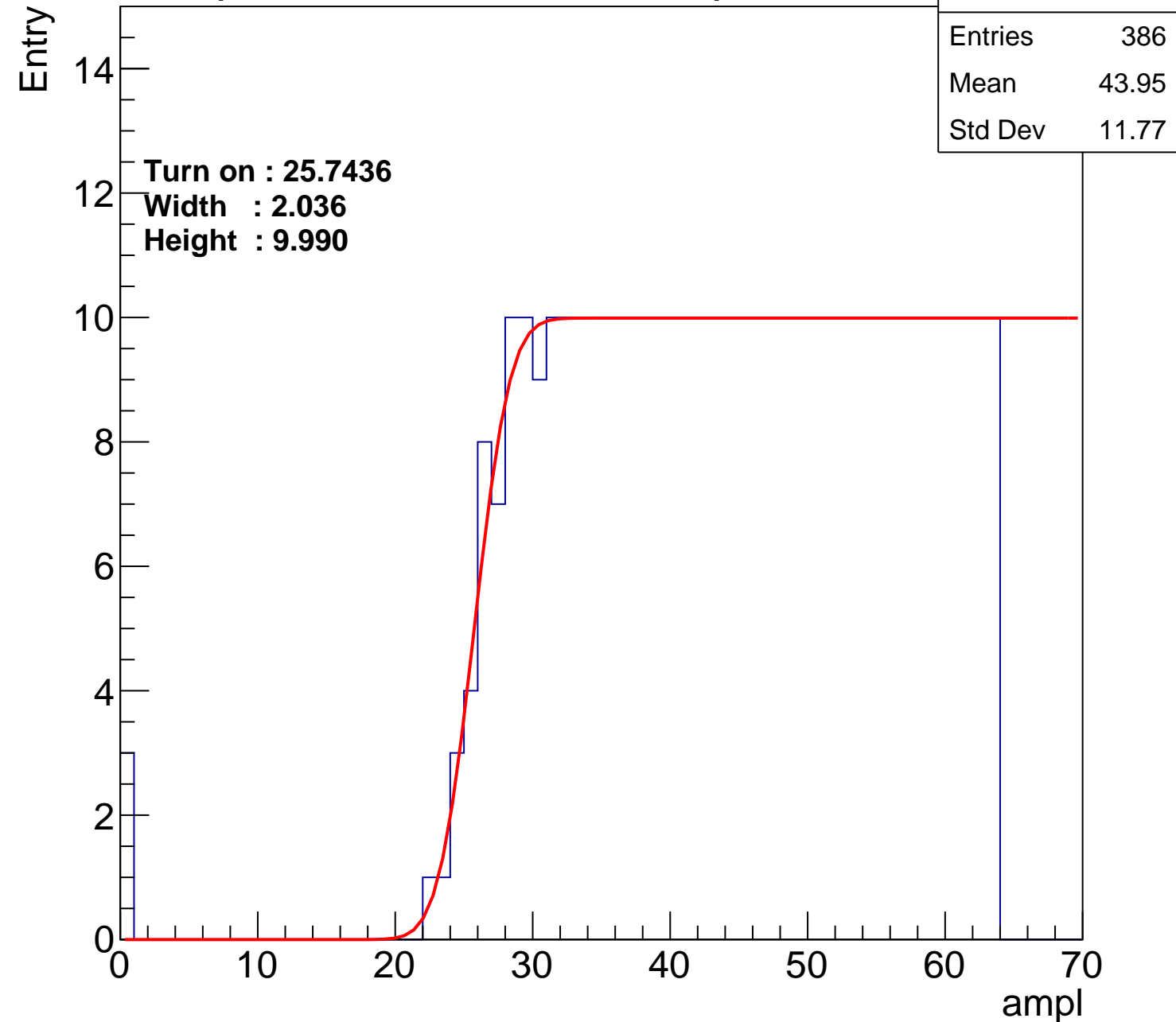
Width : 2.036

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch35

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.53 |
| Std Dev | 12.25 |

Turn on : 25.6249

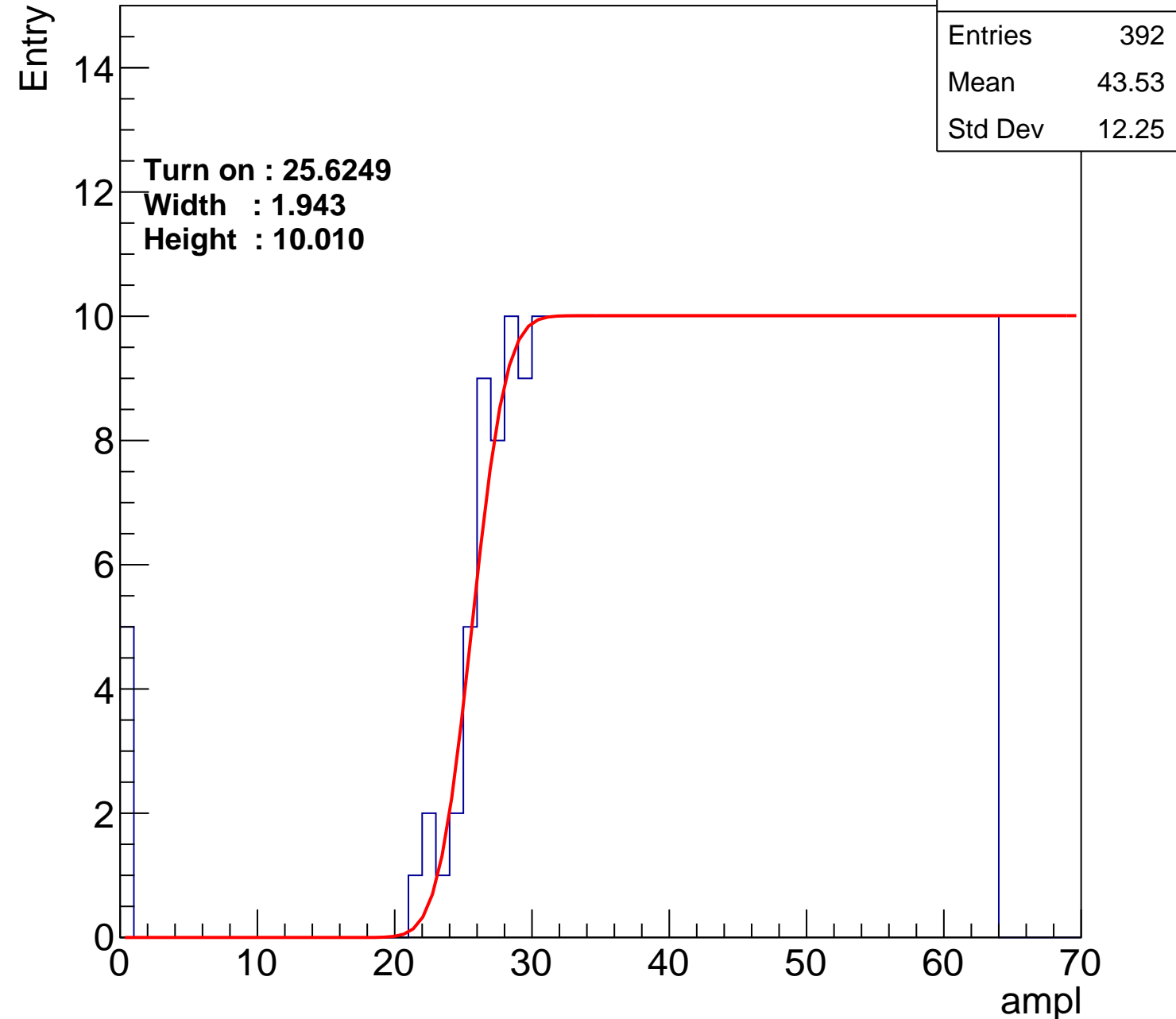
Width : 1.943

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch36

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.24 |
| Std Dev | 12.04 |

Turn on : 24.1273

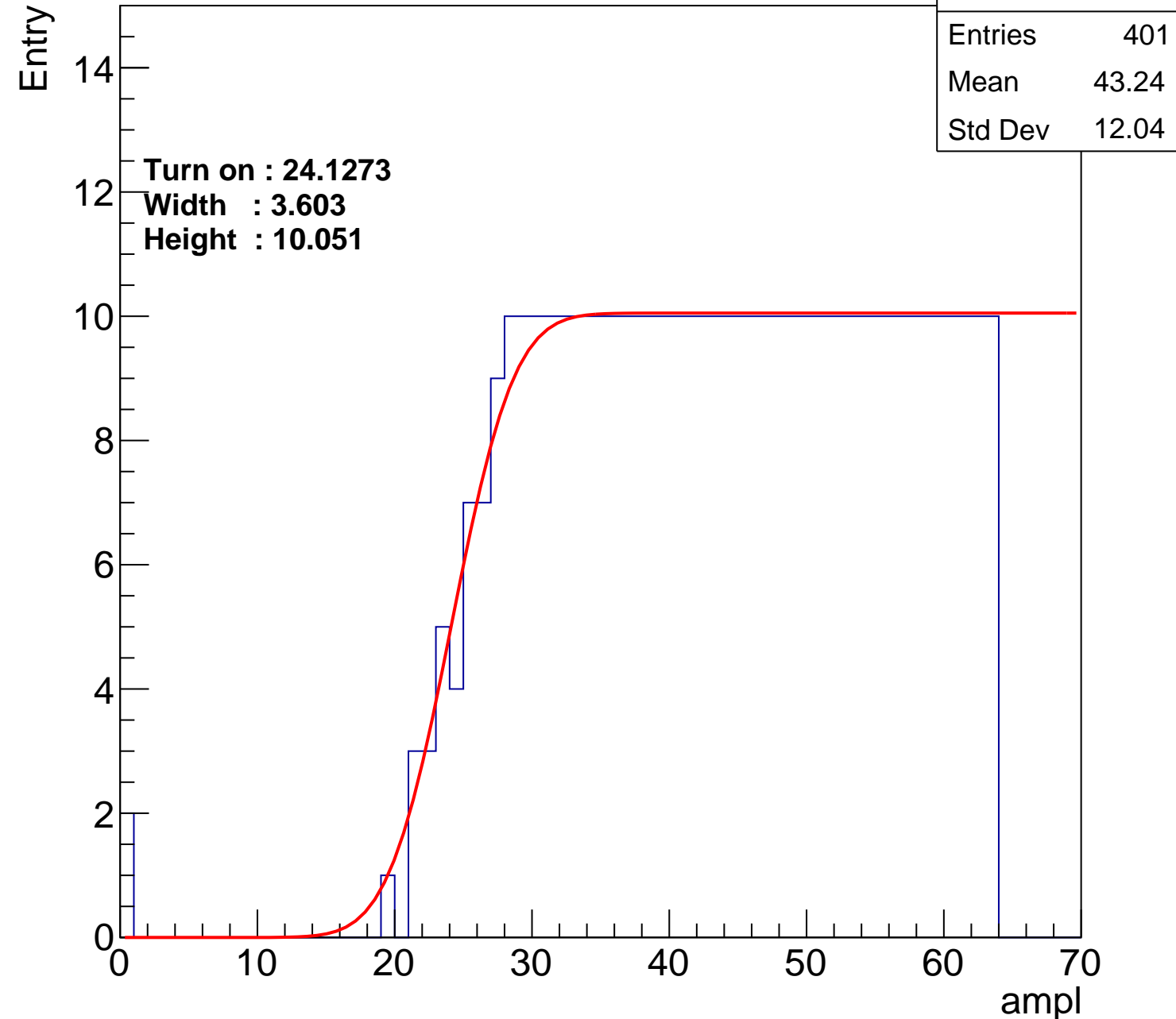
Width : 3.603

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch37

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.02 |
| Std Dev | 10.96 |

Turn on : 28.0221

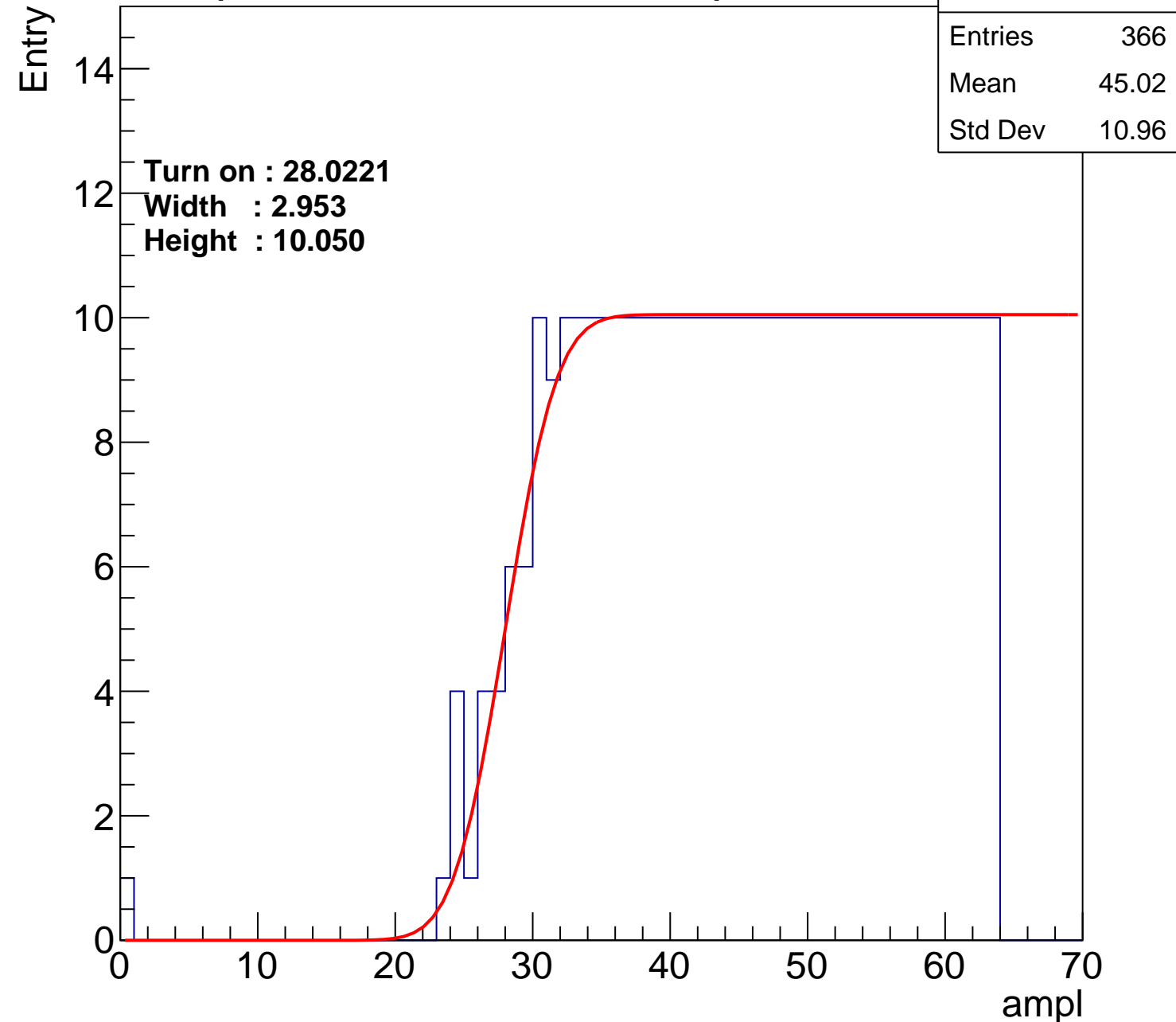
Width : 2.953

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch38

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.33 |
| Std Dev | 11.45 |

Turn on : 26.5127

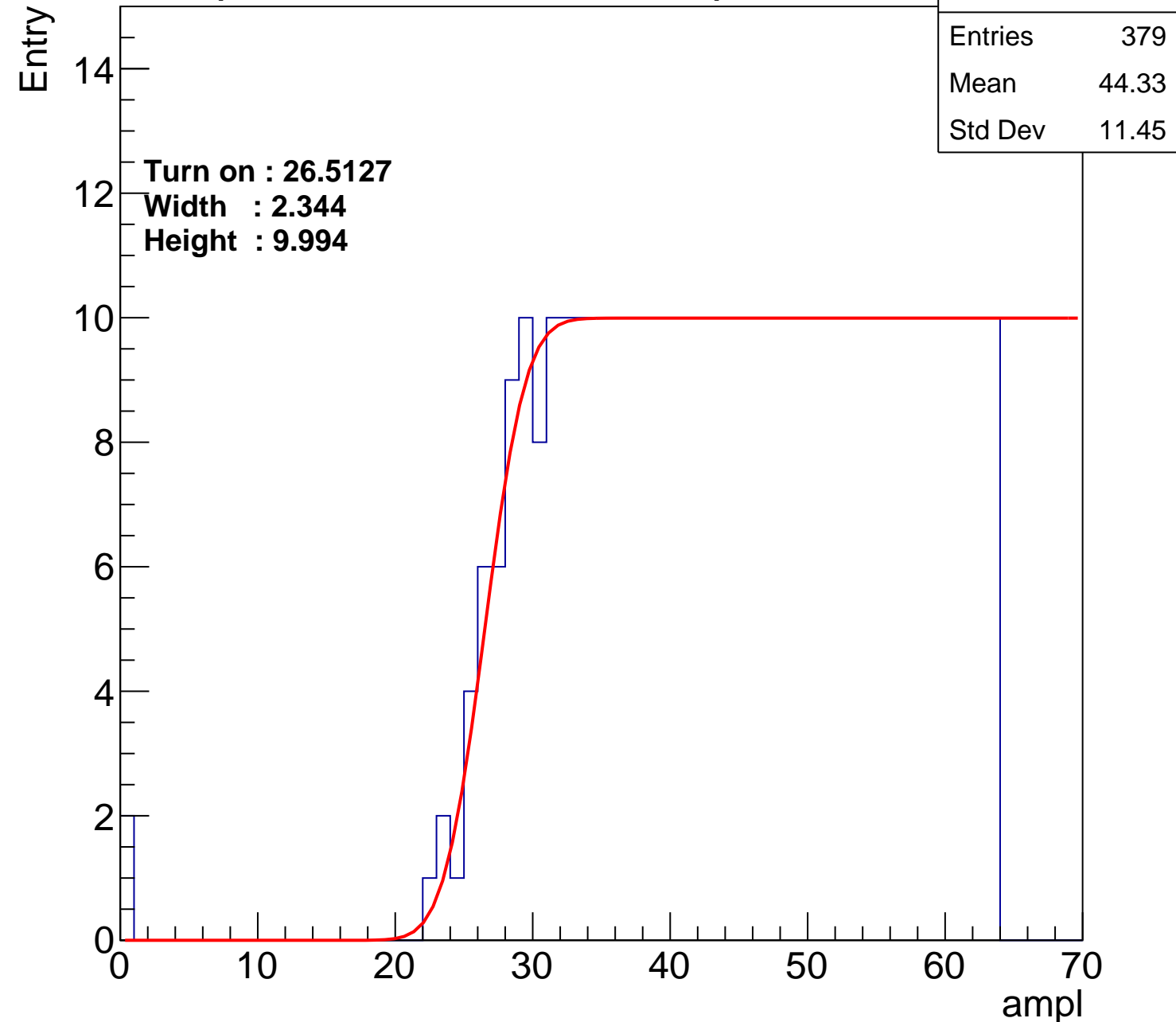
Width : 2.344

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch39

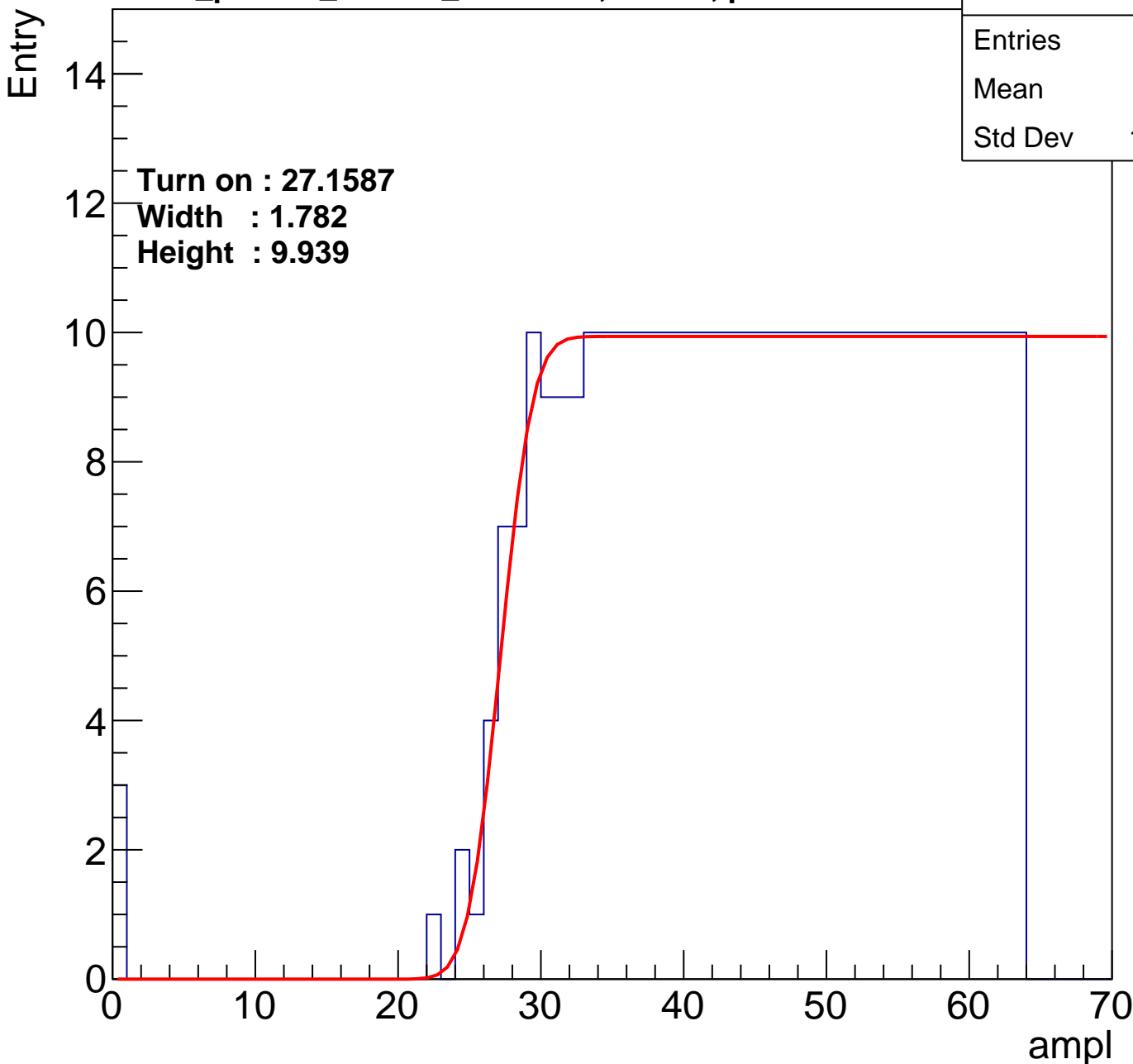
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 27.1587

Width : 1.782

Height : 9.939

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.6 |
| Std Dev | 11.48 |



B1L002S, U5-ch40

calib_packv5_042523_0143.root, FC#10, port B3

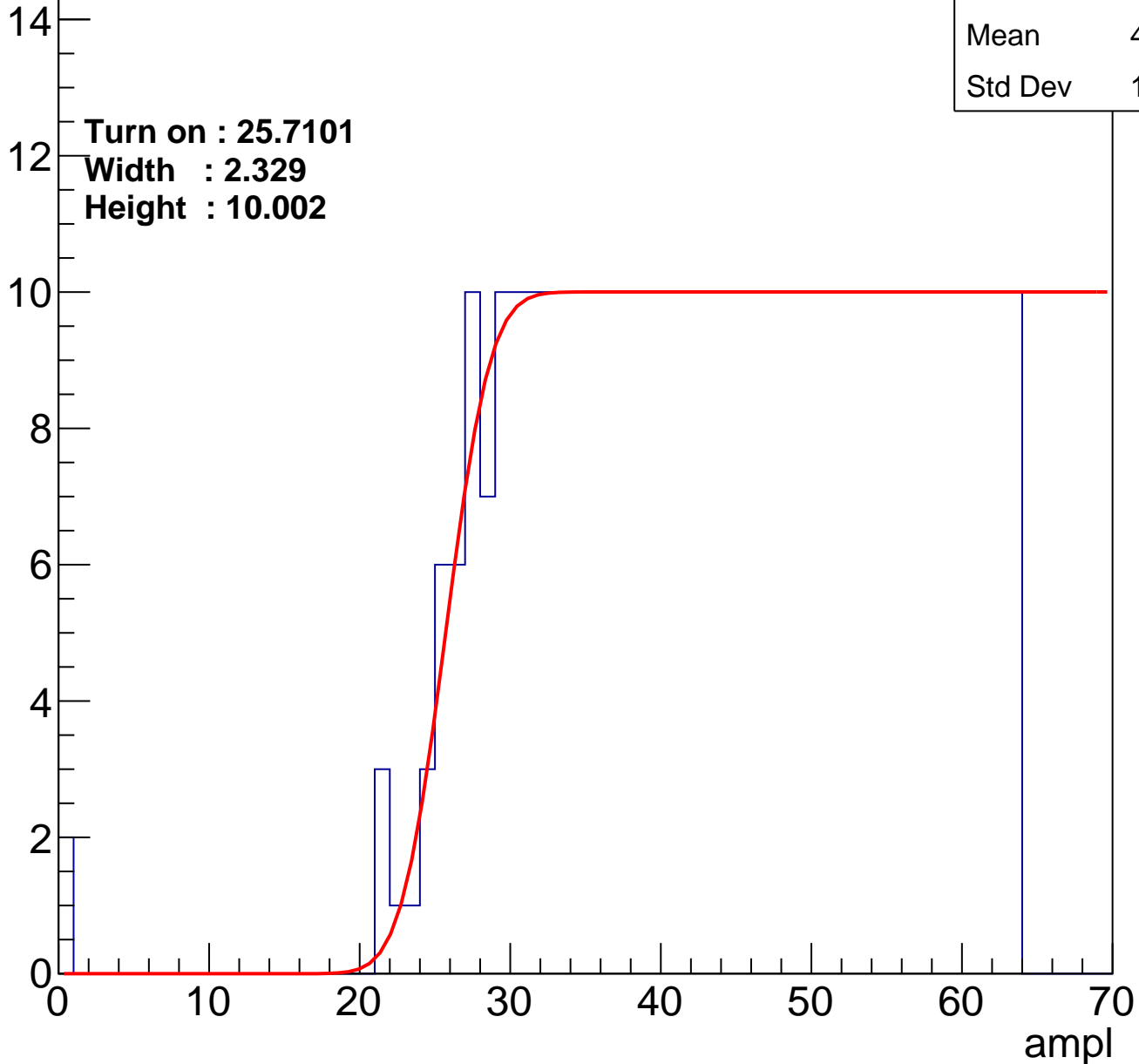
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.84 |
| Std Dev | 11.72 |

Turn on : 25.7101

Width : 2.329

Height : 10.002

Entry



B1L002S, U5-ch41

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.32 |
| Std Dev | 11.6 |

Turn on : 26.2605

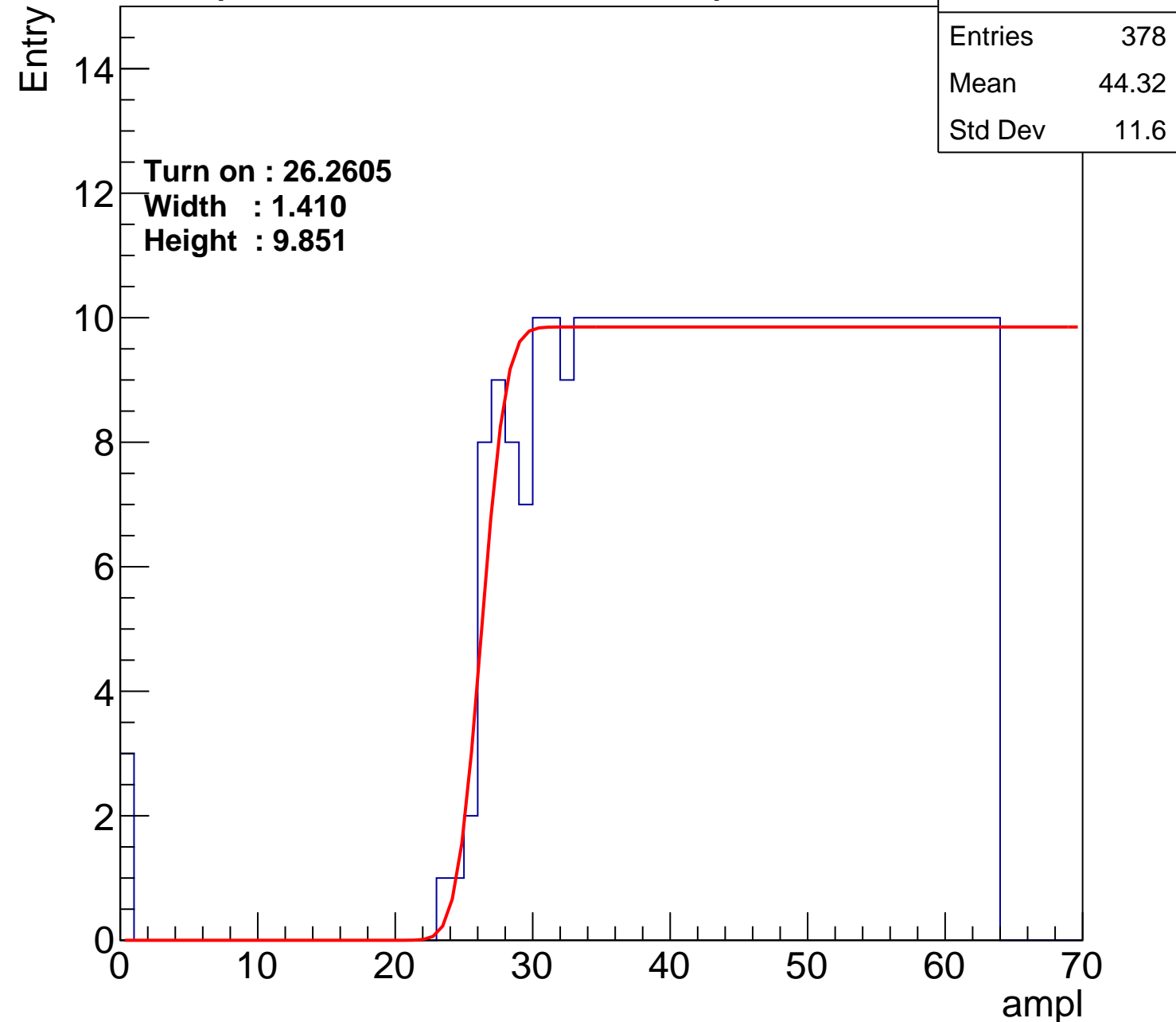
Width : 1.410

Height : 9.851

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch42

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.96 |
| Std Dev | 11.67 |

Turn on : 25.2467

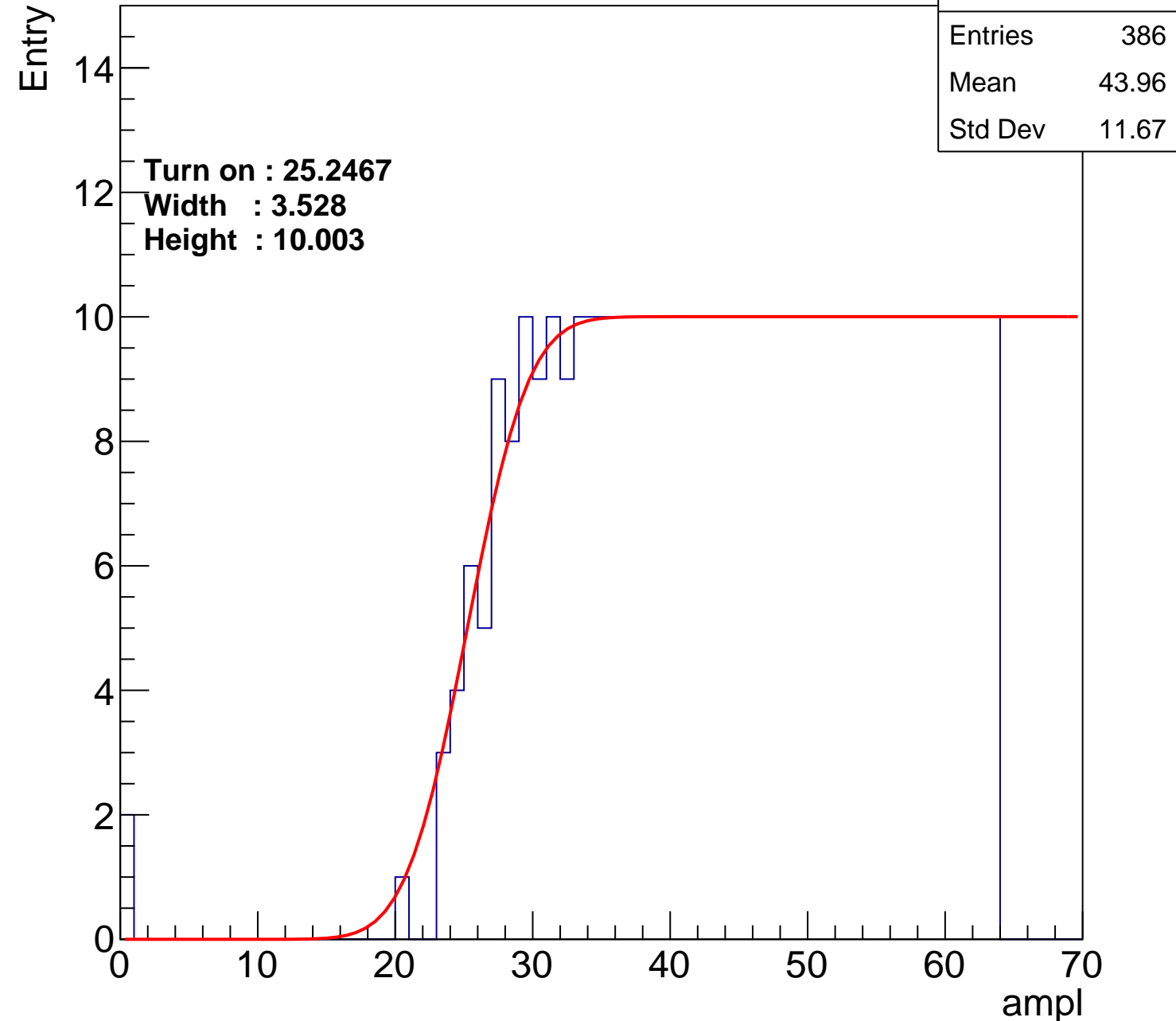
Width : 3.528

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch43

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.49 |
| Std Dev | 12.34 |

Turn on : 25.7238

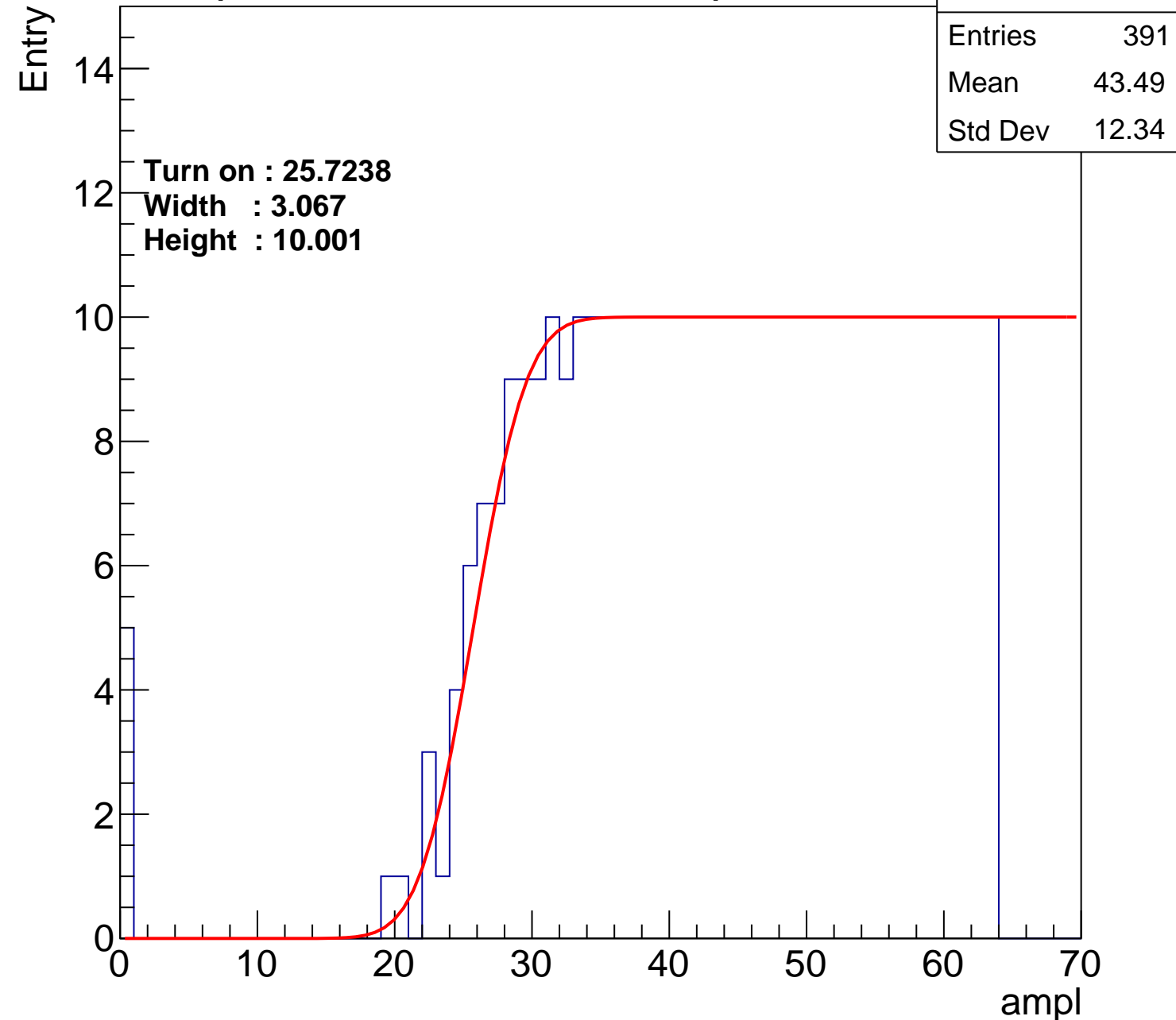
Width : 3.067

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch44

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.35 |
| Std Dev | 12.09 |

Turn on : 24.6931

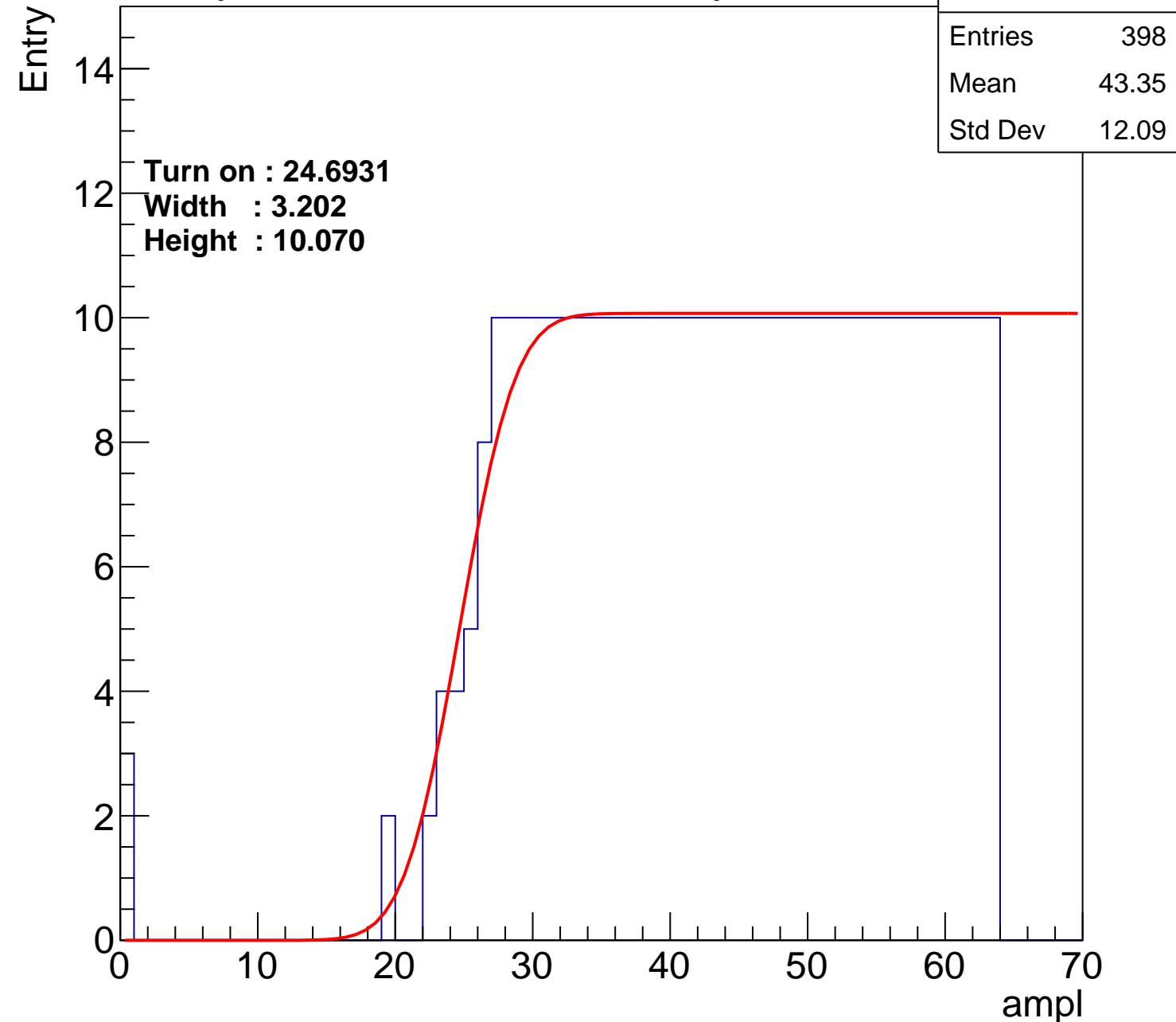
Width : 3.202

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch45

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.3 |
| Std Dev | 11.56 |

Turn on : 26.8470

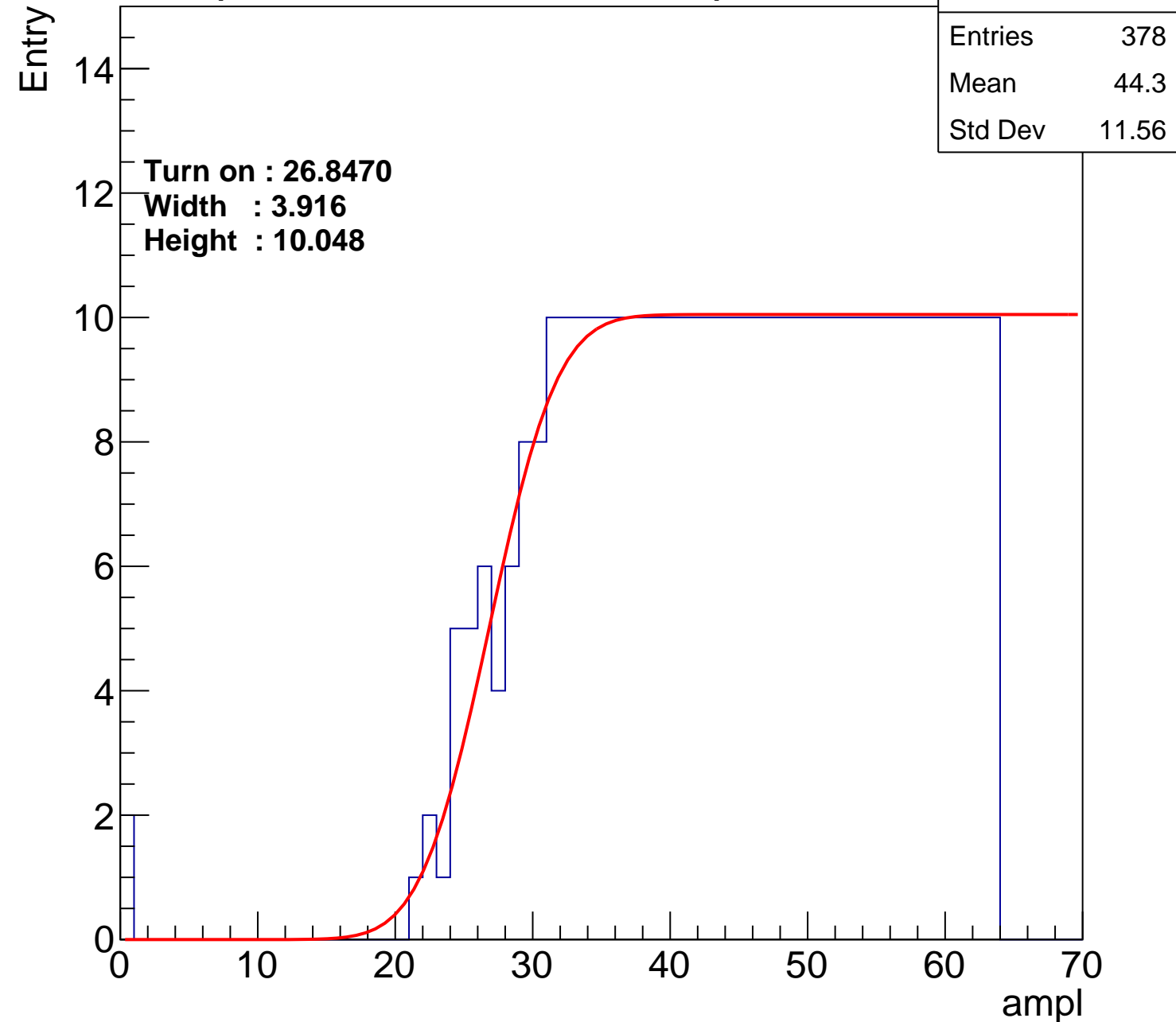
Width : 3.916

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch46

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.11 |
| Std Dev | 11.54 |

Turn on : 26.2179

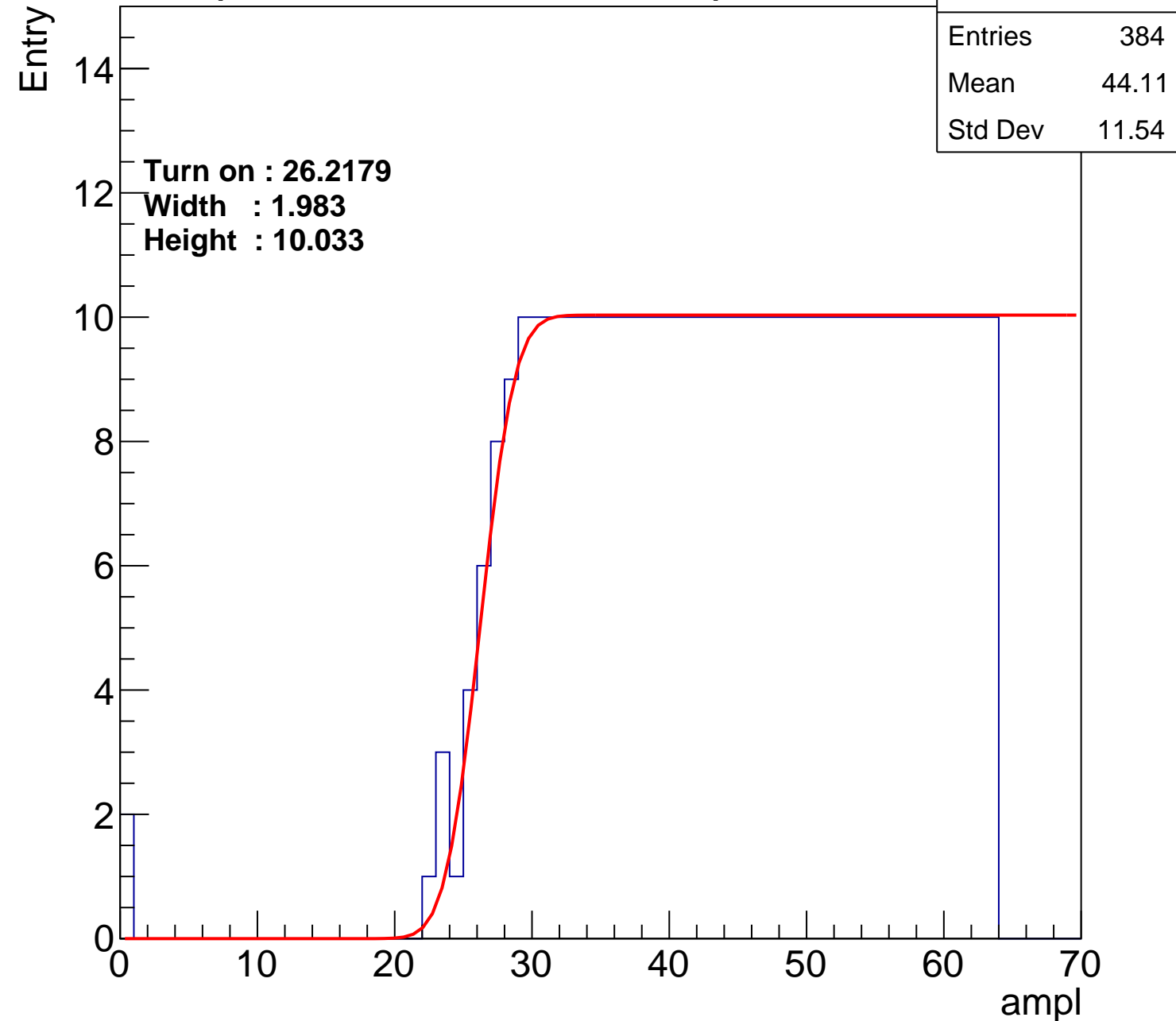
Width : 1.983

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch47

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.06 |
| Std Dev | 11.45 |

Turn on : 25.5726

Width : 3.286

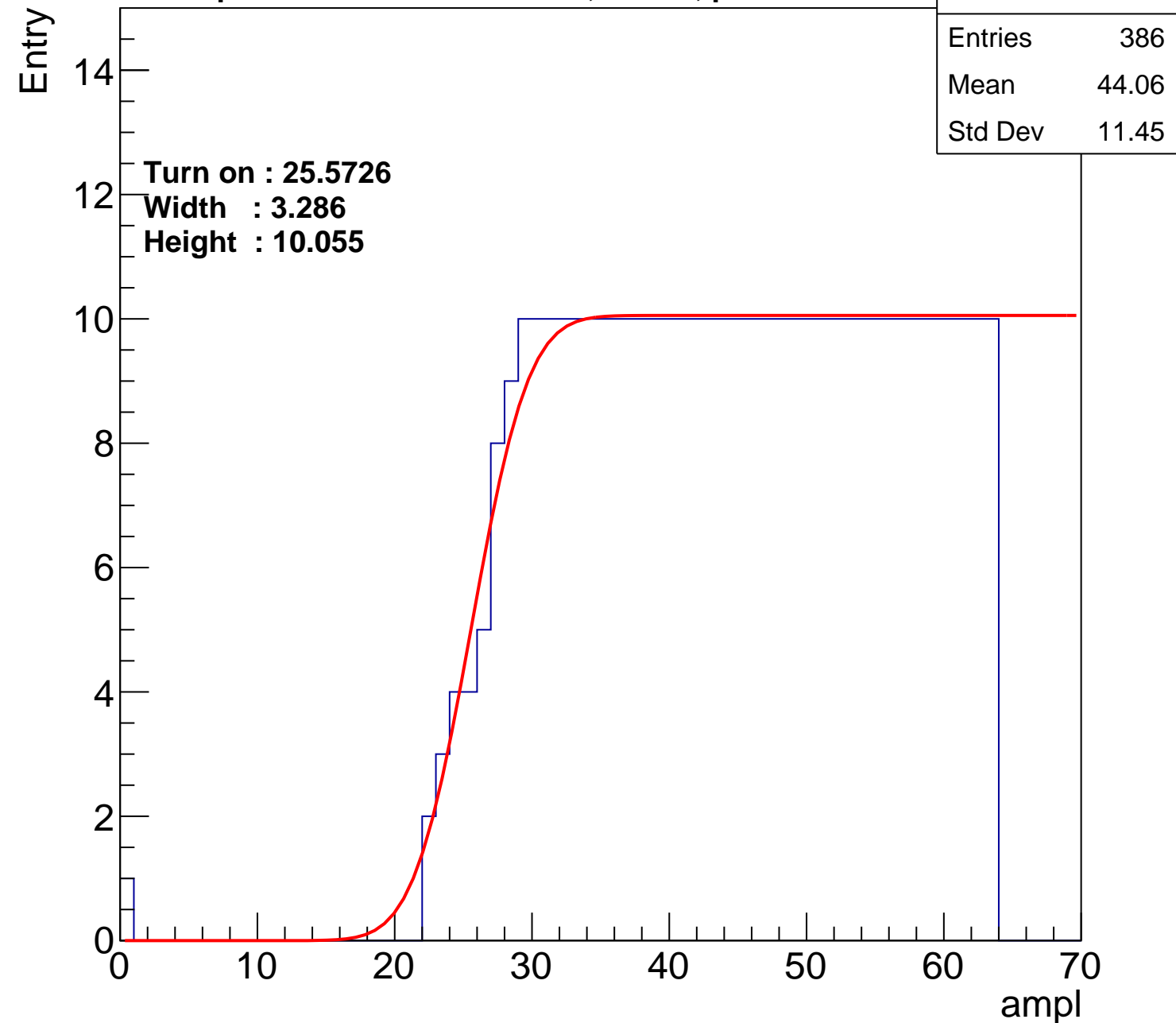
Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U5-ch48

calib_packv5_042523_0143.root, FC#10, port B3

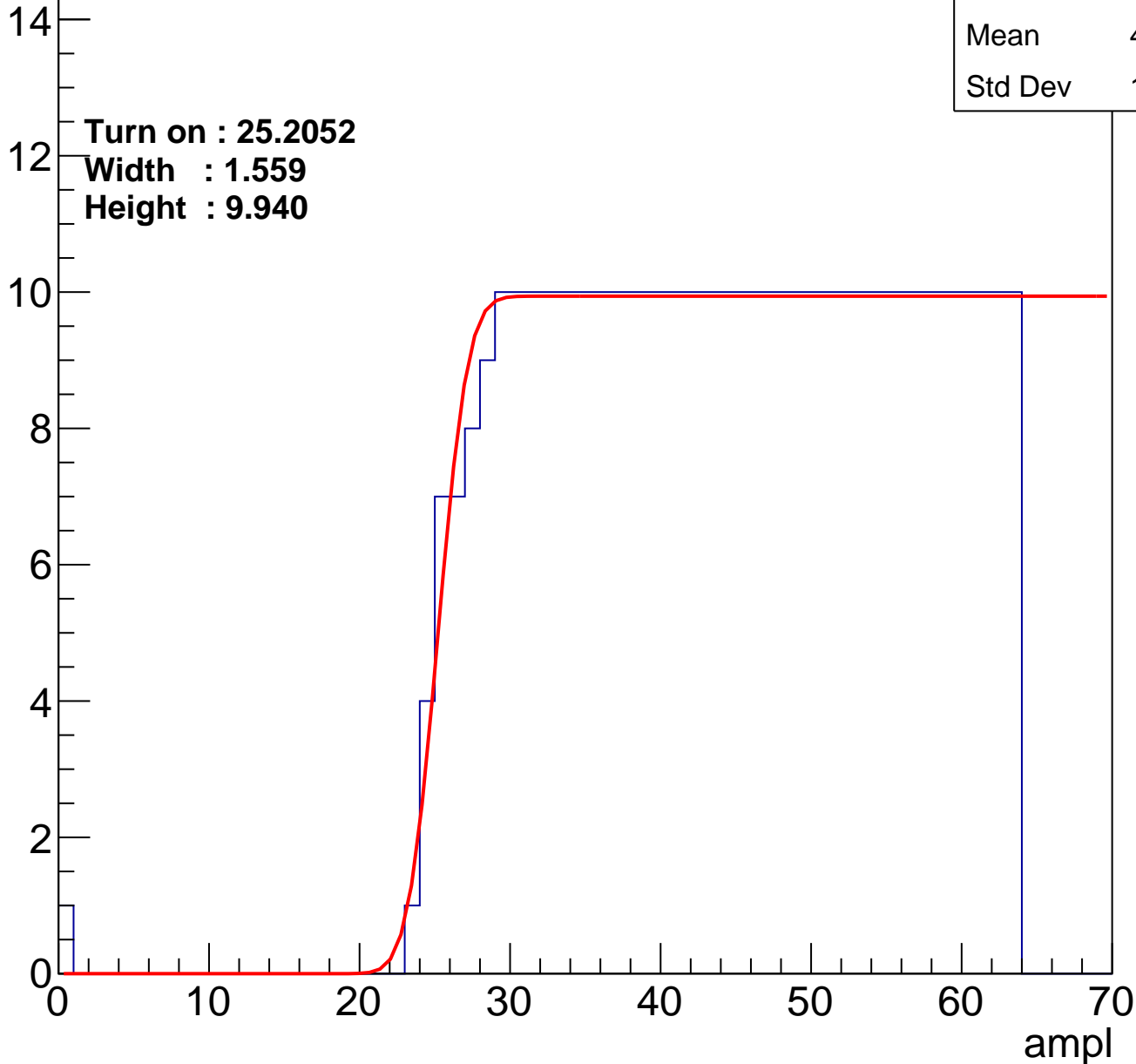
Entry

| | |
|---------|-------|
| Entries | 387 |
| Mean | 44.04 |
| Std Dev | 11.42 |

Turn on : 25.2052

Width : 1.559

Height : 9.940



B1L002S, U5-ch49

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.93 |
| Std Dev | 11.97 |

Turn on : 26.0543

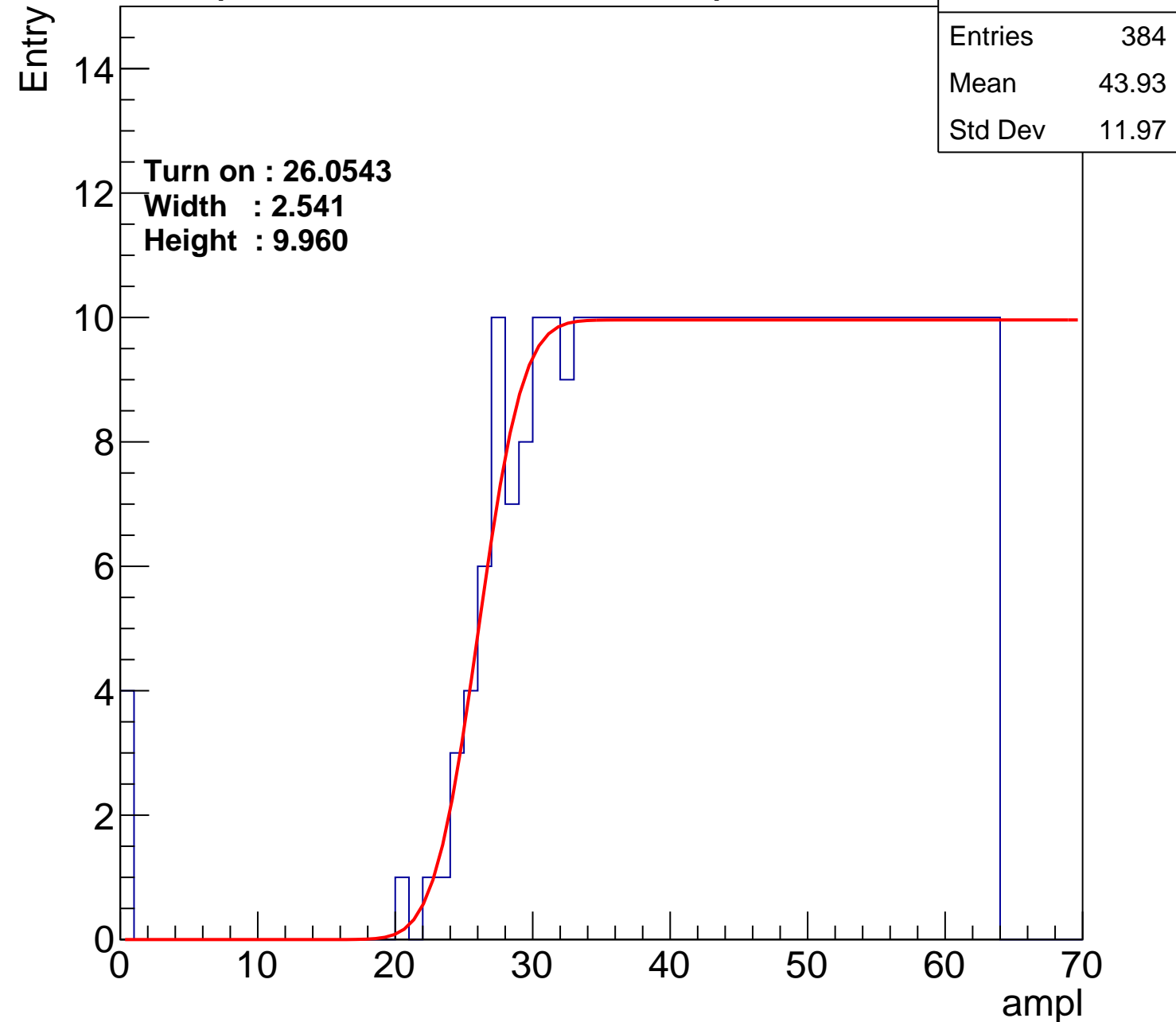
Width : 2.541

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch50

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.51 |
| Std Dev | 11.87 |

Turn on : 24.3105

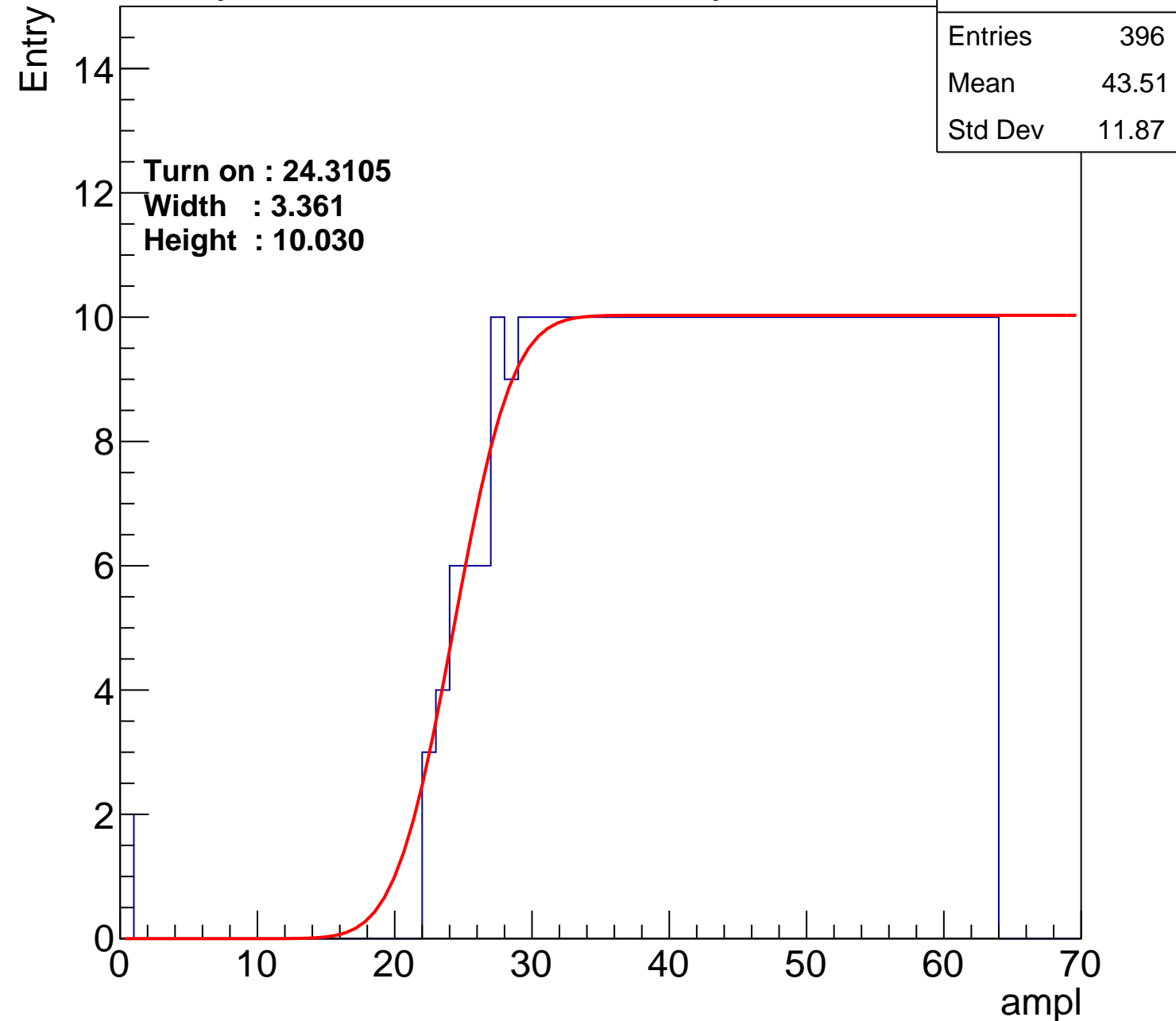
Width : 3.361

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch51

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.44 |
| Std Dev | 11.39 |

Turn on : 26.8495

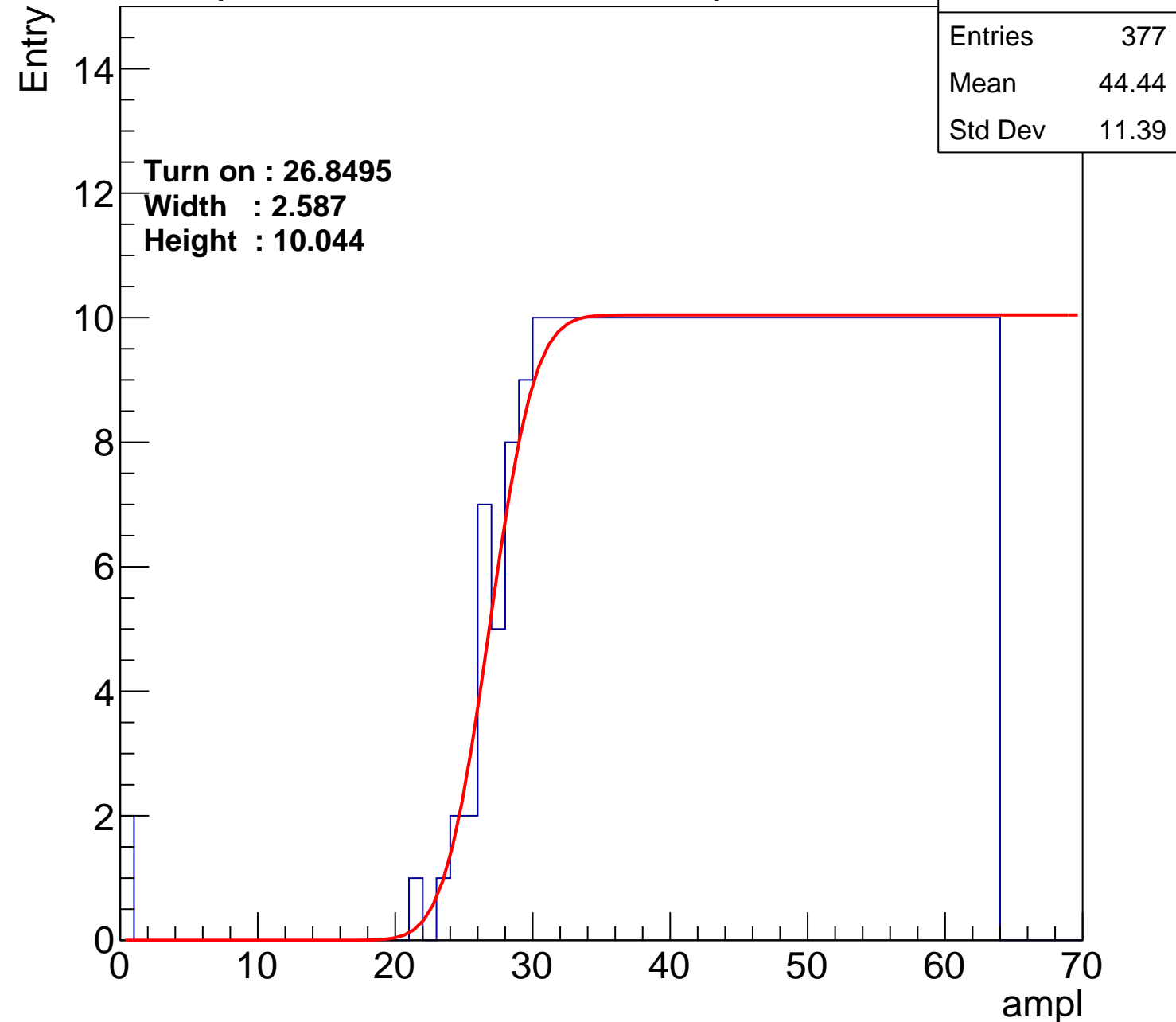
Width : 2.587

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 27.6513

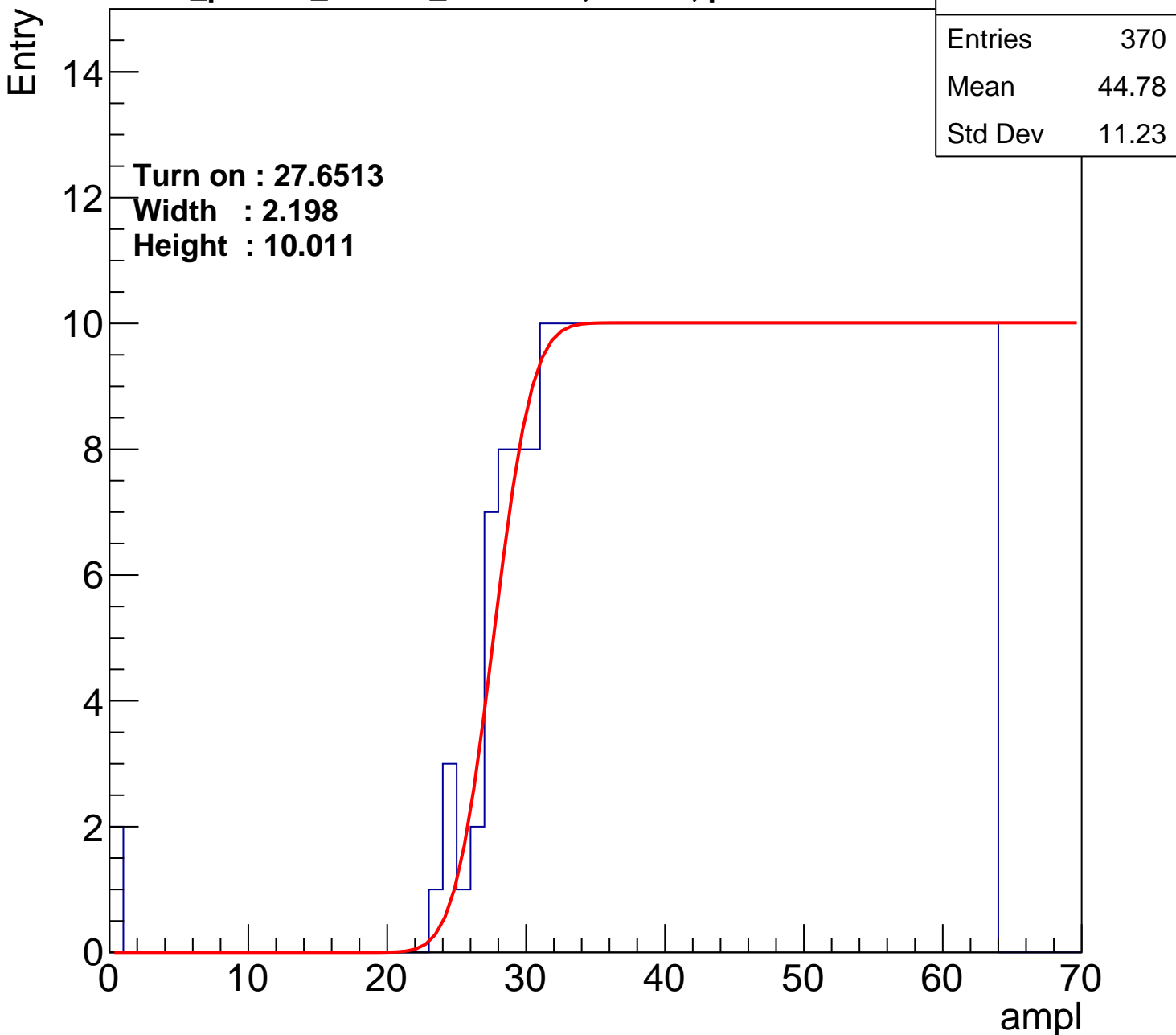
Width : 2.198

Height : 10.011

Entries 370

Mean 44.78

Std Dev 11.23



B1L002S, U5-ch53

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.79 |
| Std Dev | 11.74 |

Turn on : 25.3499

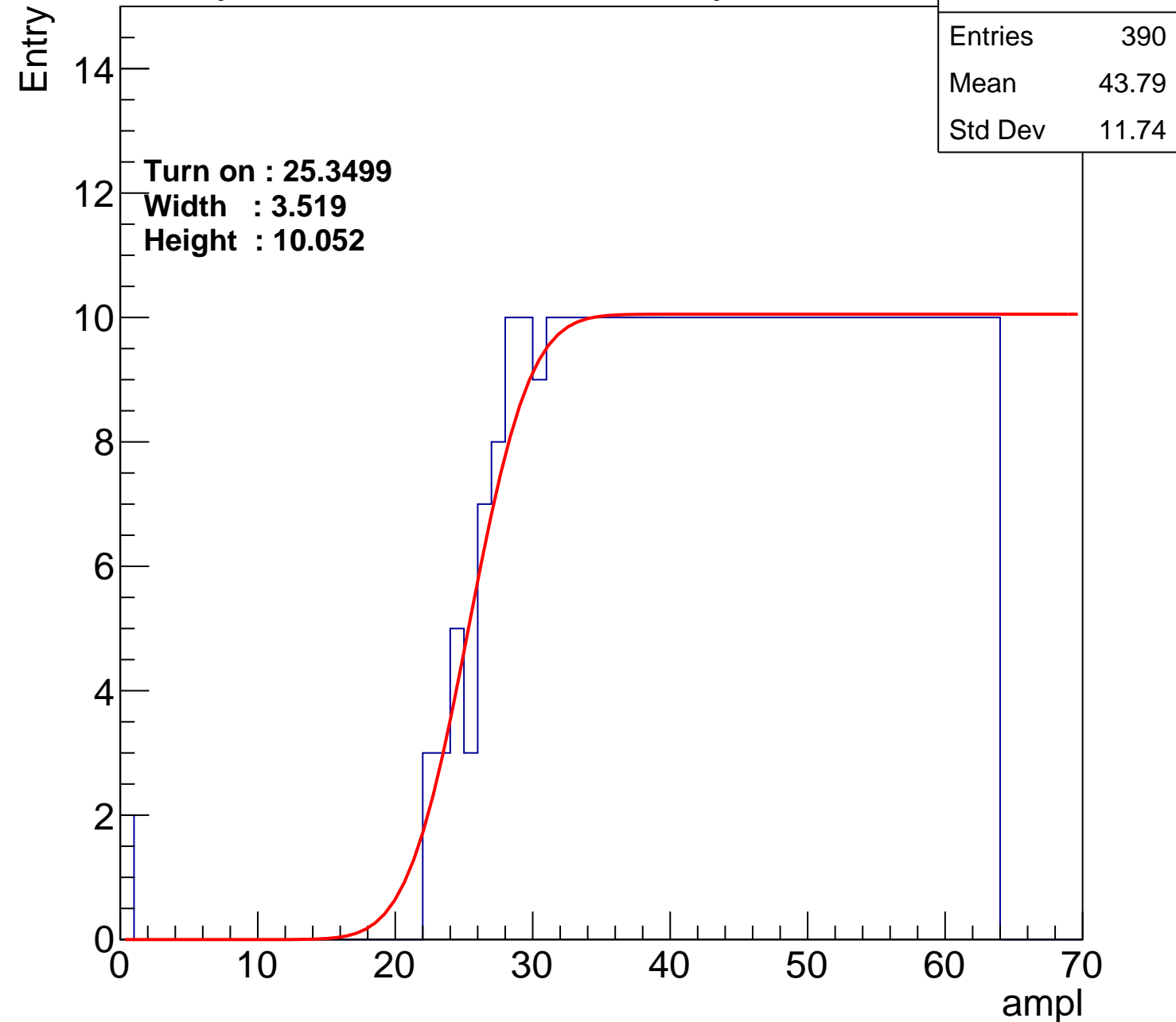
Width : 3.519

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch54

calib_packv5_042523_0143.root, FC#10, port B3

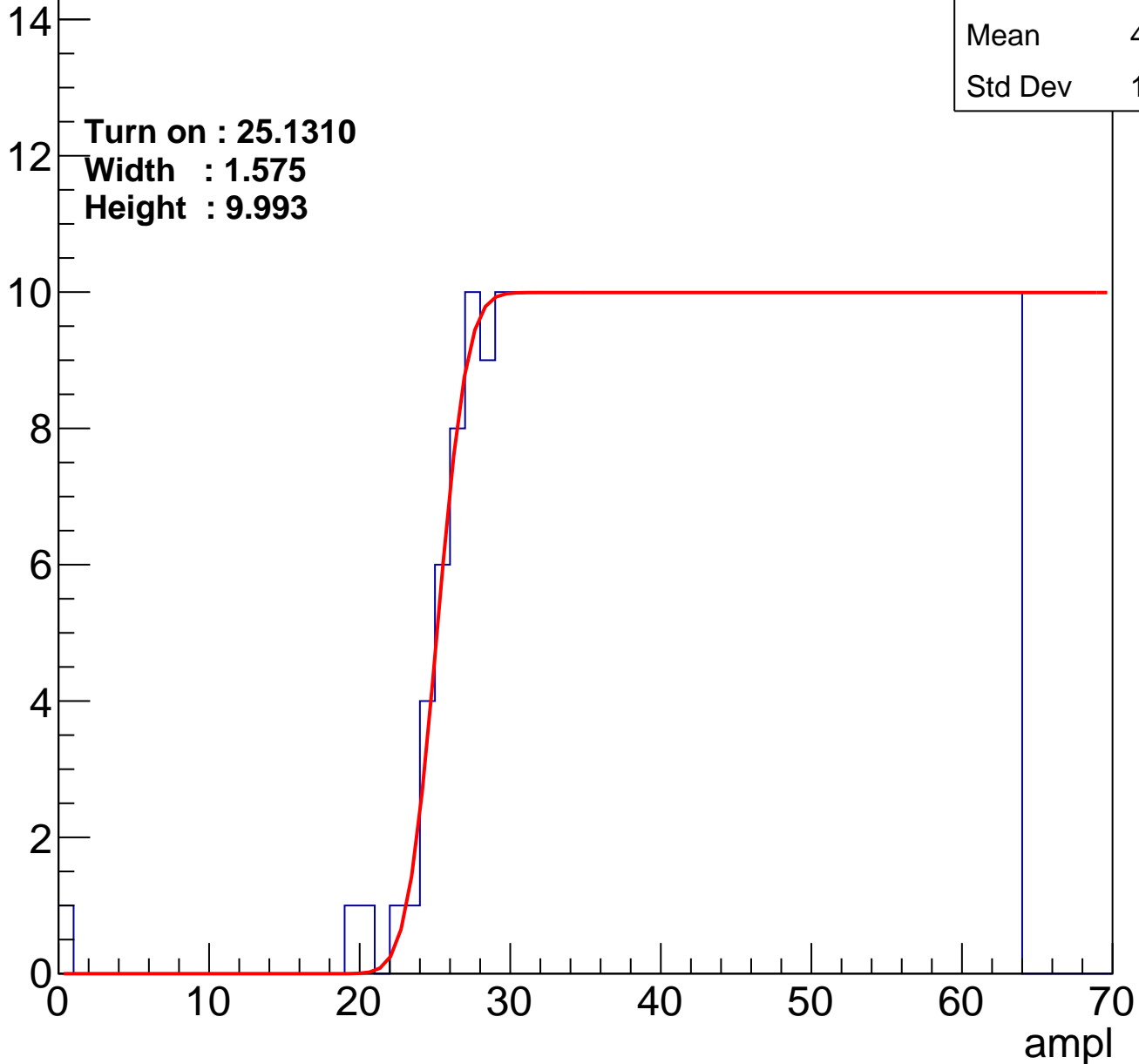
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.78 |
| Std Dev | 11.59 |

Turn on : 25.1310

Width : 1.575

Height : 9.993

Entry



B1L002S, U5-ch55

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.05 |
| Std Dev | 11.46 |

Turn on : 25.8612

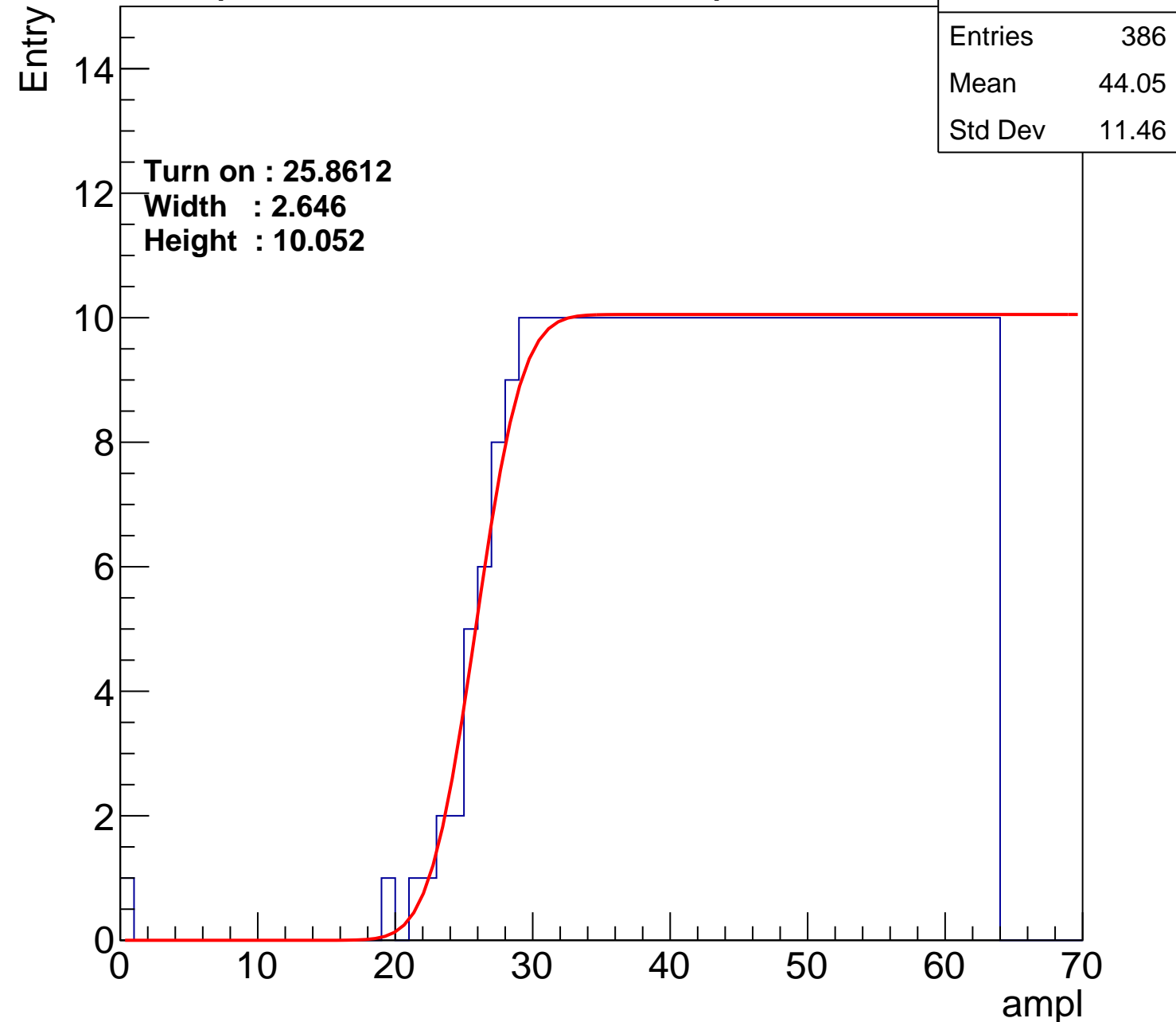
Width : 2.646

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch56

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.07 |
| Std Dev | 11.66 |

Turn on : 26.1407

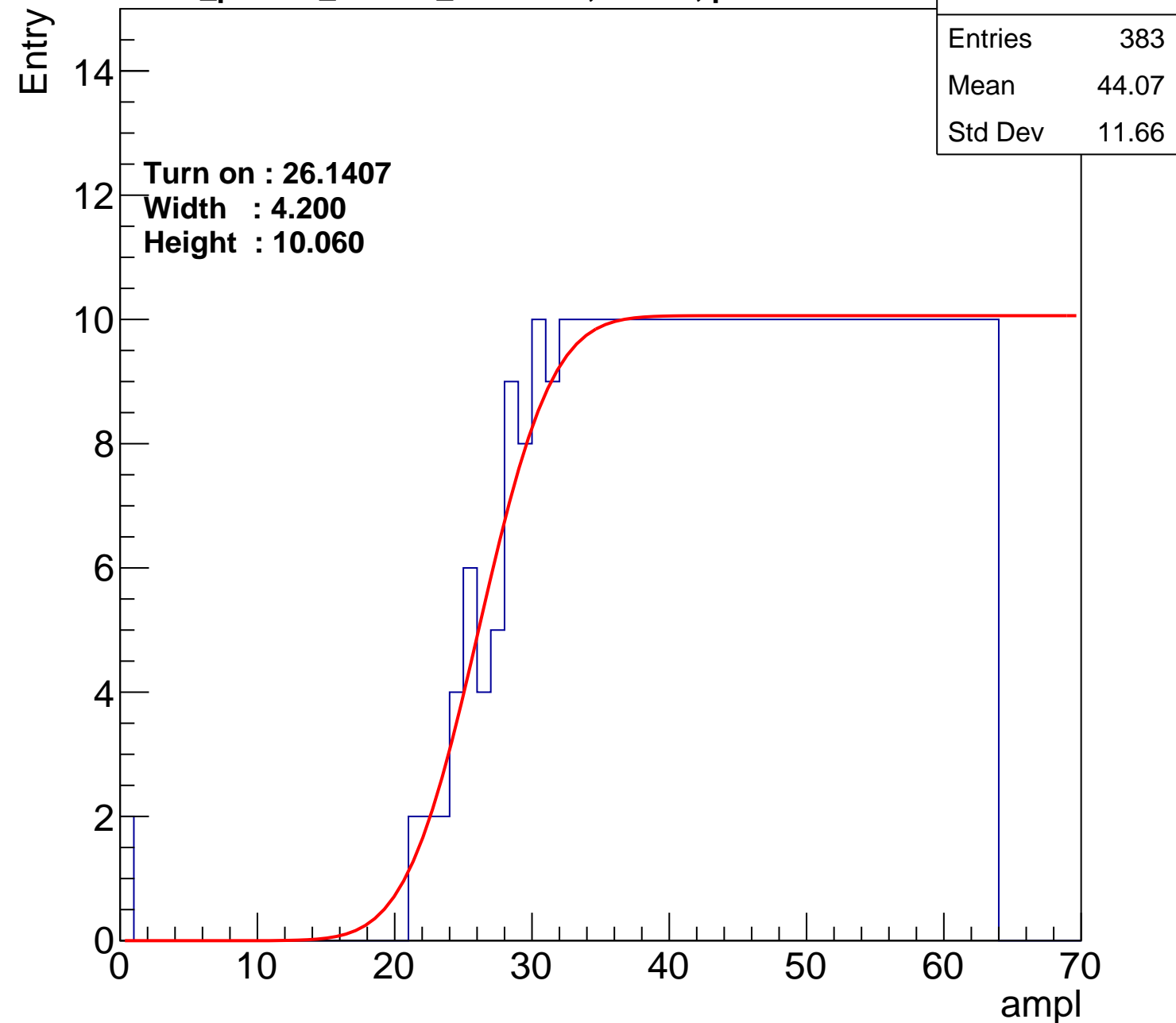
Width : 4.200

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch57

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.83 |
| Std Dev | 11.18 |

Turn on : 27.1483

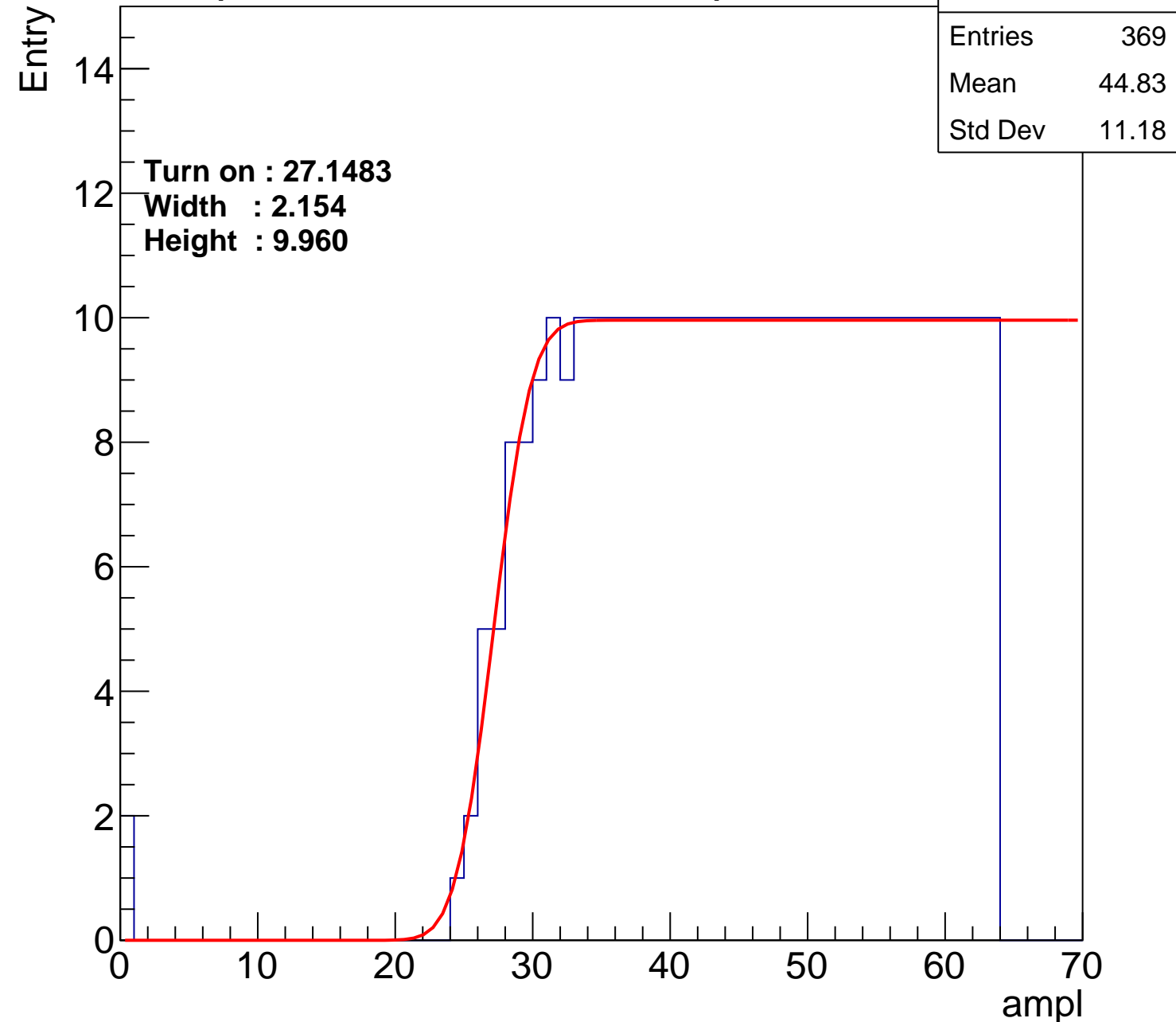
Width : 2.154

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch58

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.25 |
| Std Dev | 11.49 |

Turn on : 26.6205

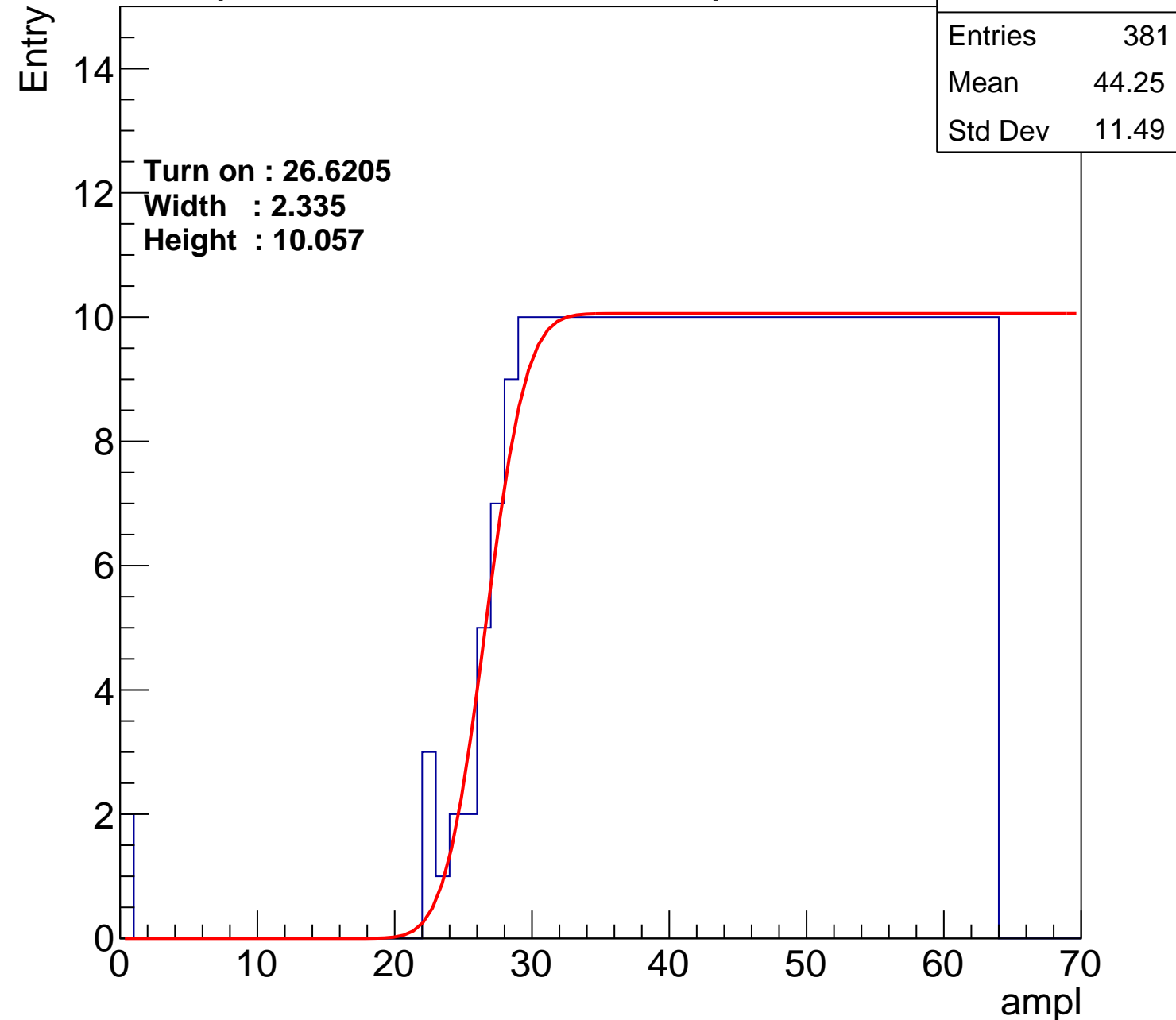
Width : 2.335

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch59

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.22 |
| Std Dev | 12.35 |

Turn on : 24.7109

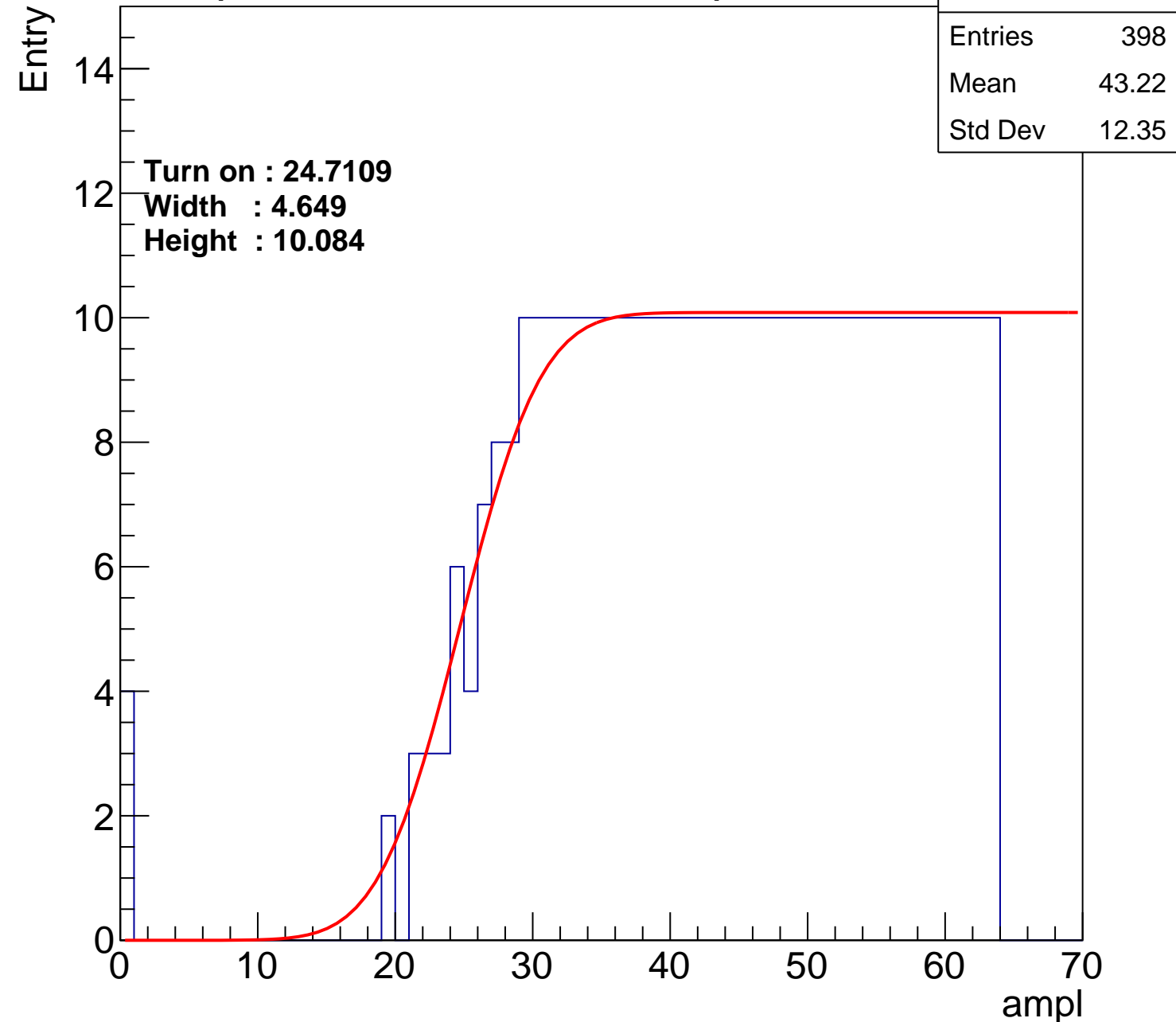
Width : 4.649

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch60

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.29 |
| Std Dev | 11.78 |

Turn on : 26.9200

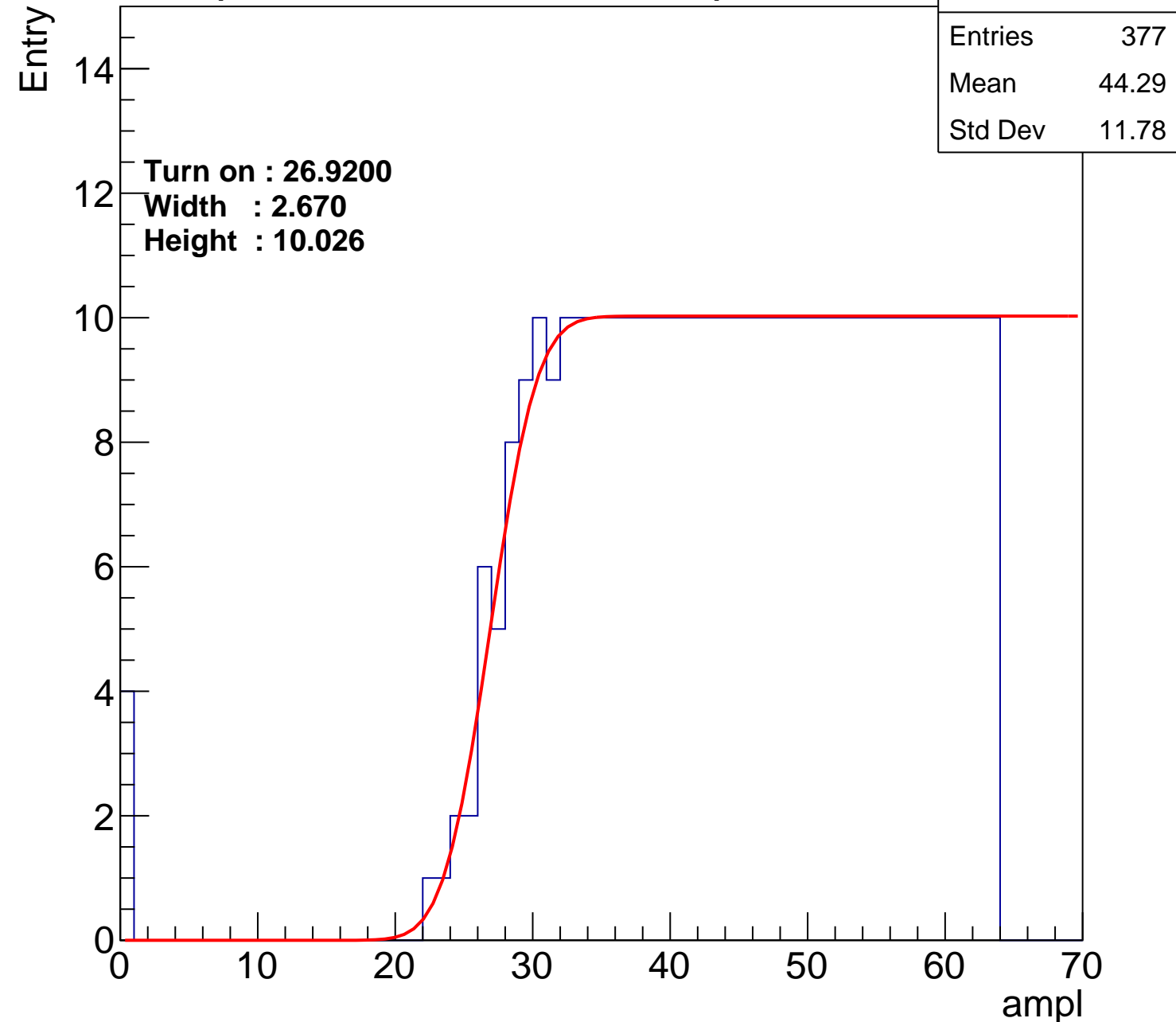
Width : 2.670

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch61

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.73 |
| Std Dev | 11.91 |

Turn on : 25.3535

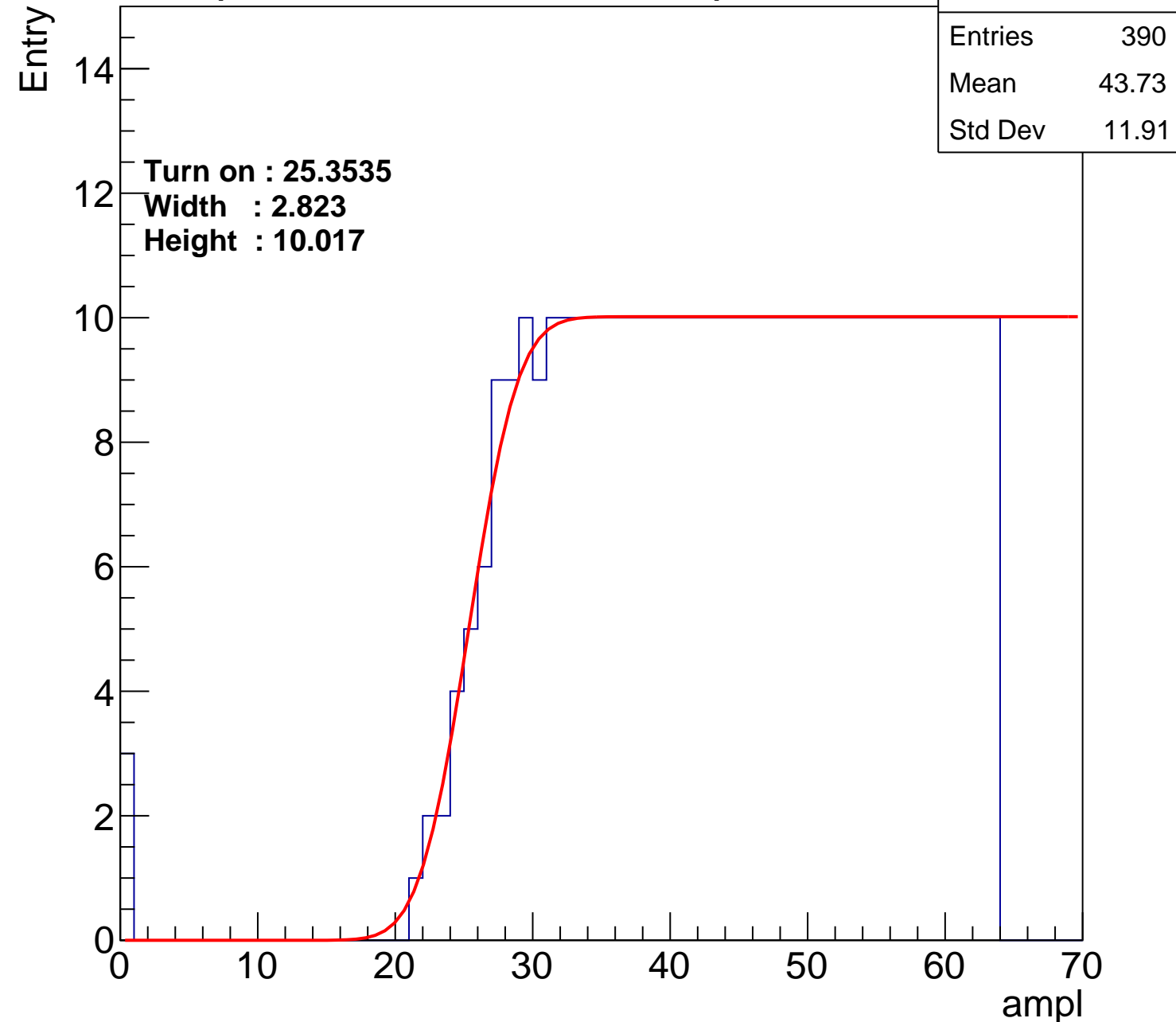
Width : 2.823

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch62

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.42 |
| Std Dev | 11.23 |

Turn on : 26.0774

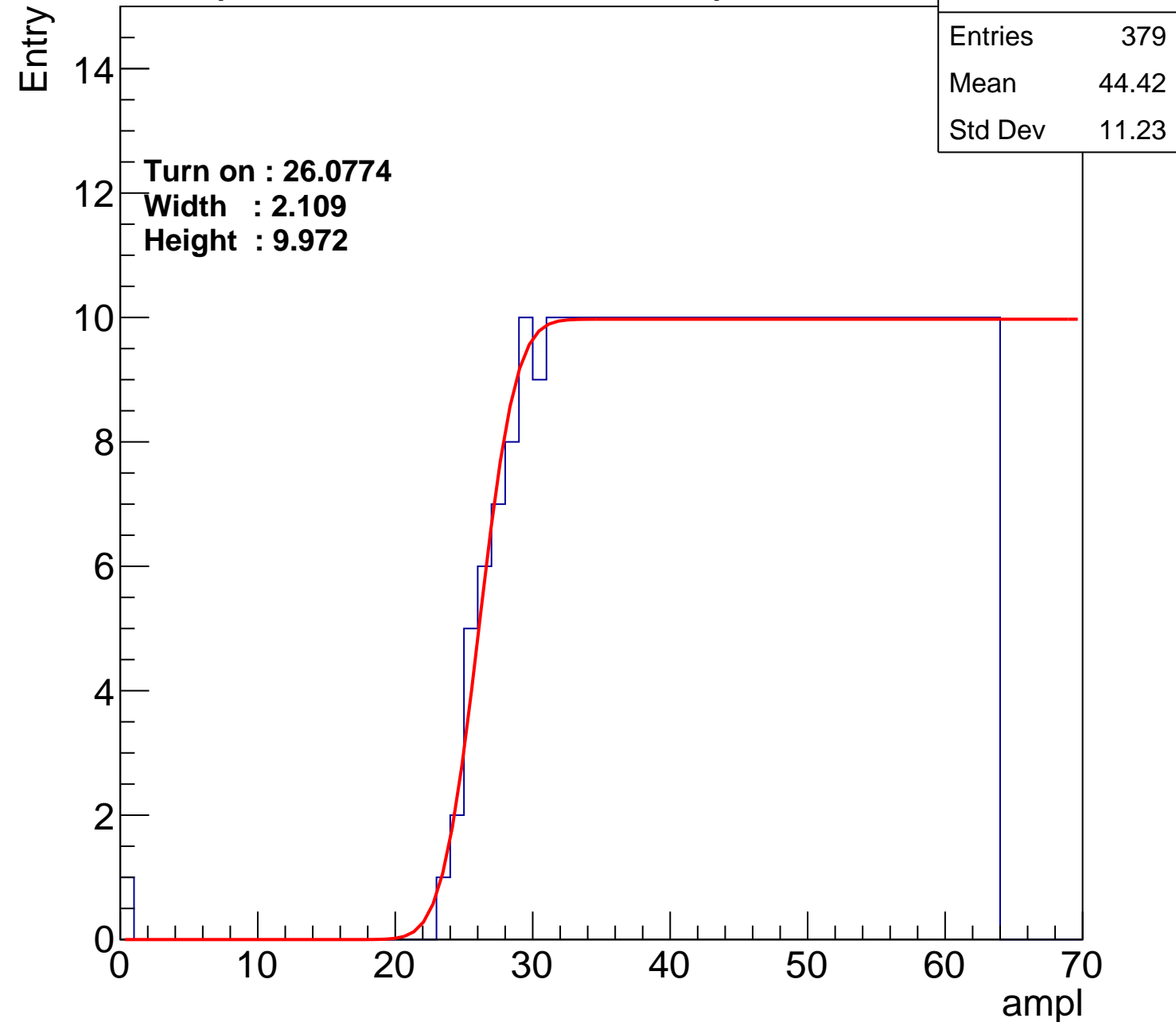
Width : 2.109

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch63

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.11 |
| Std Dev | 11.55 |

Turn on : 25.7431

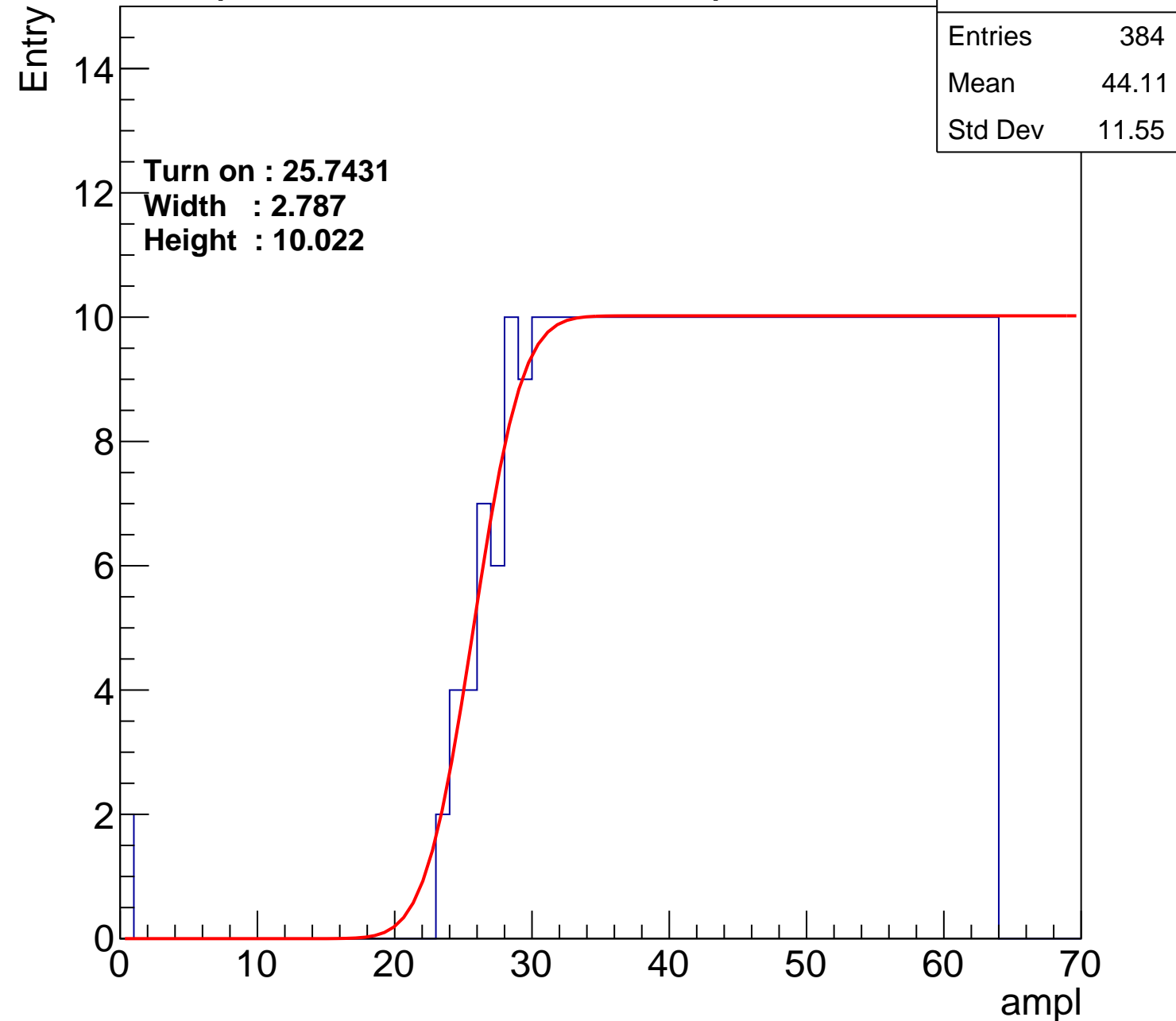
Width : 2.787

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch64

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.31 |
| Std Dev | 11.98 |

Turn on : 24.1152

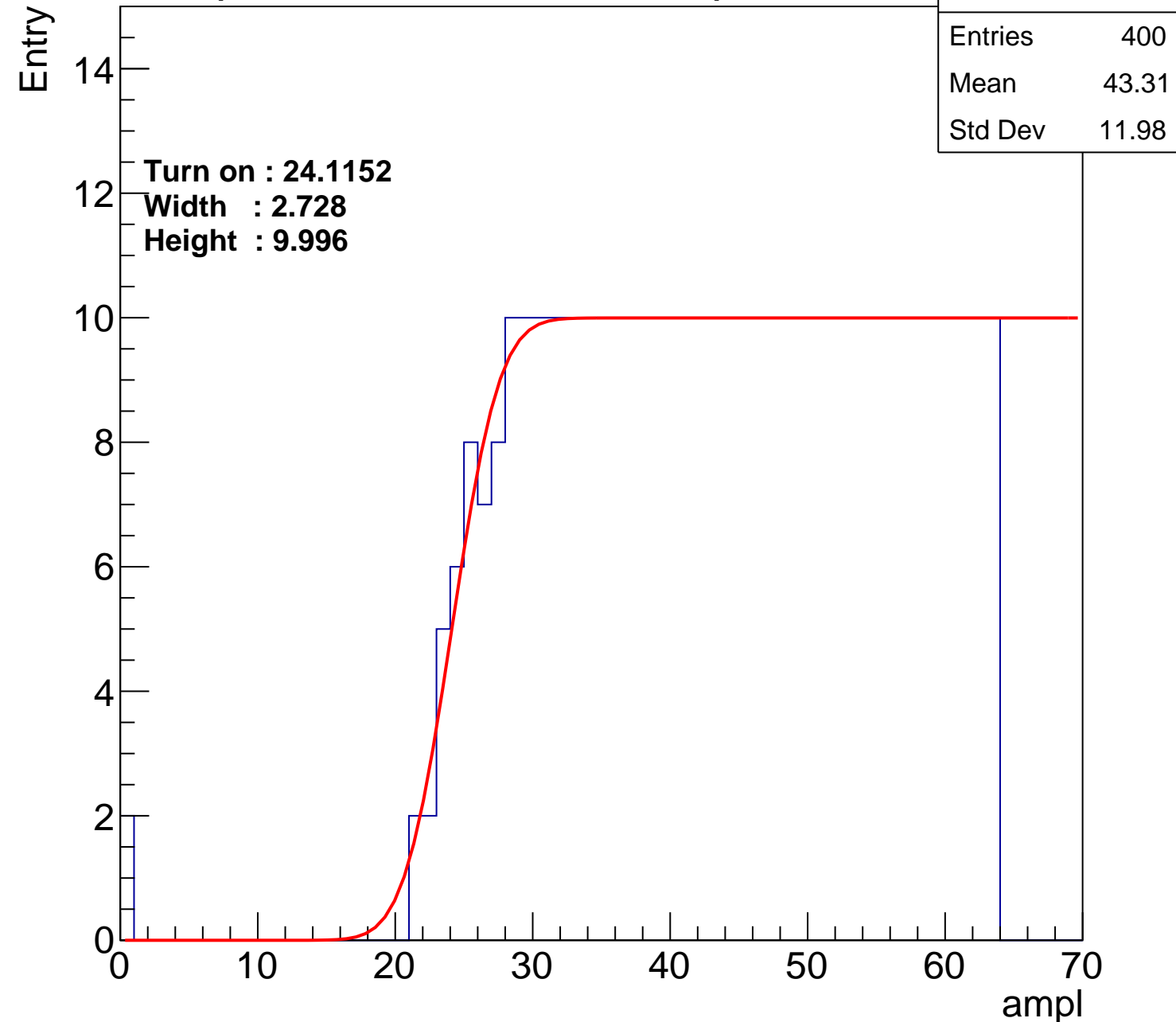
Width : 2.728

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch65

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.43 |
| Std Dev | 12.01 |

Turn on : 24.7815

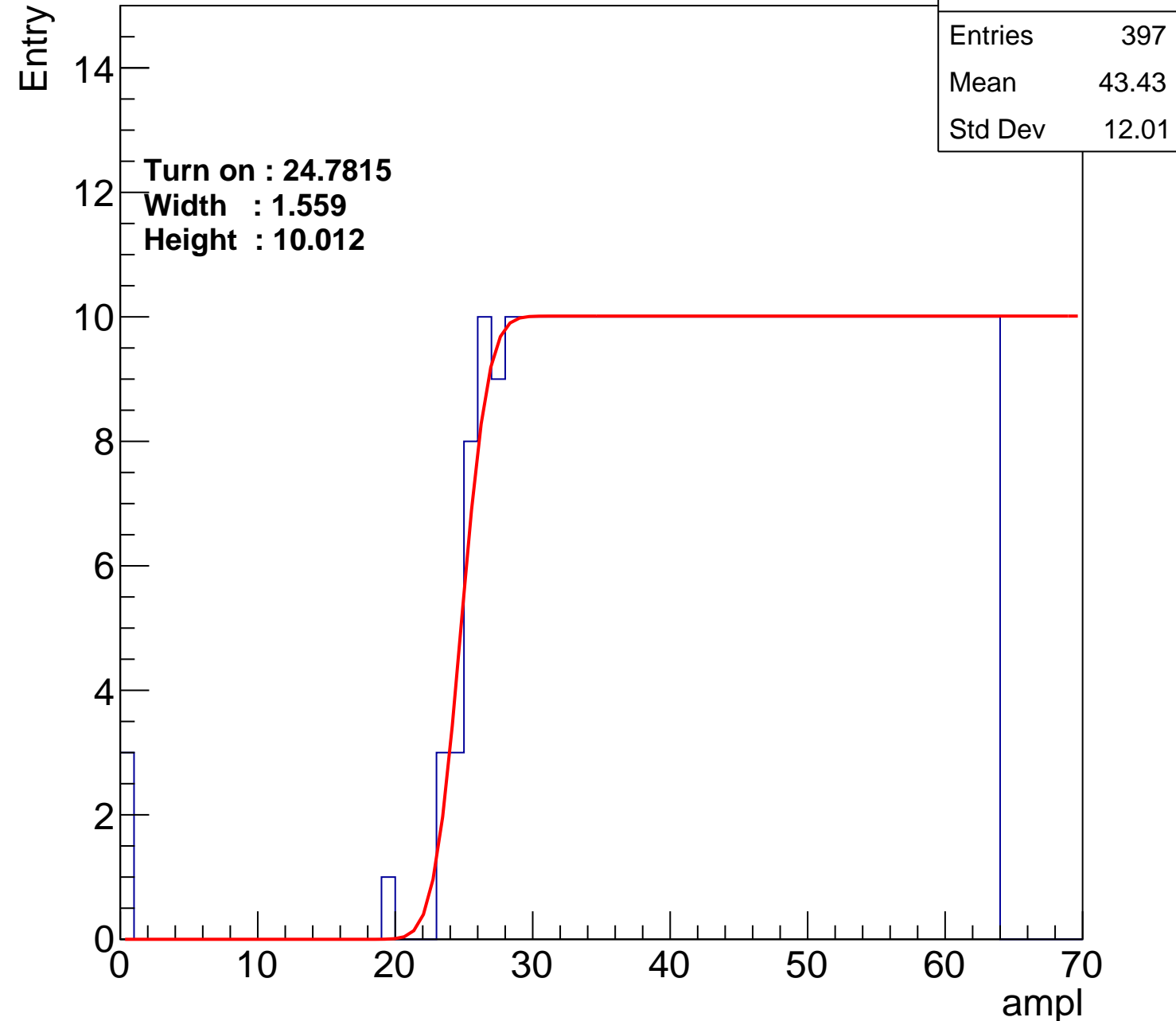
Width : 1.559

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch66

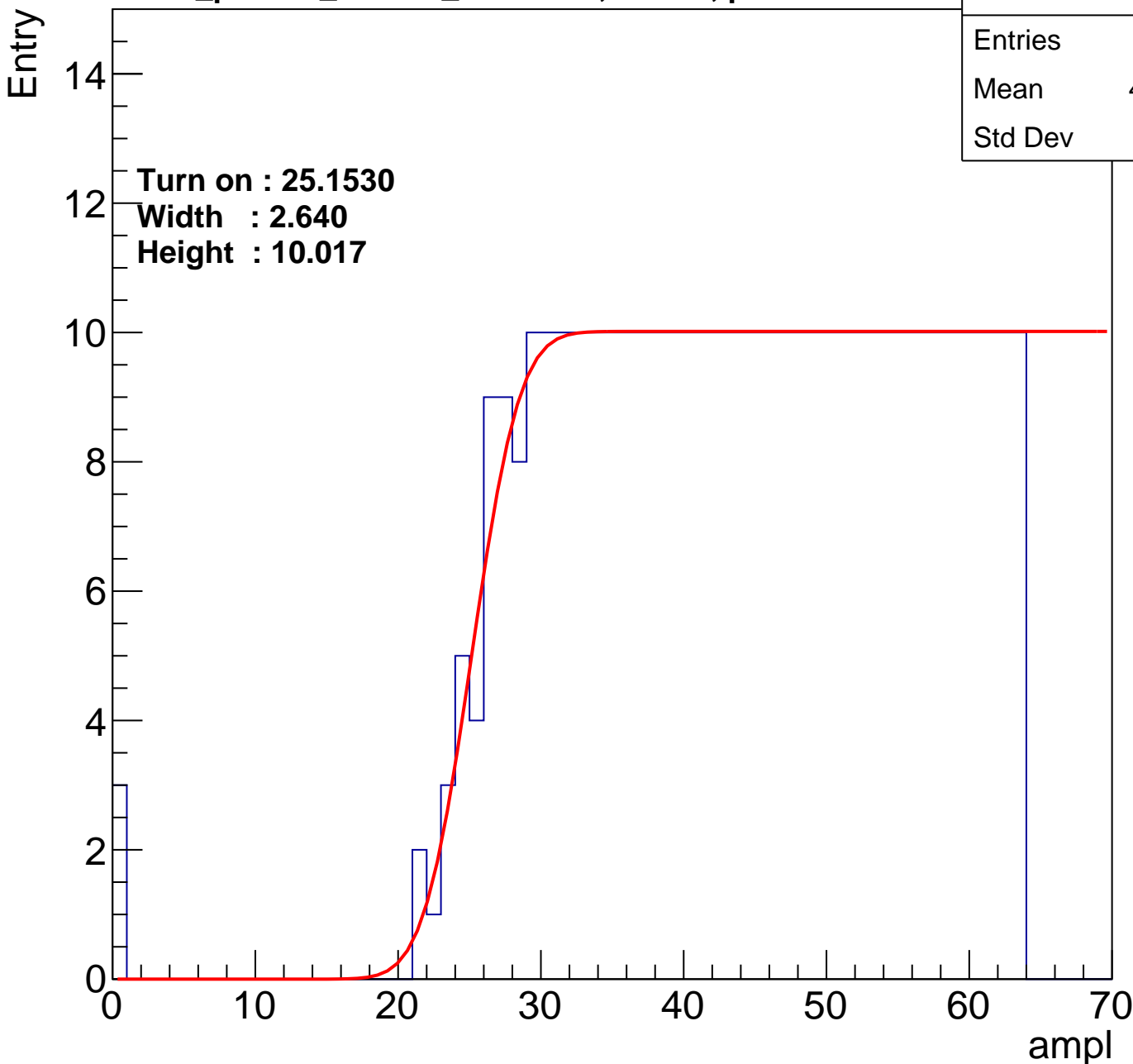
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.54 |
| Std Dev | 12 |

Turn on : 25.1530

Width : 2.640

Height : 10.017



B1L002S, U5-ch67

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.11 |
| Std Dev | 12.35 |

Turn on : 24.4213

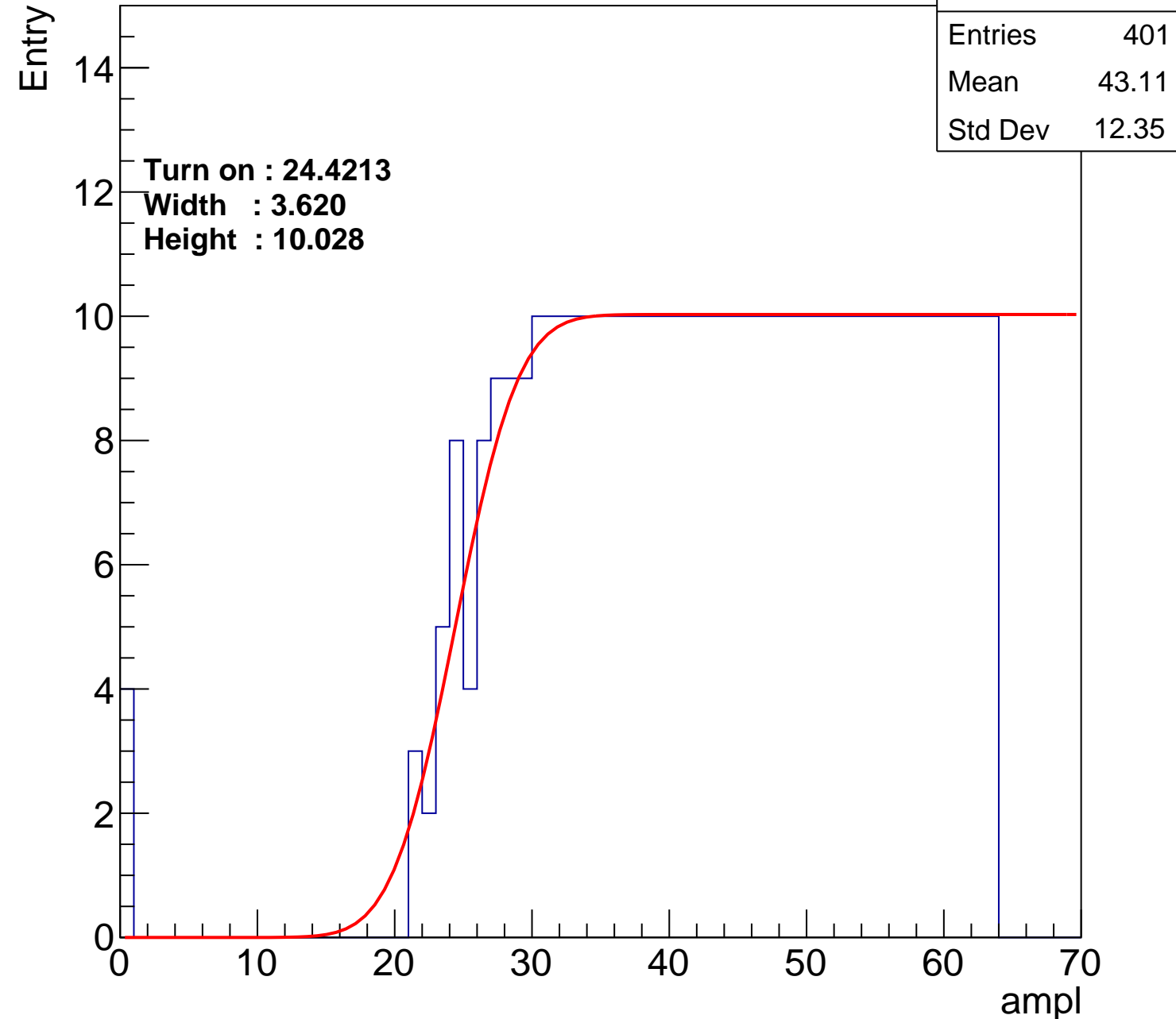
Width : 3.620

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch68

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.8 |
| Std Dev | 11.72 |

Turn on : 25.1103

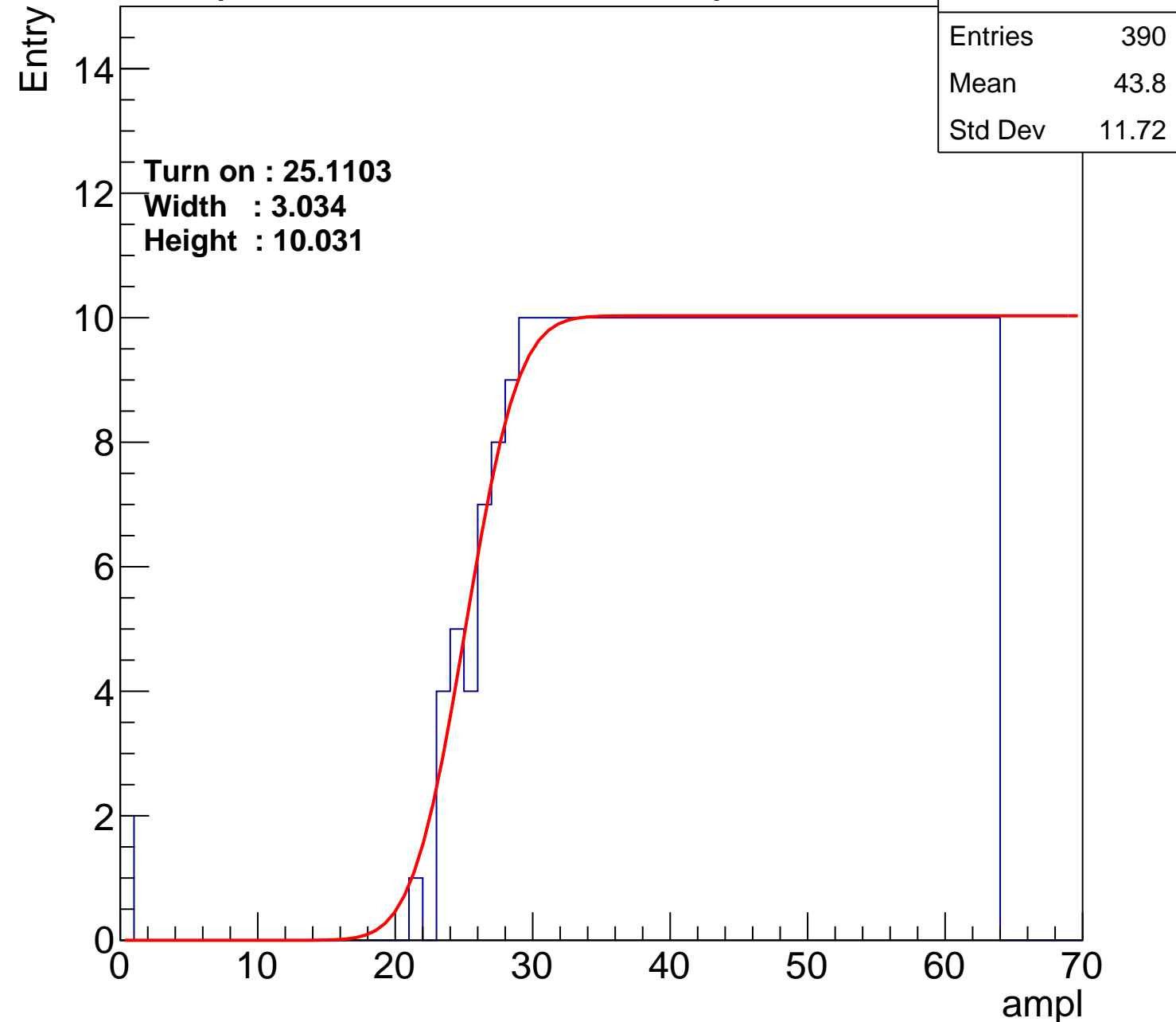
Width : 3.034

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch69

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.78 |
| Std Dev | 11.93 |

Turn on : 26.1792

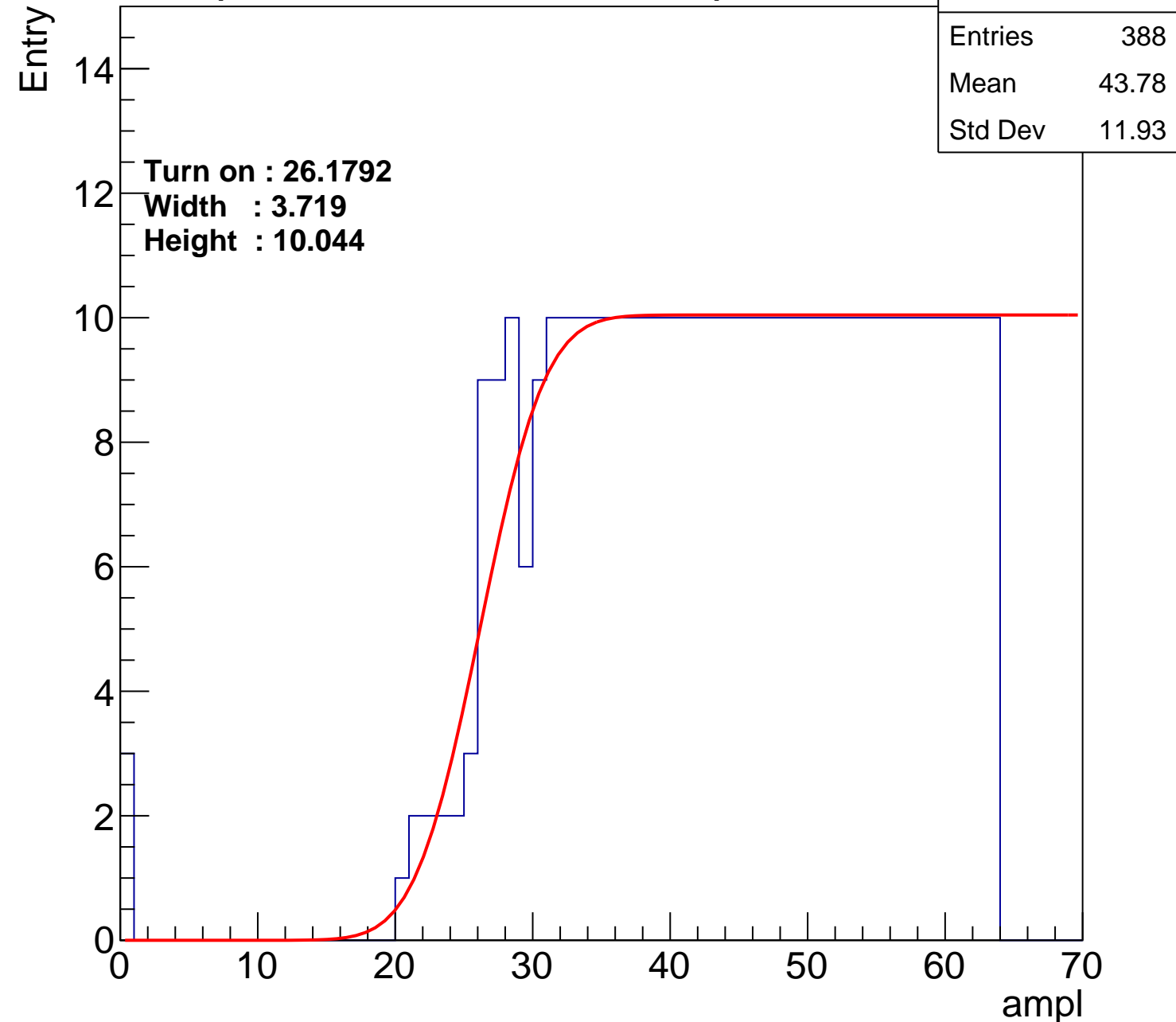
Width : 3.719

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch70

calib_packv5_042523_0143.root, FC#10, port B3

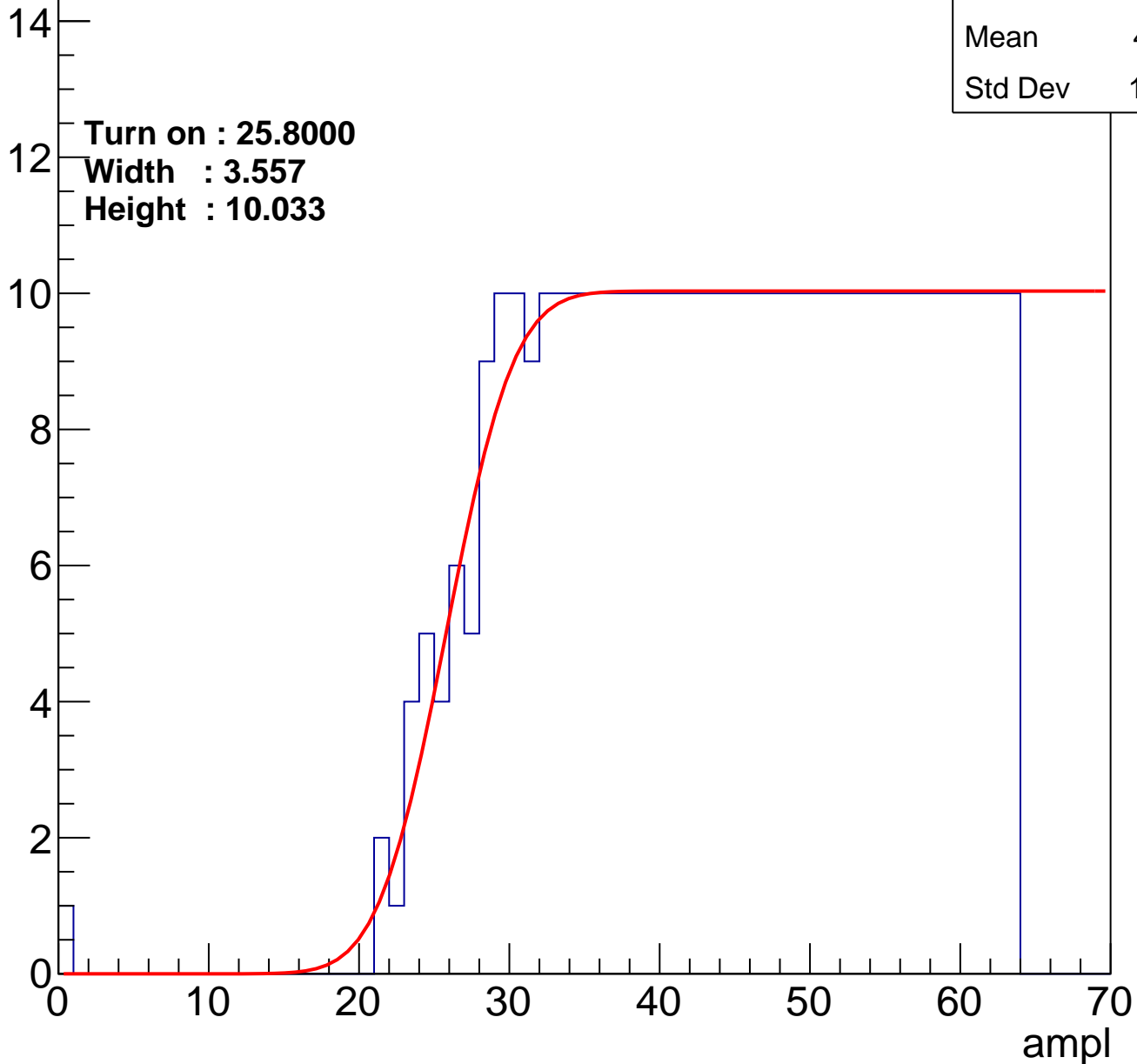
| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.01 |
| Std Dev | 11.53 |

Turn on : 25.8000

Width : 3.557

Height : 10.033

Entry



B1L002S, U5-ch71

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.67 |
| Std Dev | 11.14 |

Turn on : 26.3289

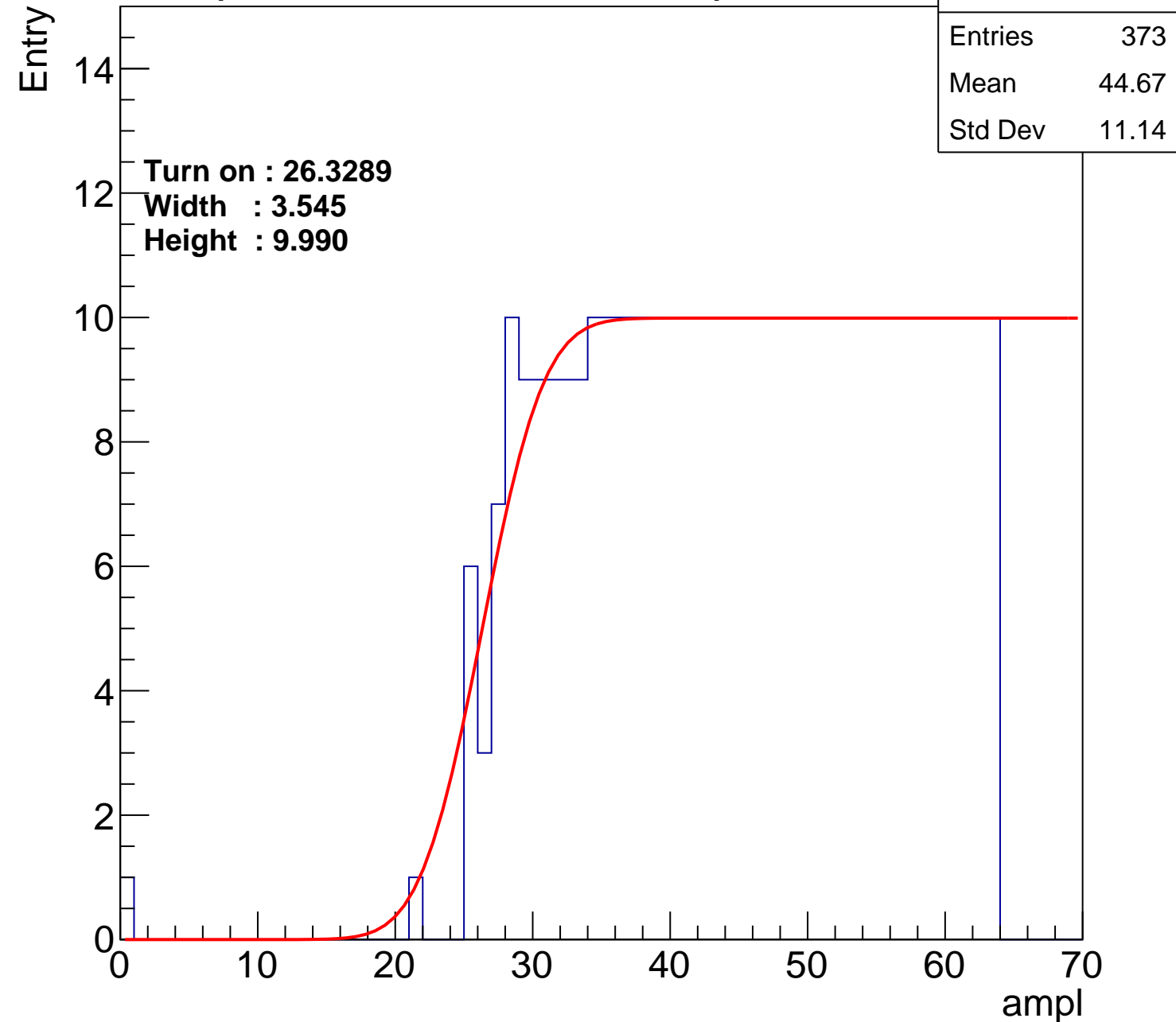
Width : 3.545

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch72

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.19 |
| Std Dev | 12.07 |

Turn on : 24.6203

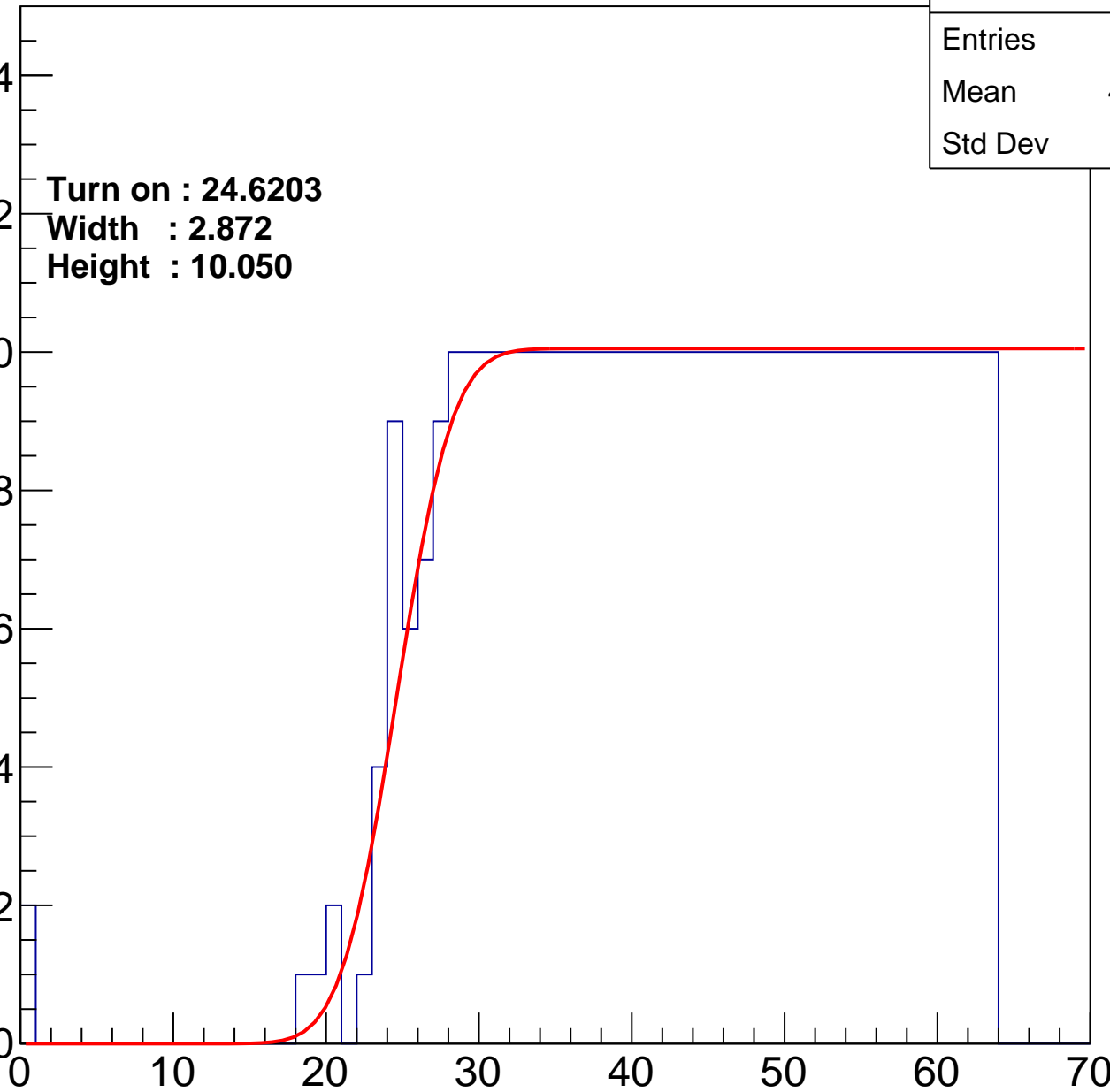
Width : 2.872

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch73

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.84 |
| Std Dev | 12.13 |

Turn on : 26.0598

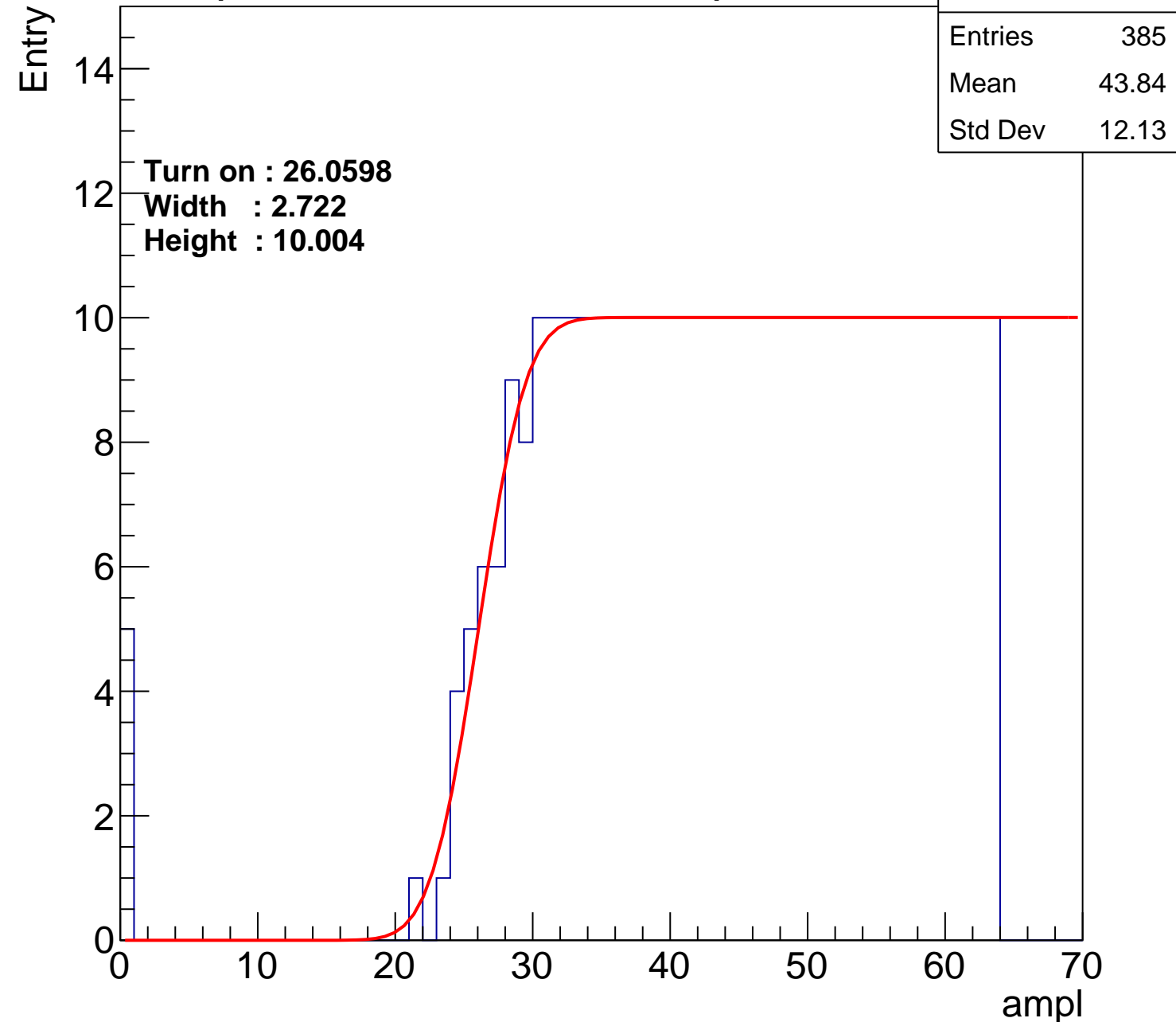
Width : 2.722

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch74

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.36 |
| Std Dev | 12.35 |

Turn on : 24.6340

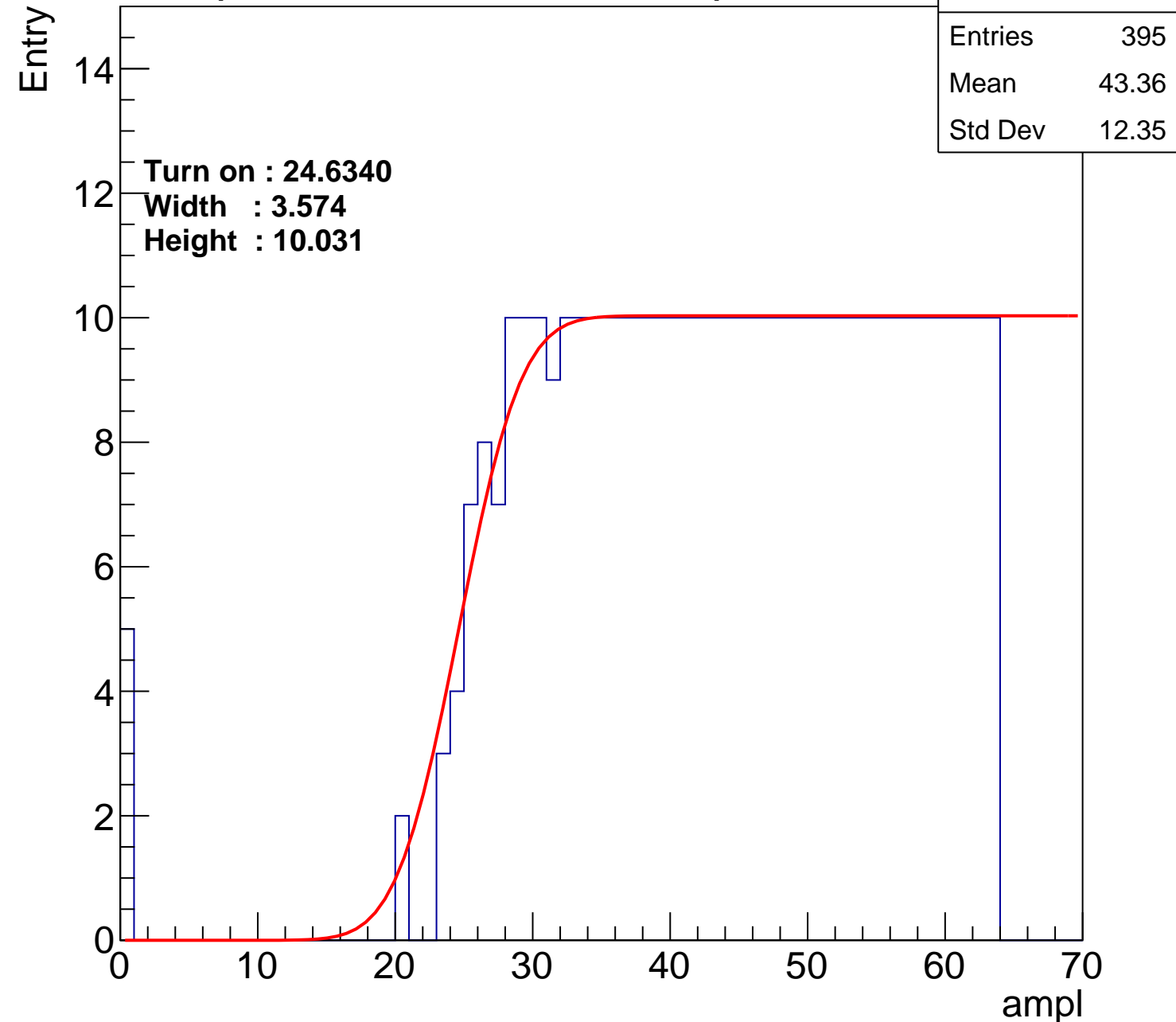
Width : 3.574

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch75

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.71 |
| Std Dev | 11.28 |

Turn on : 27.2317

Width : 3.318

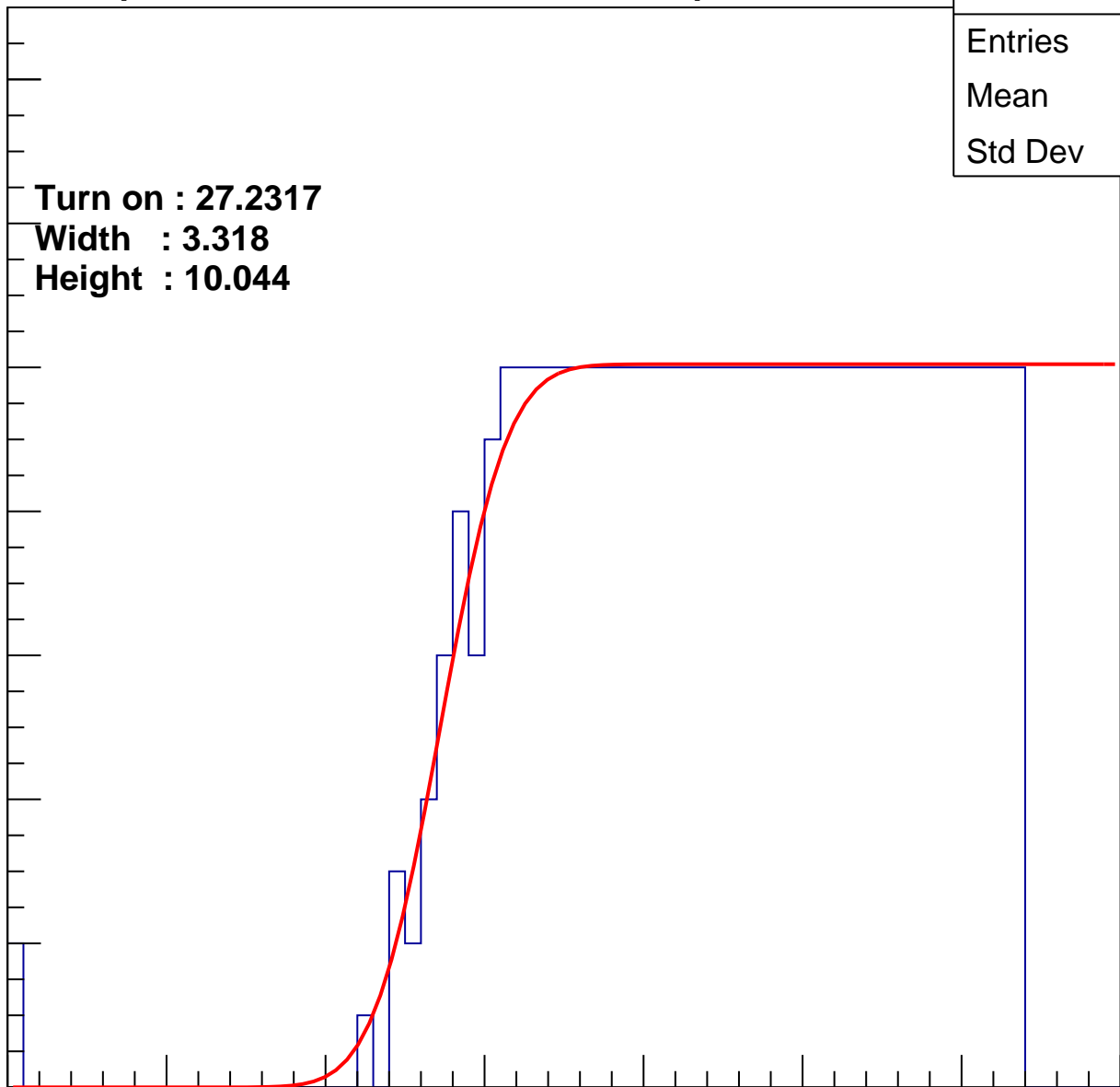
Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U5-ch76

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.27 |
| Std Dev | 12.16 |

Turn on : 24.8766

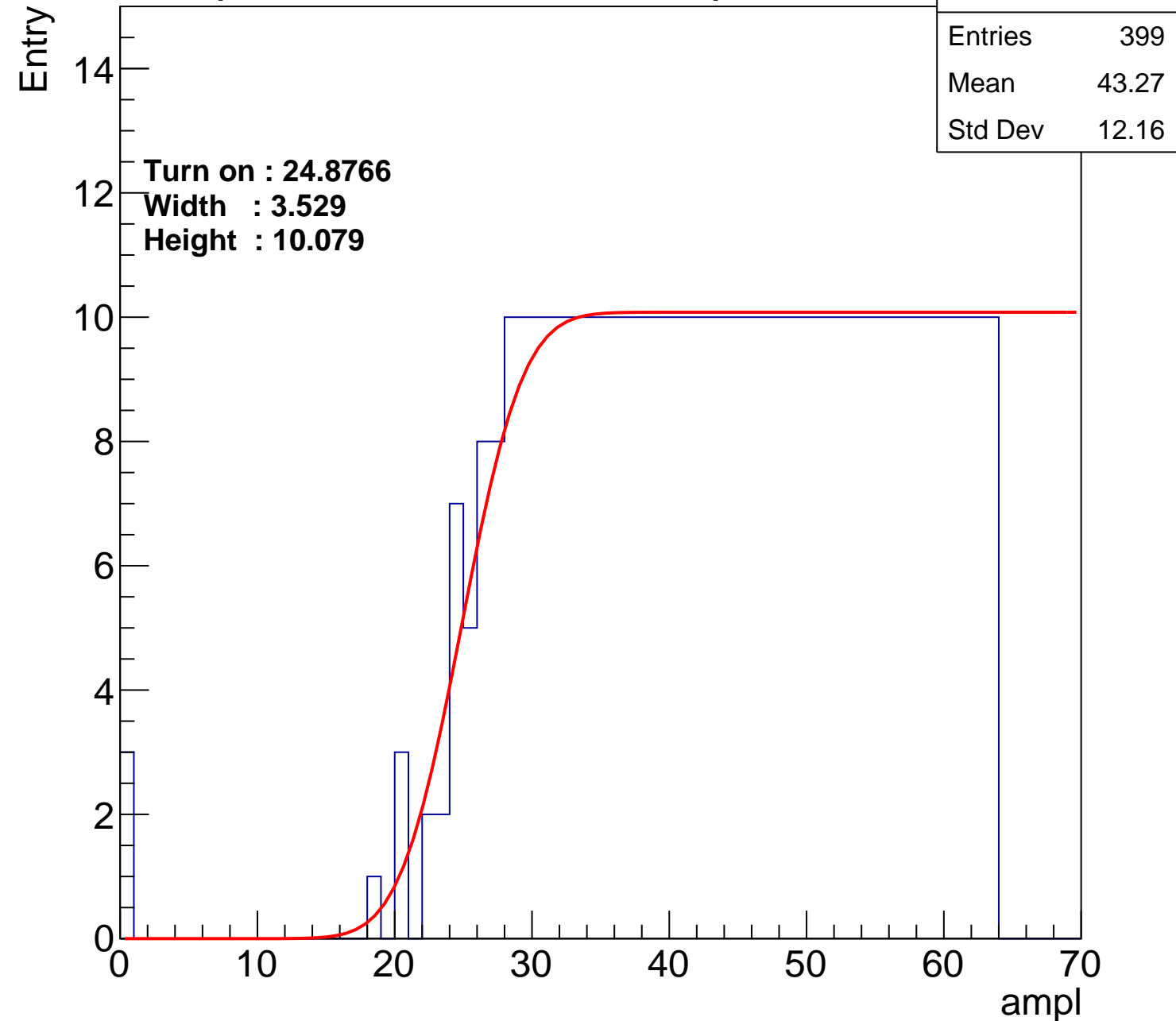
Width : 3.529

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch77

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 402 |
| Mean | 43.13 |
| Std Dev | 12.23 |

Turn on : 24.4903

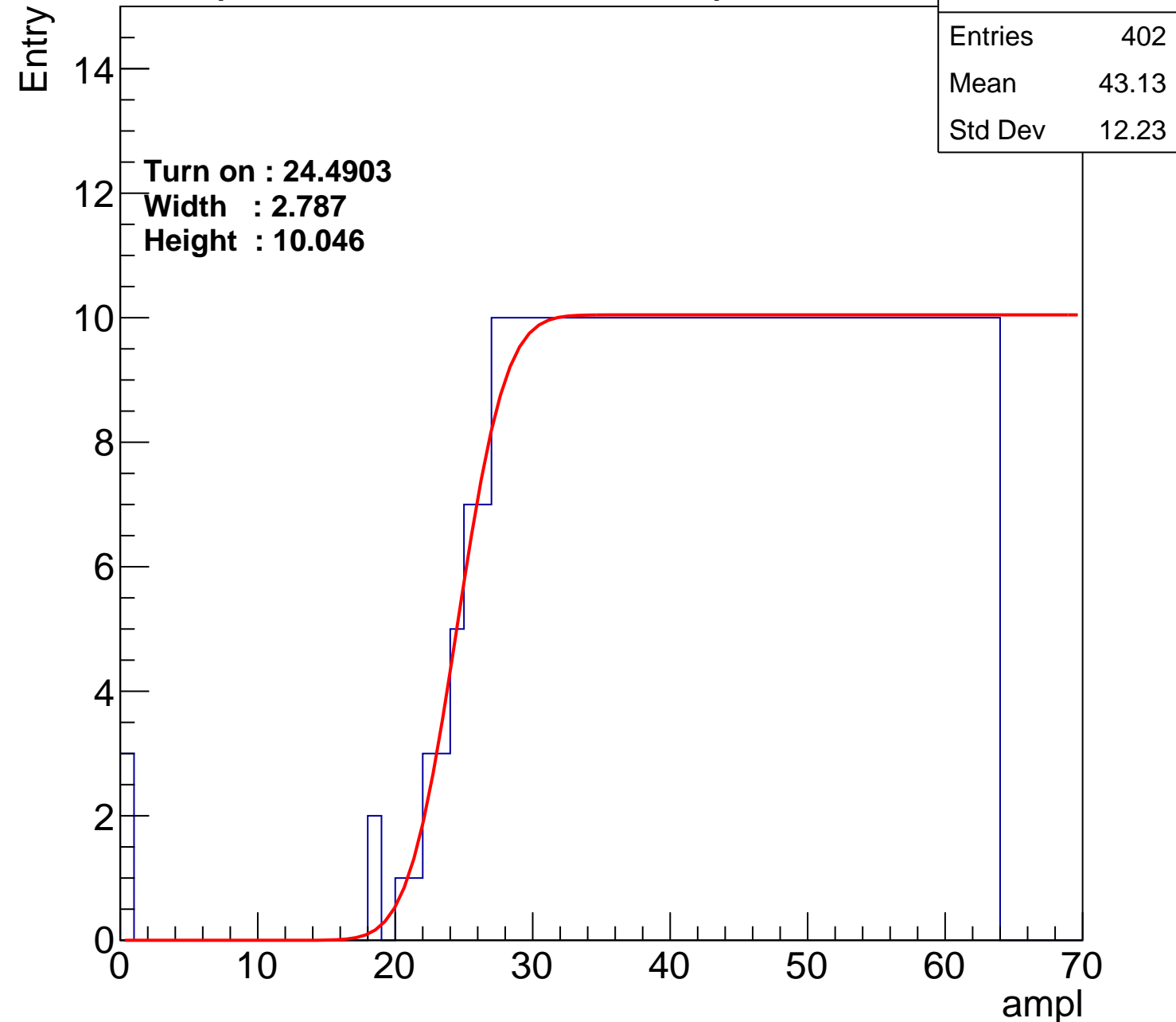
Width : 2.787

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch78

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 404 |
| Mean | 43.04 |
| Std Dev | 12.2 |

Turn on : 23.3730

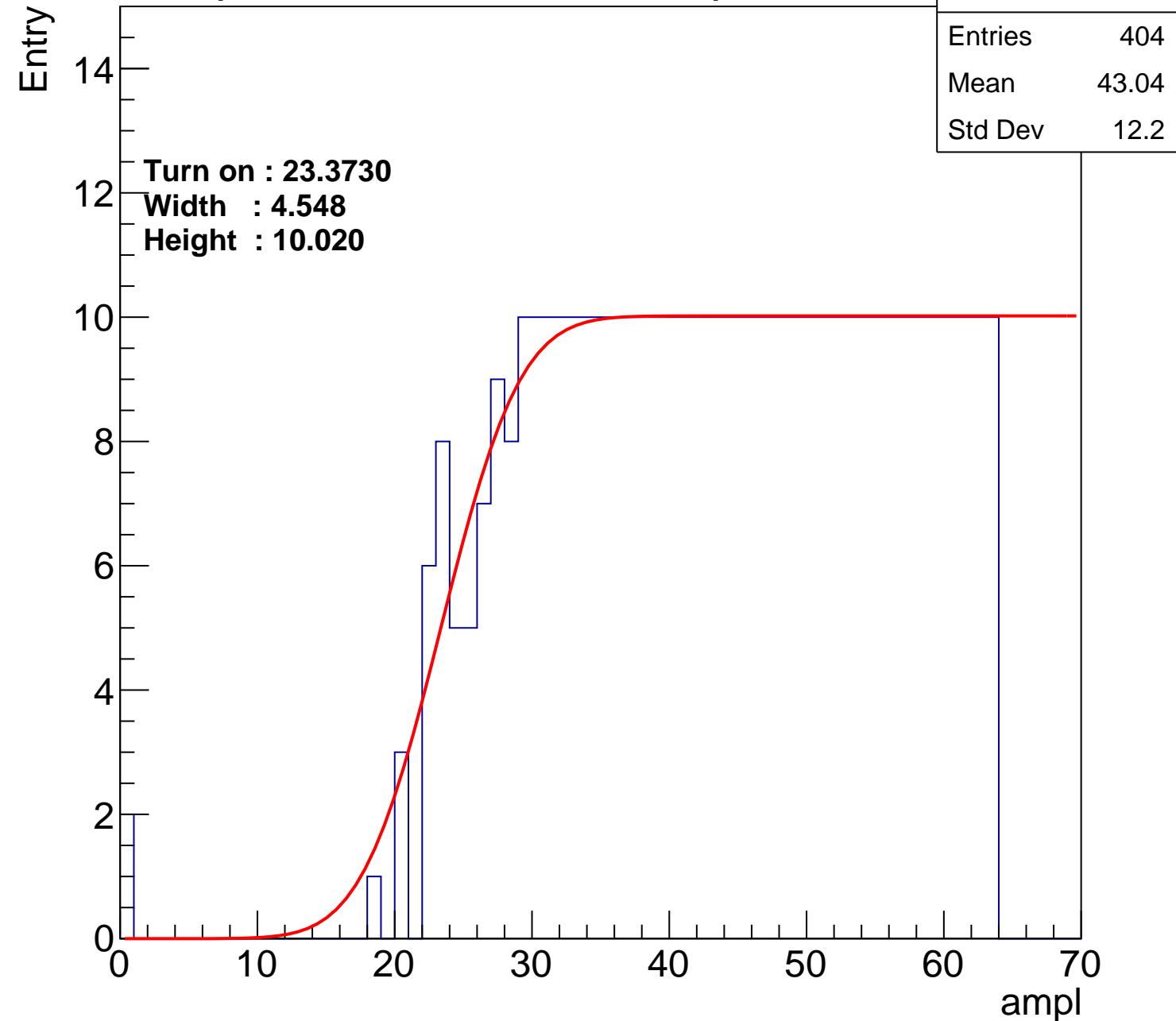
Width : 4.548

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch79

calib_packv5_042523_0143.root, FC#10, port B3

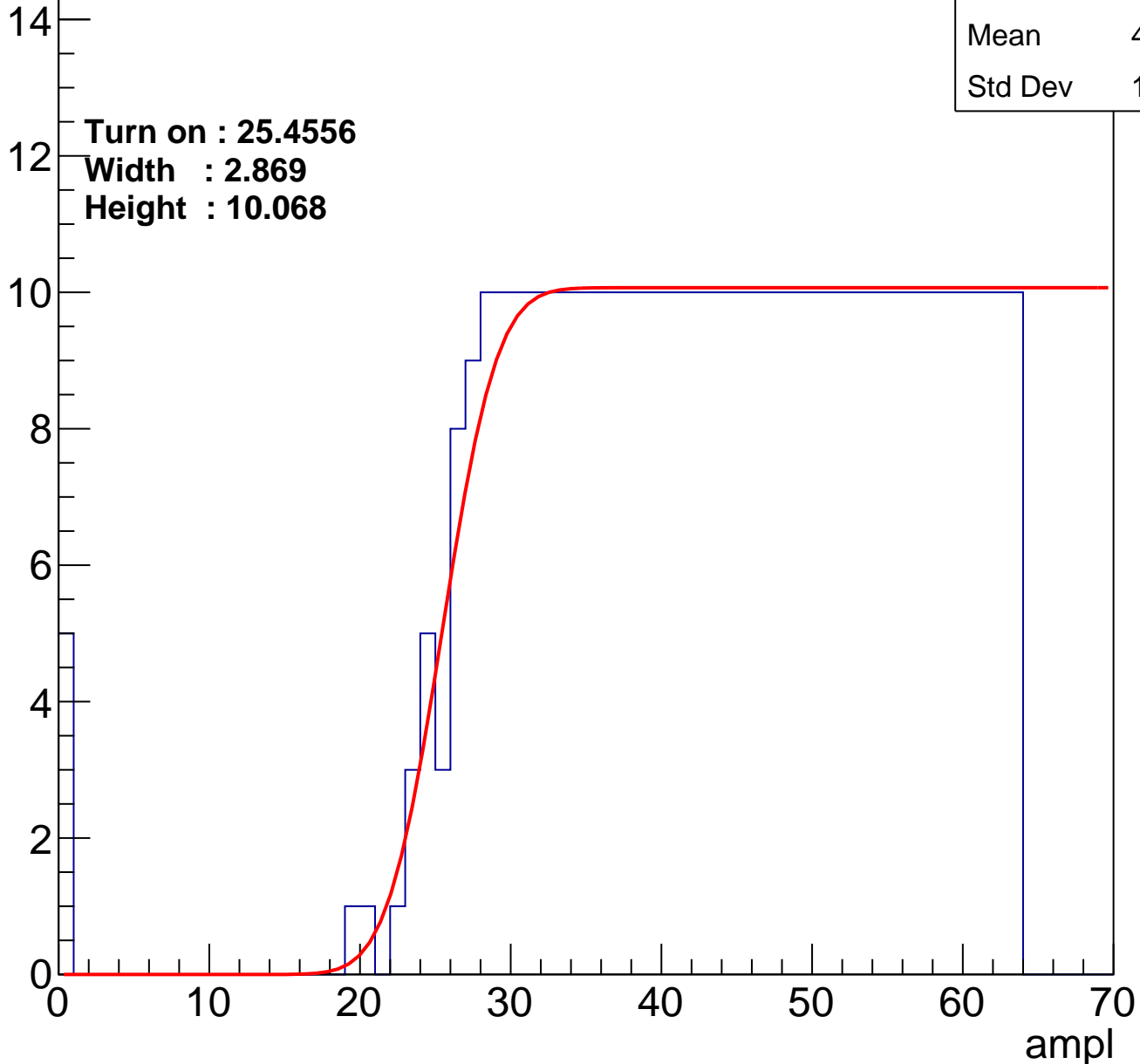
| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.32 |
| Std Dev | 12.35 |

Turn on : 25.4556

Width : 2.869

Height : 10.068

Entry



B1L002S, U5-ch80

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.39 |
| Std Dev | 11.85 |

Turn on : 24.3504

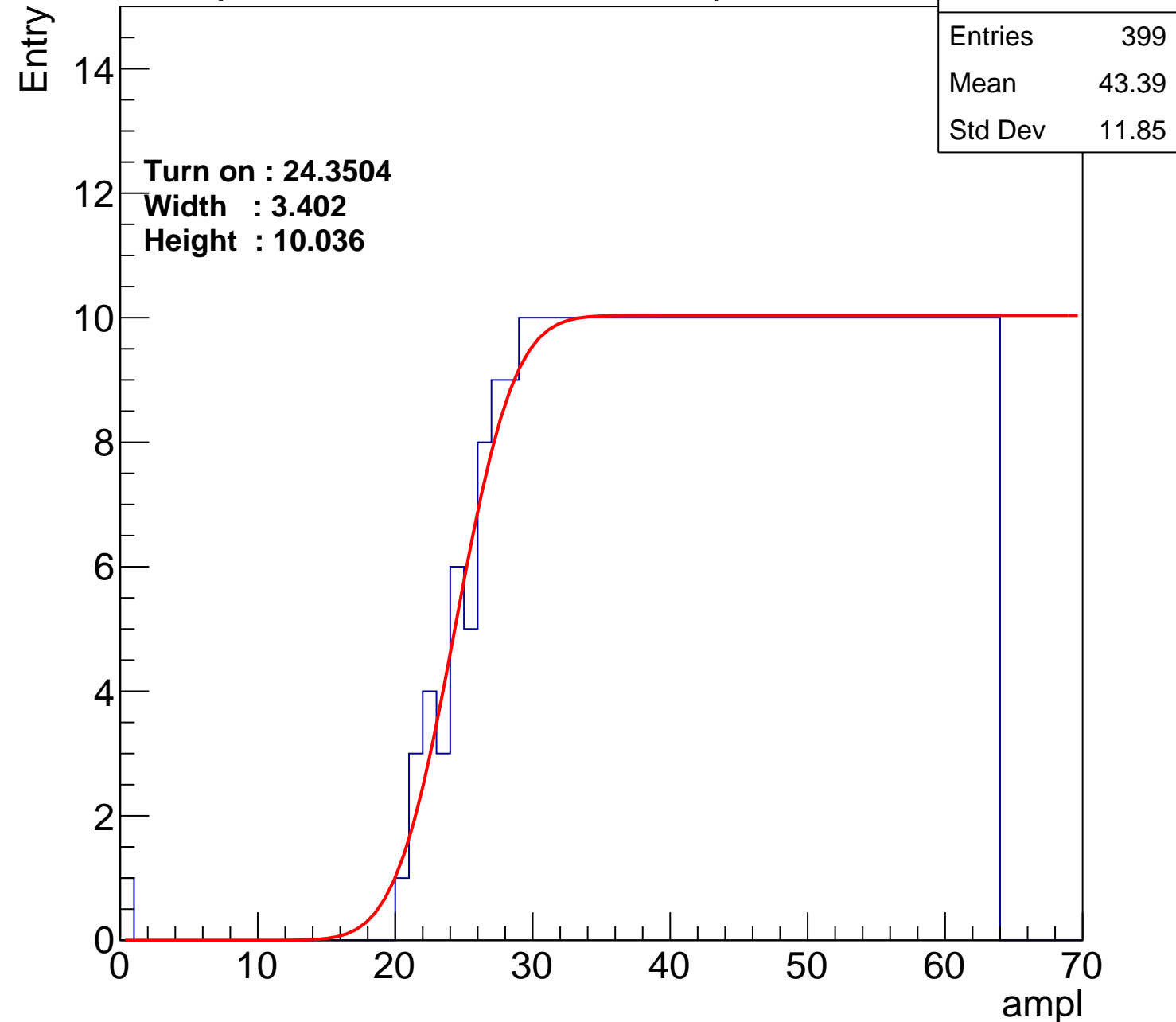
Width : 3.402

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch81

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.19 |
| Std Dev | 12.29 |

Turn on : 24.3199

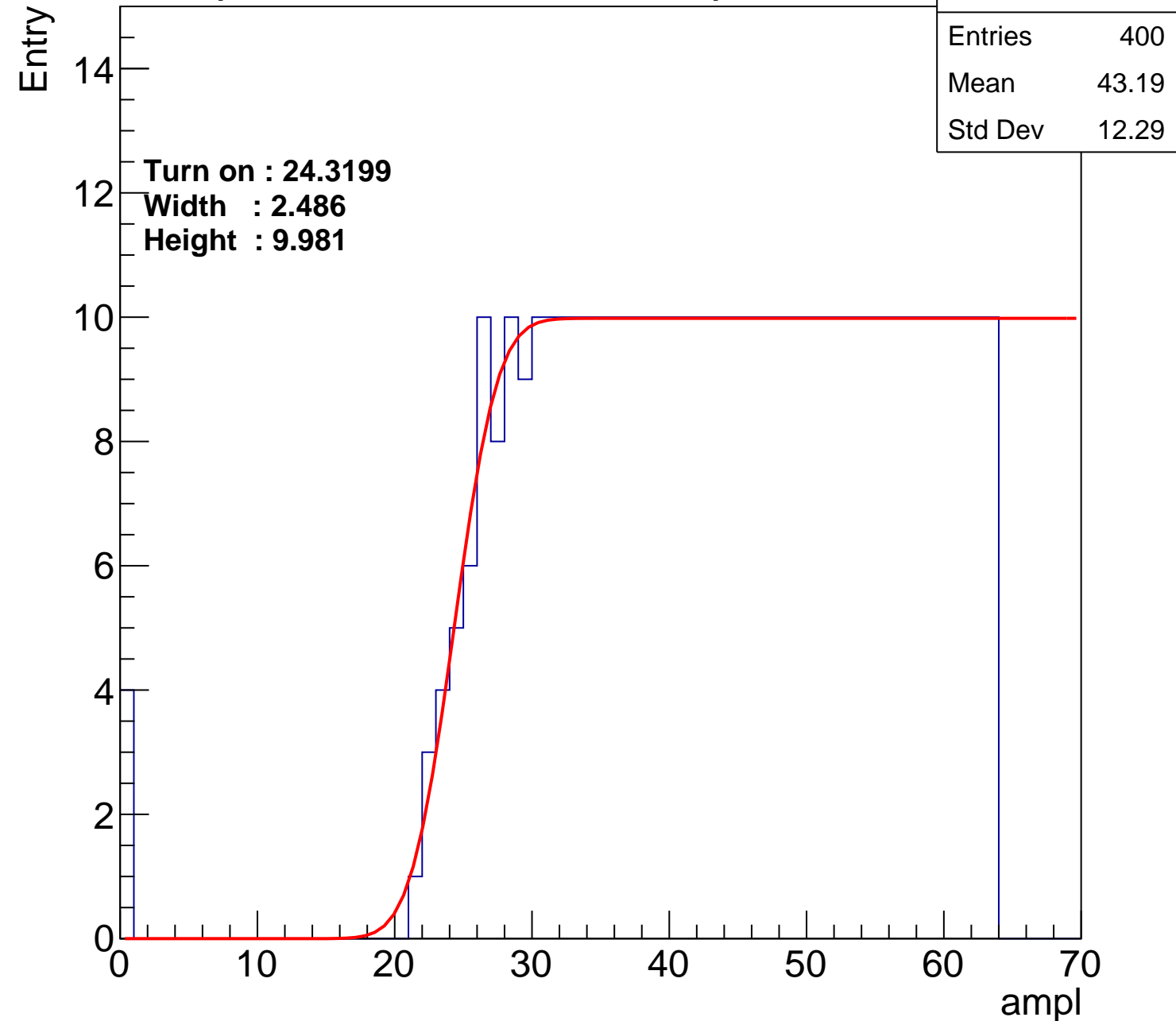
Width : 2.486

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch82

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.12 |
| Std Dev | 11.46 |

Turn on : 25.8549

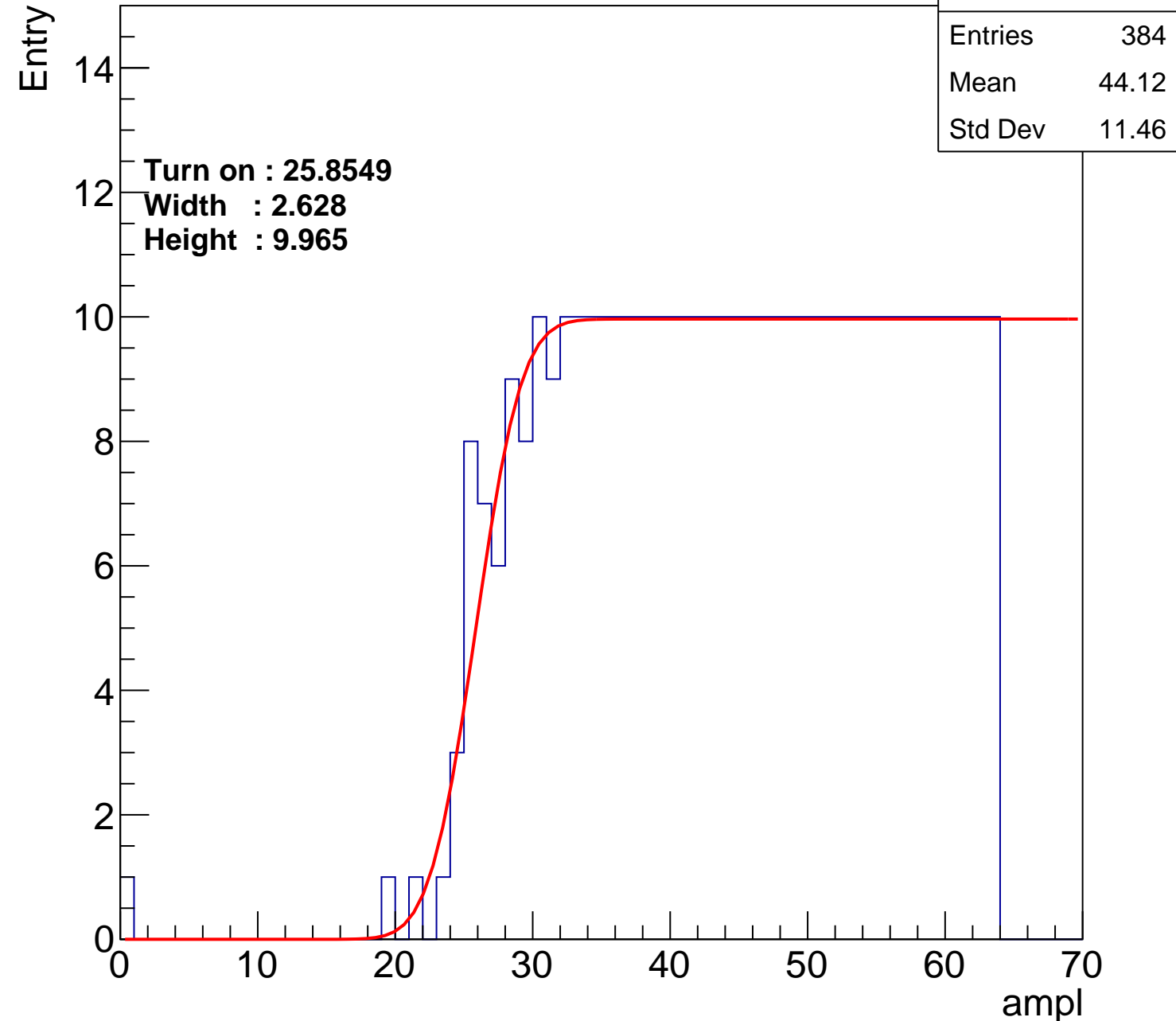
Width : 2.628

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch83

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.22 |
| Std Dev | 11.34 |

Turn on : 25.9834

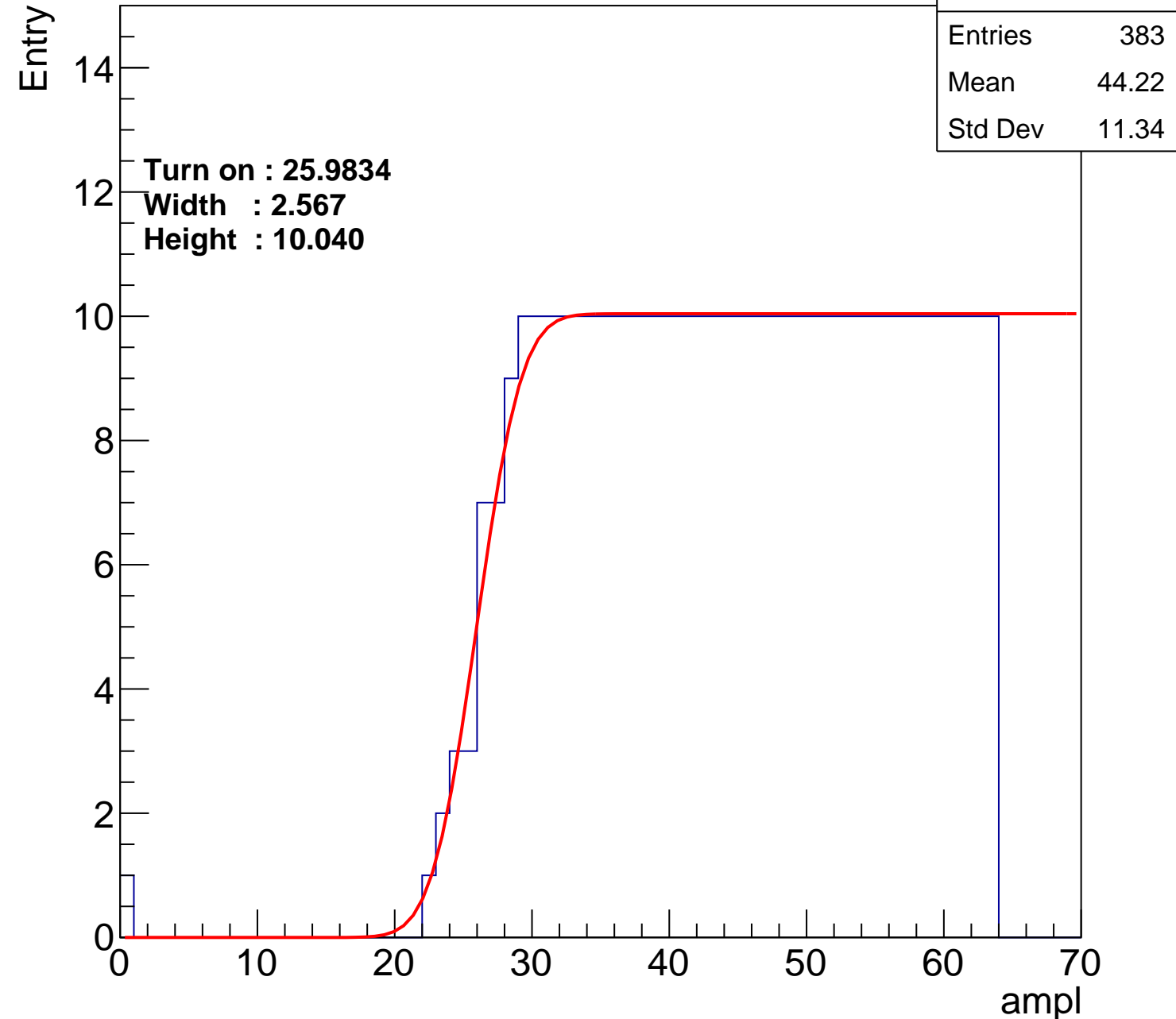
Width : 2.567

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch84

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.25 |
| Std Dev | 12.02 |

Turn on : 24.4116

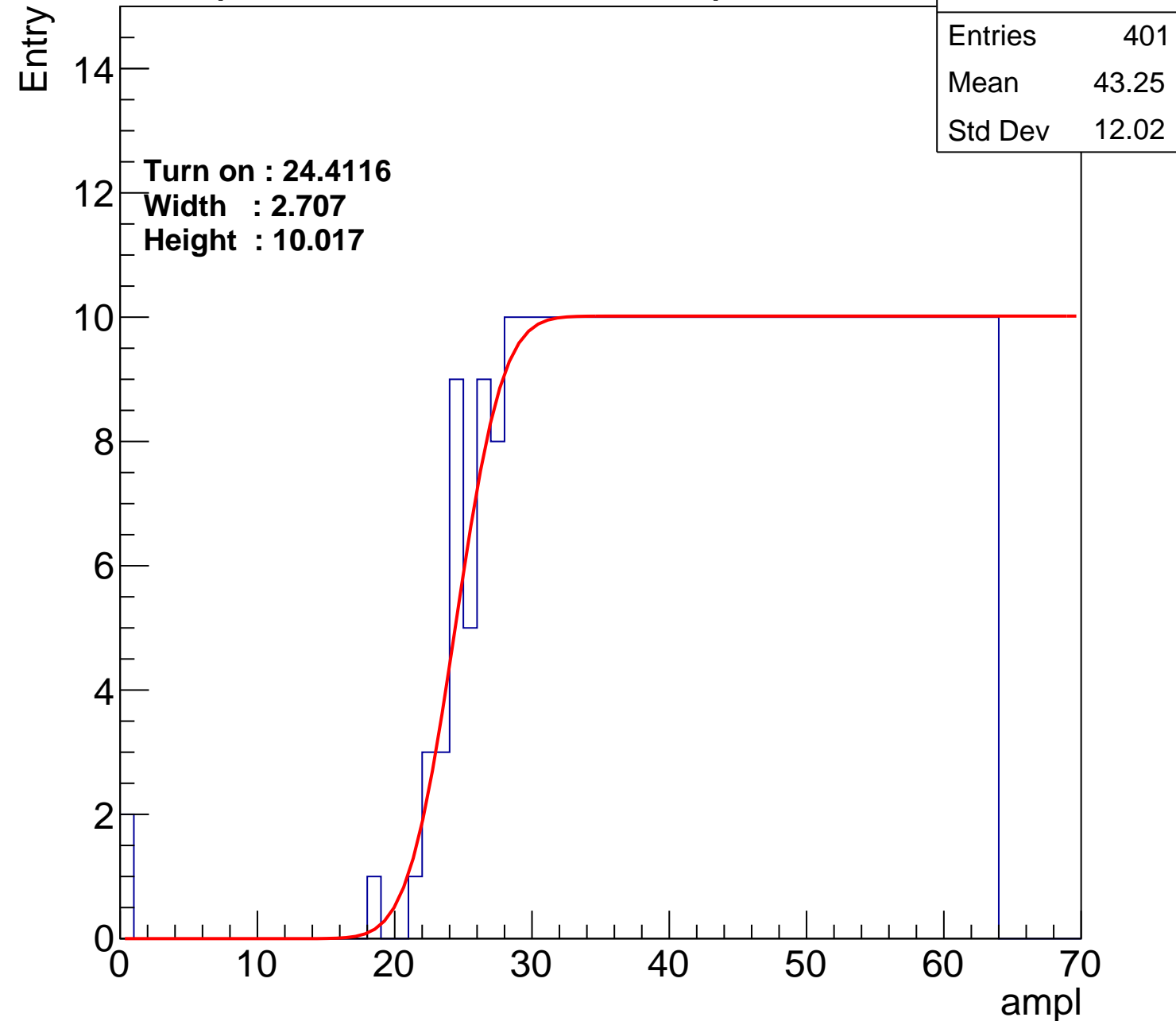
Width : 2.707

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch85

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 404 |
| Mean | 42.99 |
| Std Dev | 12.39 |

Turn on : 24.2873

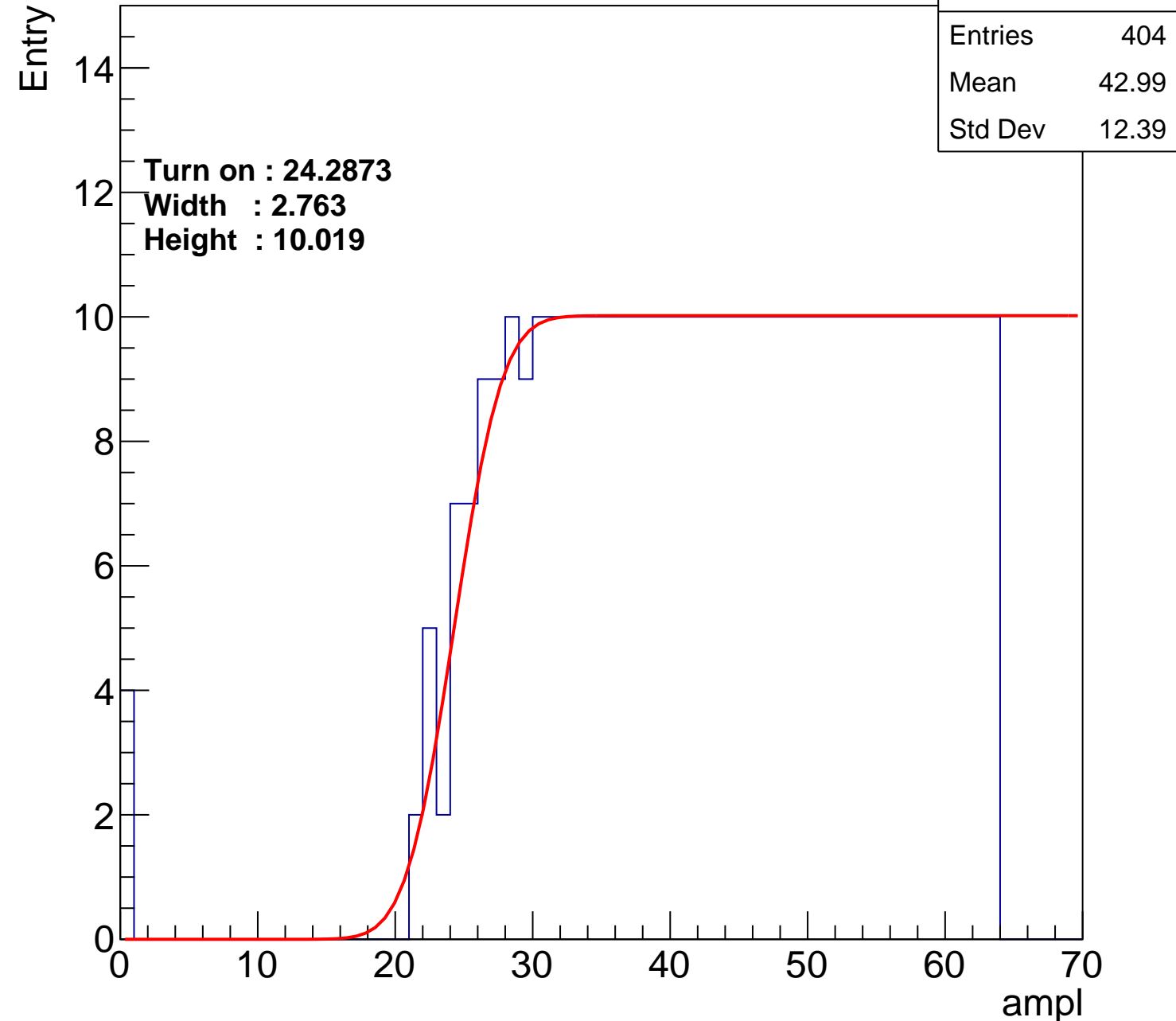
Width : 2.763

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch86

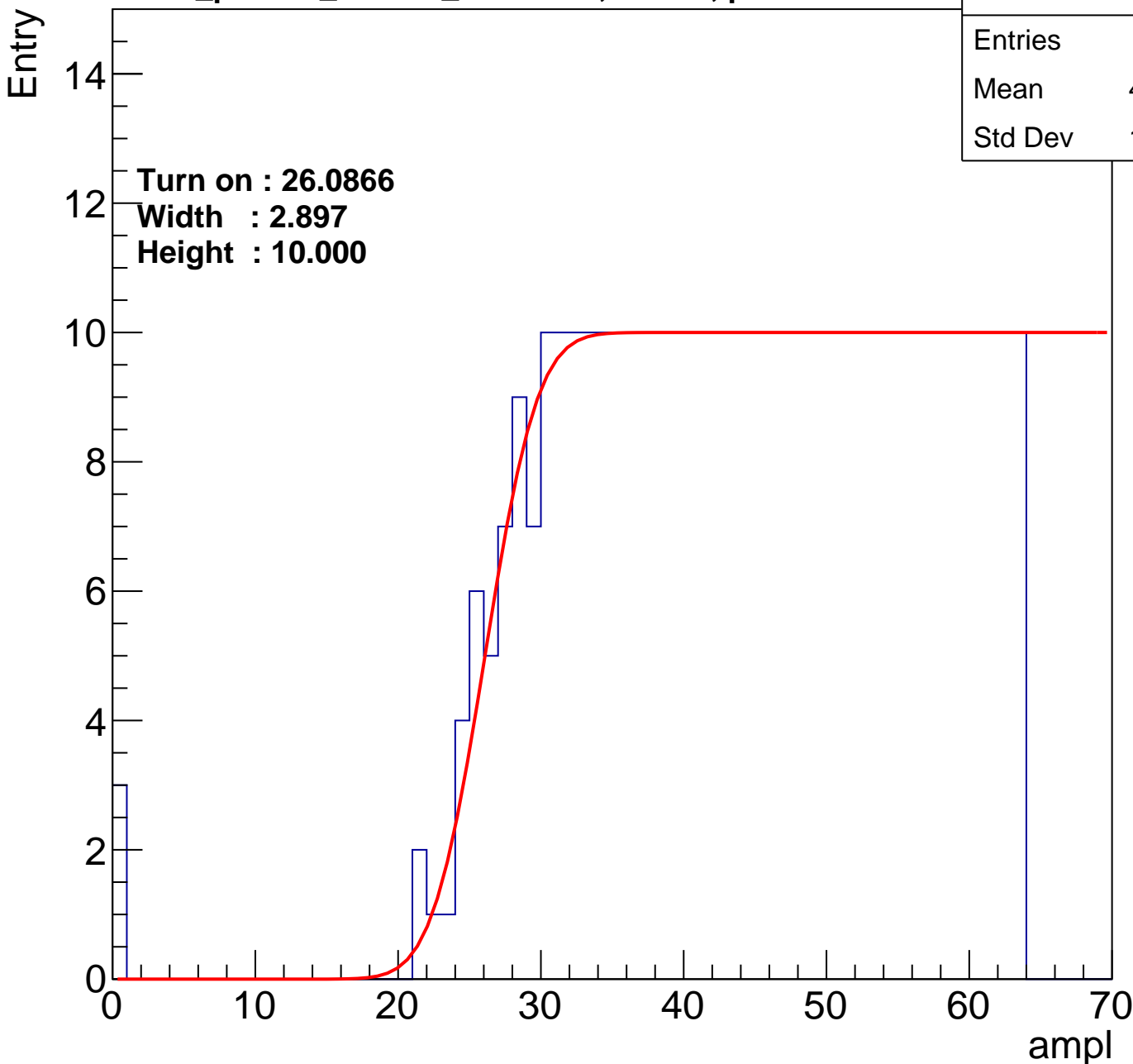
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.94 |
| Std Dev | 11.84 |

Turn on : 26.0866

Width : 2.897

Height : 10.000



B1L002S, U5-ch87

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.8 |
| Std Dev | 12.01 |

Turn on : 25.7747

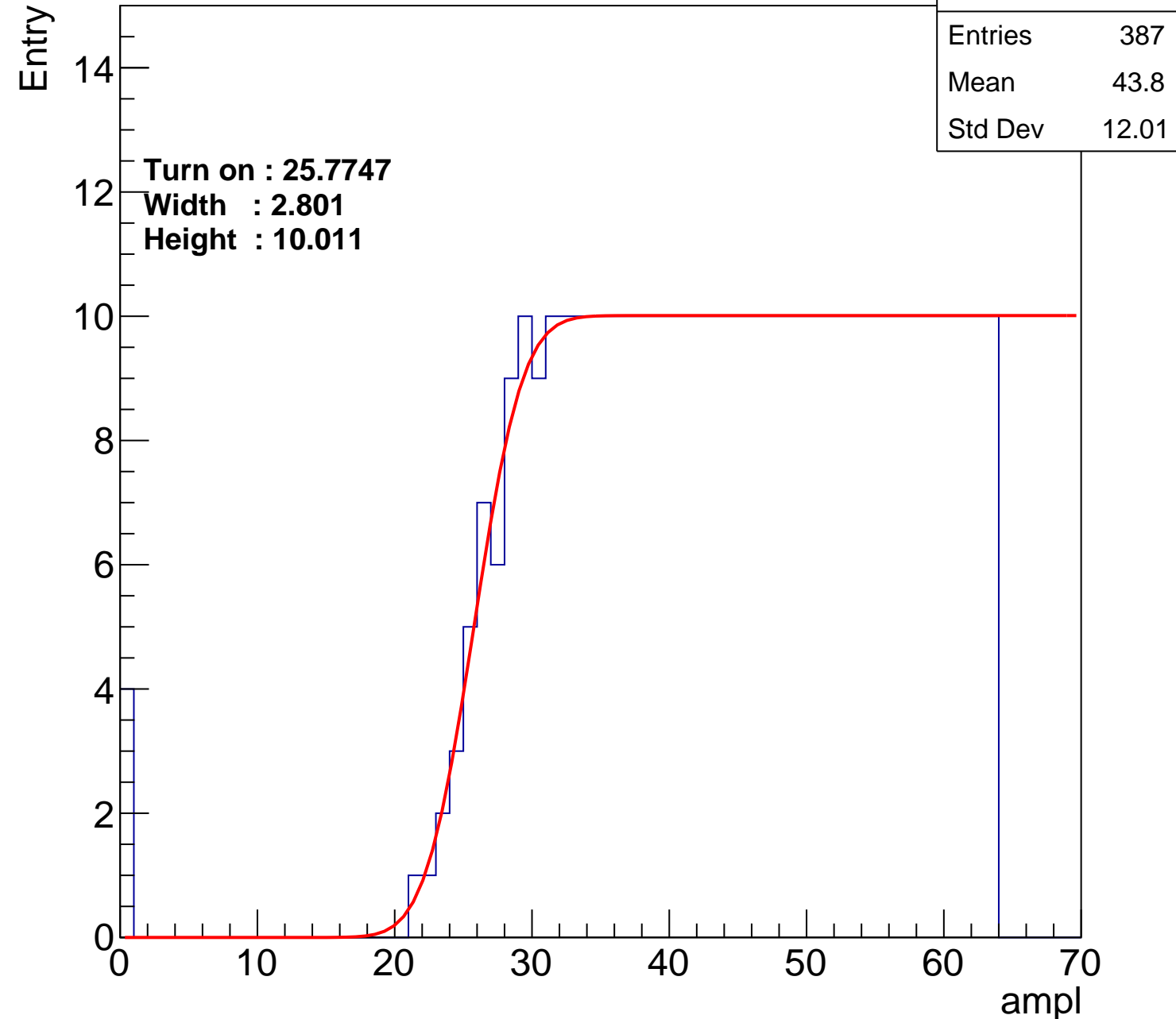
Width : 2.801

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch88

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 401 |
| Mean | 43.19 |
| Std Dev | 12.18 |

Turn on : 24.3598

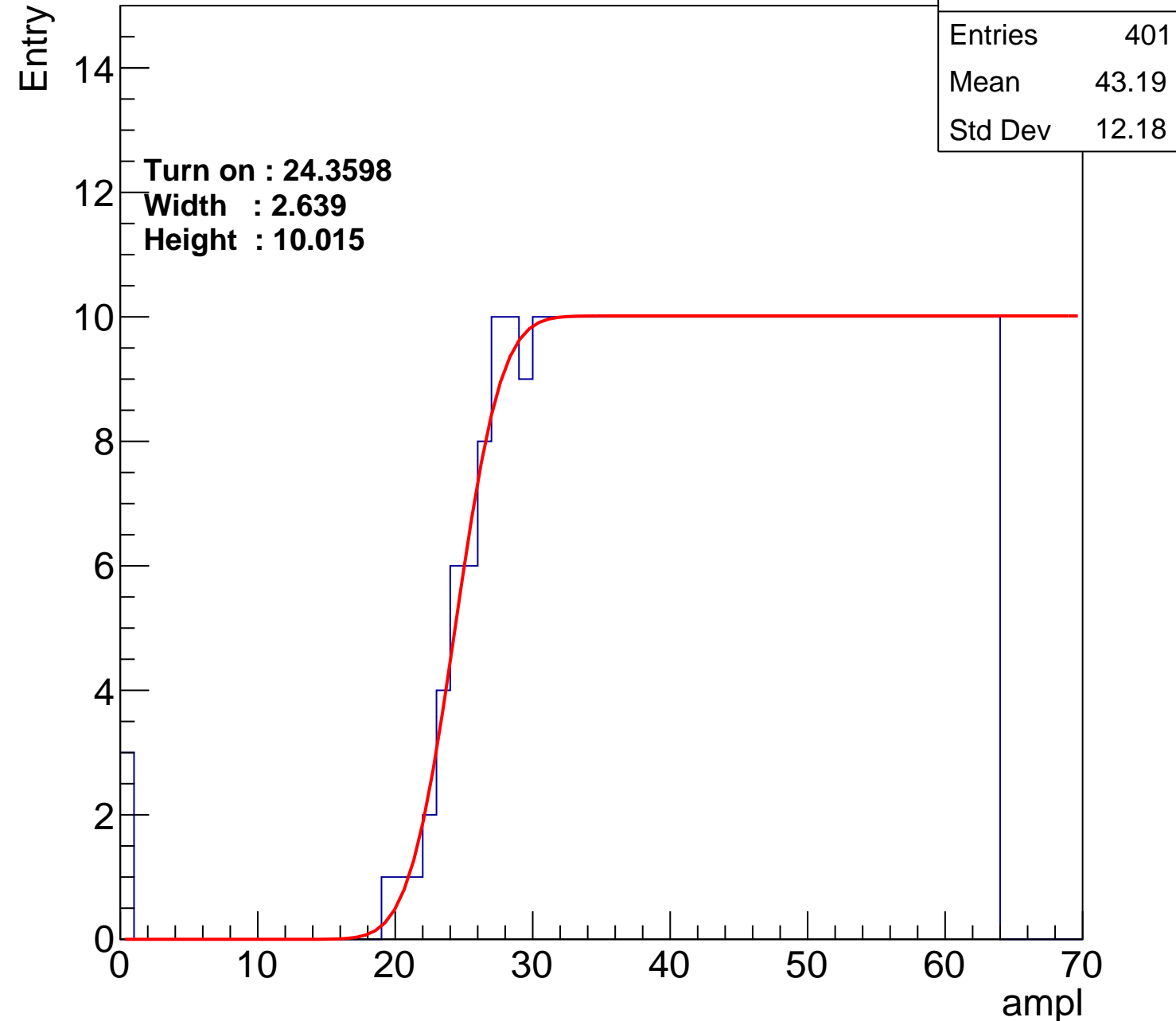
Width : 2.639

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch89

calib_packv5_042523_0143.root, FC#10, port B3

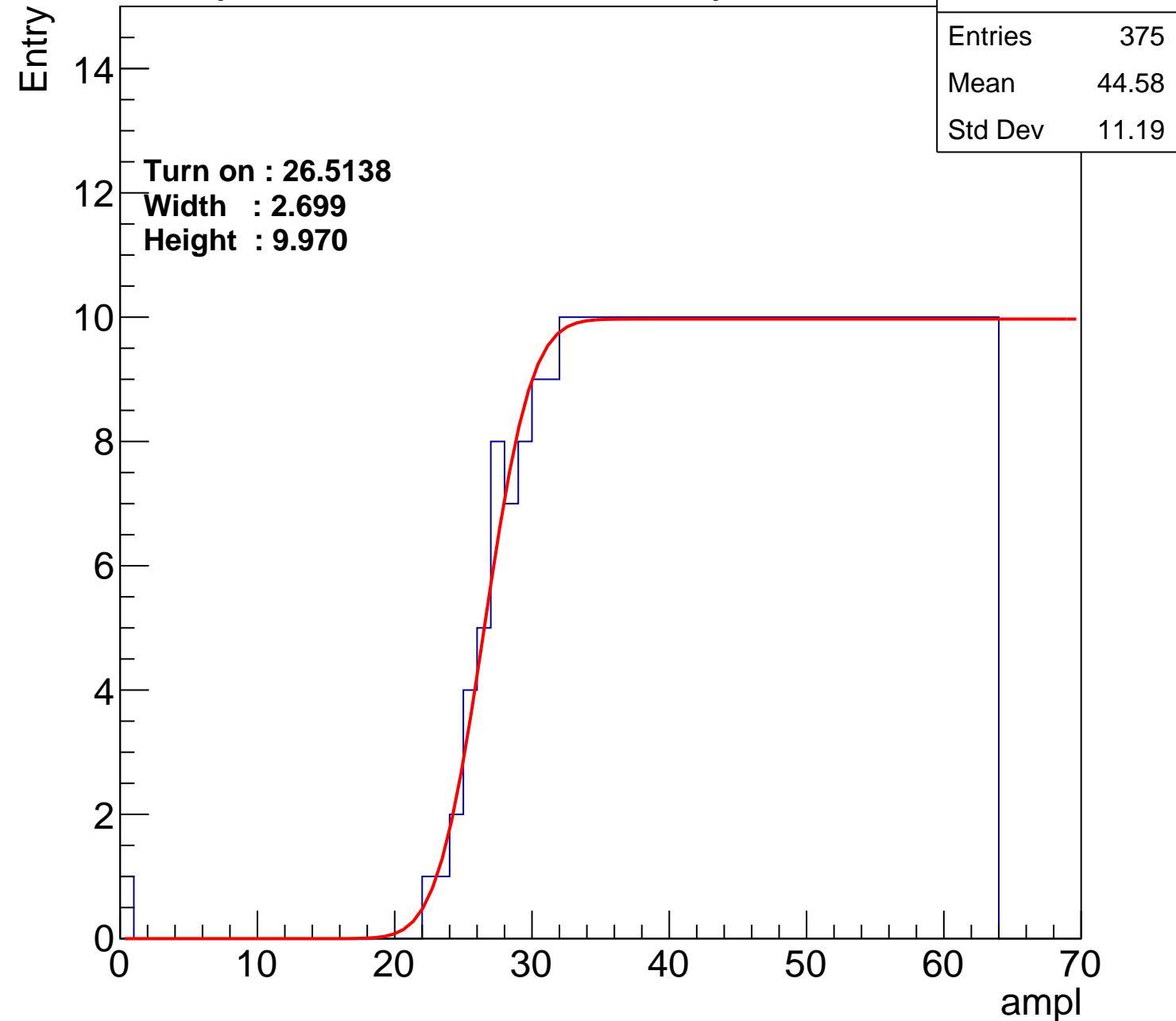
Entry

14
12
10
8
6
4
2
0

Turn on : 26.5138
Width : 2.699
Height : 9.970

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.58 |
| Std Dev | 11.19 |

ampl



B1L002S, U5-ch90

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.69 |
| Std Dev | 12.08 |

Turn on : 25.6905

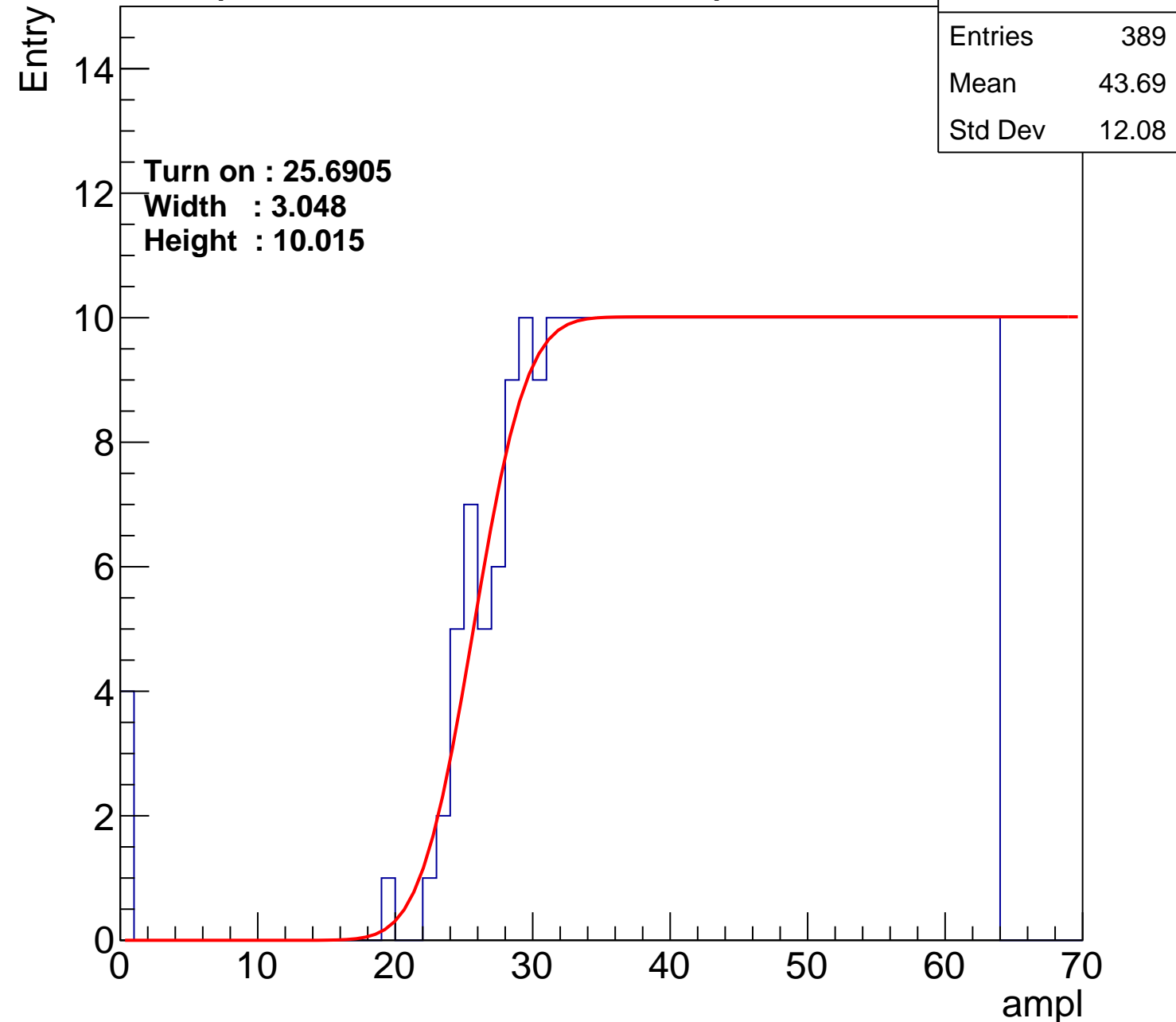
Width : 3.048

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch91

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.12 |
| Std Dev | 11.46 |

Turn on : 25.8617

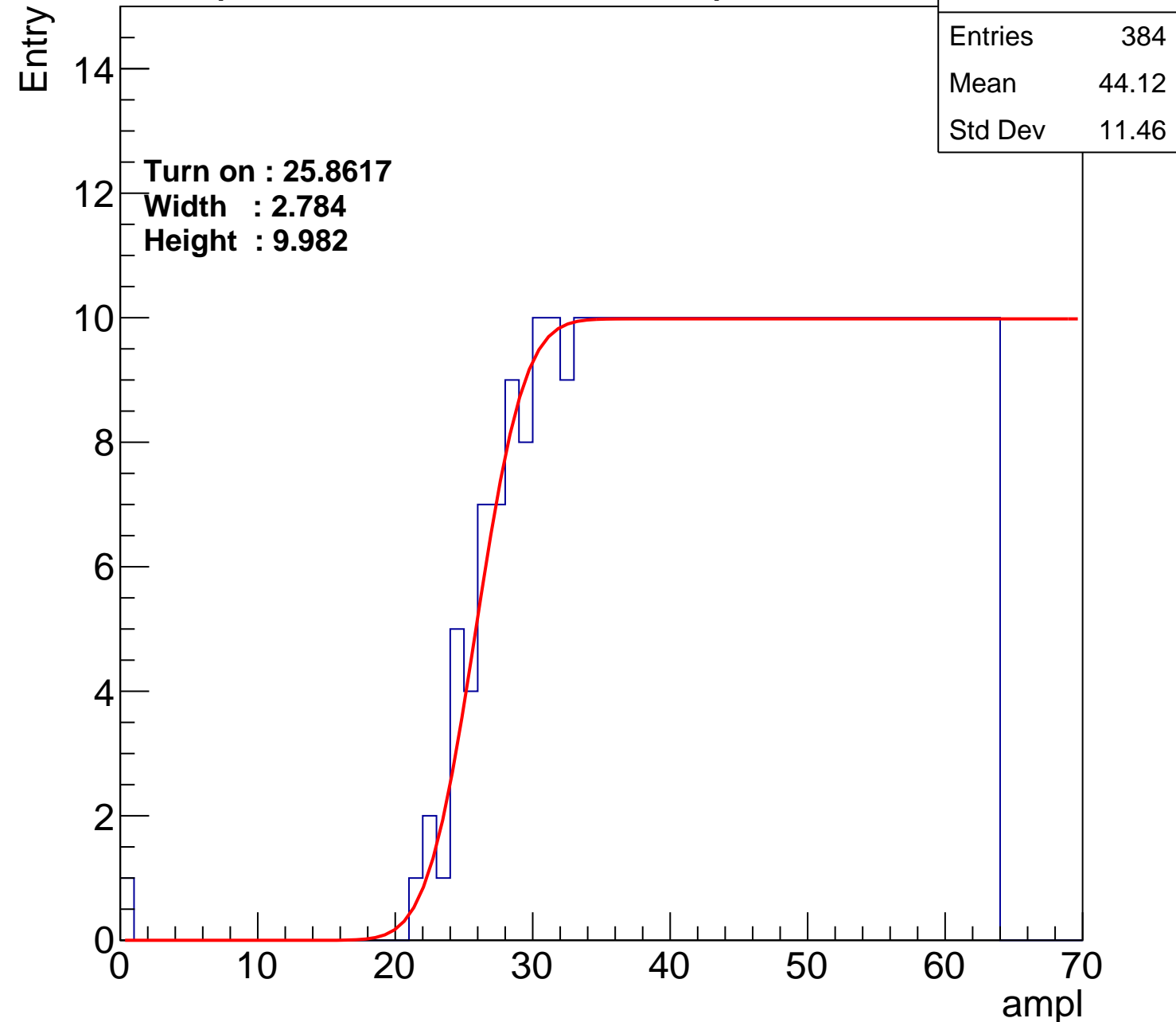
Width : 2.784

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch92

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.85 |
| Std Dev | 11.58 |

Turn on : 25.0433

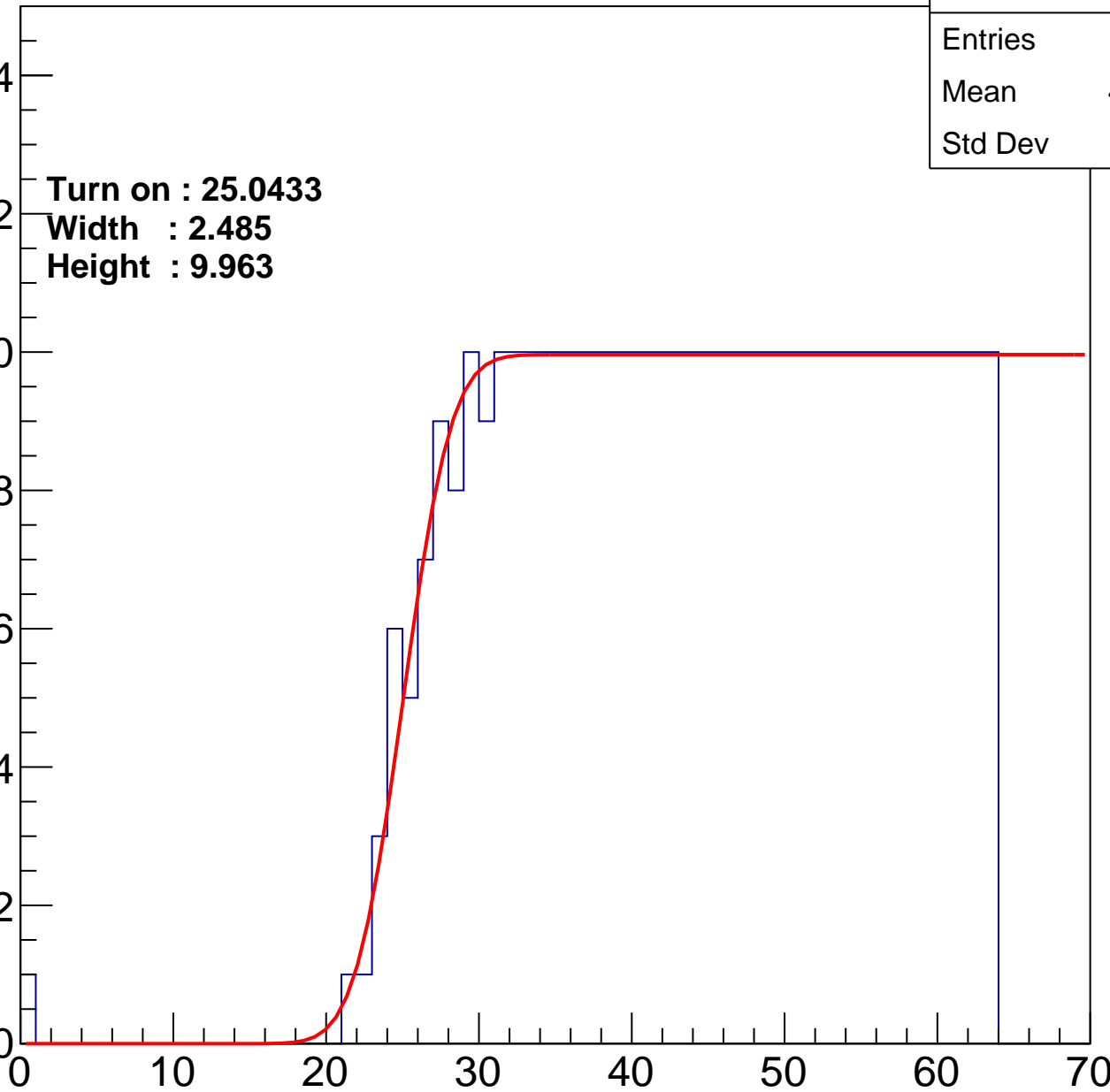
Width : 2.485

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch93

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.63 |
| Std Dev | 11.84 |

Turn on : 24.2553

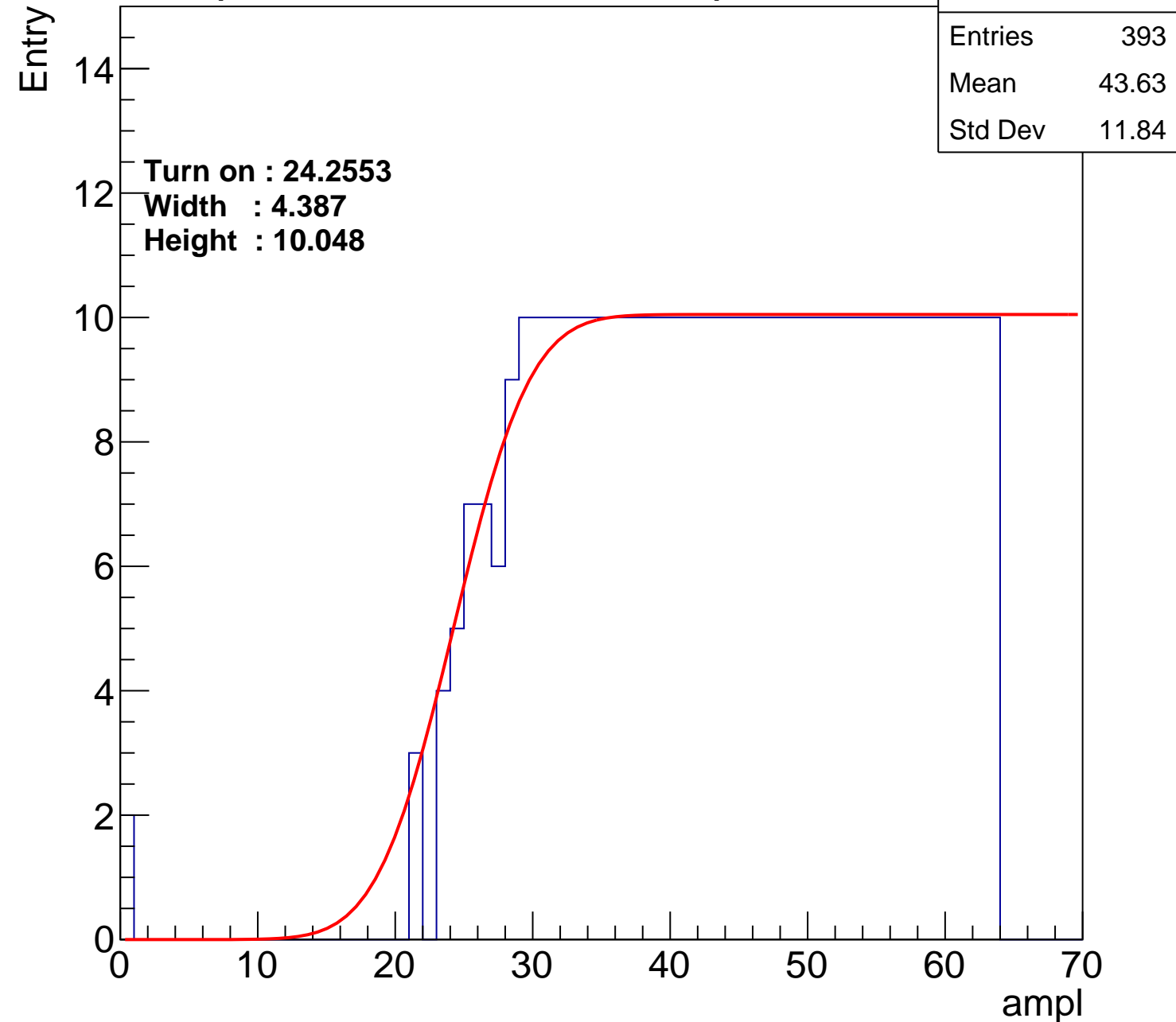
Width : 4.387

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch94

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.92 |
| Std Dev | 11.69 |

Turn on : 25.6666

Width : 3.030

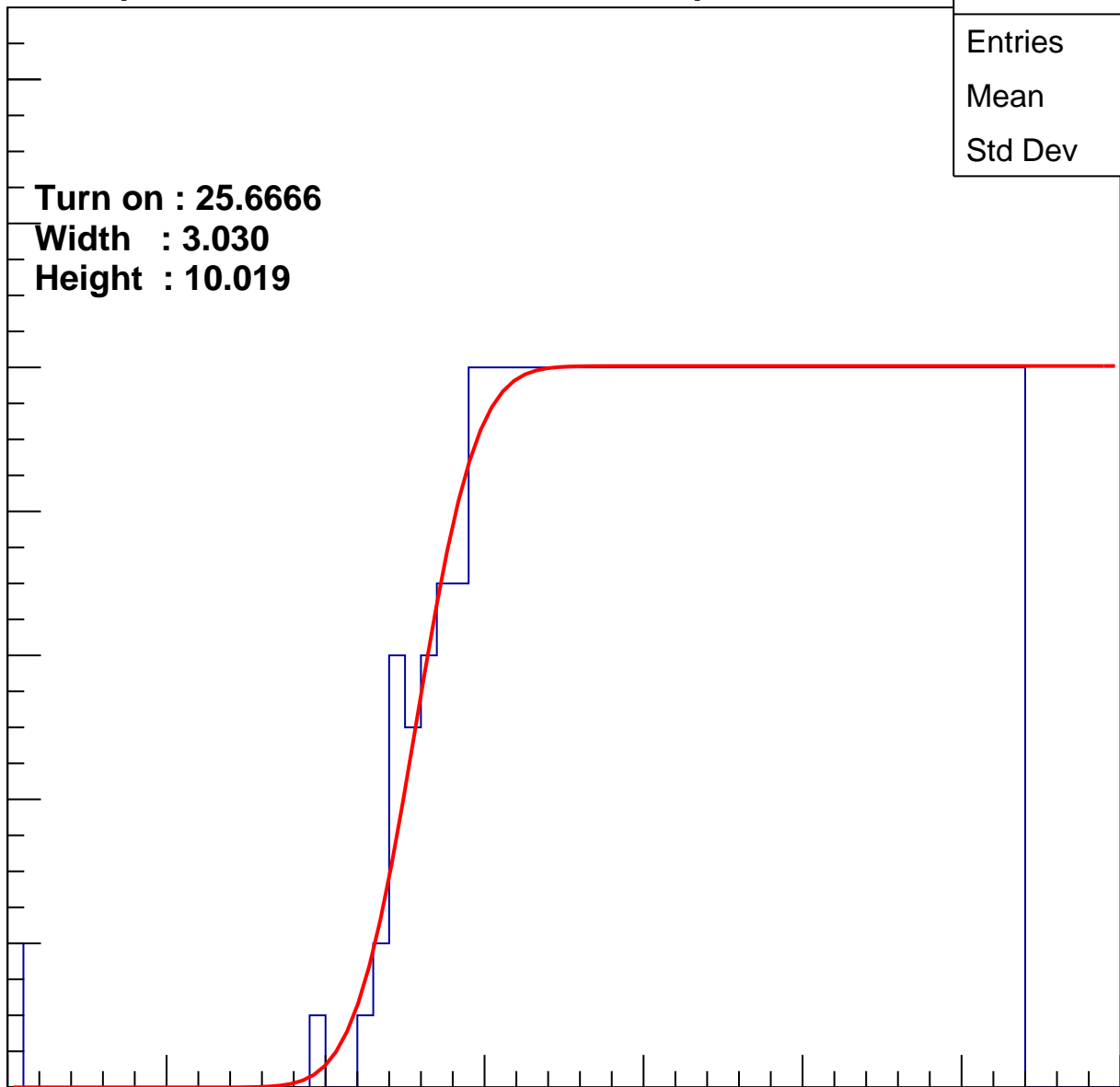
Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U5-ch95

calib_packv5_042523_0143.root, FC#10, port B3

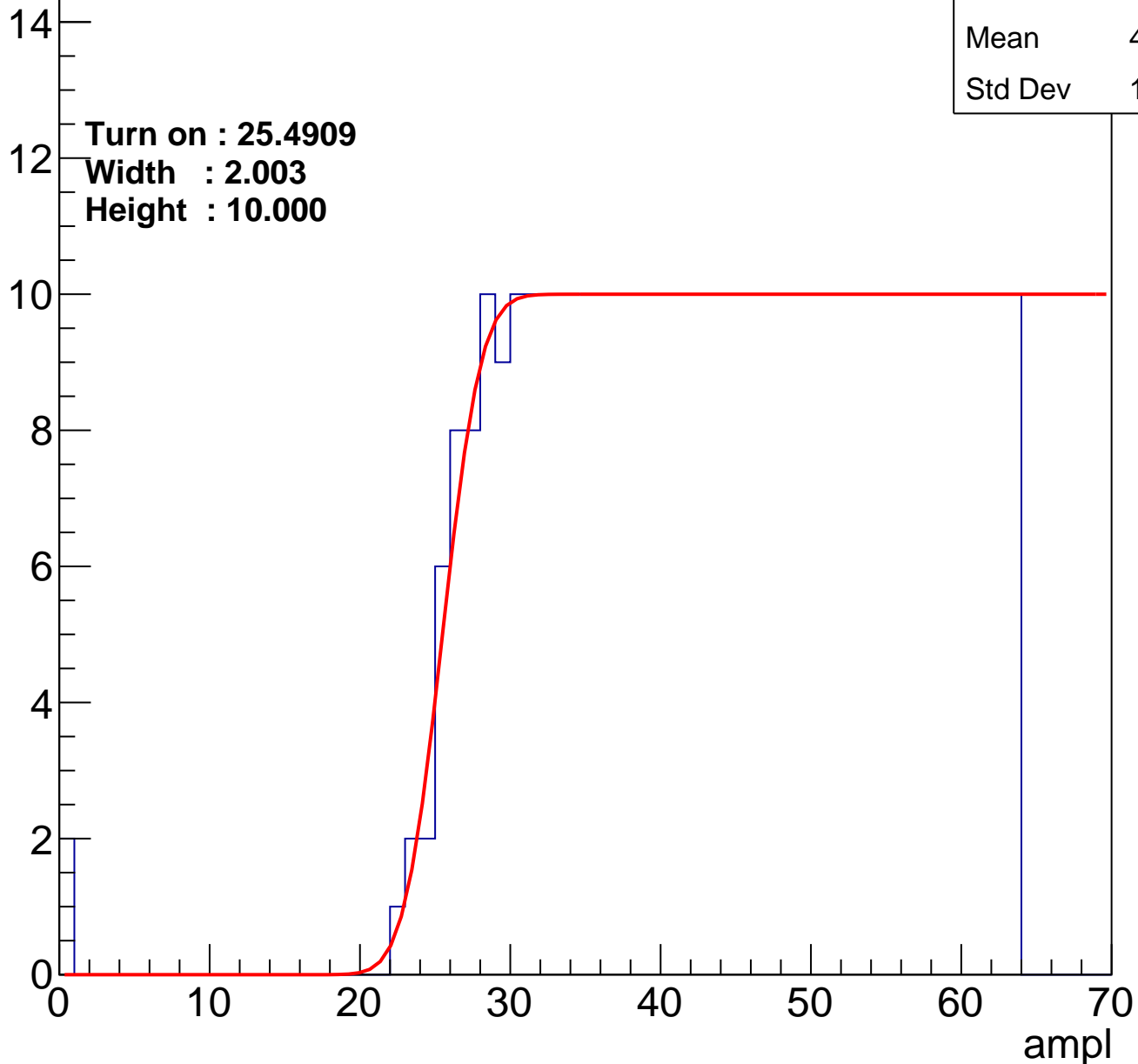
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.92 |
| Std Dev | 11.64 |

Turn on : 25.4909

Width : 2.003

Height : 10.000

Entry



B1L002S, U5-ch96

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.21 |
| Std Dev | 11.85 |

Turn on : 26.8167

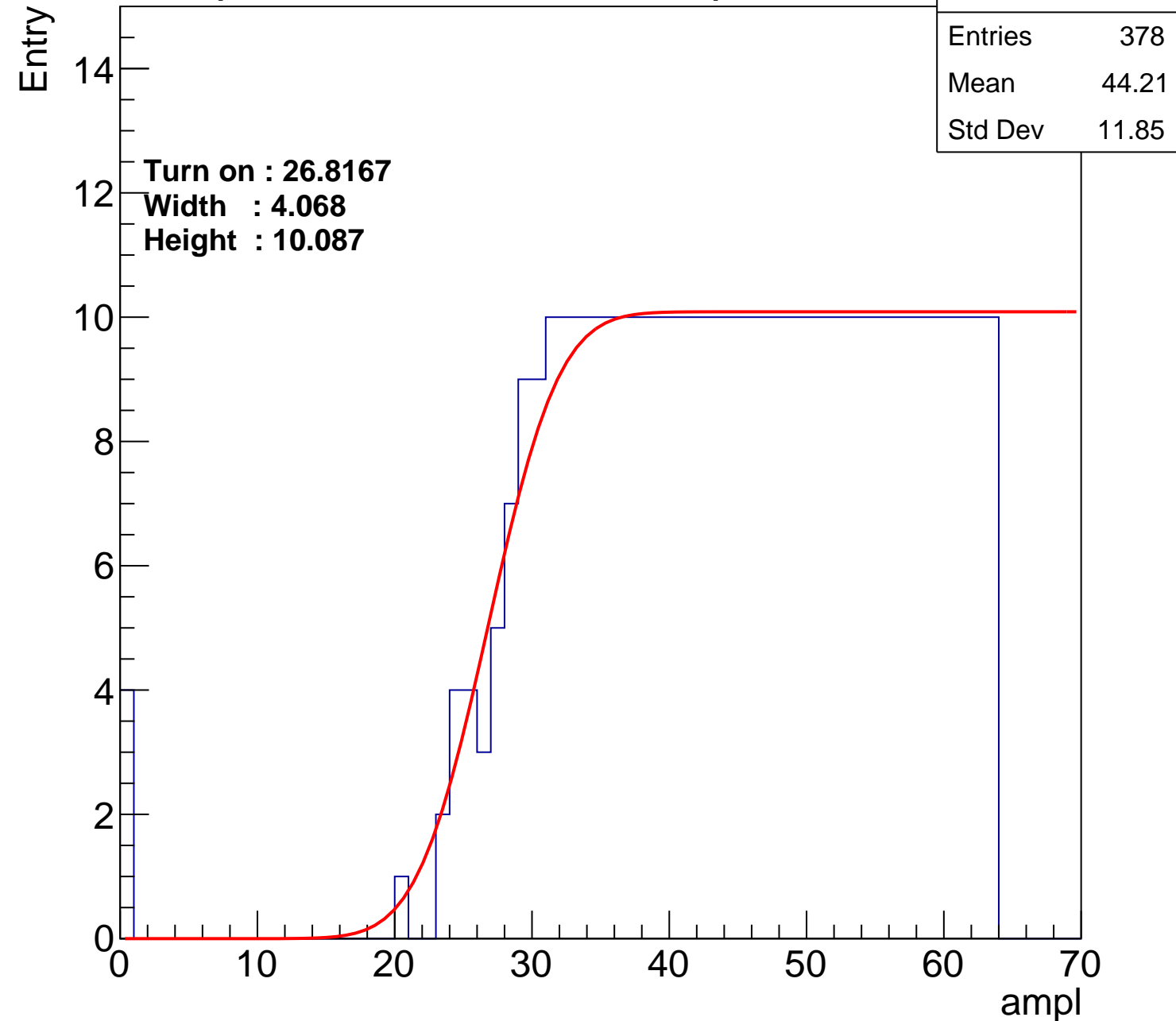
Width : 4.068

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch97

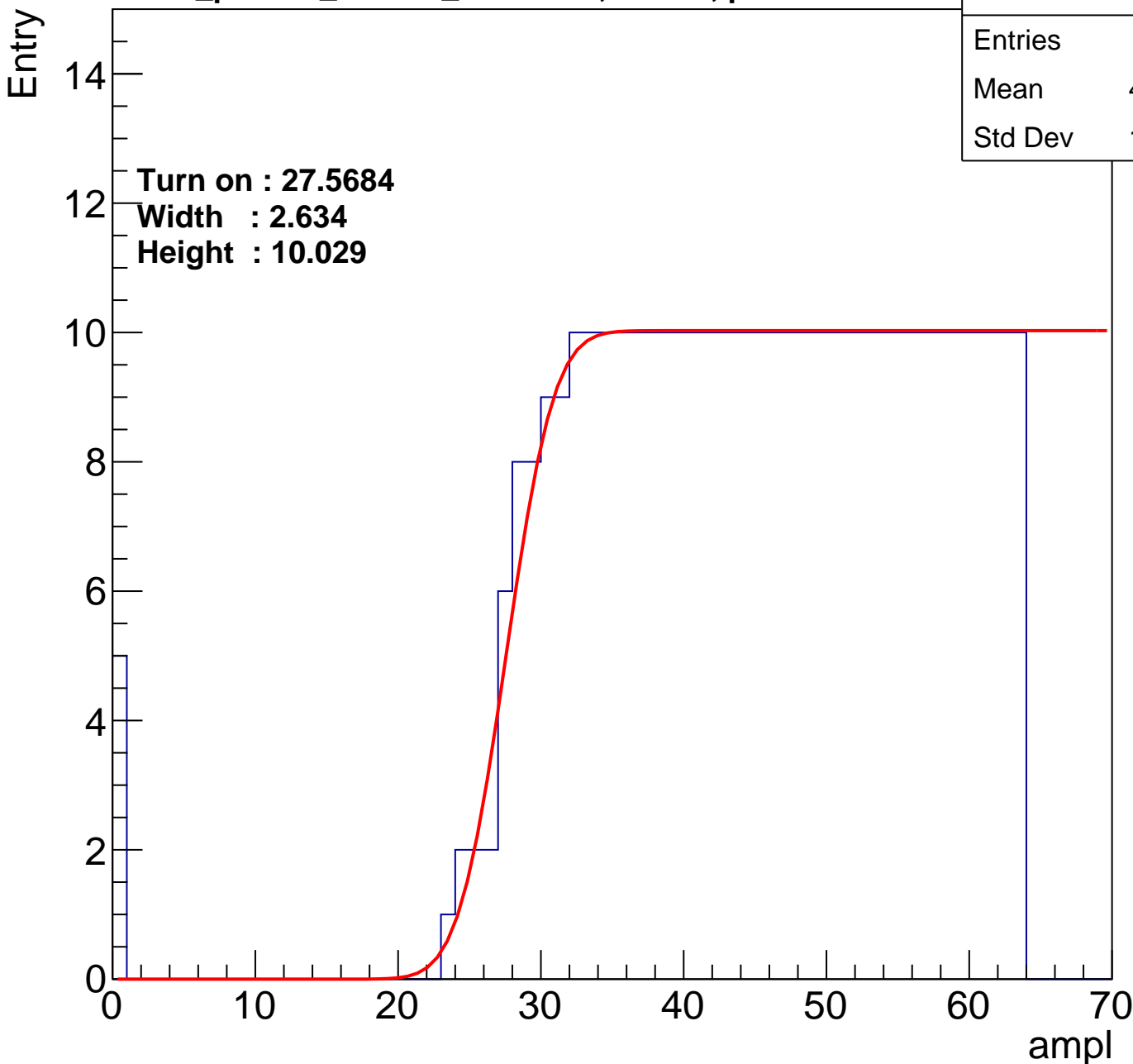
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.46 |
| Std Dev | 11.85 |

Turn on : 27.5684

Width : 2.634

Height : 10.029



B1L002S, U5-ch98

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.03 |
| Std Dev | 12.65 |

Turn on : 24.6492

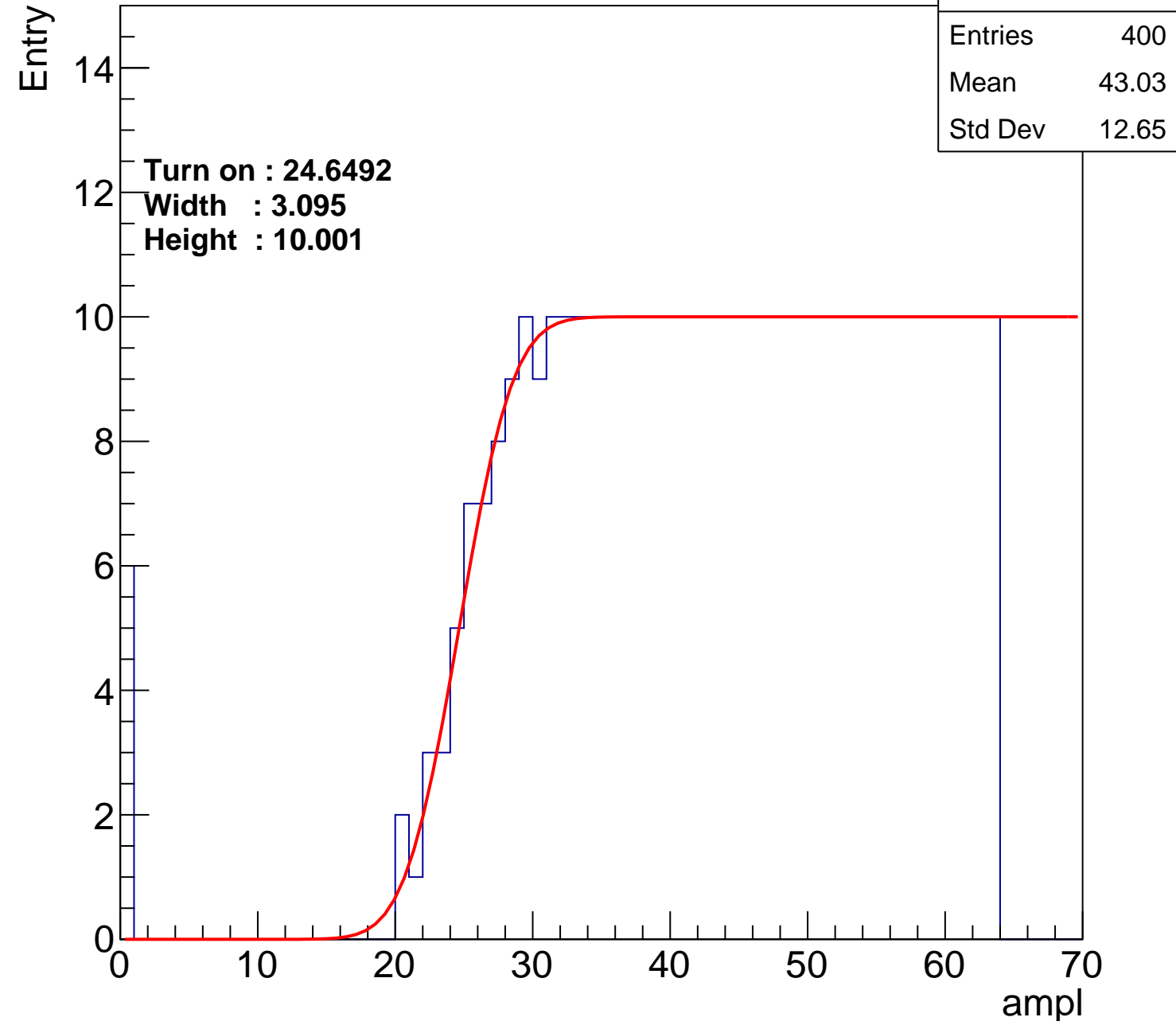
Width : 3.095

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch99

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.21 |
| Std Dev | 12.31 |

Turn on : 25.4481

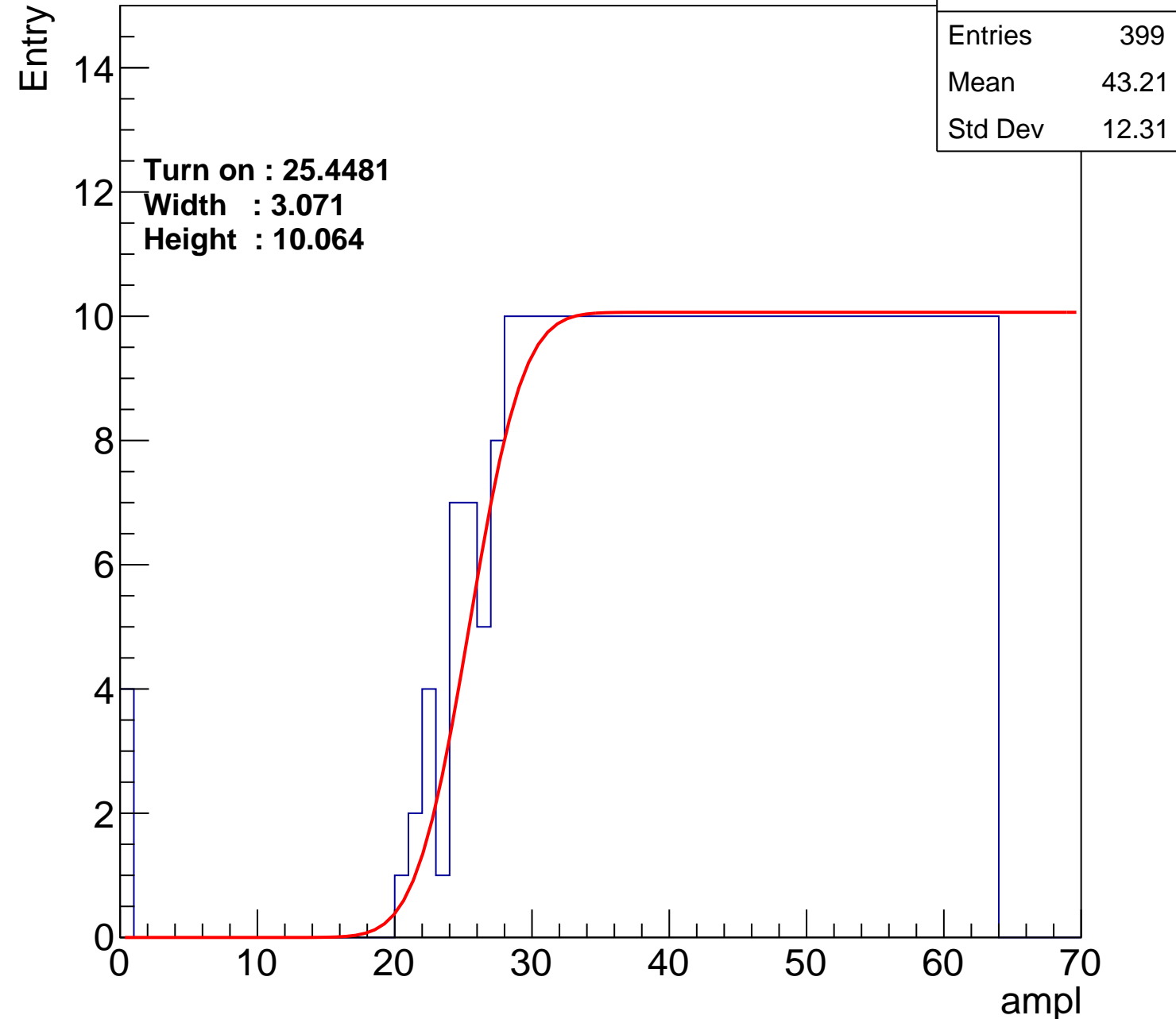
Width : 3.071

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch100

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.66 |
| Std Dev | 11.96 |

Turn on : 25.2823

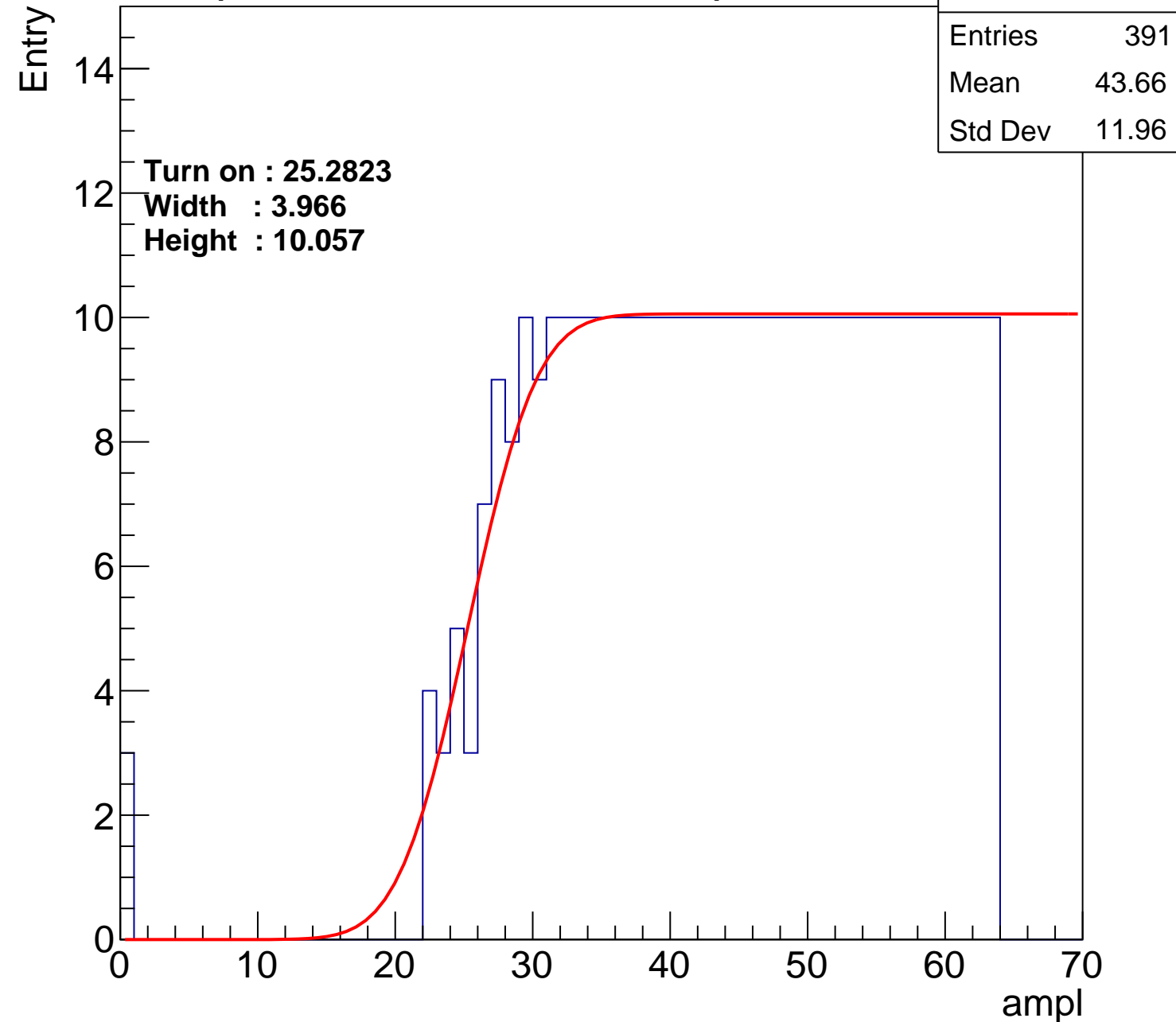
Width : 3.966

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch101

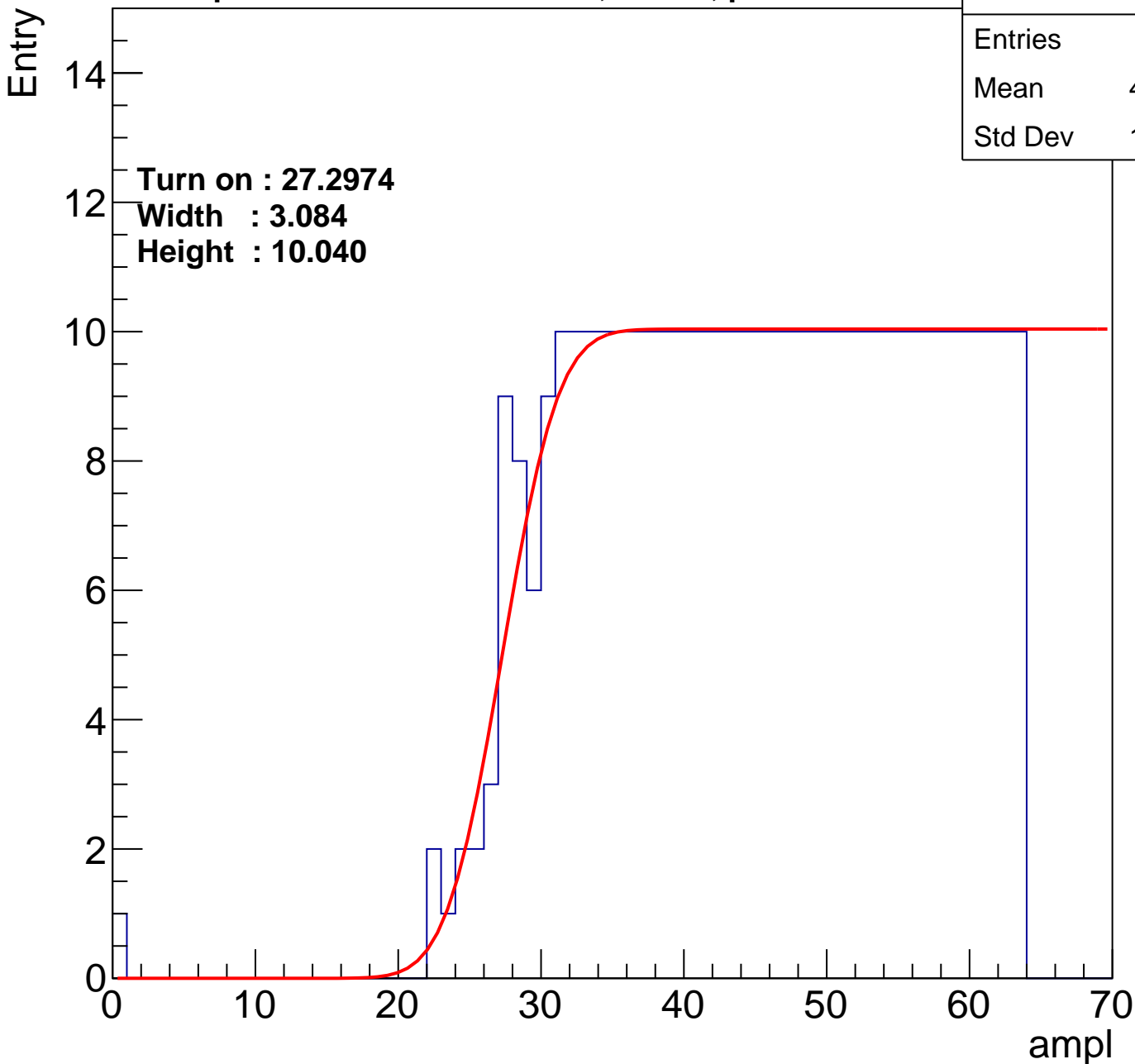
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.68 |
| Std Dev | 11.14 |

Turn on : 27.2974

Width : 3.084

Height : 10.040



B1L002S, U5-ch102

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|------|
| Entries | 380 |
| Mean | 44.2 |
| Std Dev | 11.7 |

Turn on : 26.4259

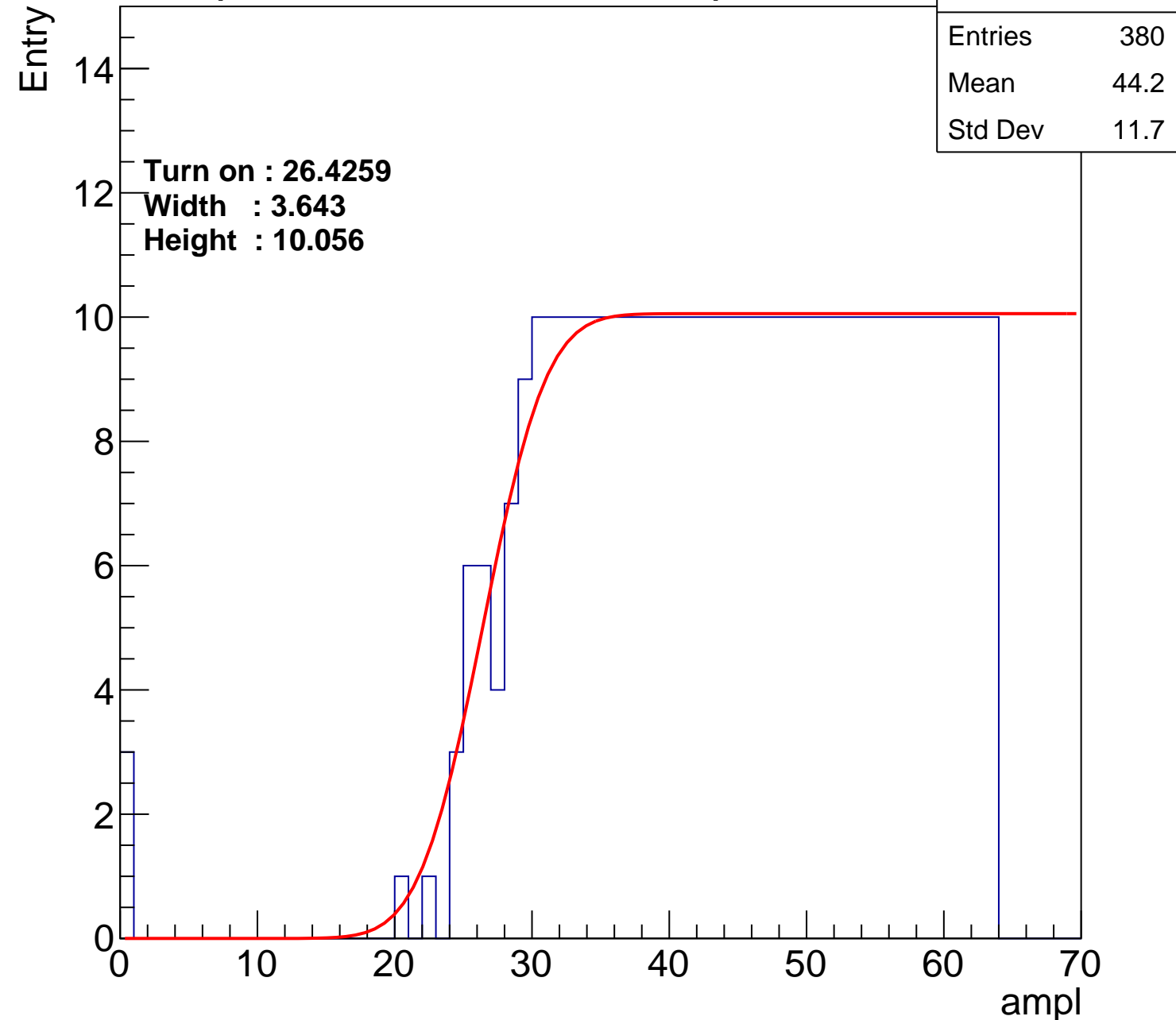
Width : 3.643

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch103

calib_packv5_042523_0143.root, FC#10, port B3

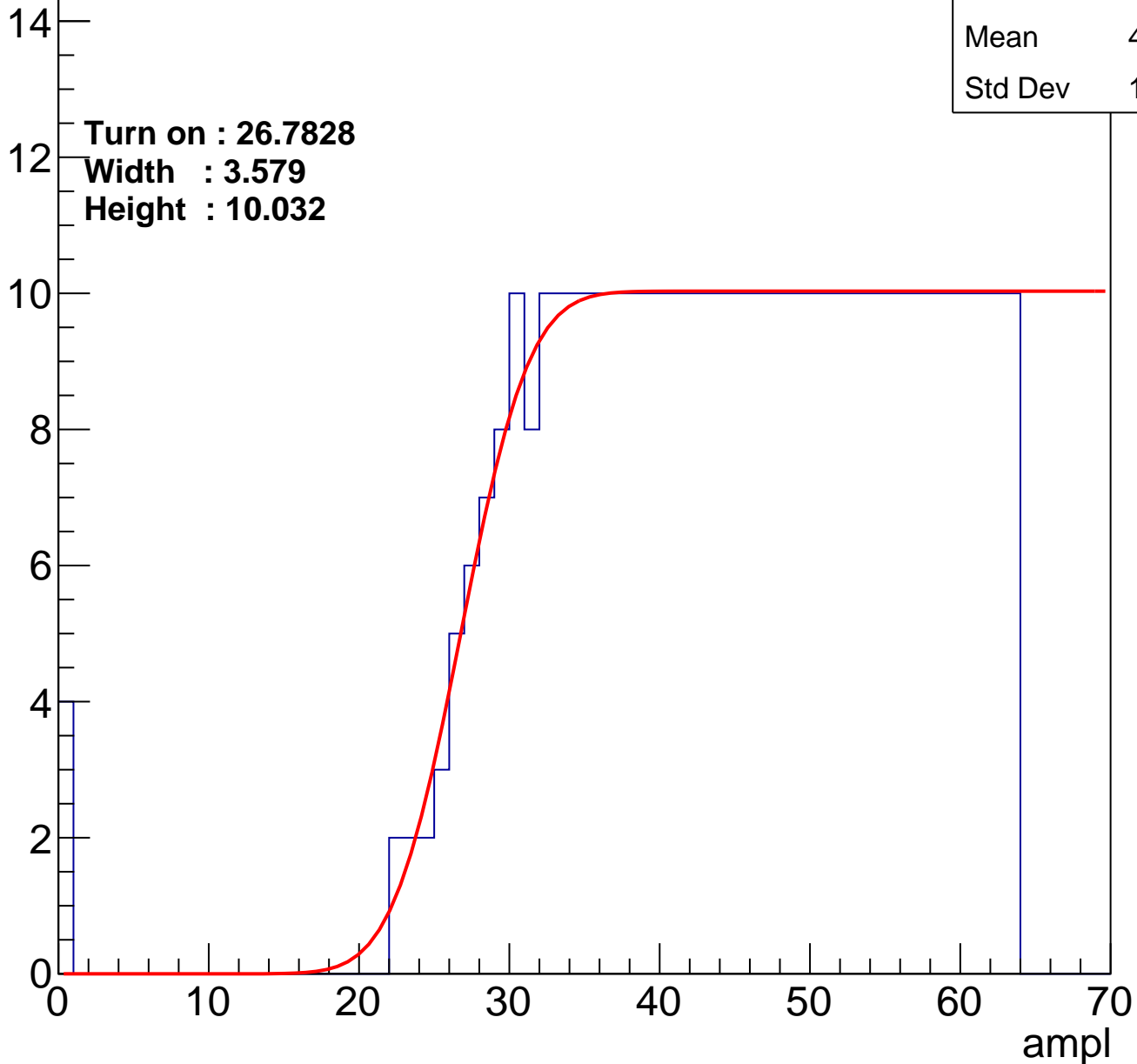
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.25 |
| Std Dev | 11.85 |

Turn on : 26.7828

Width : 3.579

Height : 10.032

Entry



B1L002S, U5-ch104

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.79 |
| Std Dev | 11.74 |

Turn on : 25.5631

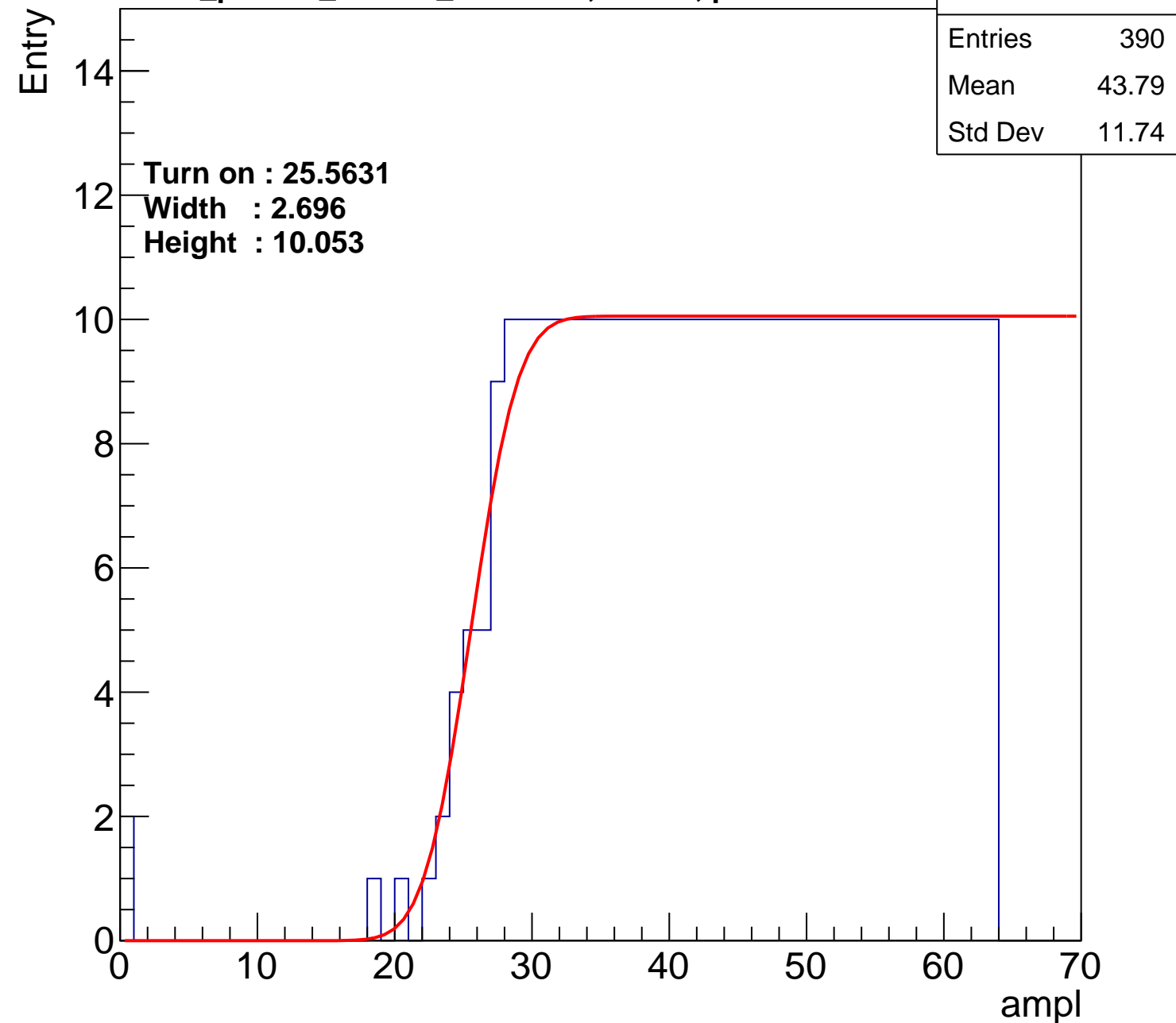
Width : 2.696

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch105

calib_packv5_042523_0143.root, FC#10, port B3

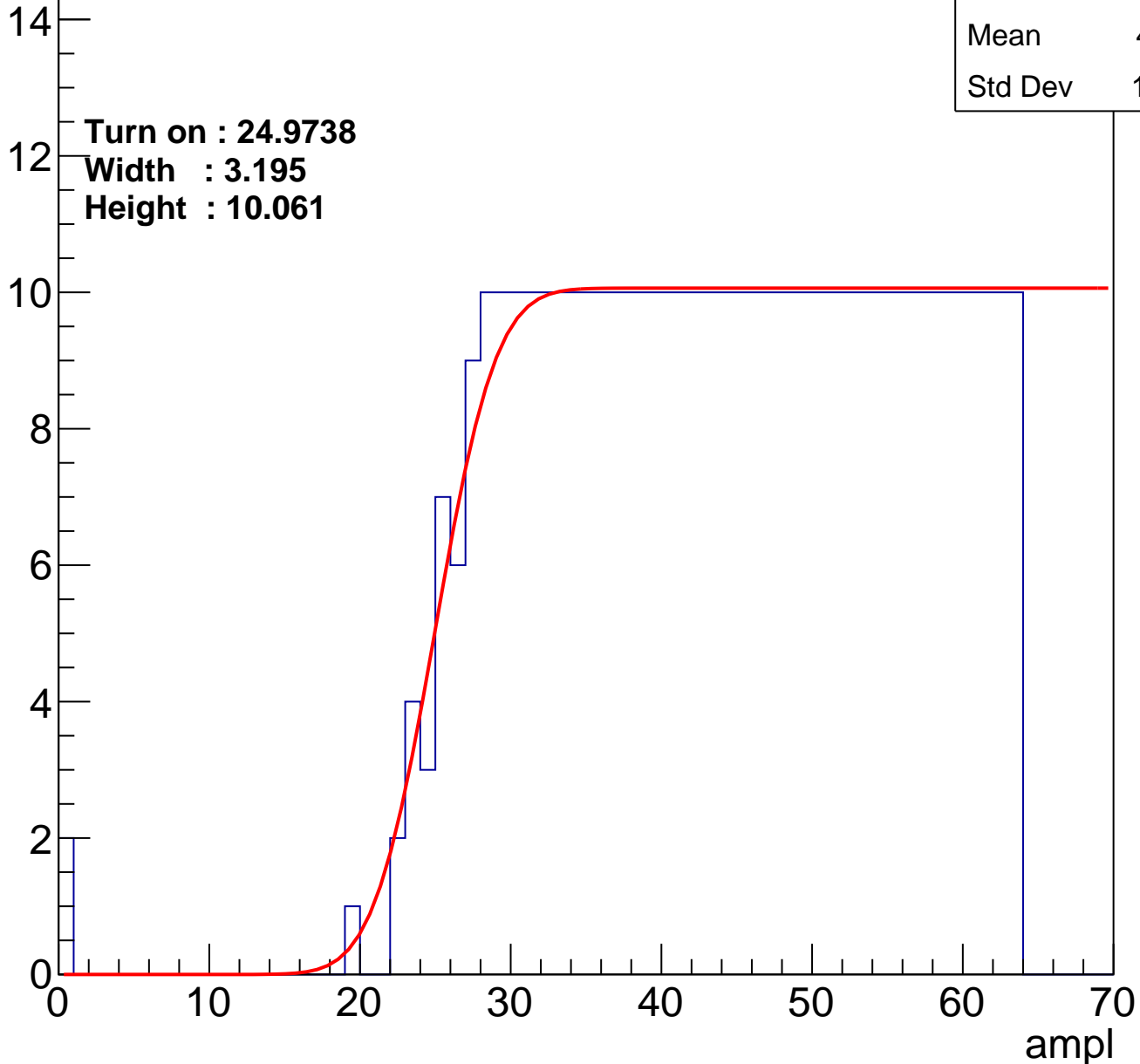
| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.61 |
| Std Dev | 11.83 |

Turn on : 24.9738

Width : 3.195

Height : 10.061

Entry



B1L002S, U5-ch106

calib_packv5_042523_0143.root, FC#10, port B3

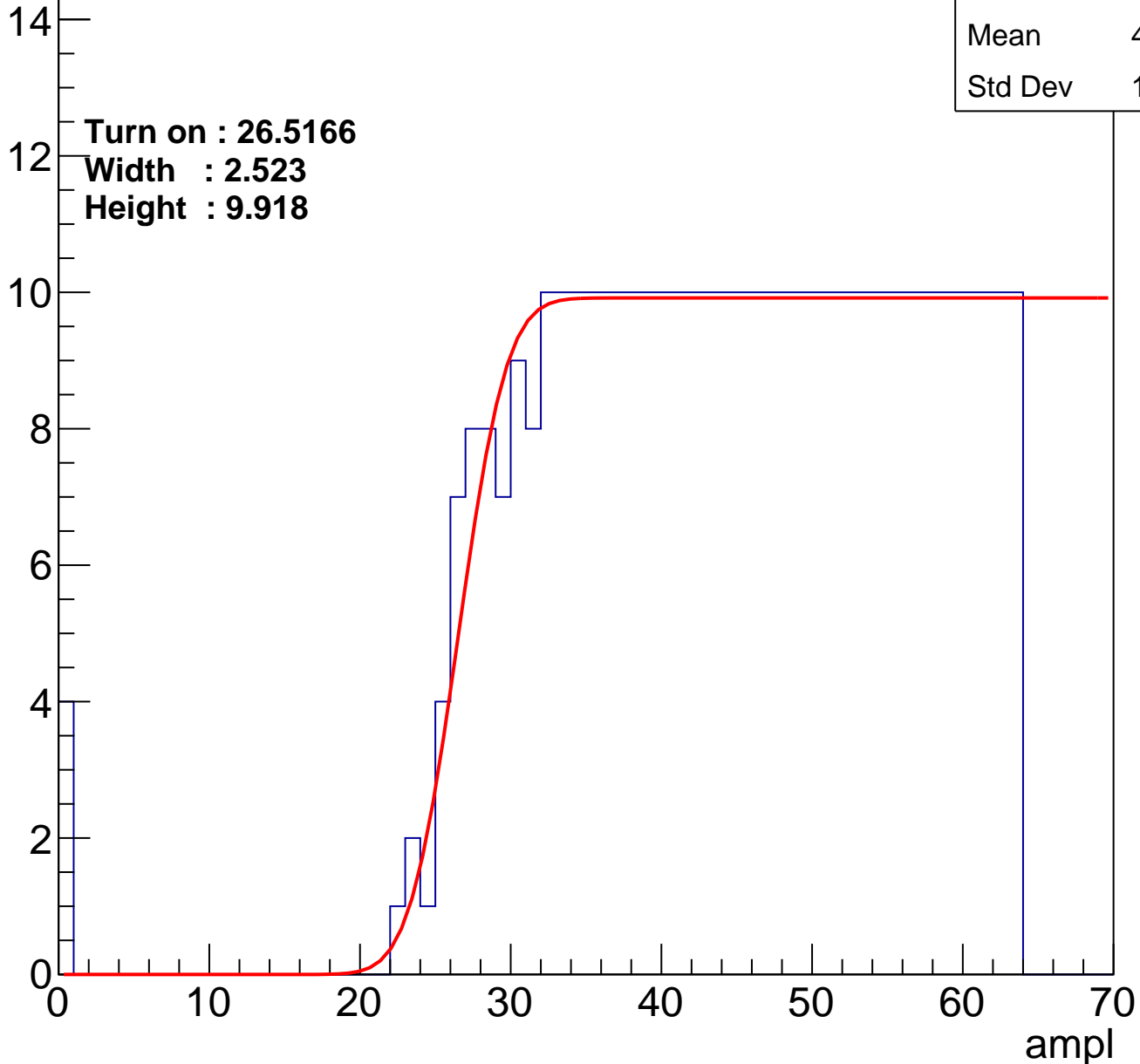
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.16 |
| Std Dev | 11.88 |

Turn on : 26.5166

Width : 2.523

Height : 9.918

Entry



B1L002S, U5-ch107

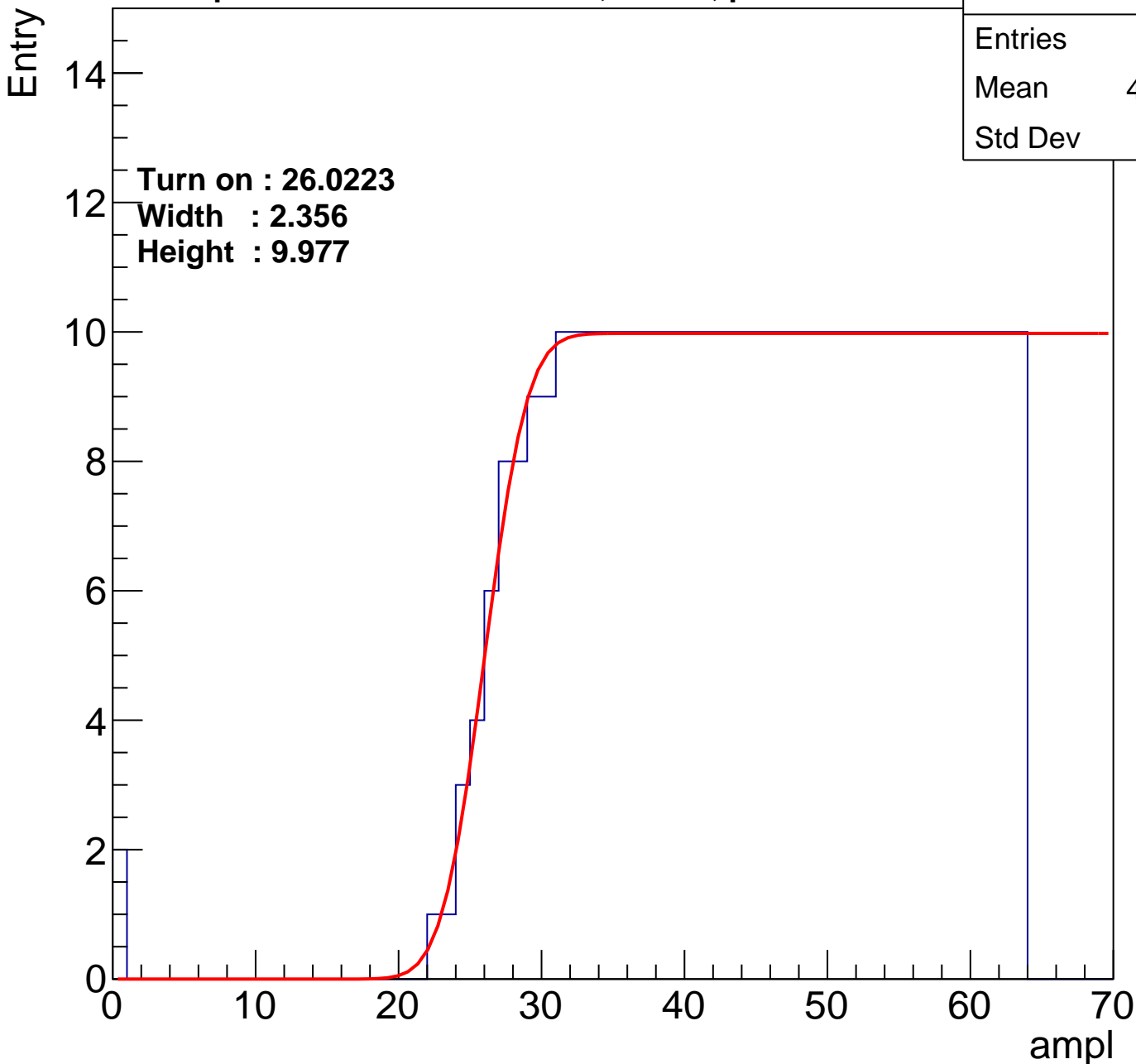
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.24 |
| Std Dev | 11.5 |

Turn on : 26.0223

Width : 2.356

Height : 9.977



B1L002S, U5-ch108

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.48 |
| Std Dev | 11.37 |

Turn on : 26.7585

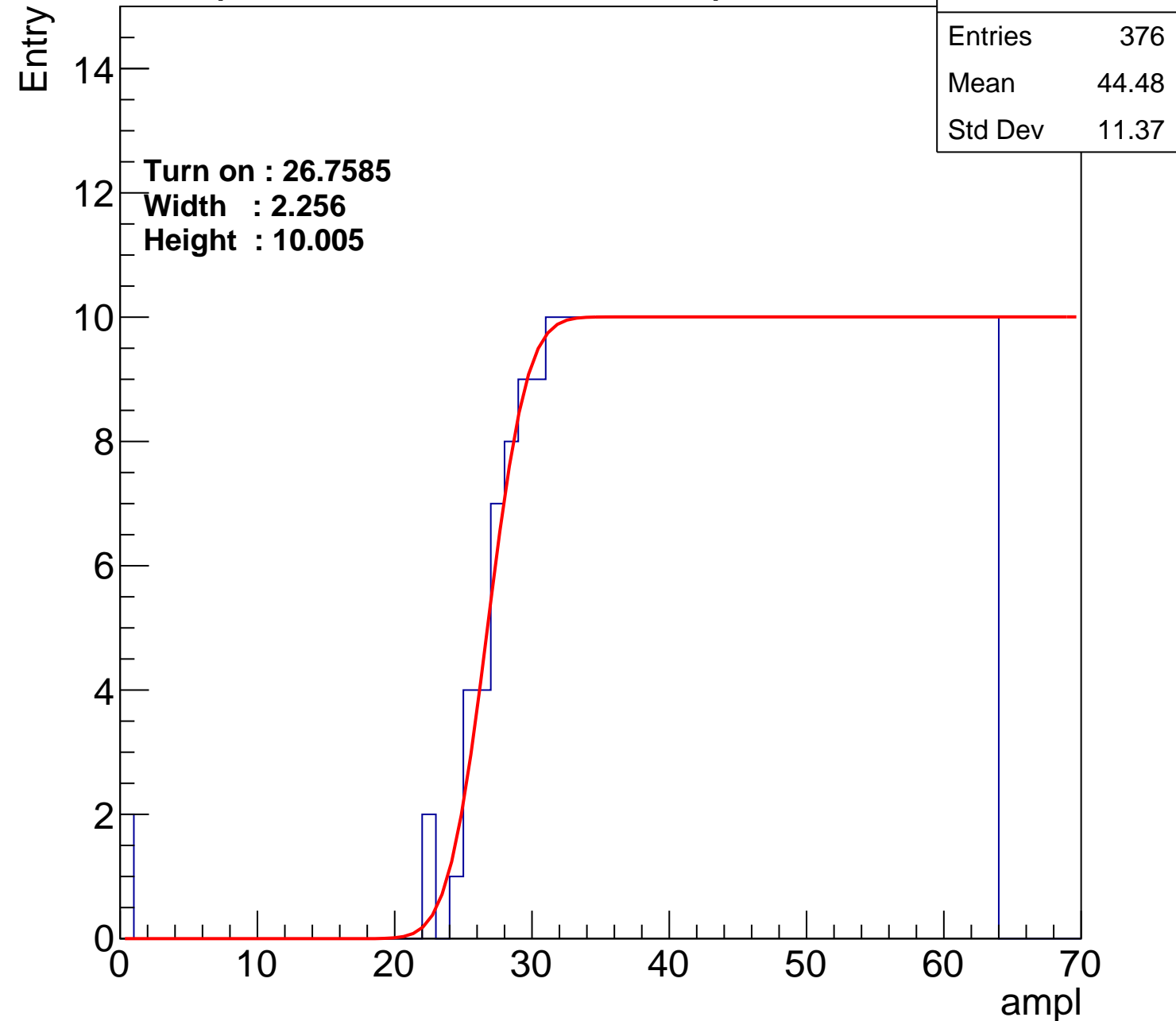
Width : 2.256

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch109

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.11 |
| Std Dev | 11.74 |

Turn on : 26.7604

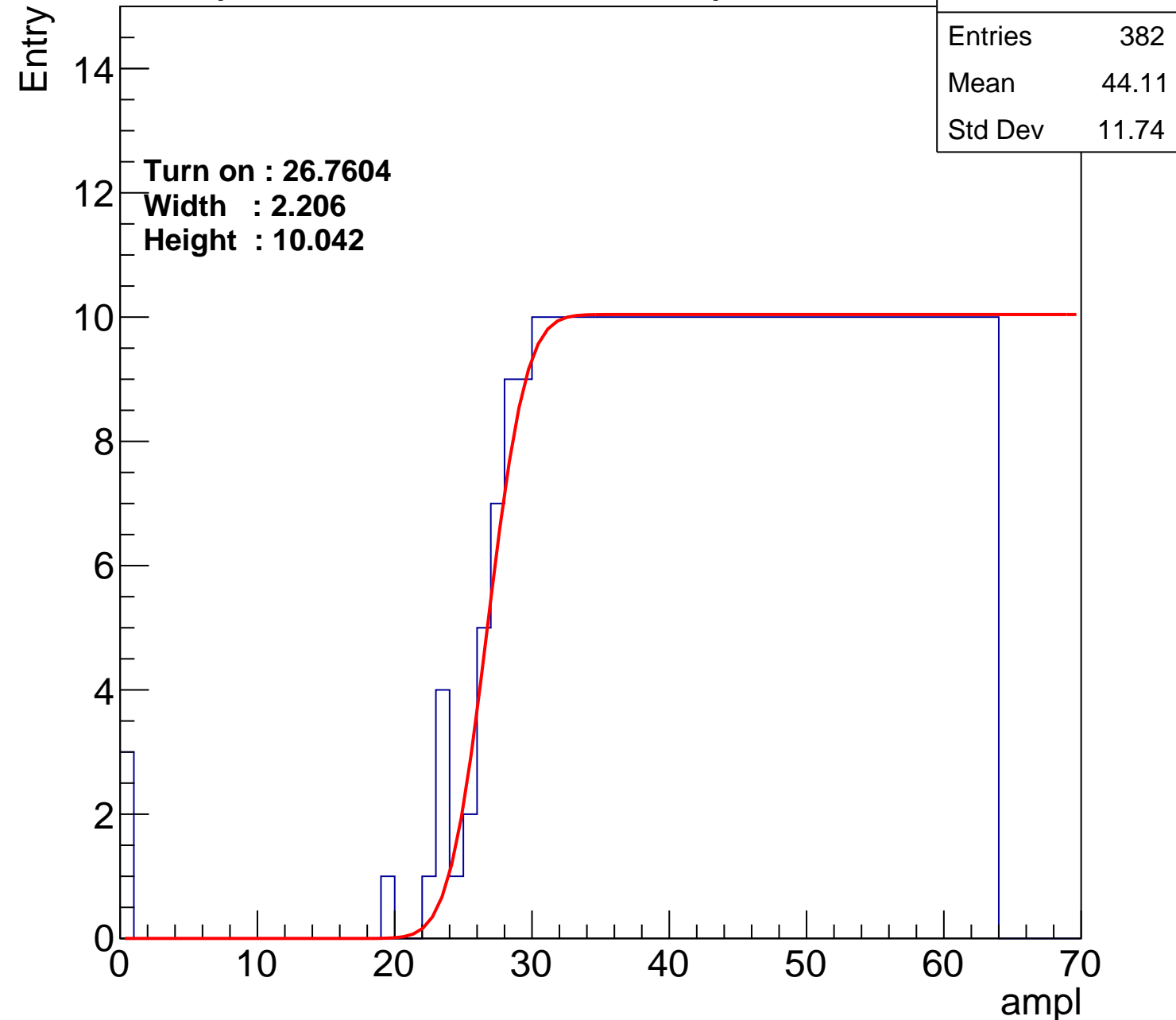
Width : 2.206

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch110

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 400 |
| Mean | 42.94 |
| Std Dev | 12.89 |

Turn on : 24.7860

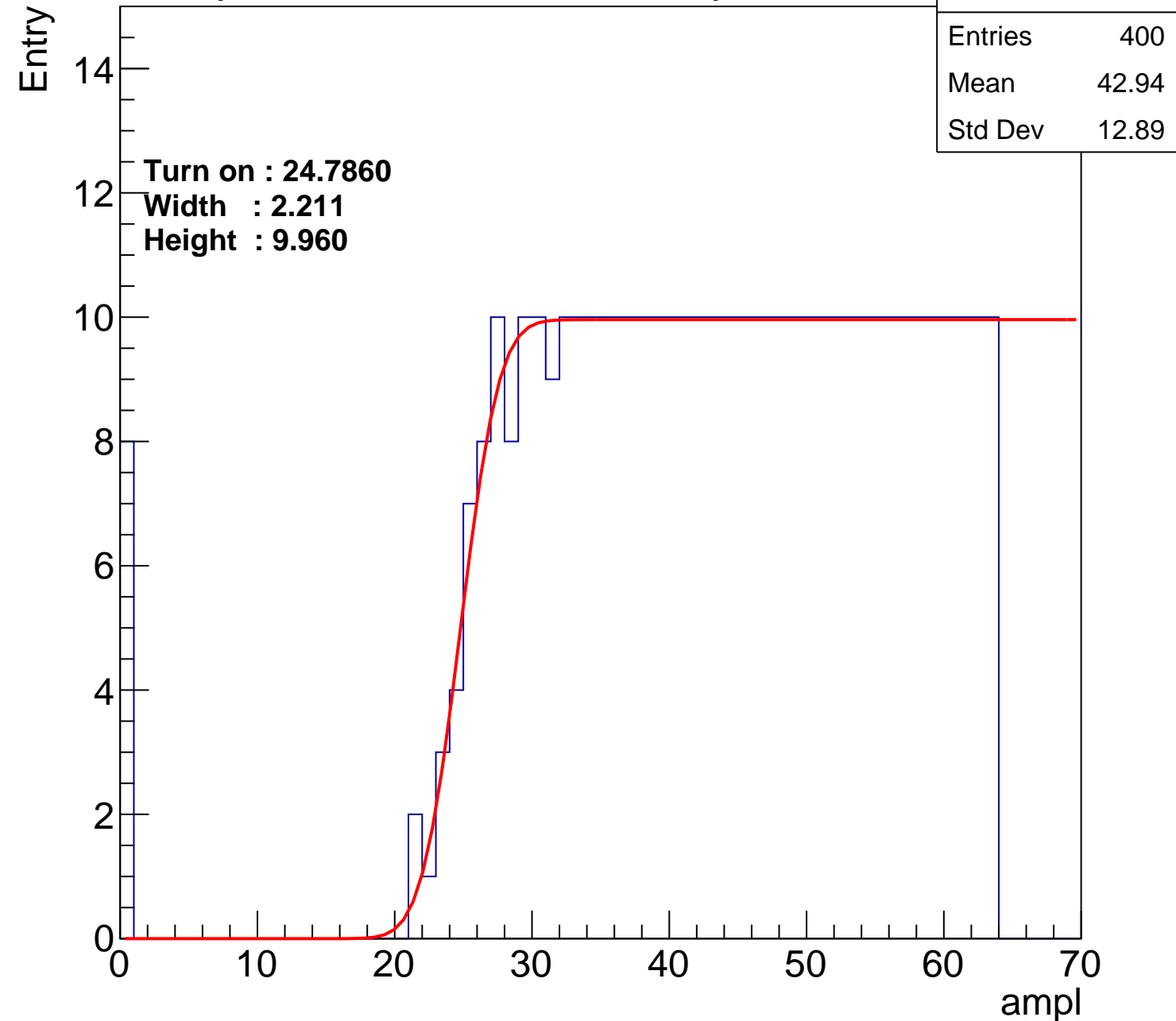
Width : 2.211

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch111

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 411 |
| Mean | 42.61 |
| Std Dev | 12.62 |

Turn on : 23.7595

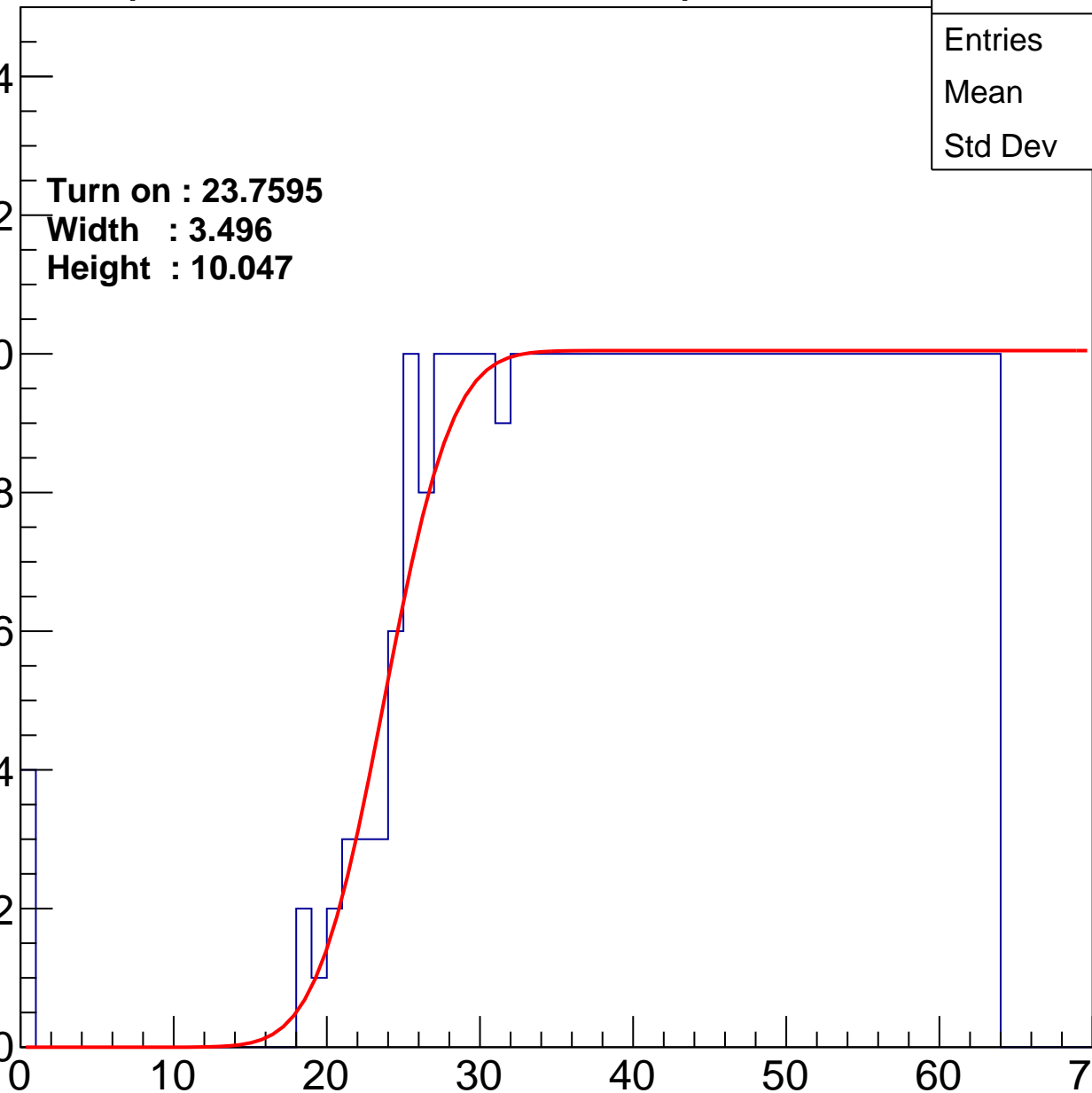
Width : 3.496

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch112

calib_packv5_042523_0143.root, FC#10, port B3

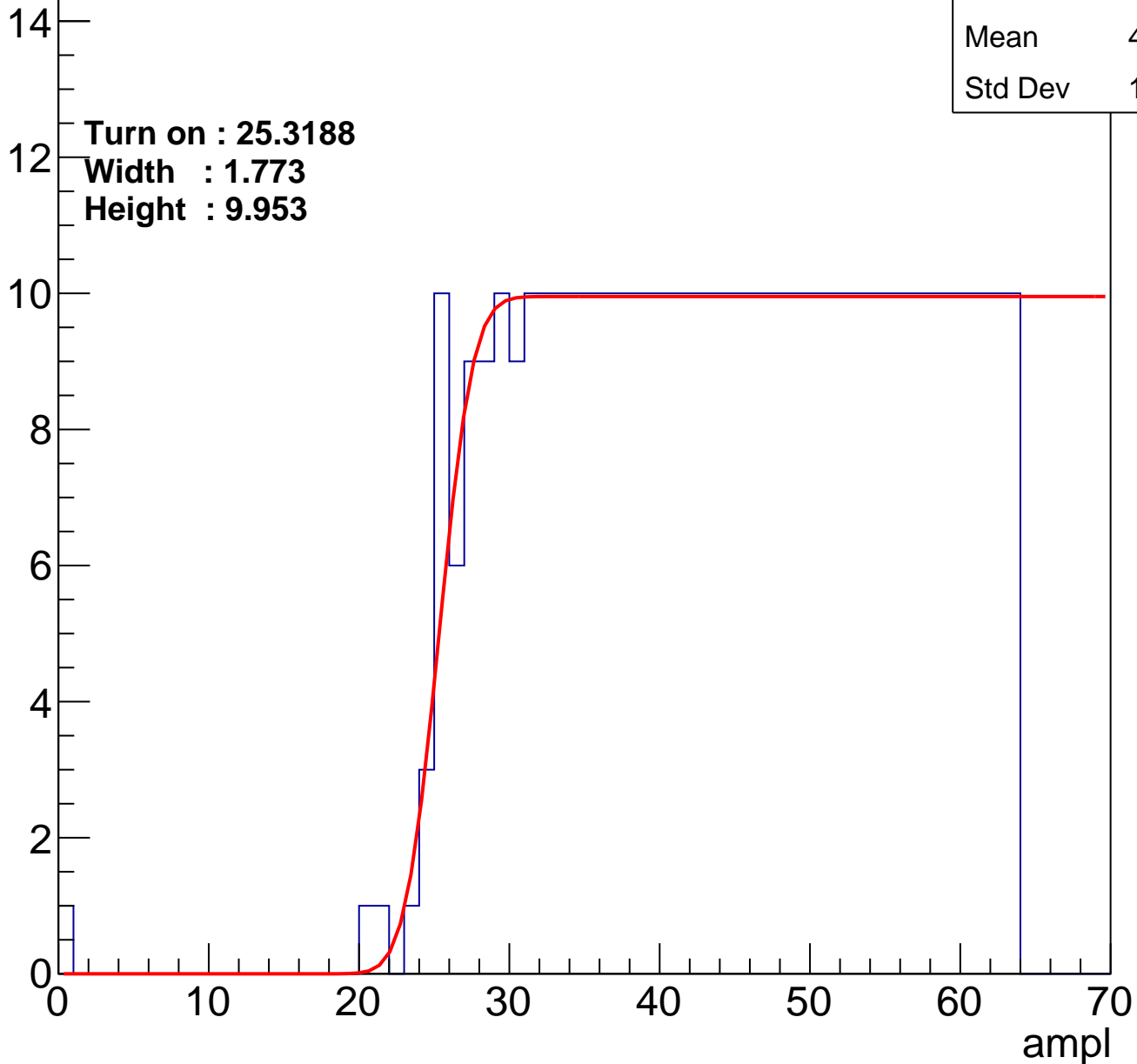
| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.86 |
| Std Dev | 11.55 |

Turn on : 25.3188

Width : 1.773

Height : 9.953

Entry



B1L002S, U5-ch113

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.36 |
| Std Dev | 12.02 |

Turn on : 24.9126

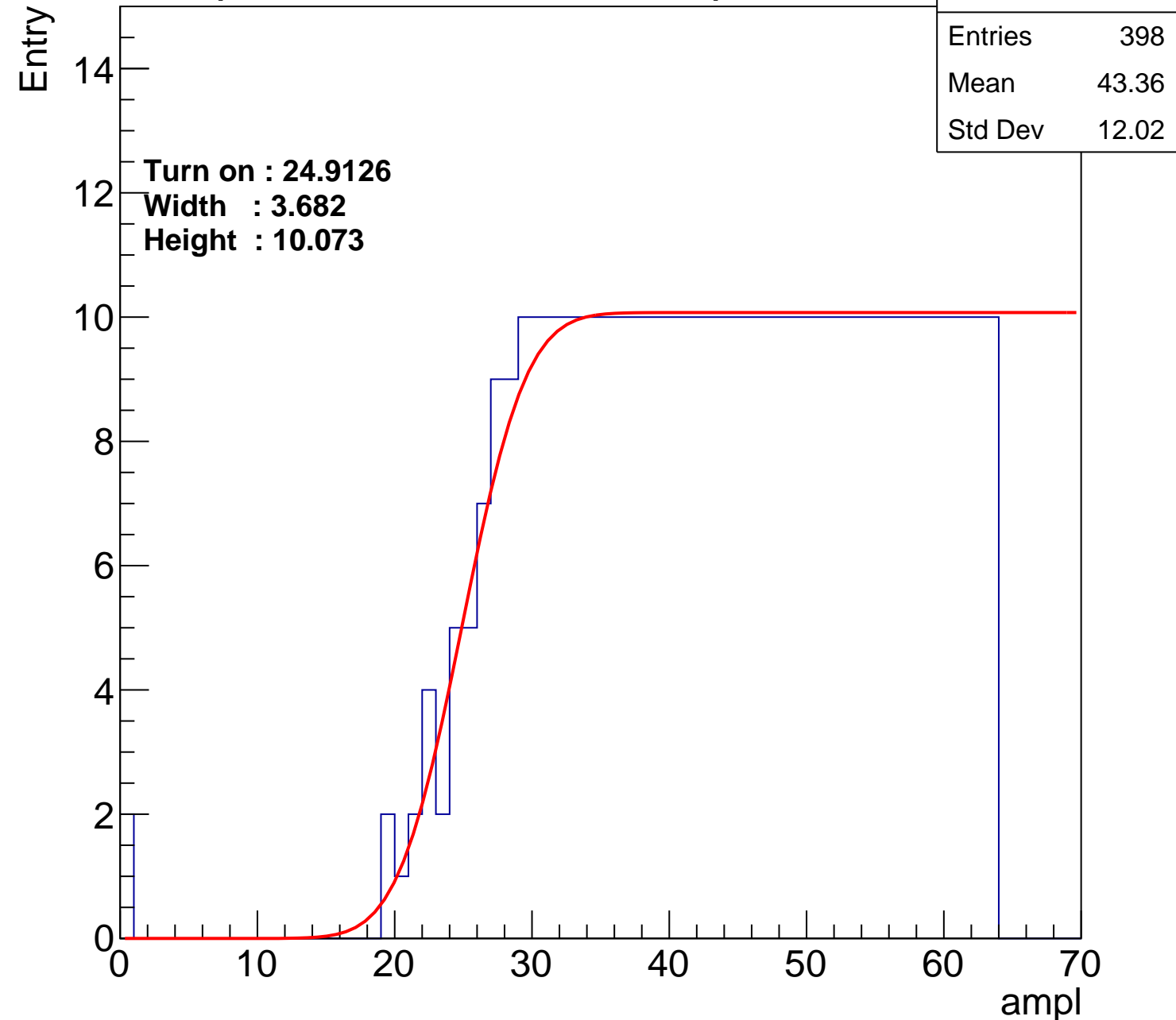
Width : 3.682

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch114

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.24 |
| Std Dev | 11.37 |

Turn on : 26.0384

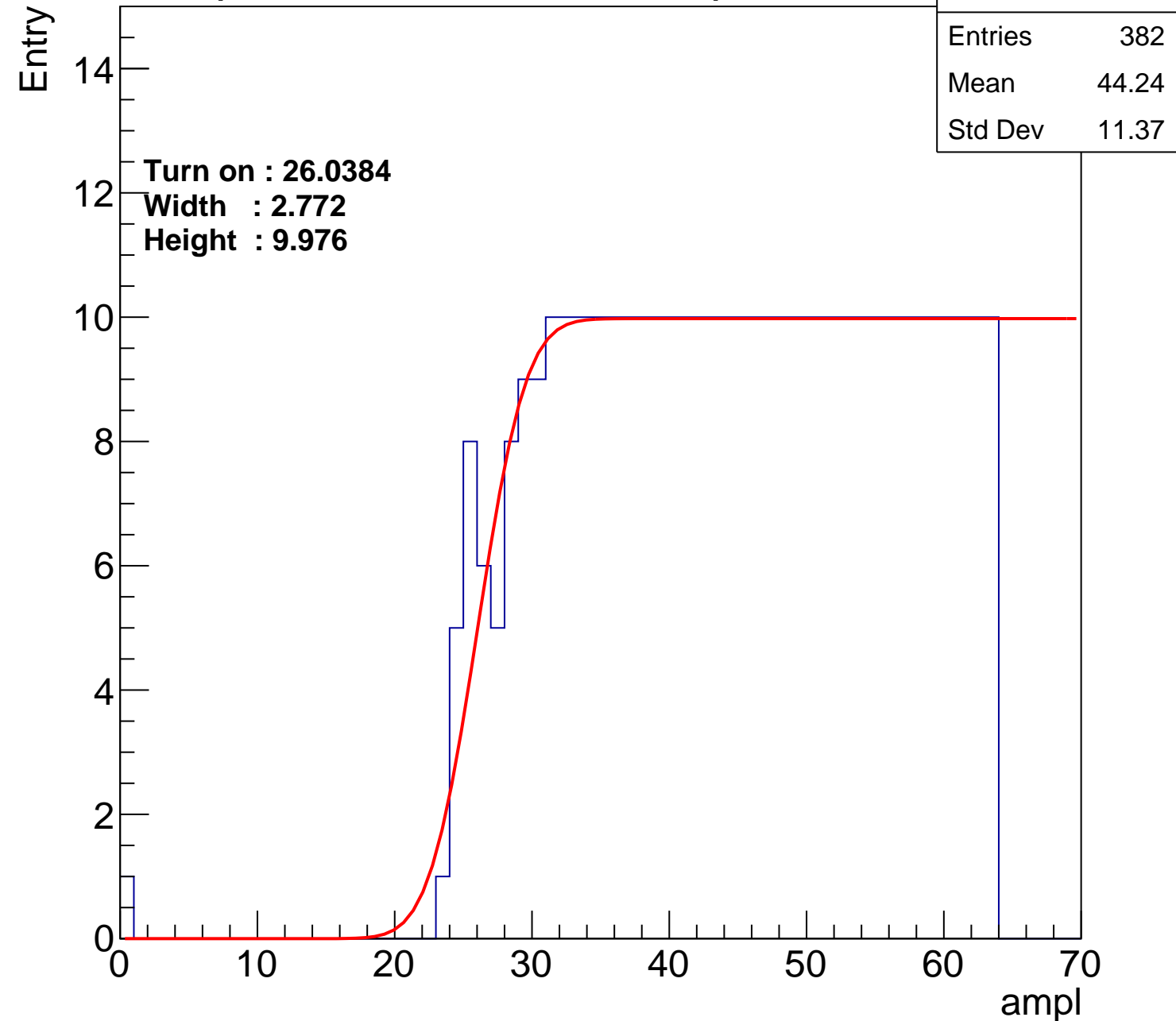
Width : 2.772

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch115

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.76 |
| Std Dev | 11.74 |

Turn on : 25.7615

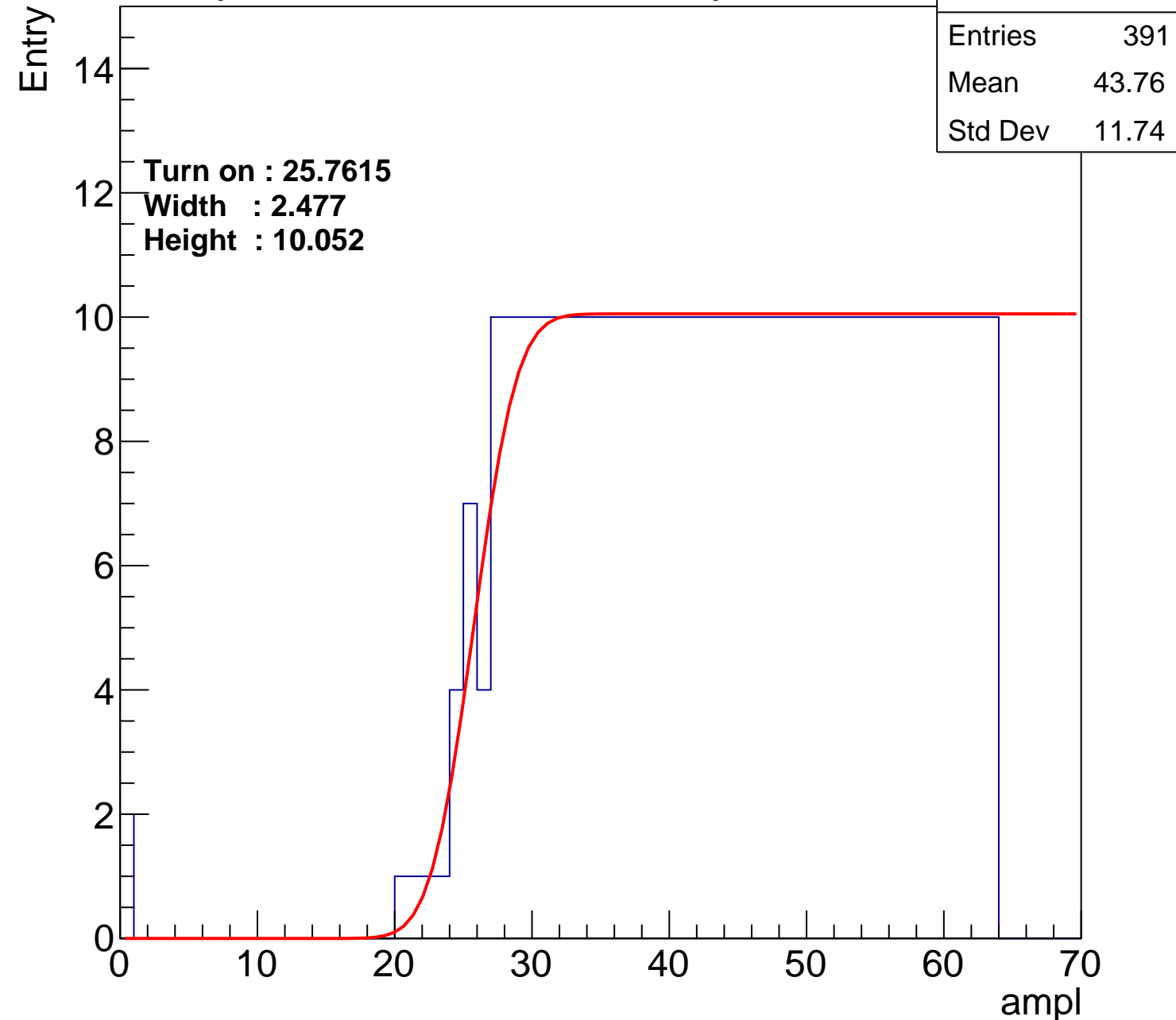
Width : 2.477

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch116

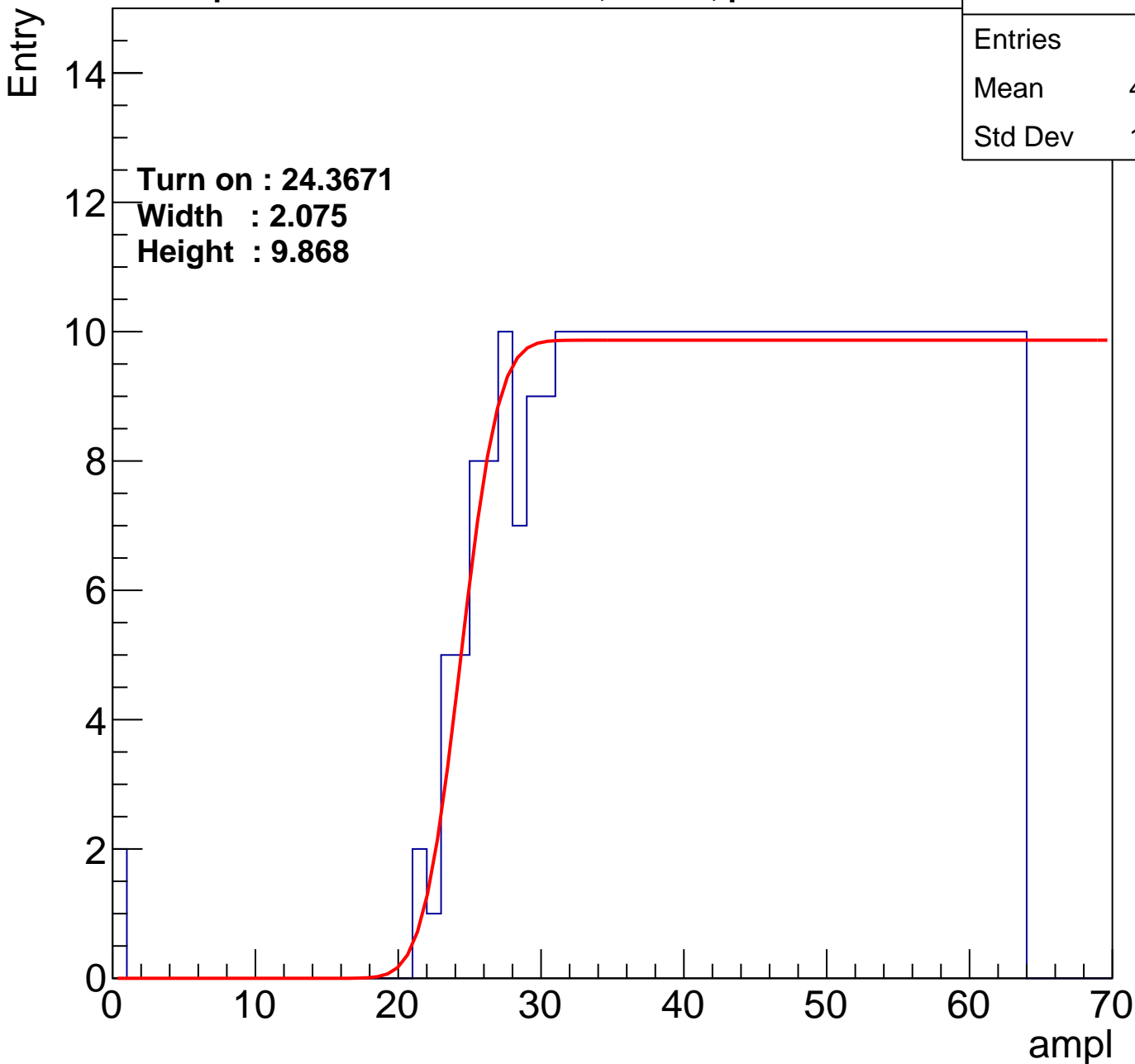
calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.47 |
| Std Dev | 11.93 |

Turn on : 24.3671

Width : 2.075

Height : 9.868



B1L002S, U5-ch117

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.72 |
| Std Dev | 11.26 |

Turn on : 27.2795

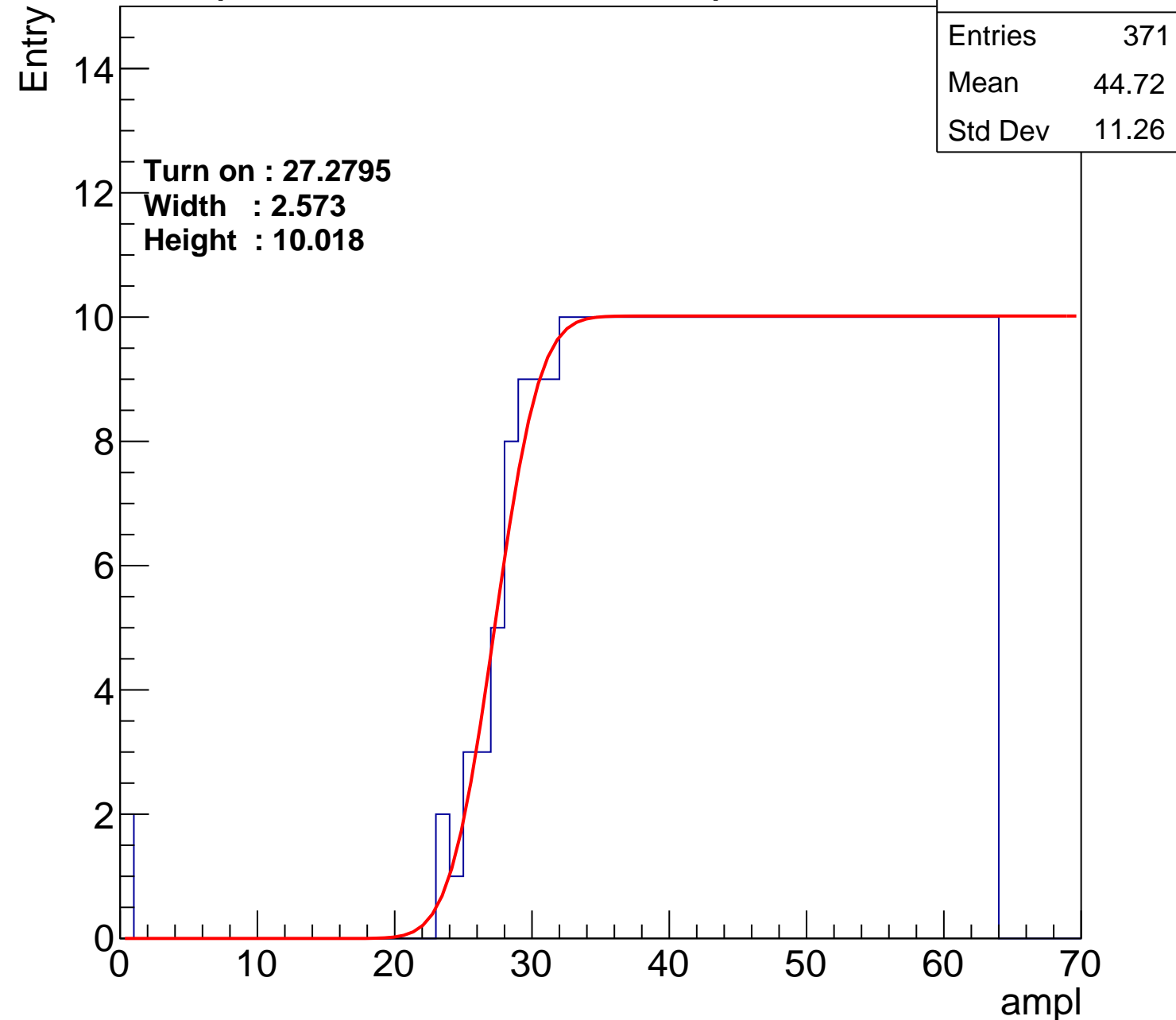
Width : 2.573

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch118

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.97 |
| Std Dev | 11.68 |

Turn on : 25.9280

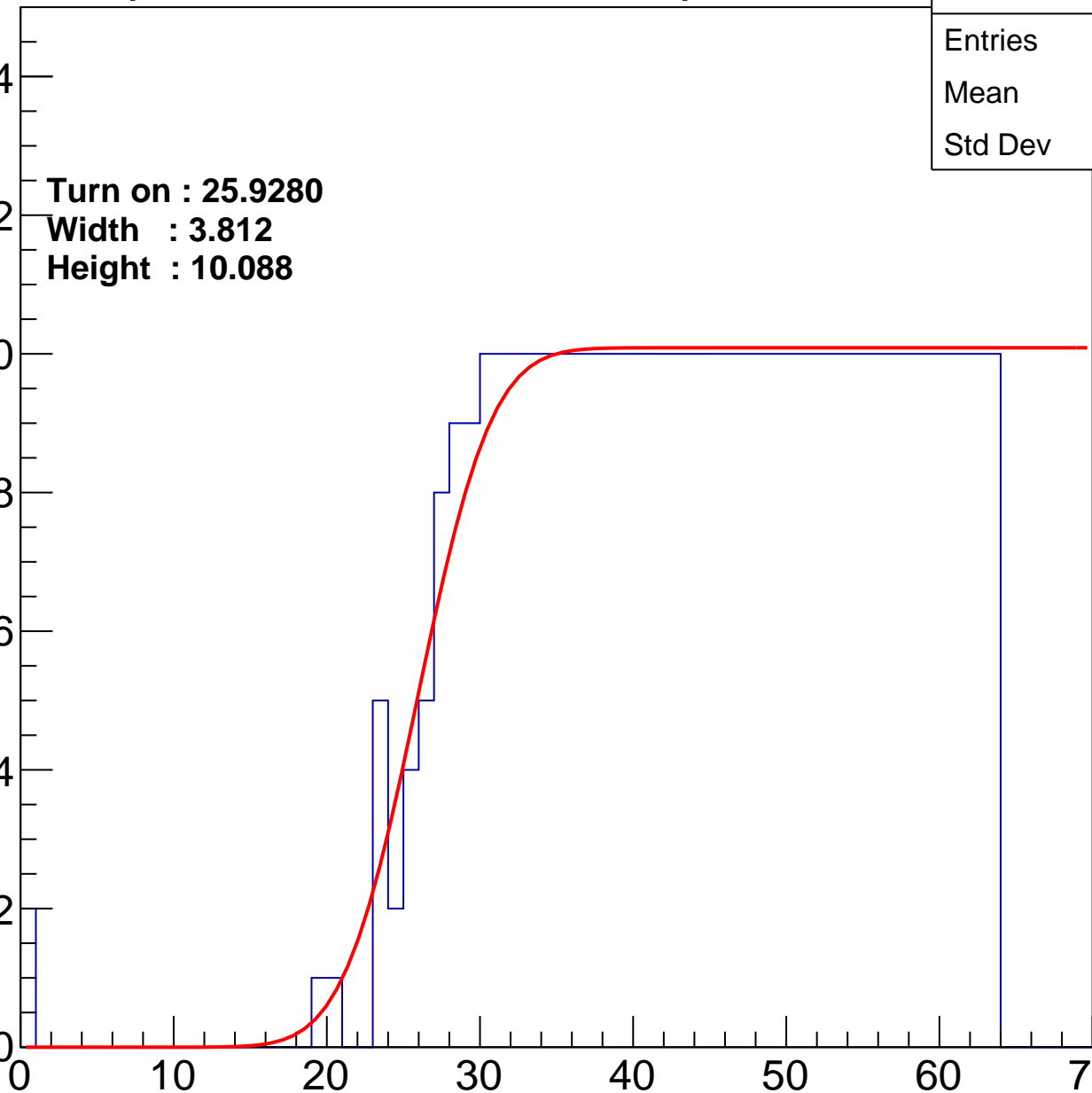
Width : 3.812

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch119

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.78 |
| Std Dev | 11.21 |

Turn on : 27.0508

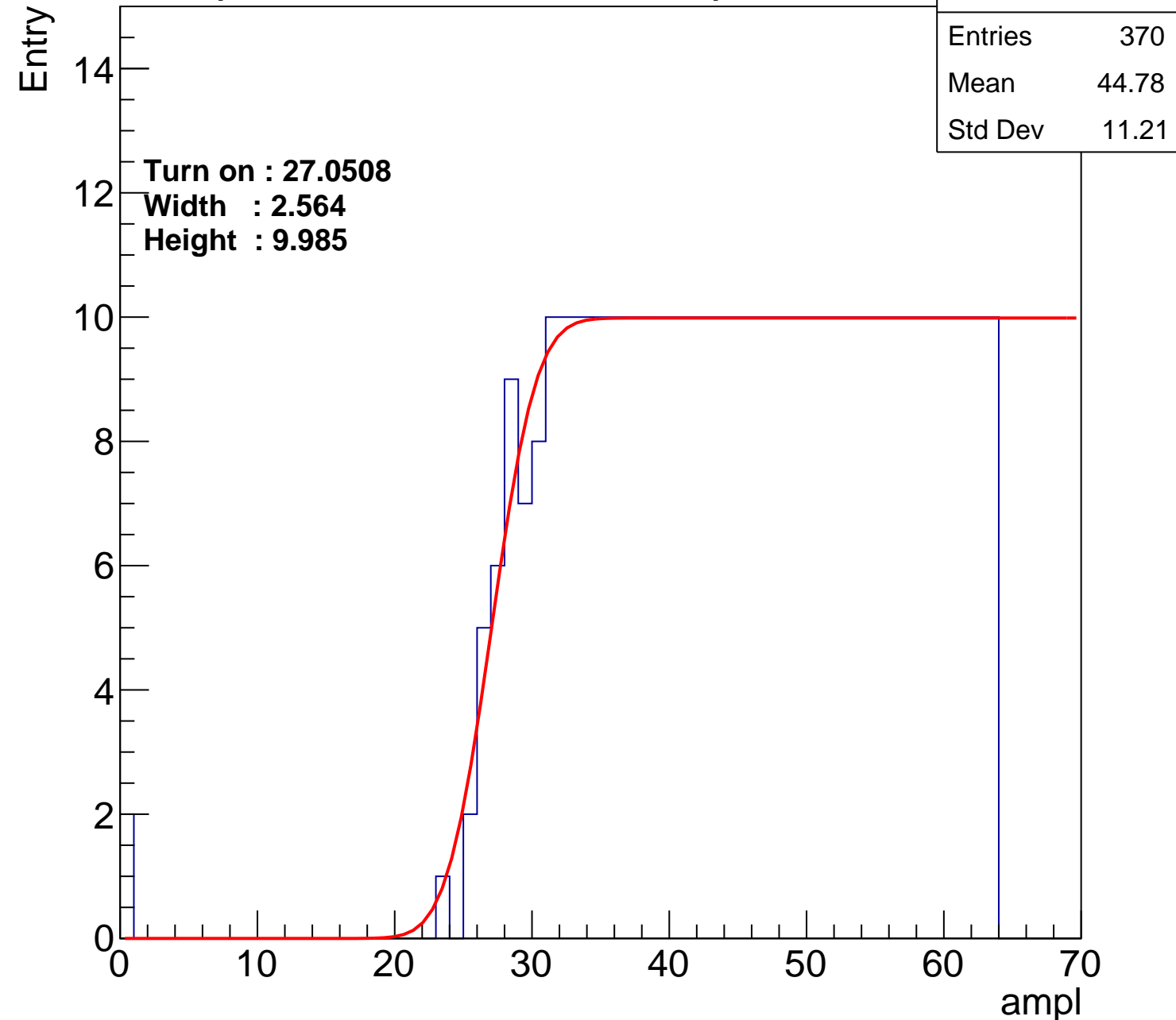
Width : 2.564

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch120

calib_packv5_042523_0143.root, FC#10, port B3

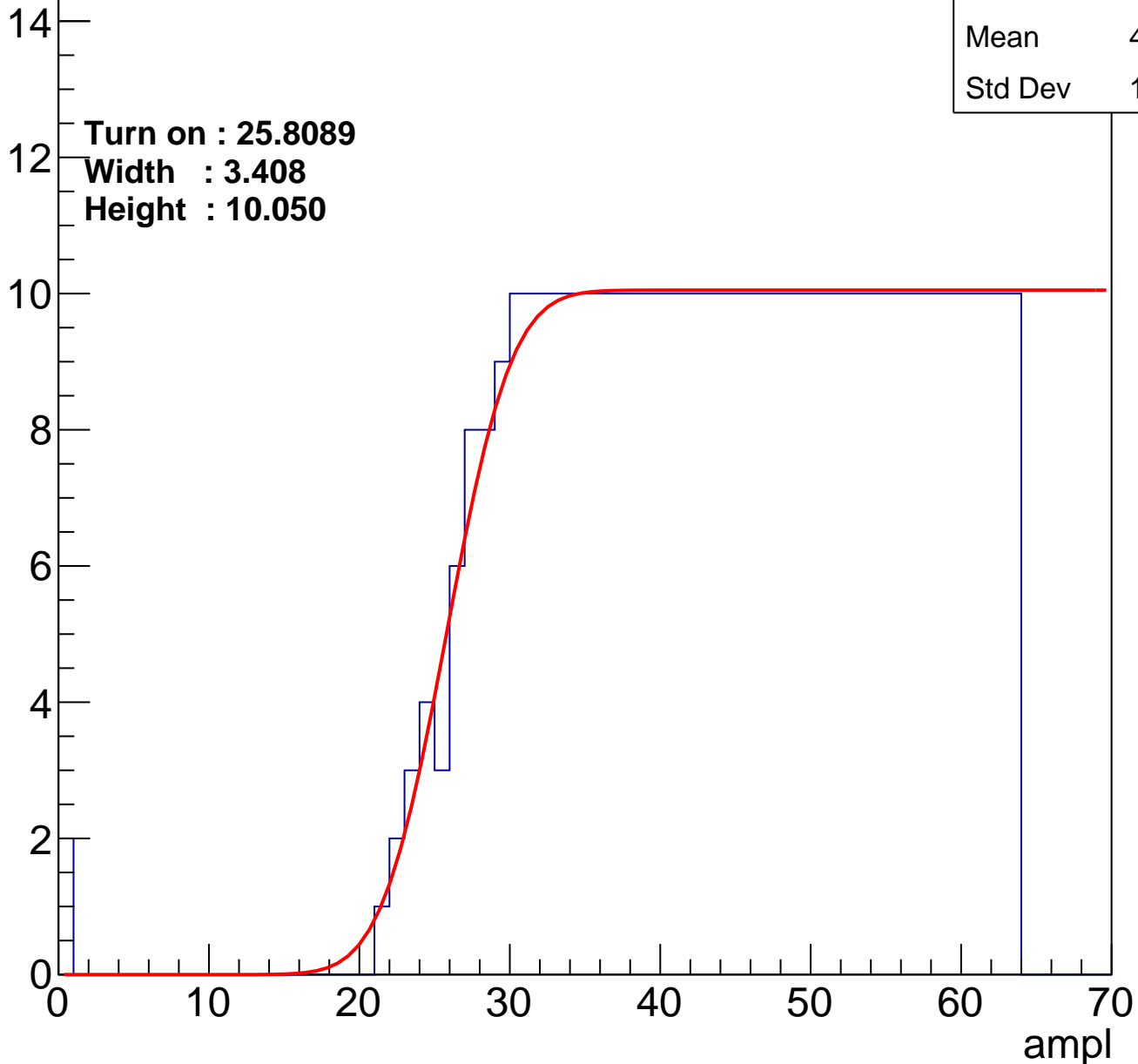
| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.97 |
| Std Dev | 11.67 |

Turn on : 25.8089

Width : 3.408

Height : 10.050

Entry



B1L002S, U5-ch121

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.29 |
| Std Dev | 11.46 |

Turn on : 26.5426

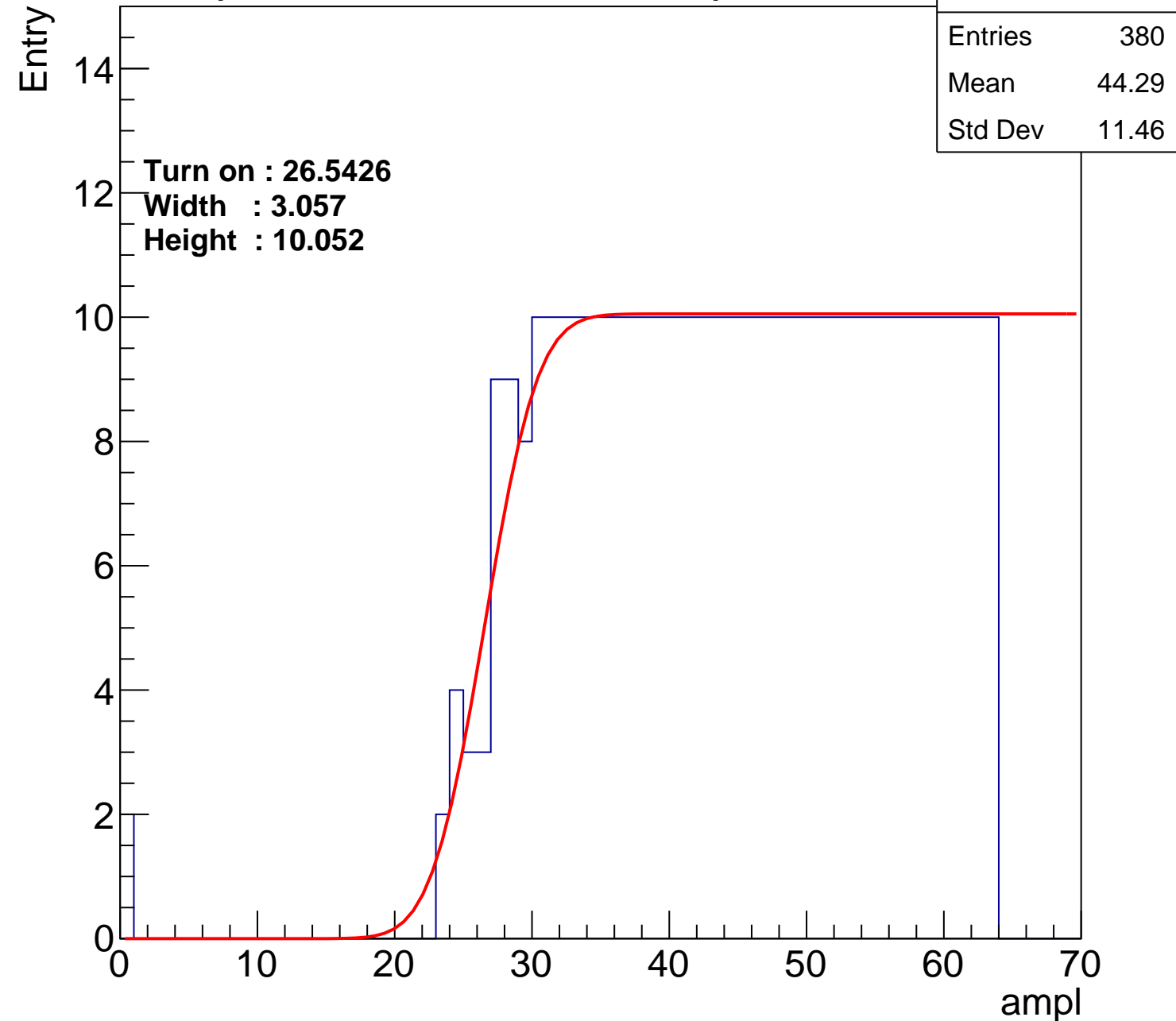
Width : 3.057

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch122

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.62 |
| Std Dev | 11.31 |

Turn on : 26.9556

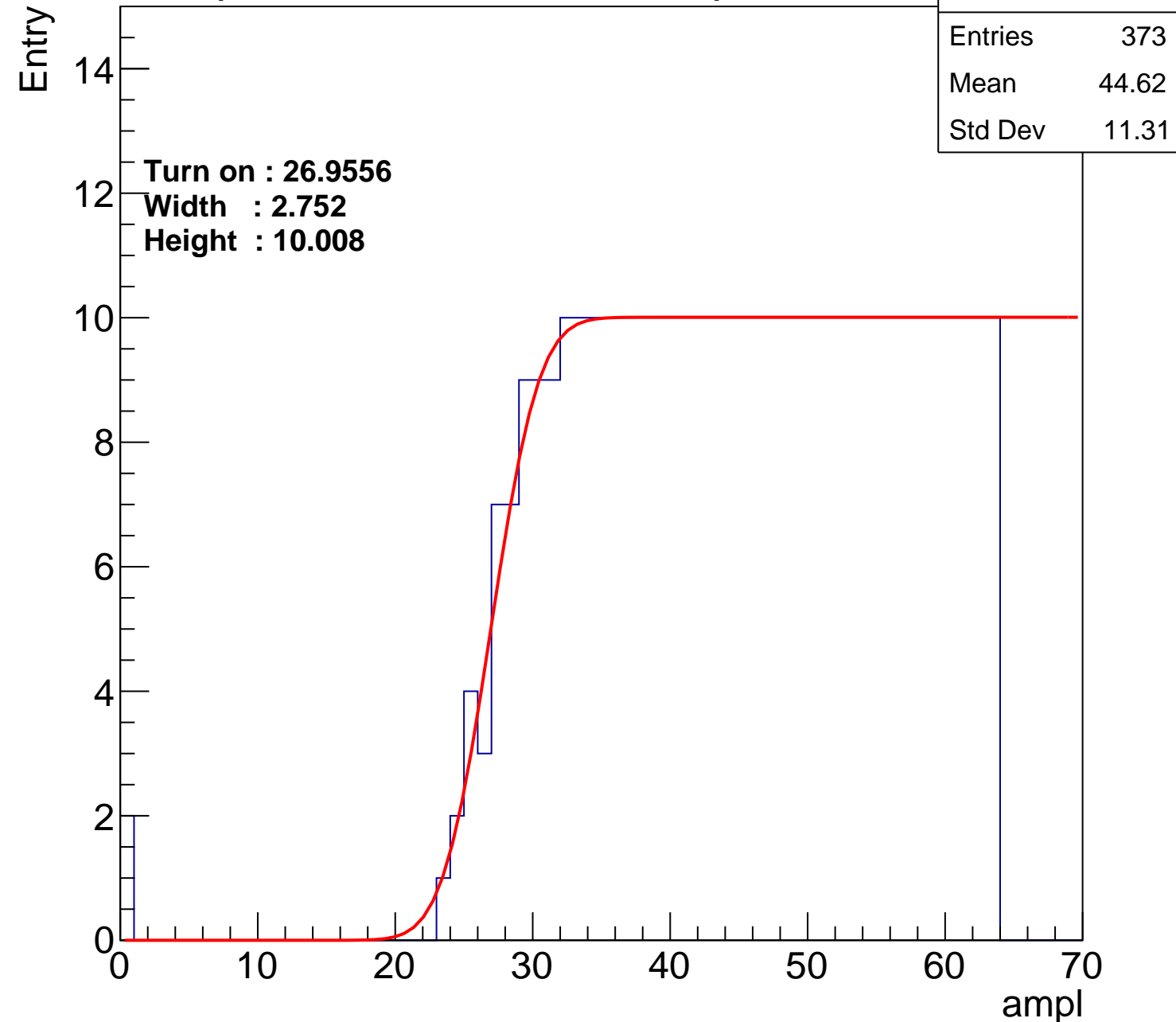
Width : 2.752

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch123

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.91 |
| Std Dev | 11.7 |

Turn on : 24.3626

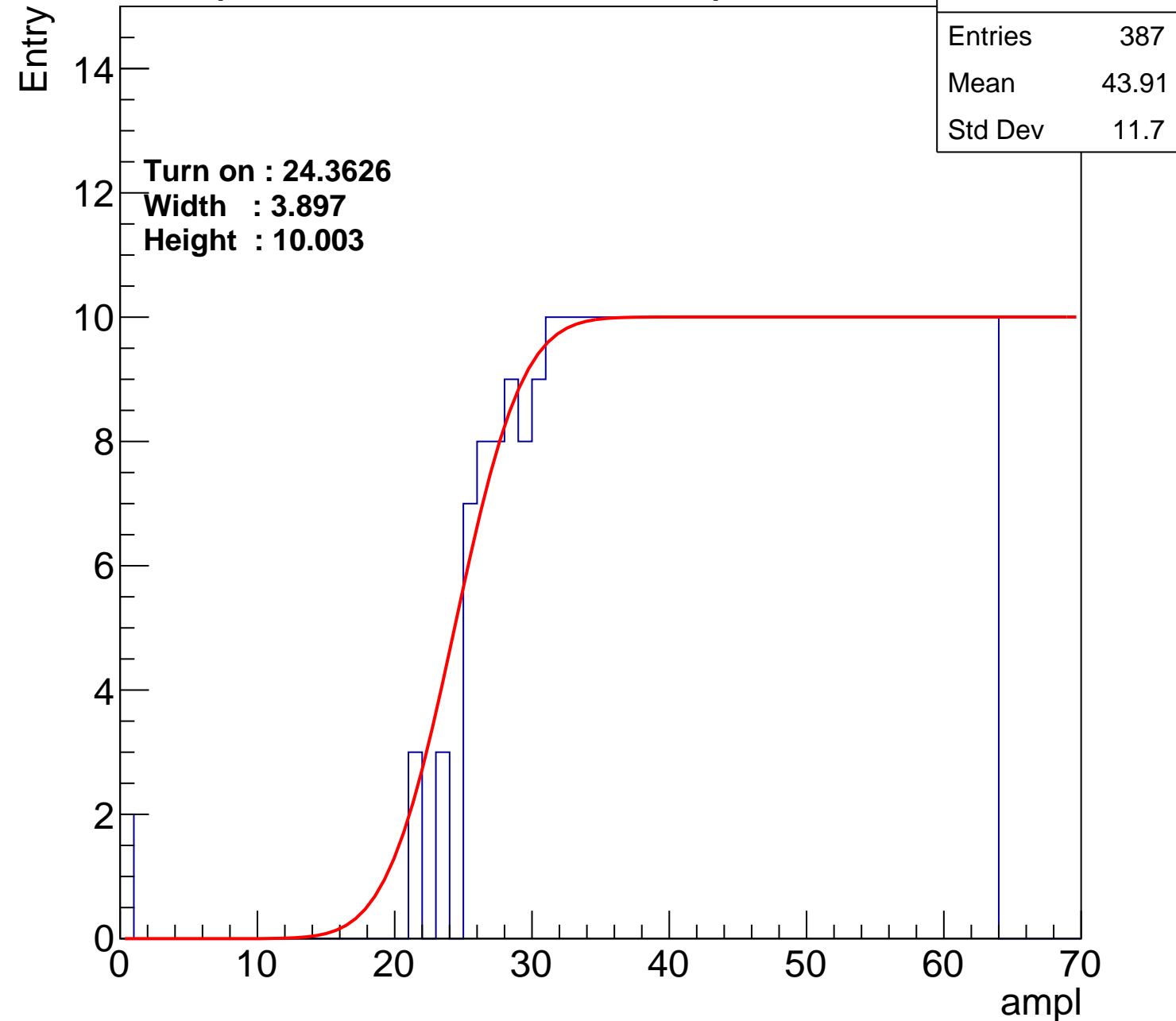
Width : 3.897

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch124

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.44 |
| Std Dev | 12.09 |

Turn on : 24.5273

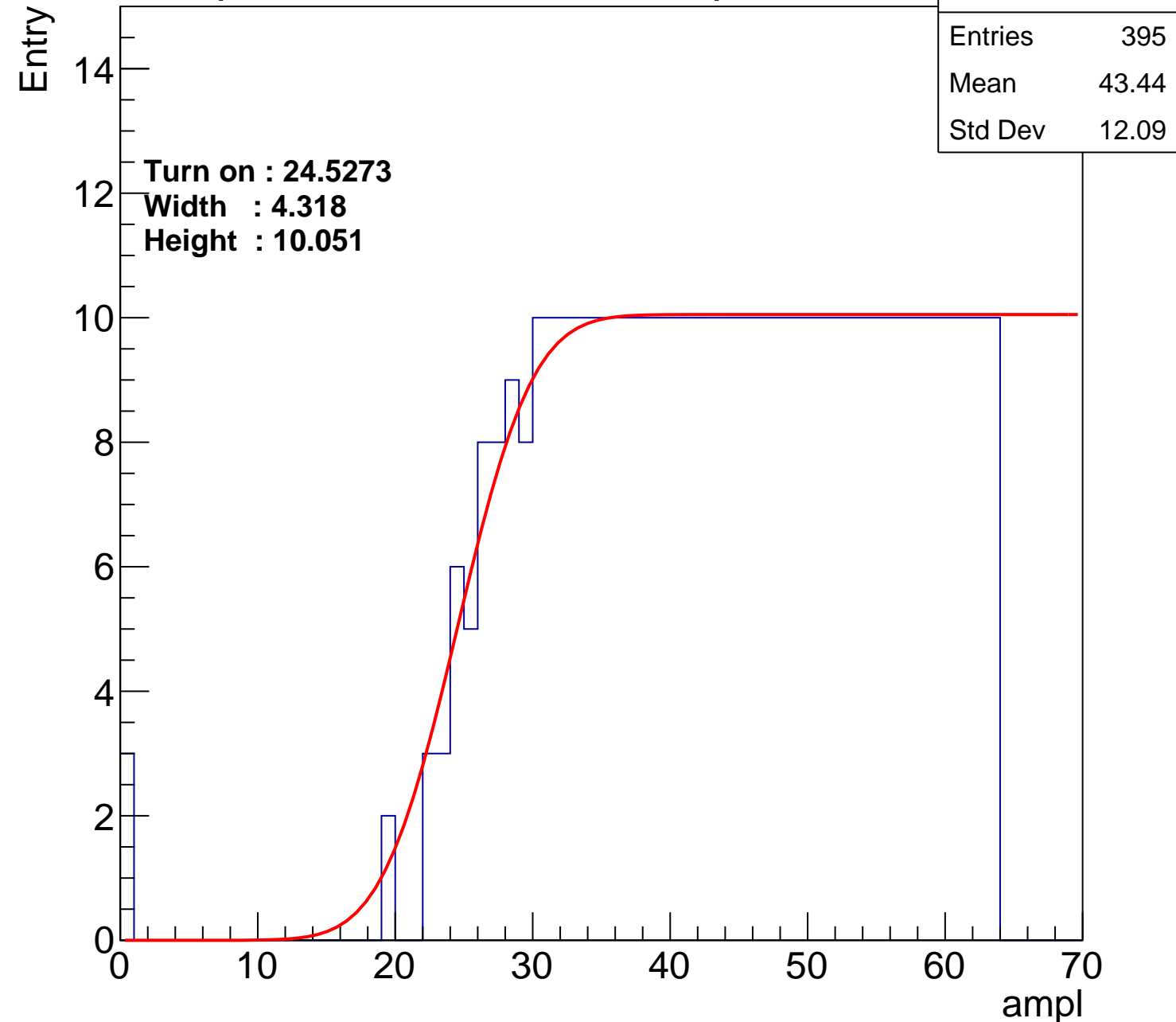
Width : 4.318

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch125

calib_packv5_042523_0143.root, FC#10, port B3

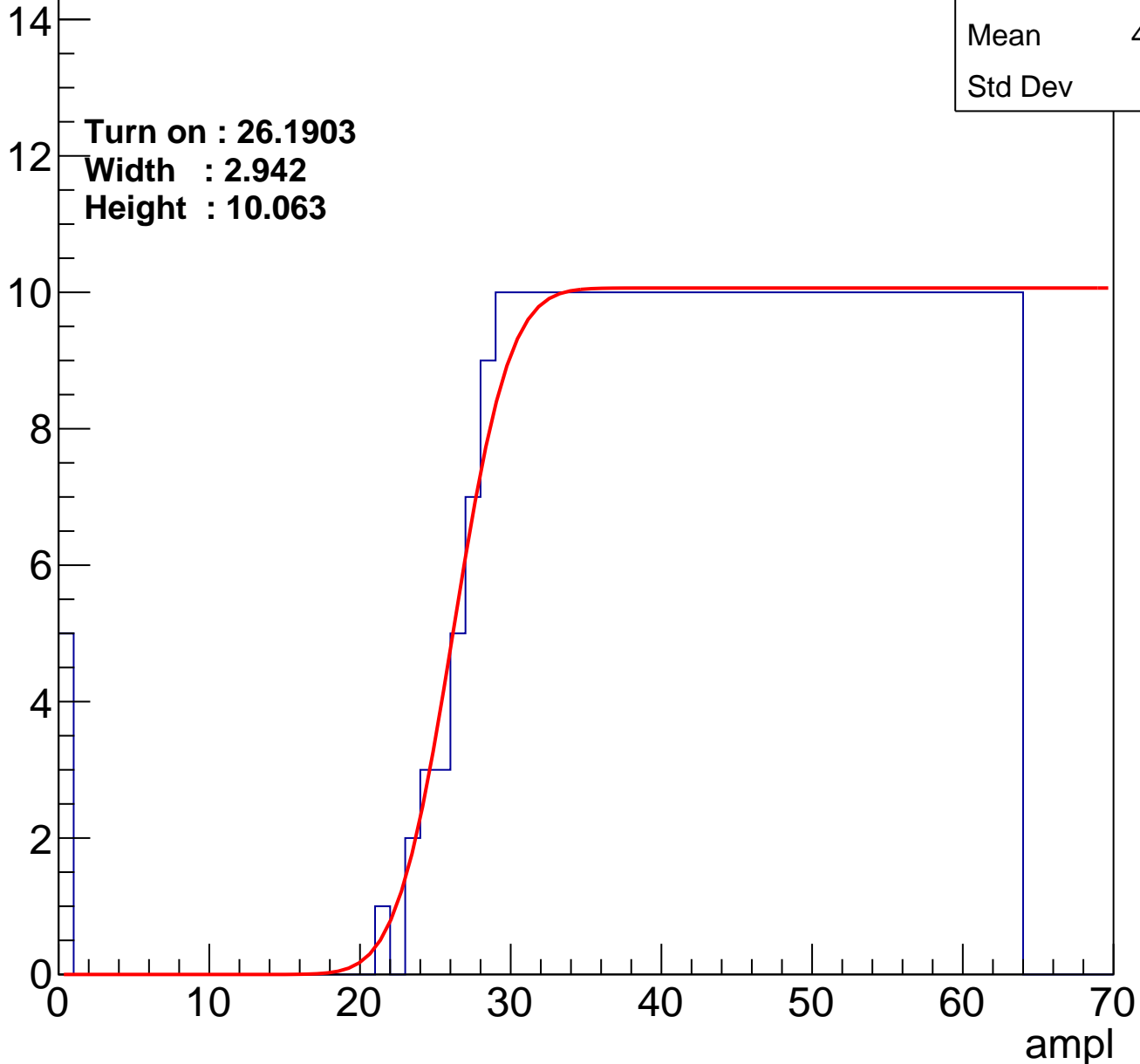
| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.86 |
| Std Dev | 12.1 |

Turn on : 26.1903

Width : 2.942

Height : 10.063

Entry



B1L002S, U5-ch126

calib_packv5_042523_0143.root, FC#10, port B3

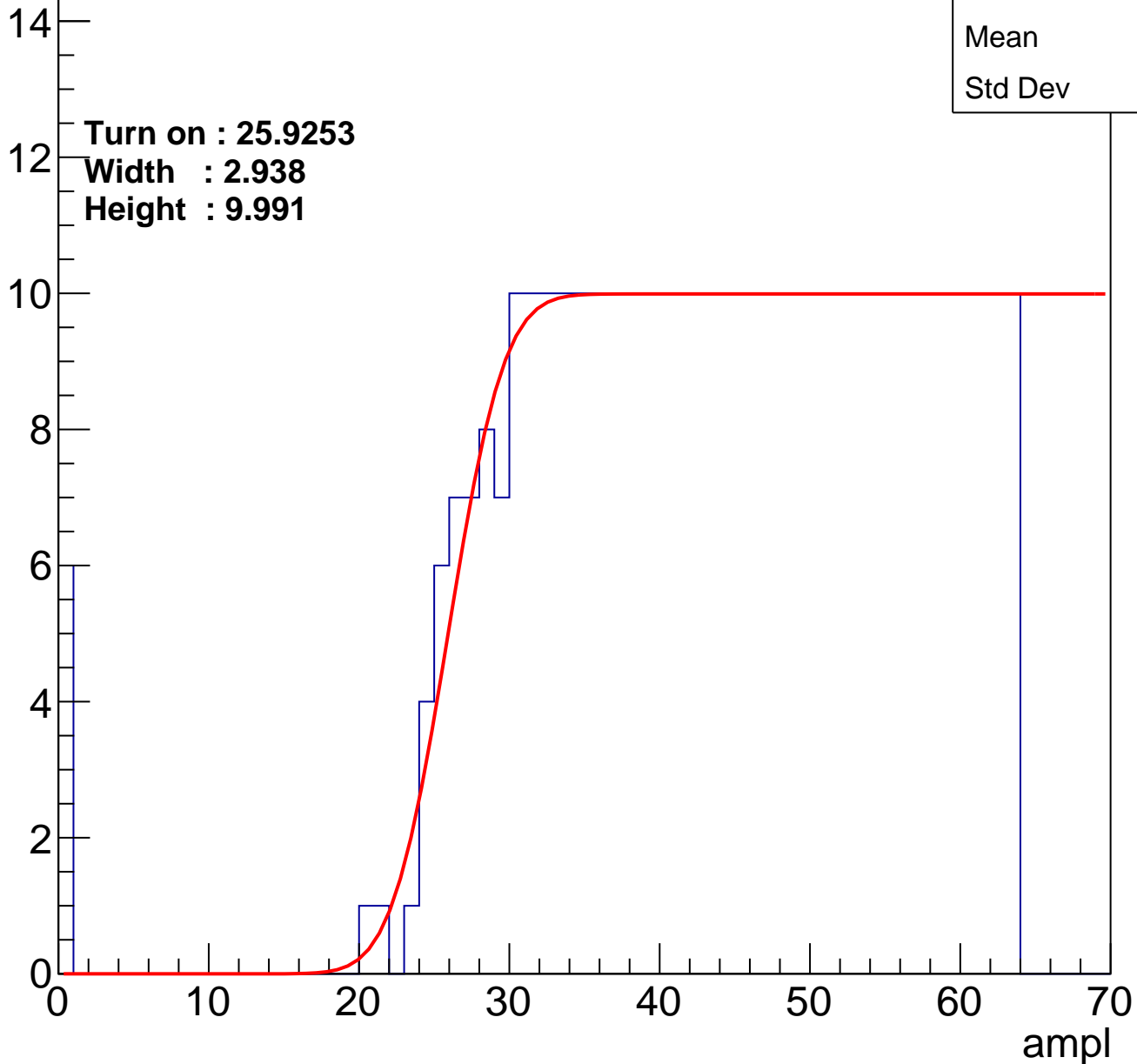
| | |
|---------|------|
| Entries | 388 |
| Mean | 43.6 |
| Std Dev | 12.4 |

Turn on : 25.9253

Width : 2.938

Height : 9.991

Entry



B1L002S, U5-ch127

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.76 |
| Std Dev | 11.66 |

Turn on : 25.2871

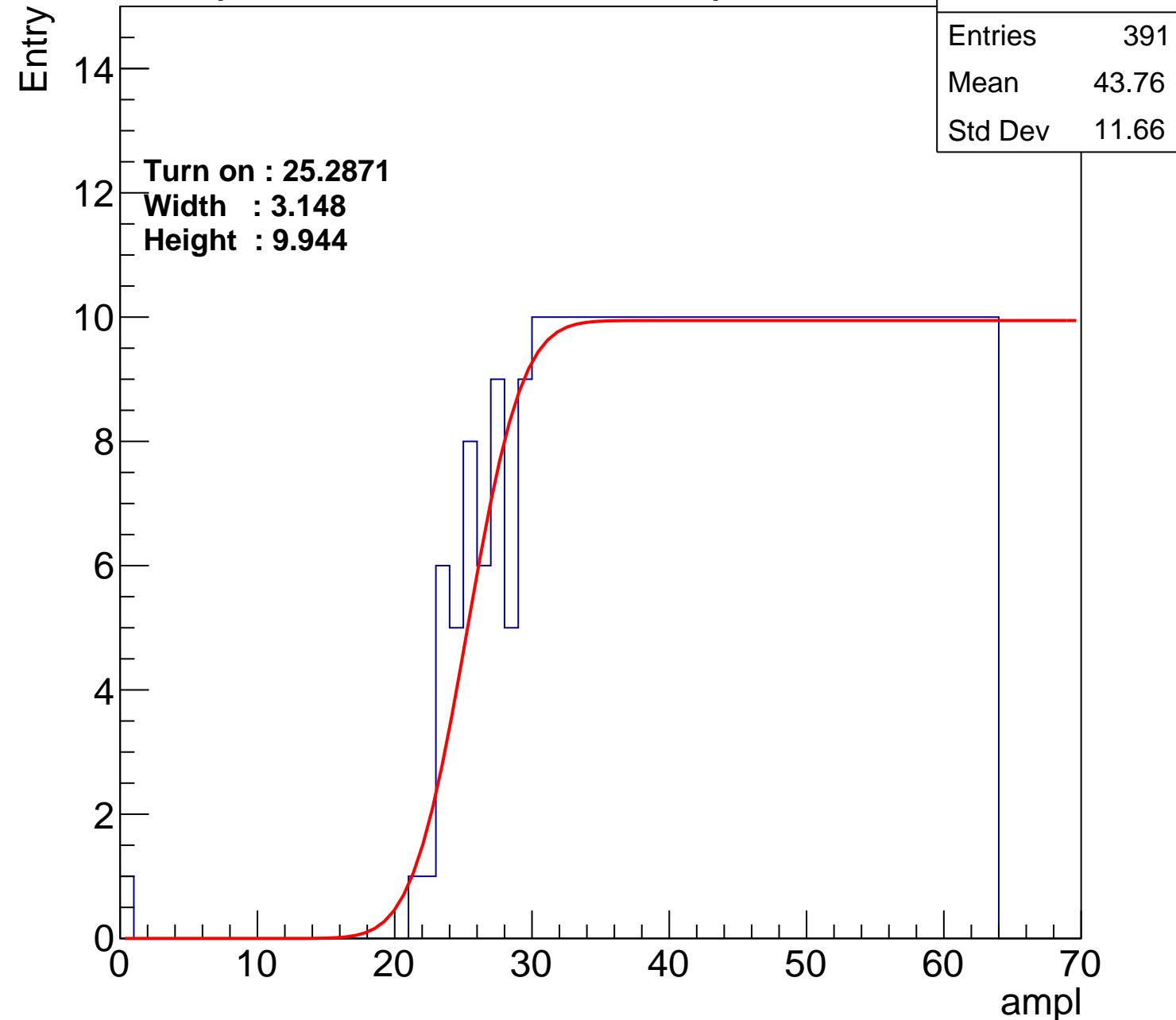
Width : 3.148

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U5-ch127

calib_packv5_042523_0143.root, FC#10, port B3

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.76 |
| Std Dev | 11.66 |

Turn on : 25.2871

Width : 3.148

Height : 9.944

Entry

