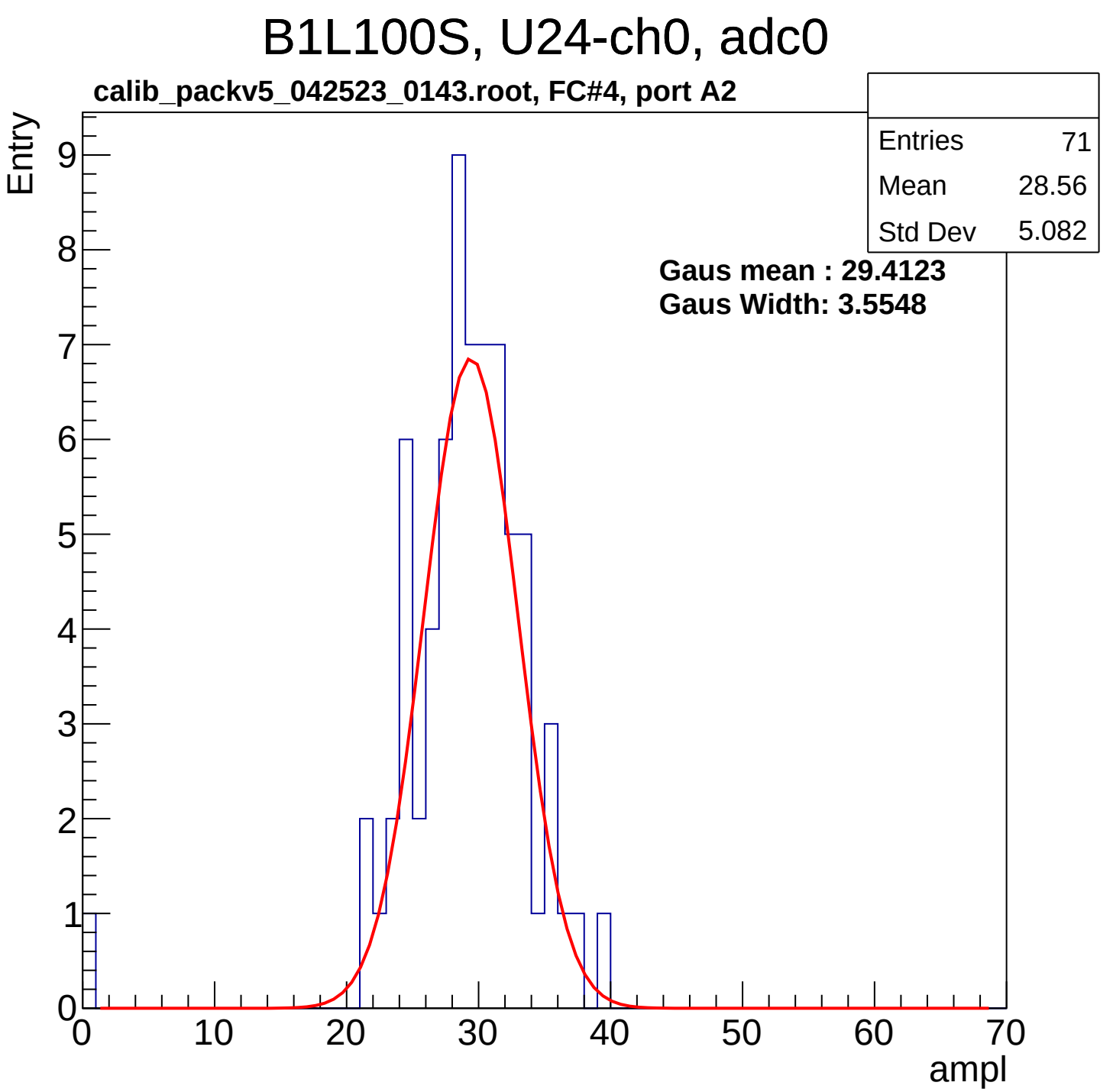
B1L100S, U24-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 28.56
Std Dev 5.082
Gaus mean : 29.4123
Gaus Width: 3.5548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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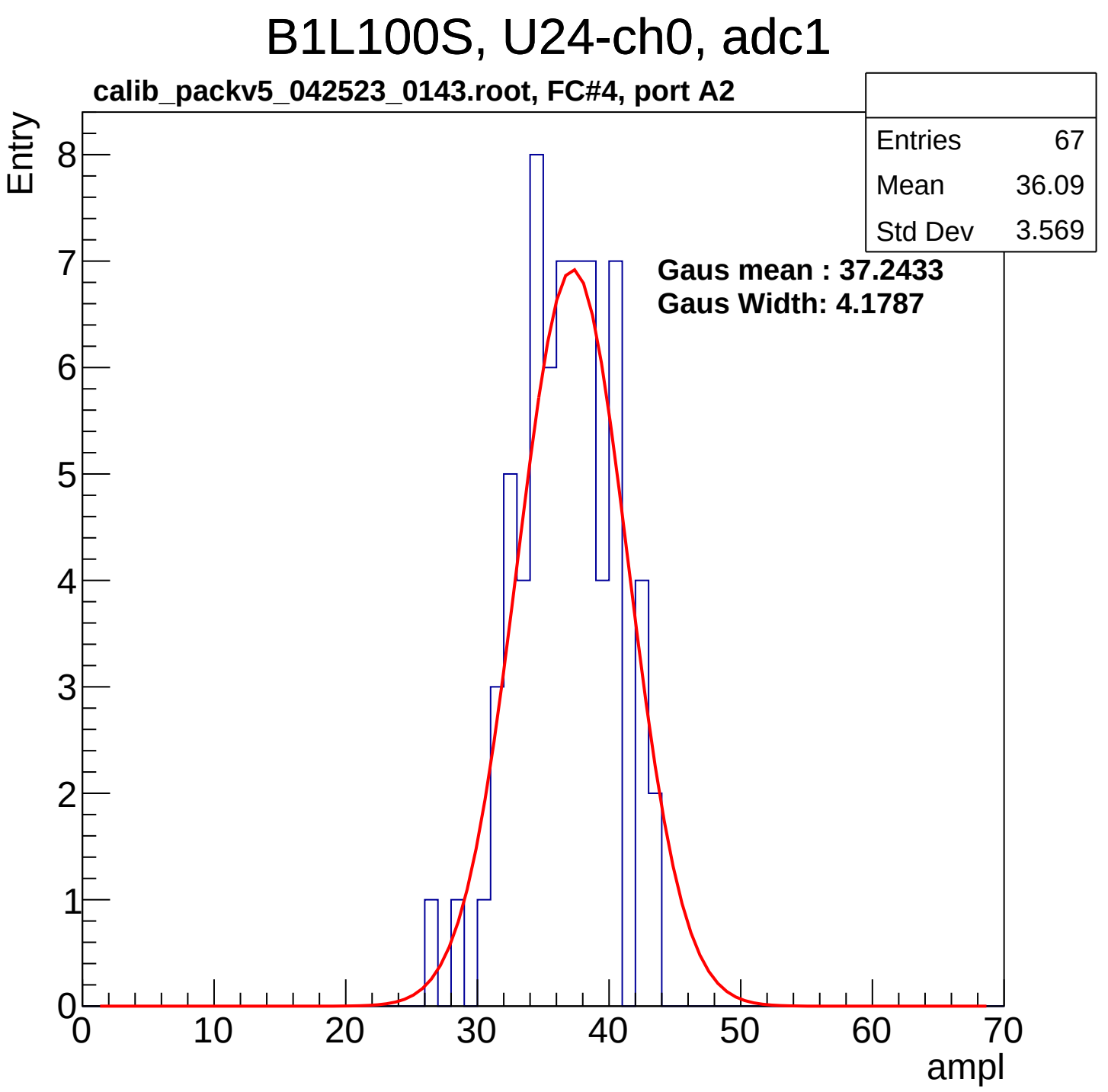
B1L100S, U24-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.09
Std Dev 3.569
Gaus mean : 37.2433
Gaus Width: 4.1787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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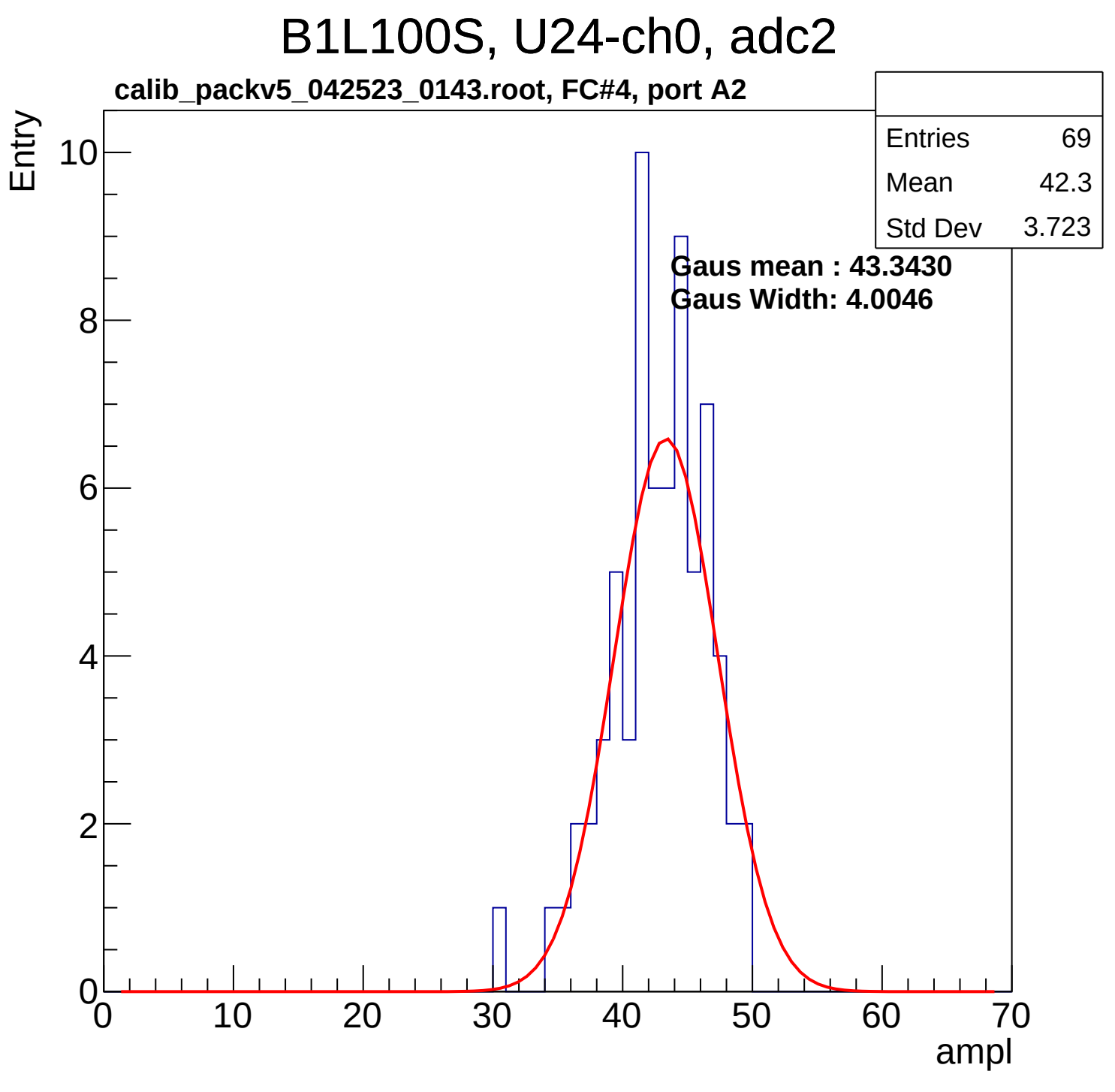
B1L100S, U24-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.3
Std Dev 3.723
Gaus mean : 43.3430
Gaus Width: 4.0046
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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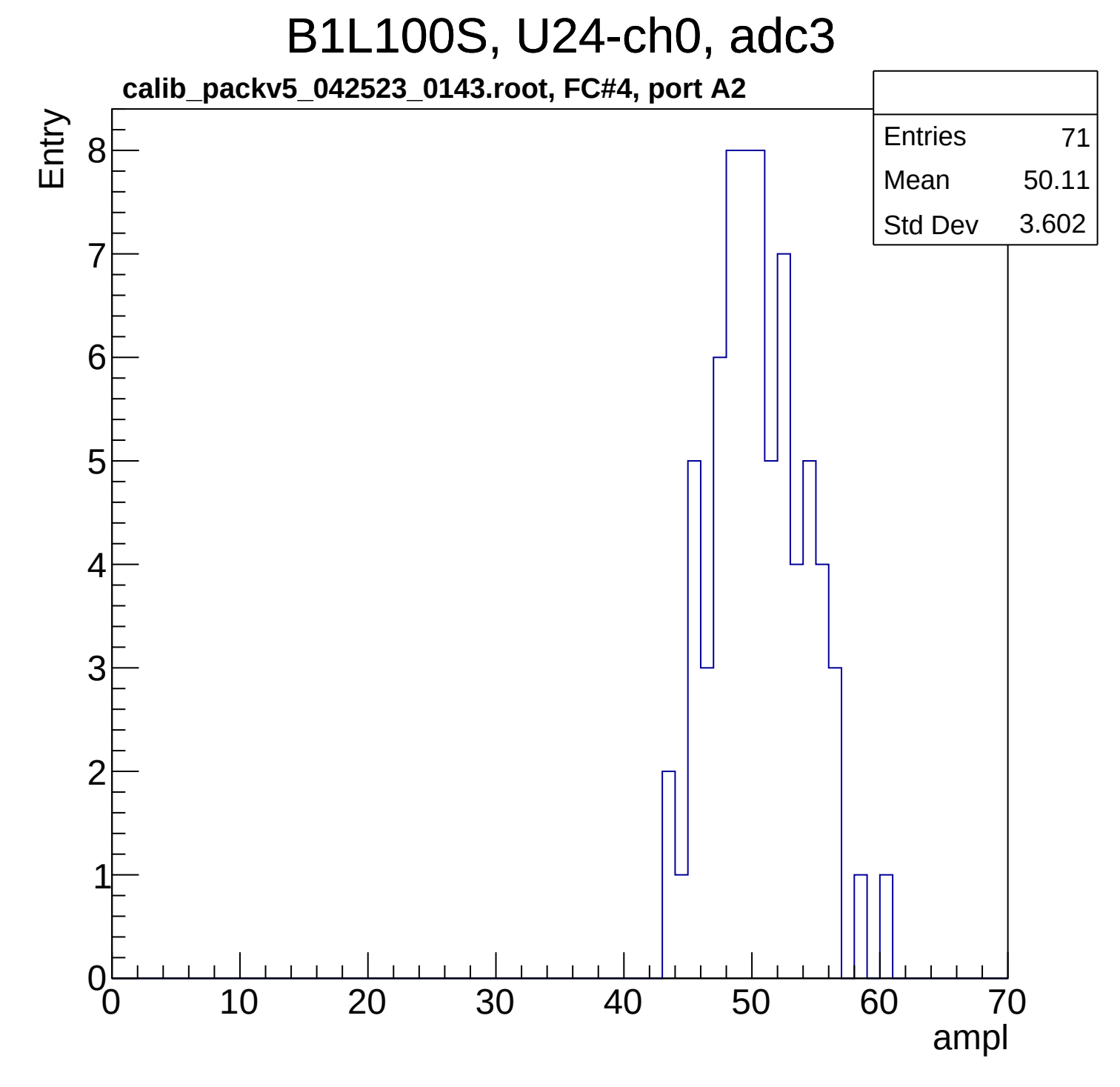
B1L100S, U24-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.11
Std Dev 3.602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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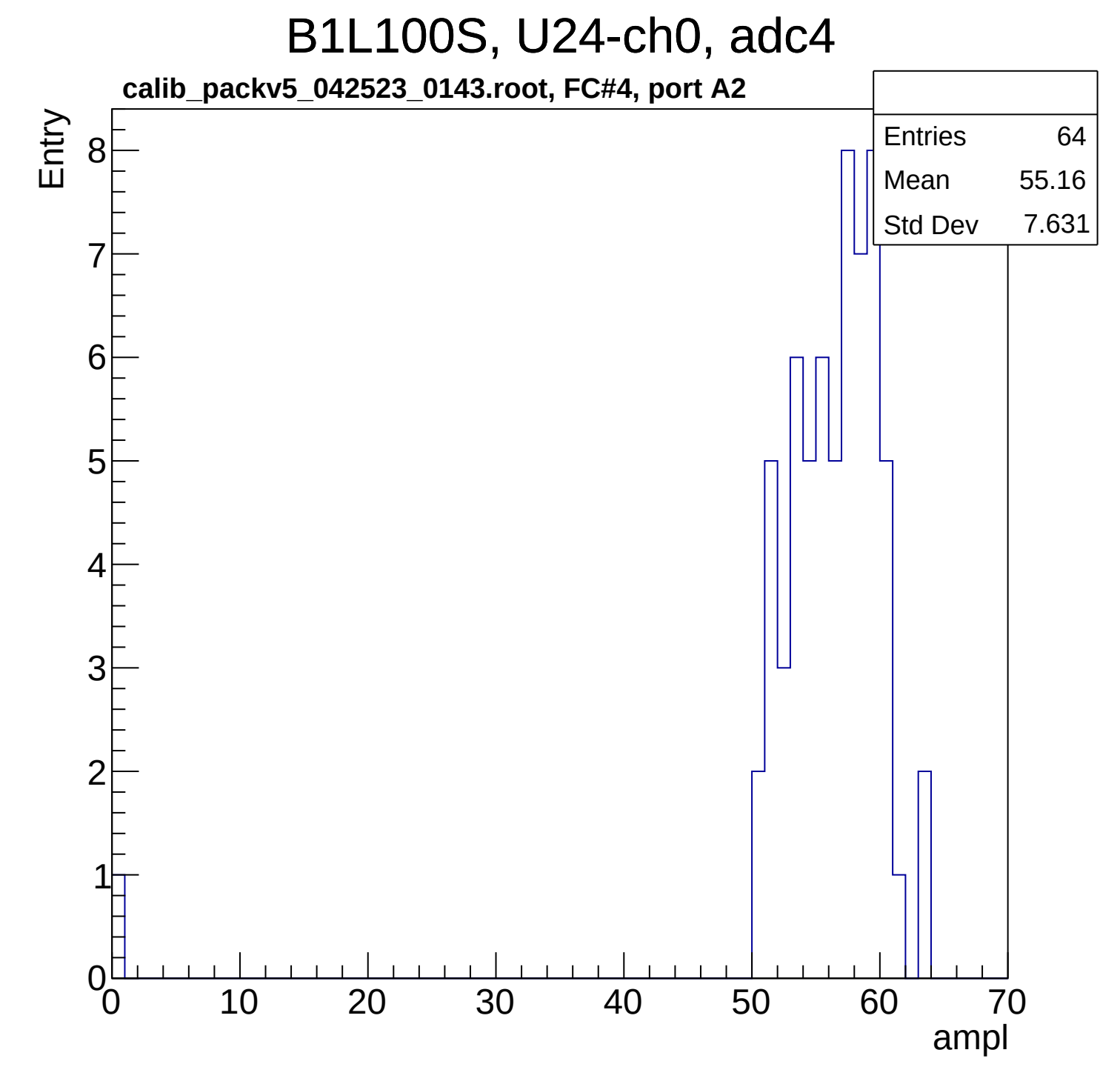
B1L100S, U24-ch0, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.16
Std Dev 7.631
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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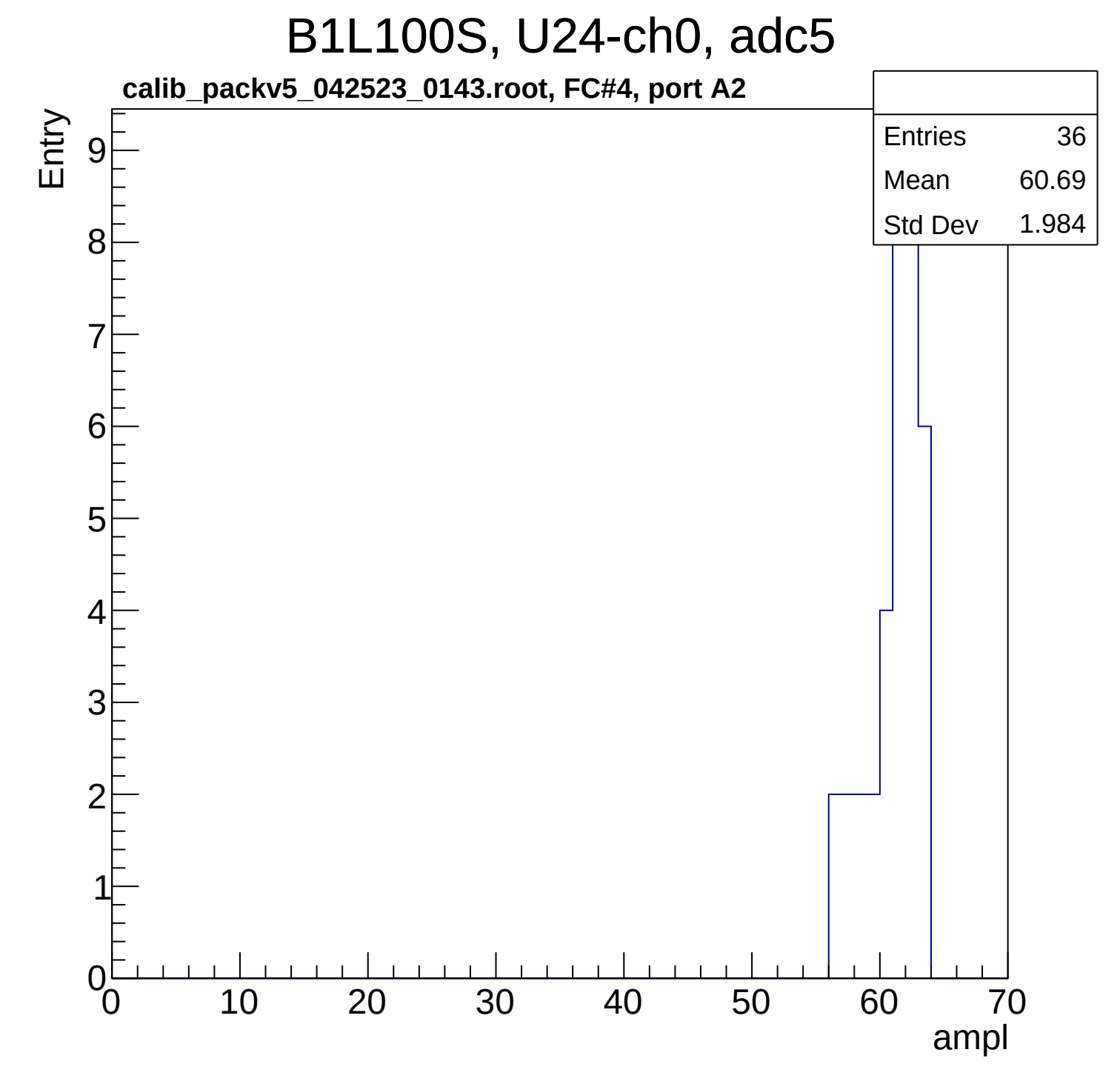
B1L100S, U24-ch0, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.69
Std Dev 1.984
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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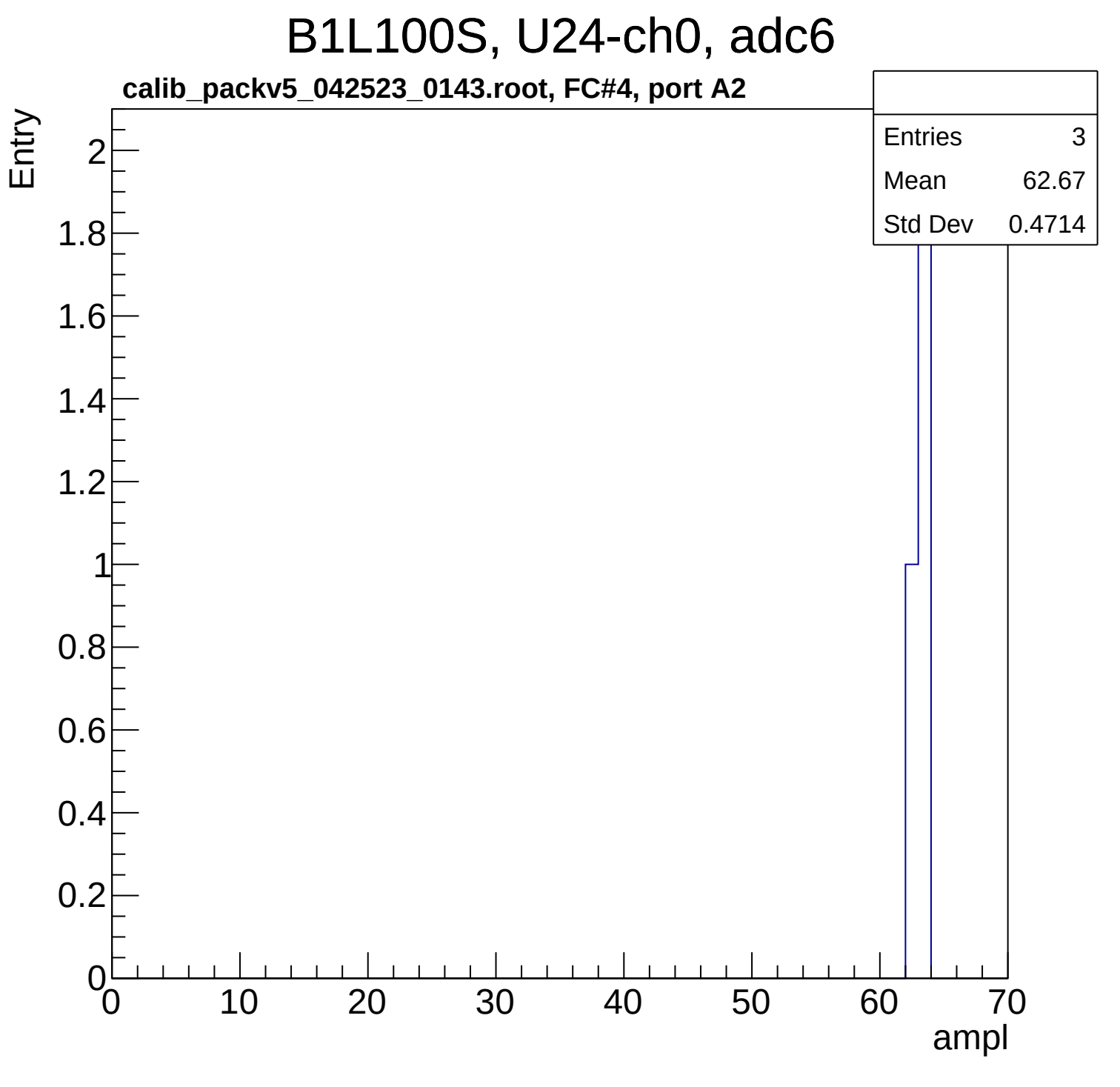
B1L100S, U24-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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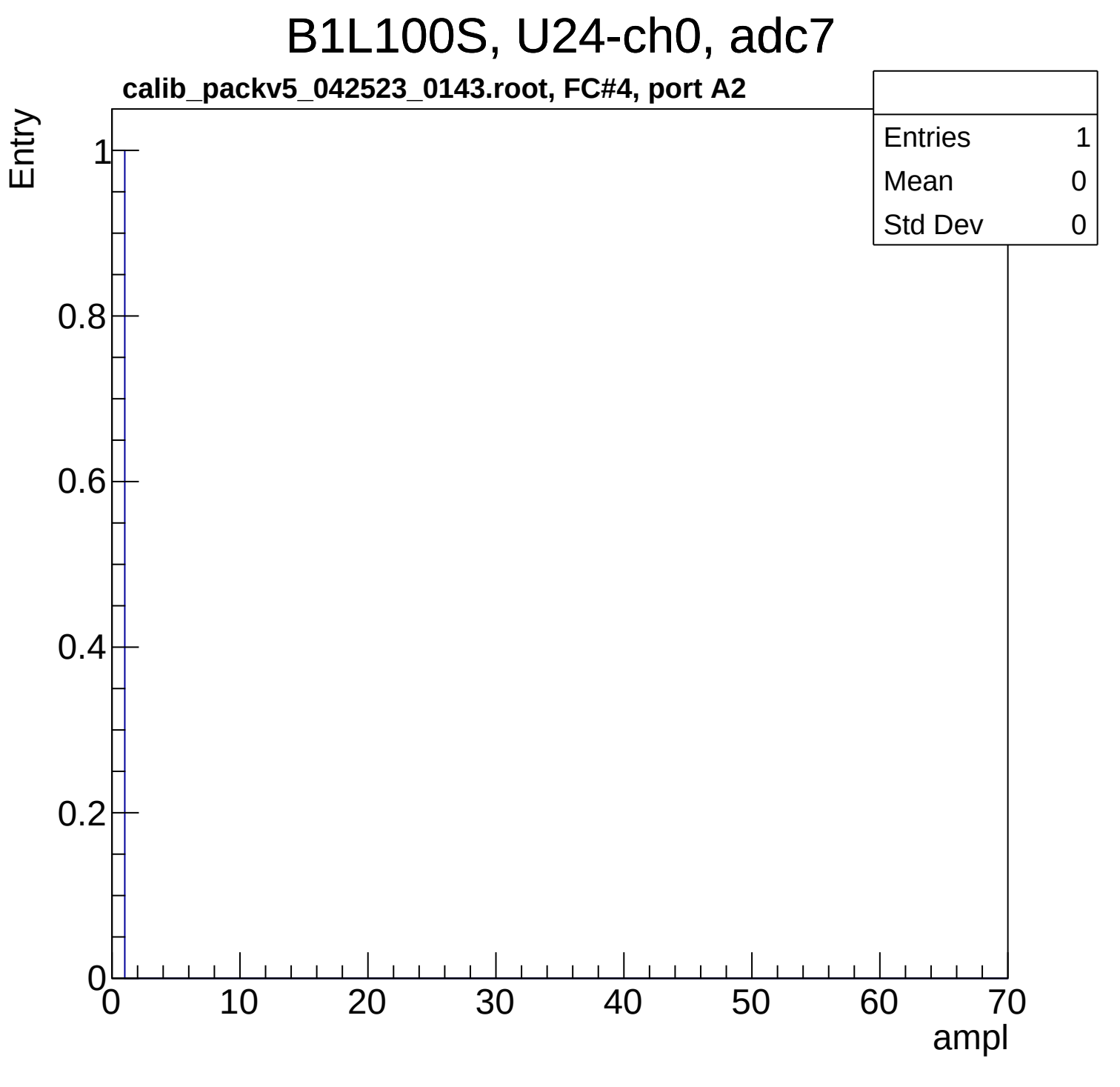
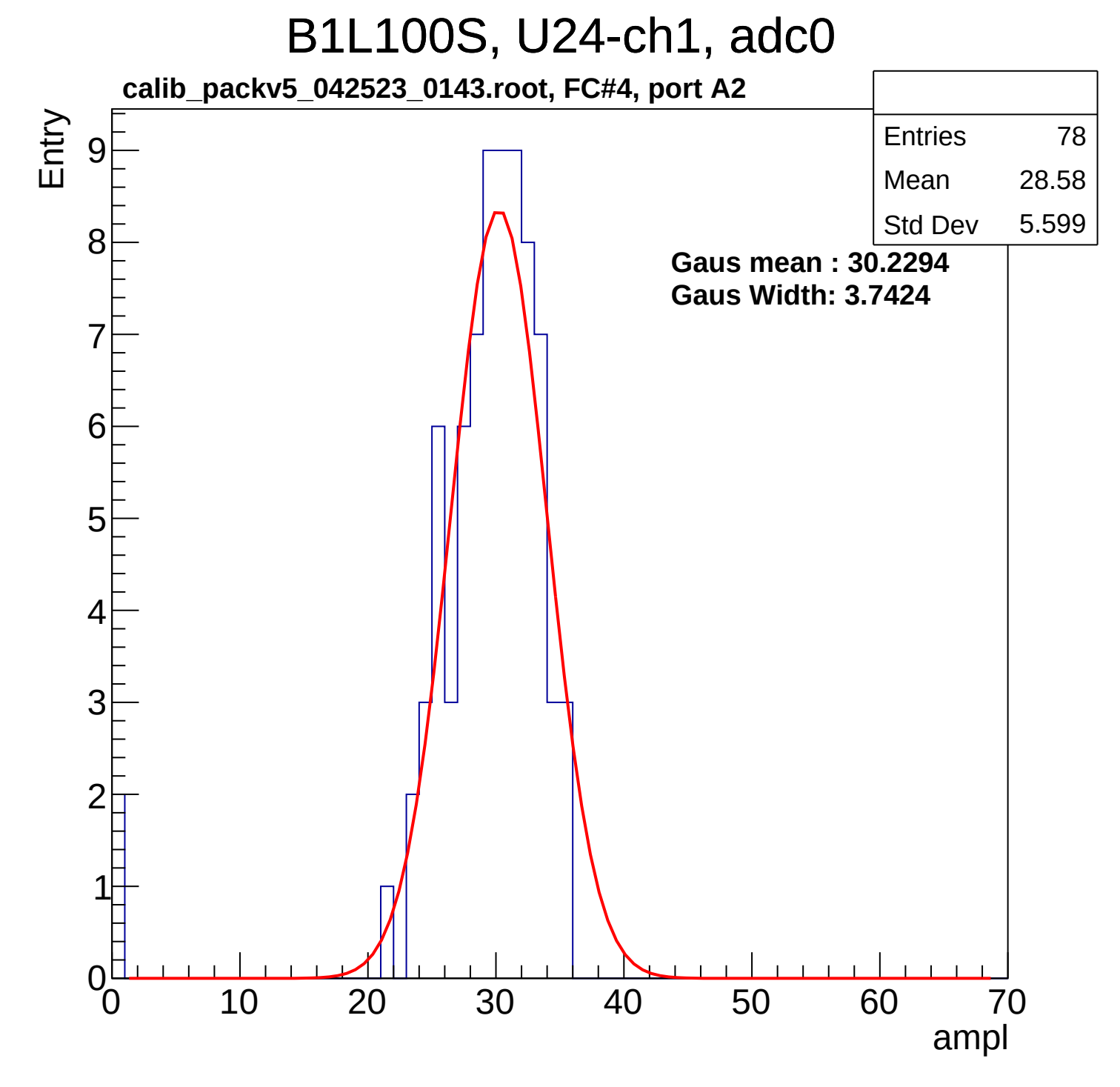

B1L100S, U24-ch0, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 28.58
Std Dev 5.599
Gaus mean : 30.2294
Gaus Width: 3.7424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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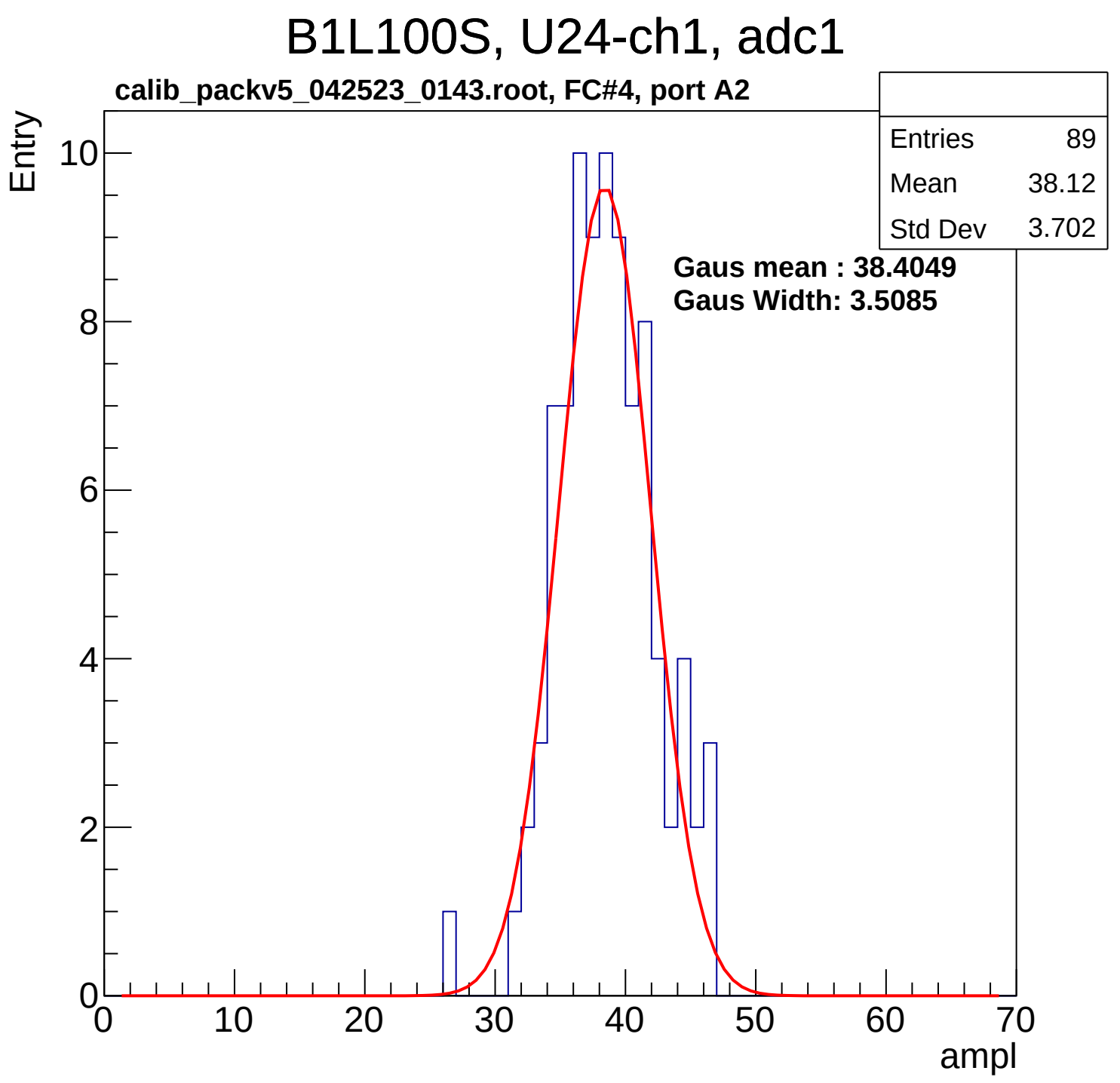
B1L100S, U24-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 38.12
Std Dev 3.702
Gaus mean : 38.4049
Gaus Width: 3.5085
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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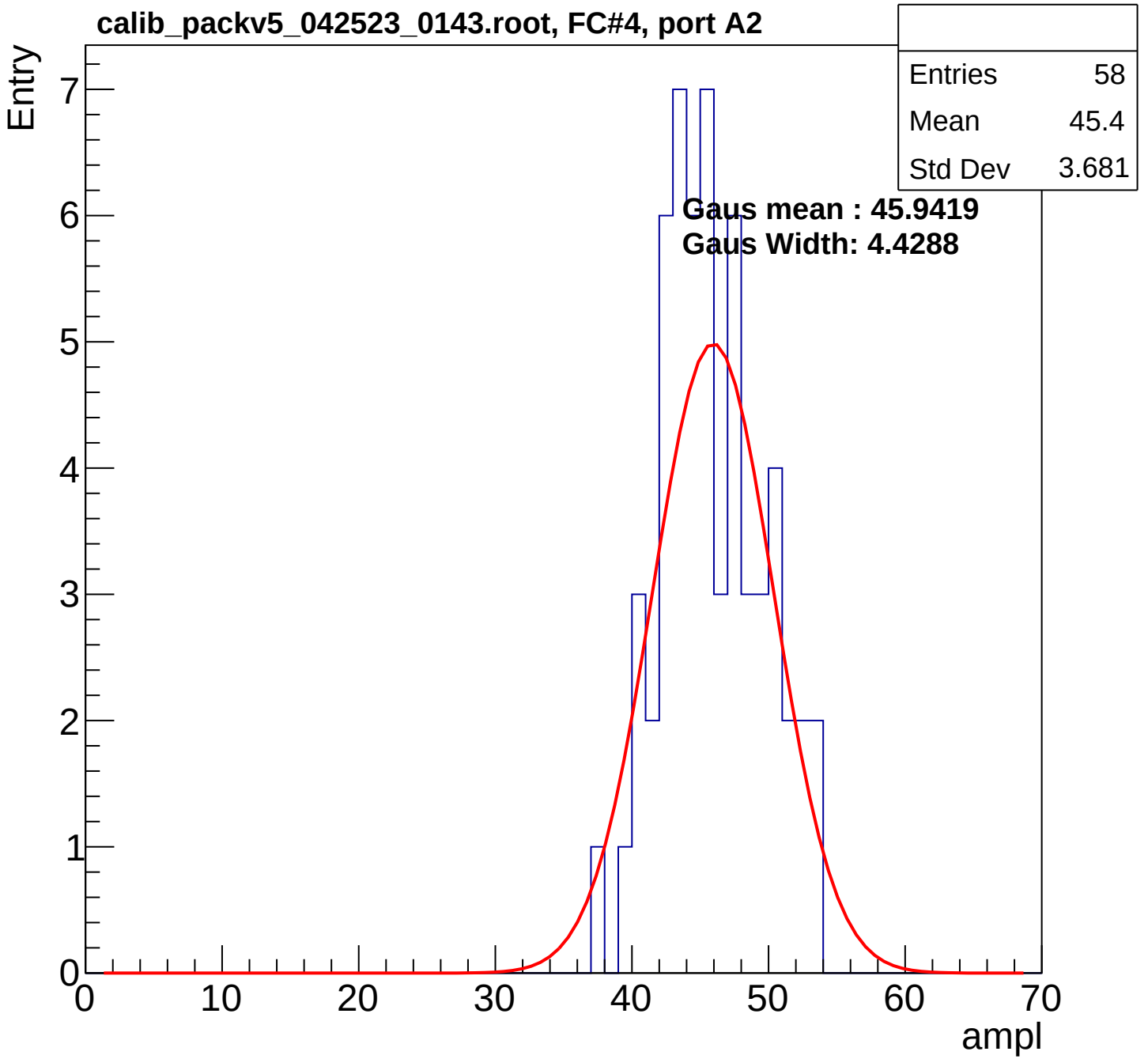
B1L100S, U24-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 45.4
Std Dev 3.681
Gaus mean : 45.9419
Gaus Width: 4.4288
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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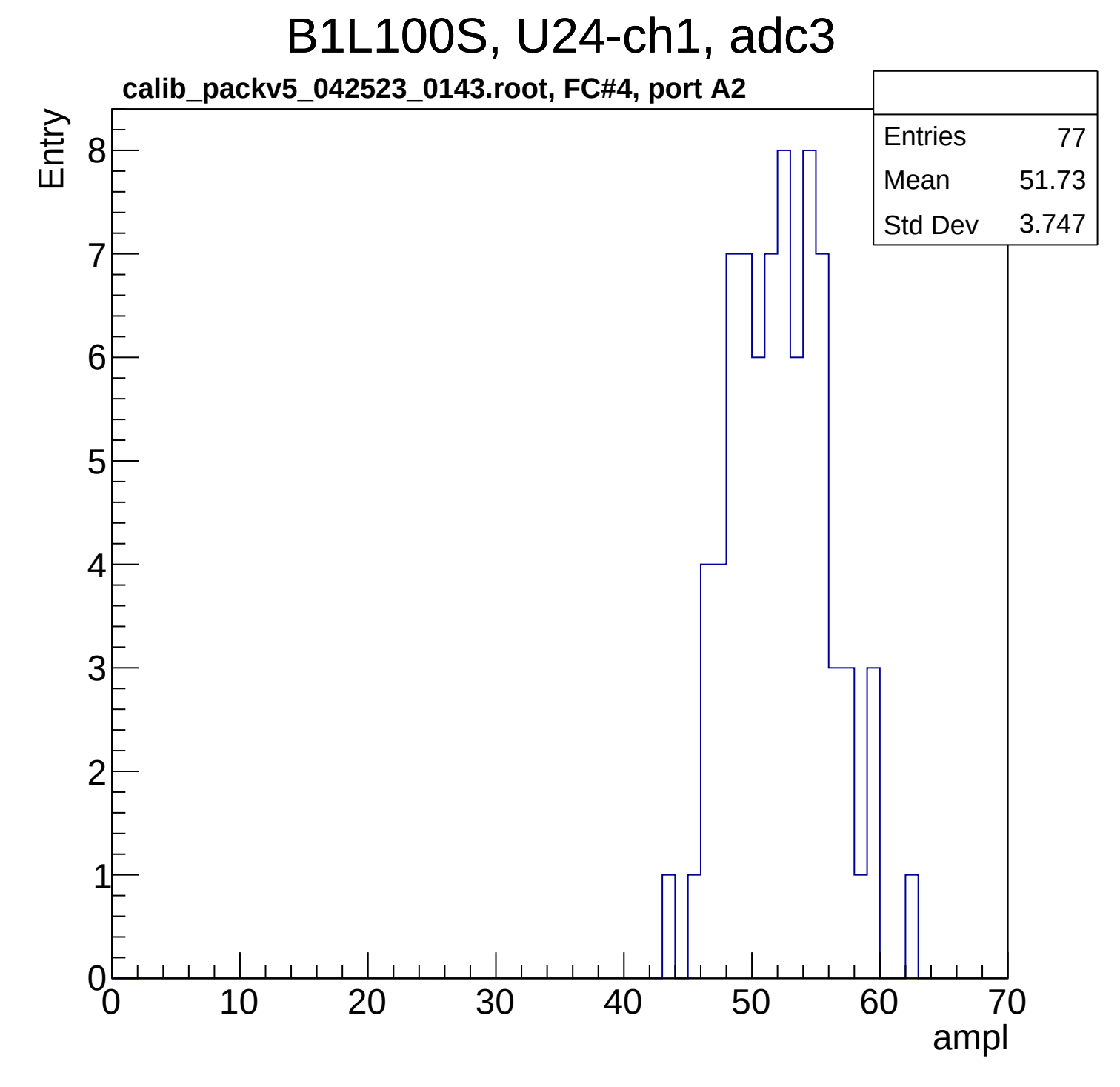
B1L100S, U24-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 51.73
Std Dev 3.747
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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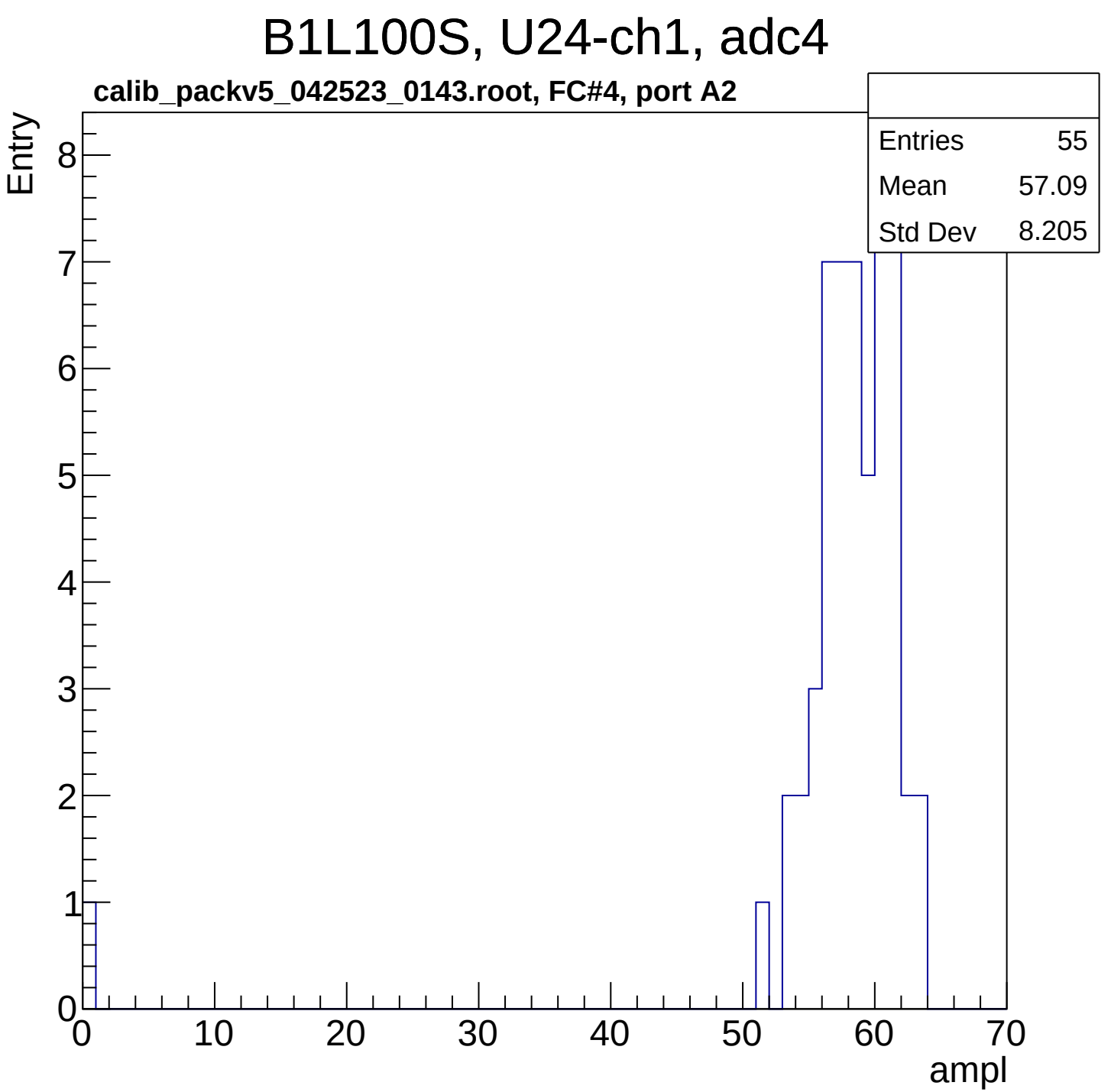
B1L100S, U24-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 57.09
Std Dev 8.205
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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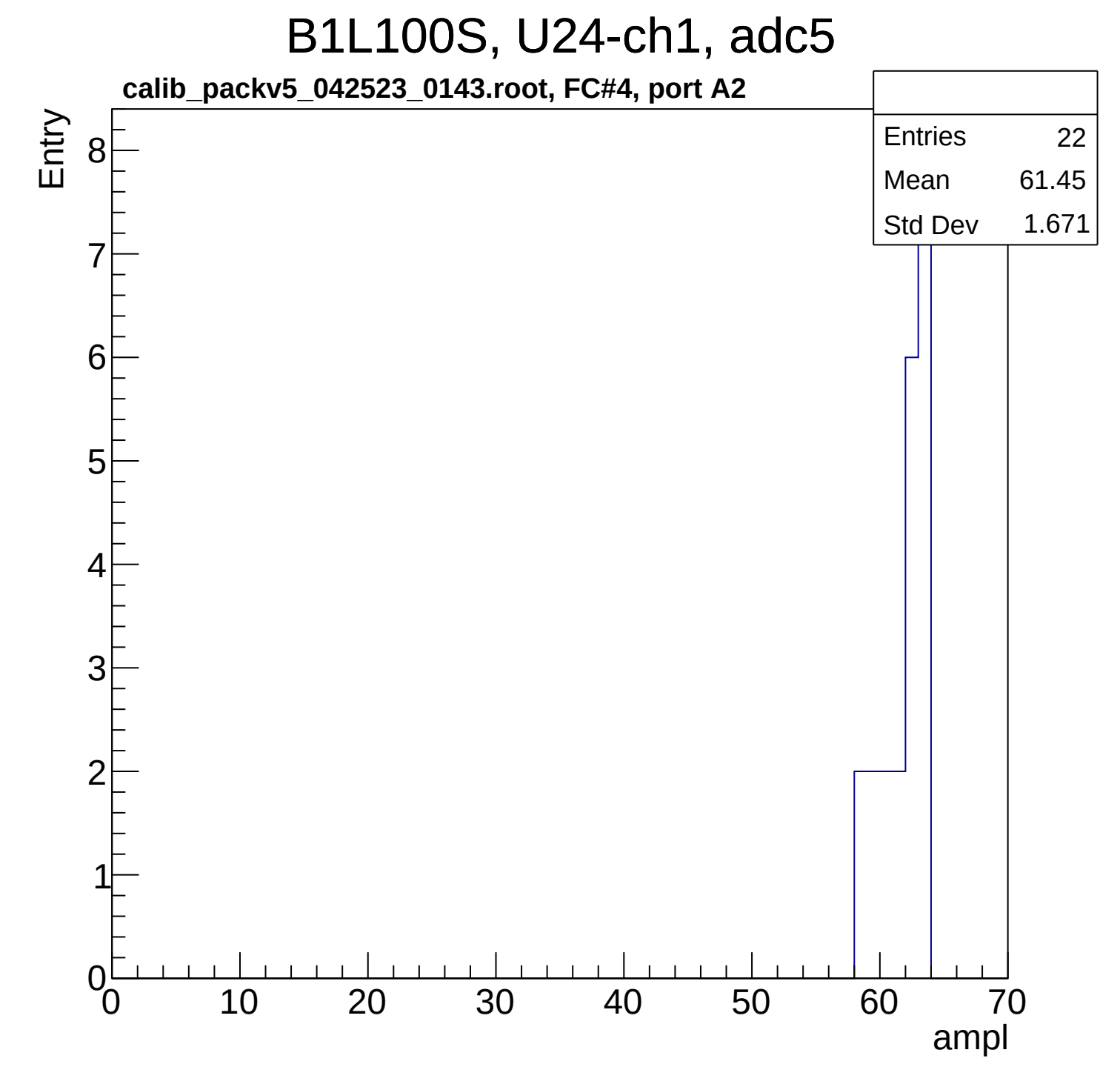
B1L100S, U24-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 61.45
Std Dev 1.671
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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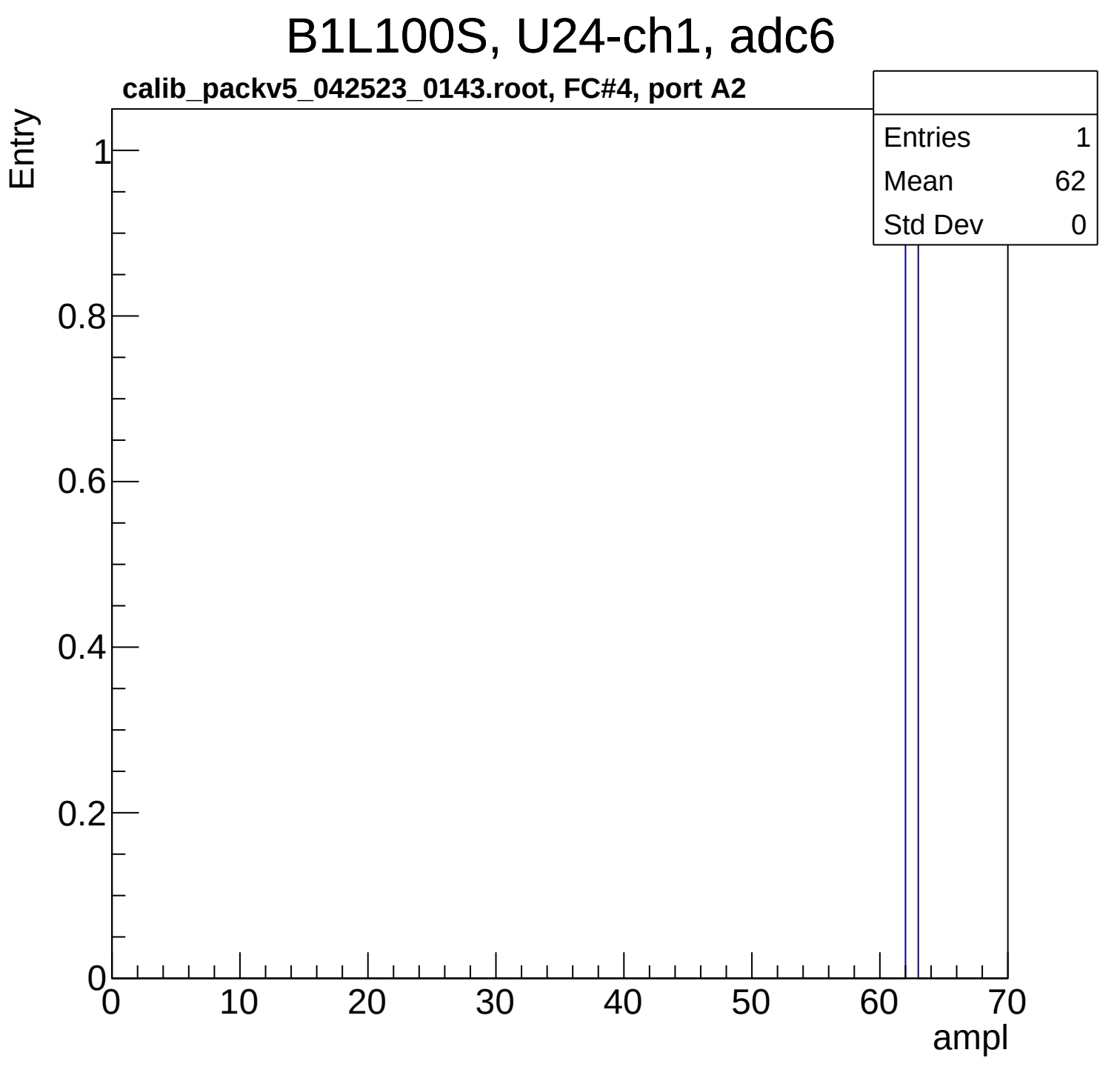
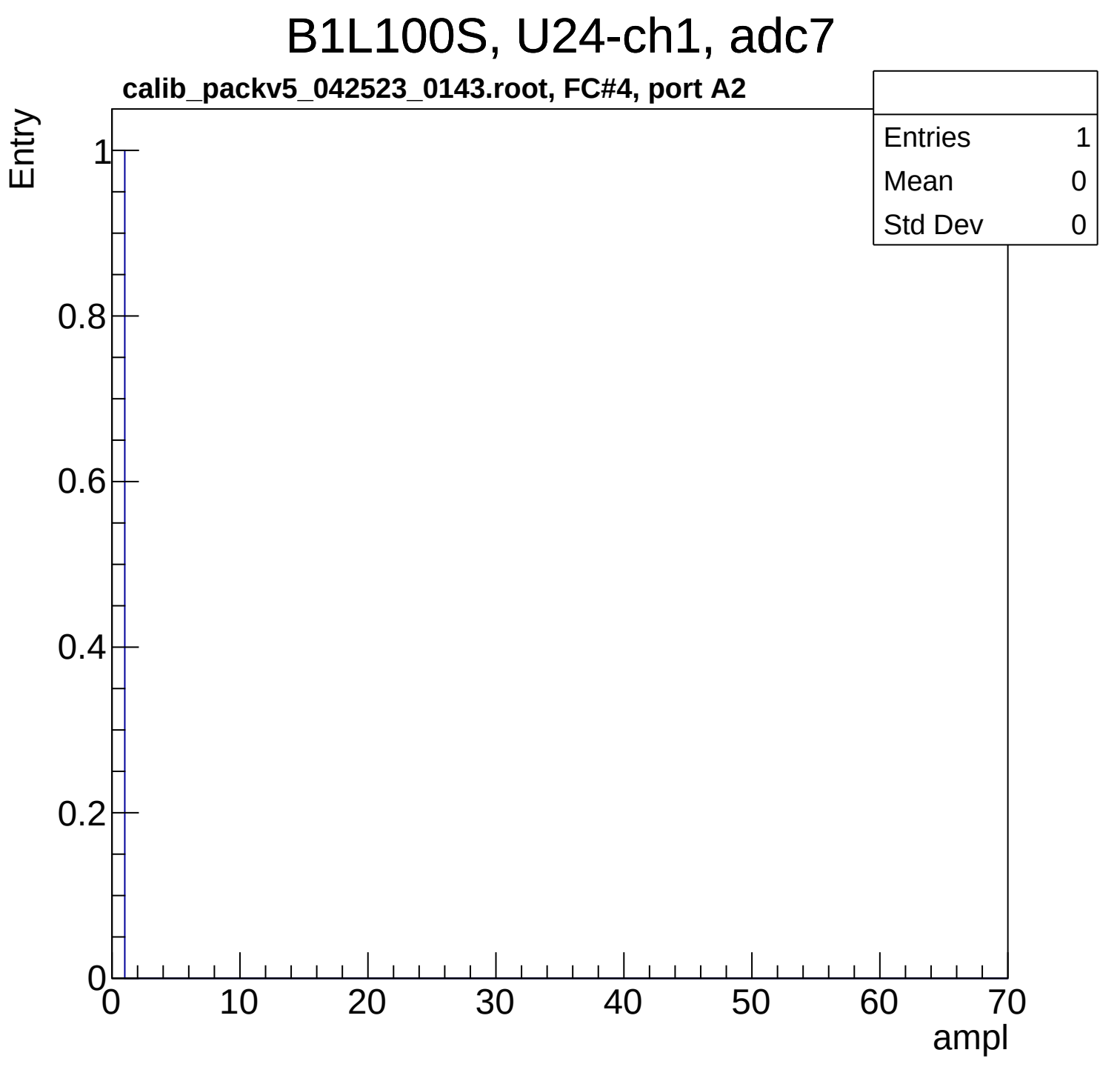
40
50
60
70


B1L100S, U24-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch1, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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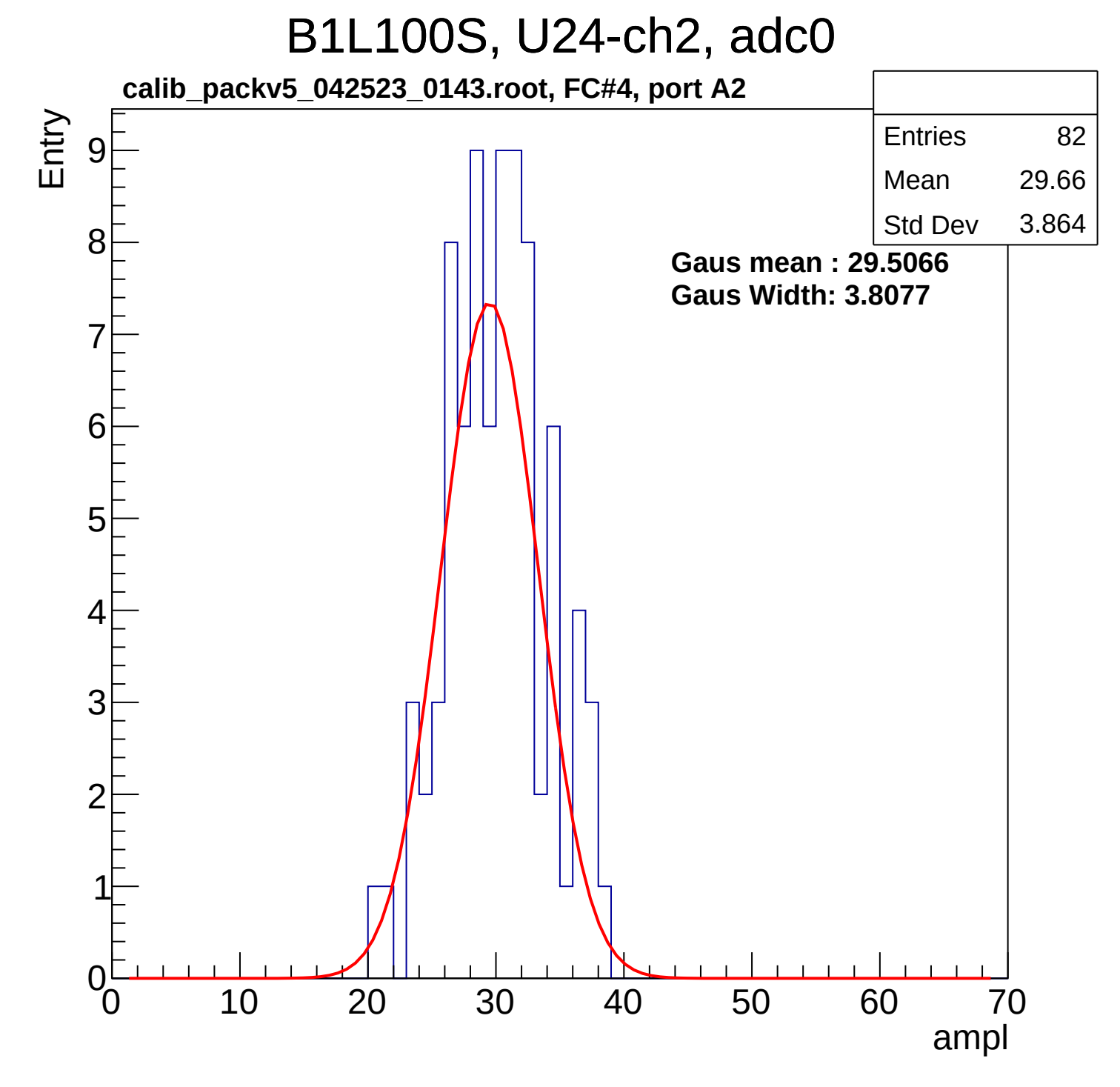
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 29.66
Std Dev 3.864
Gaus mean : 29.5066
Gaus Width: 3.8077
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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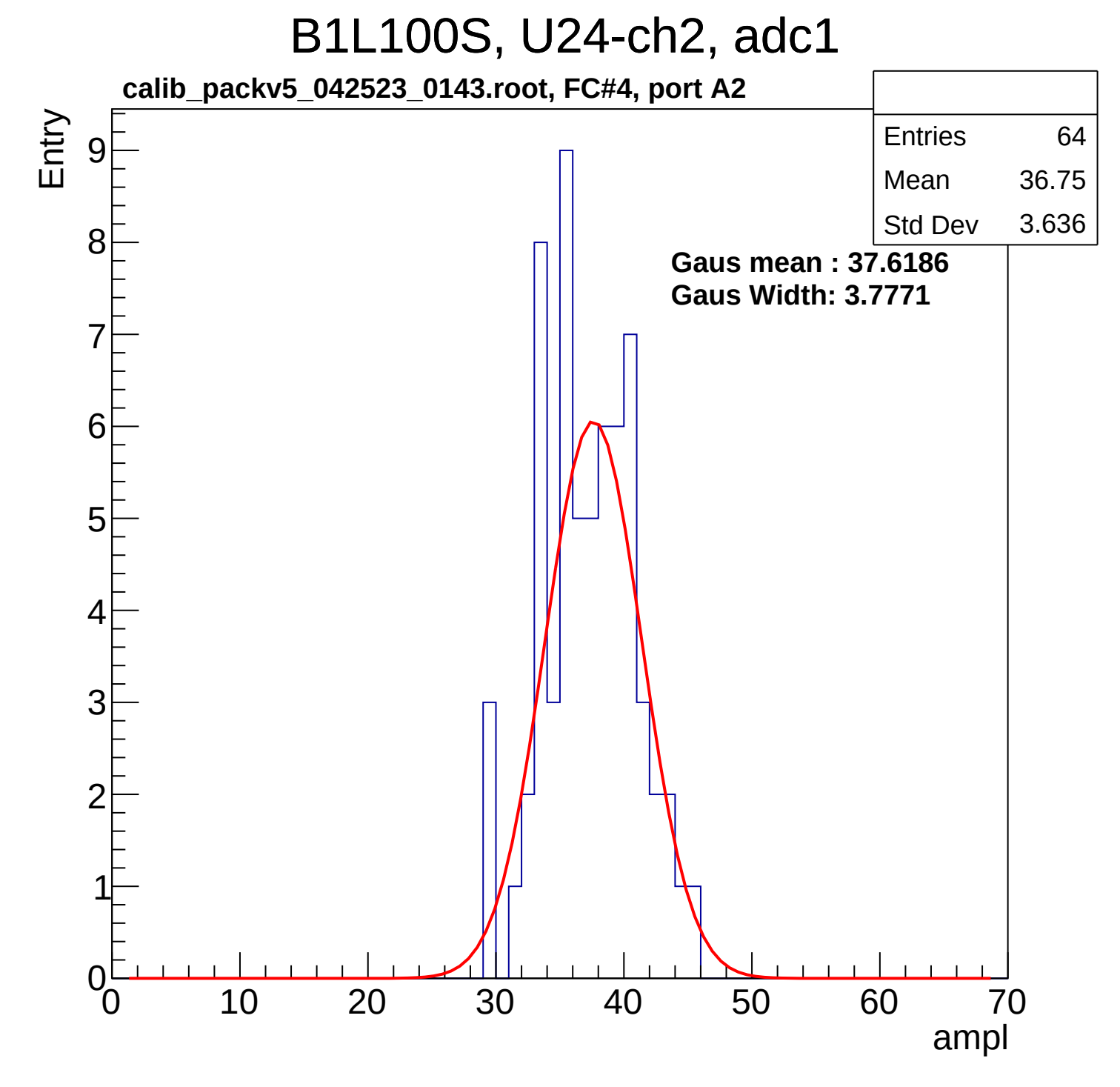
40
50
60
70


B1L100S, U24-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.75
Std Dev 3.636
Gaus mean : 37.6186
Gaus Width: 3.7771
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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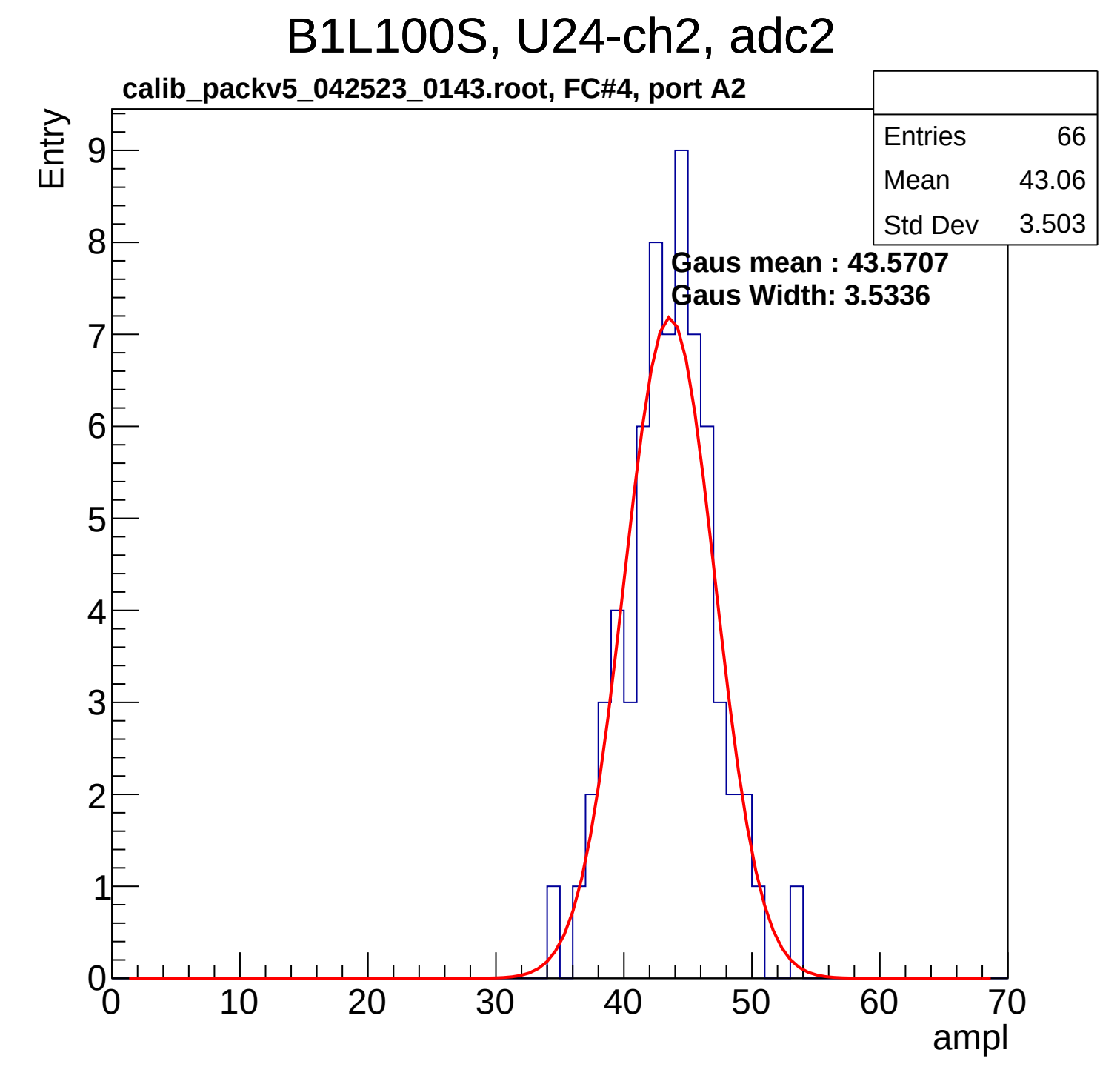
B1L100S, U24-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.06
Std Dev 3.503
Gaus mean : 43.5707
Gaus Width: 3.5336
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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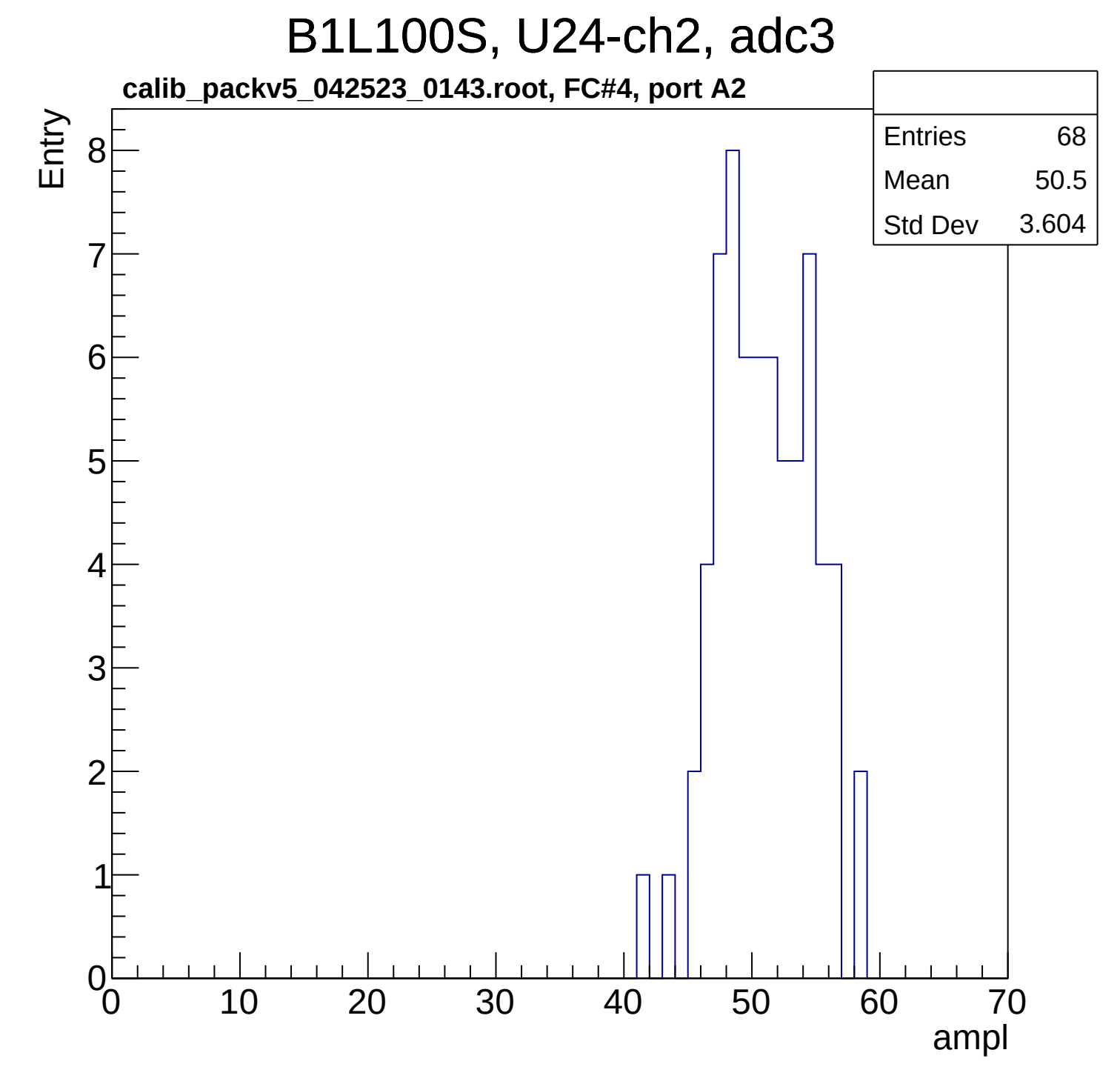
B1L100S, U24-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.5
Std Dev 3.604
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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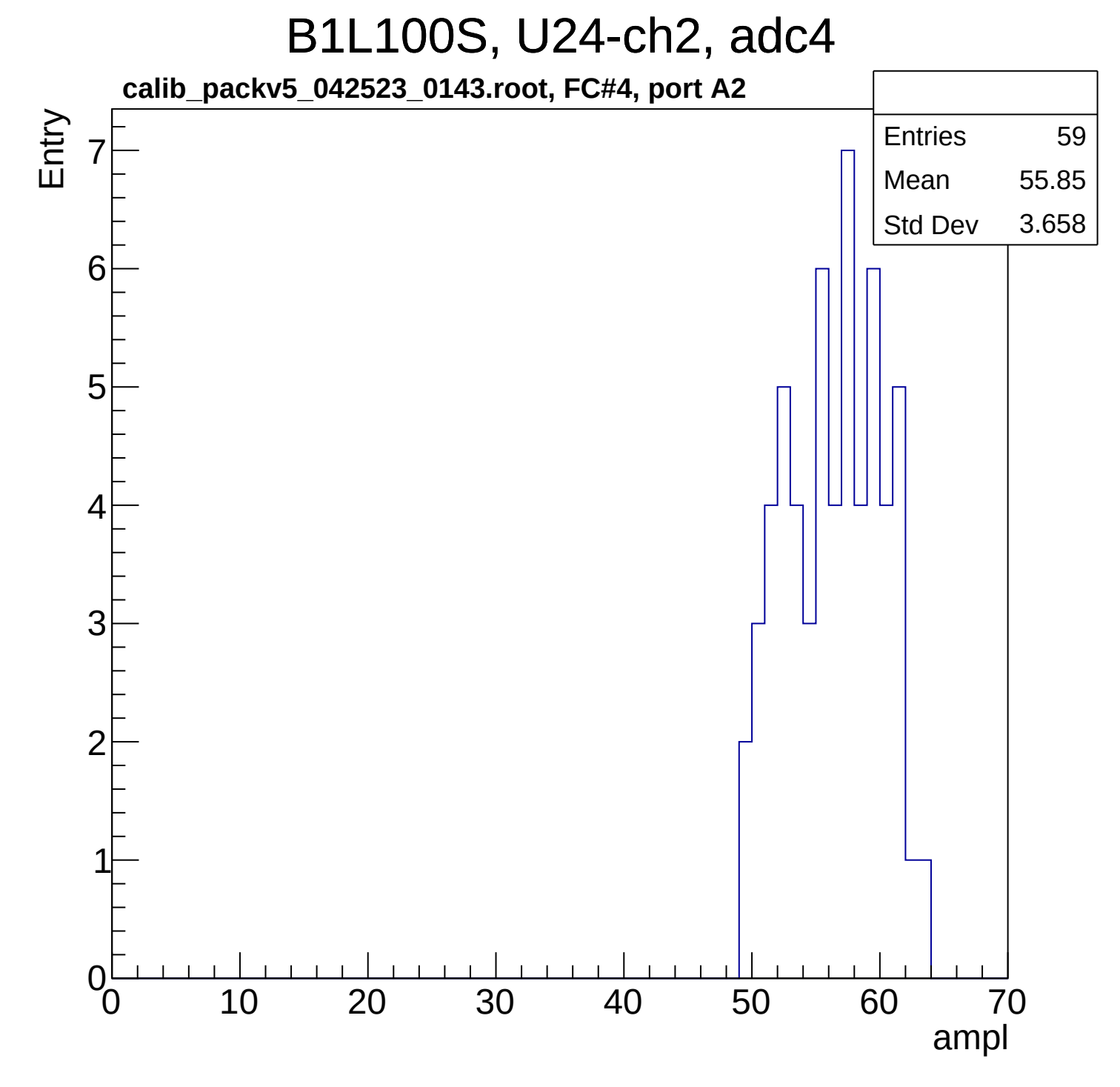
B1L100S, U24-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.85
Std Dev 3.658
Entry
ampl
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4
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0
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60
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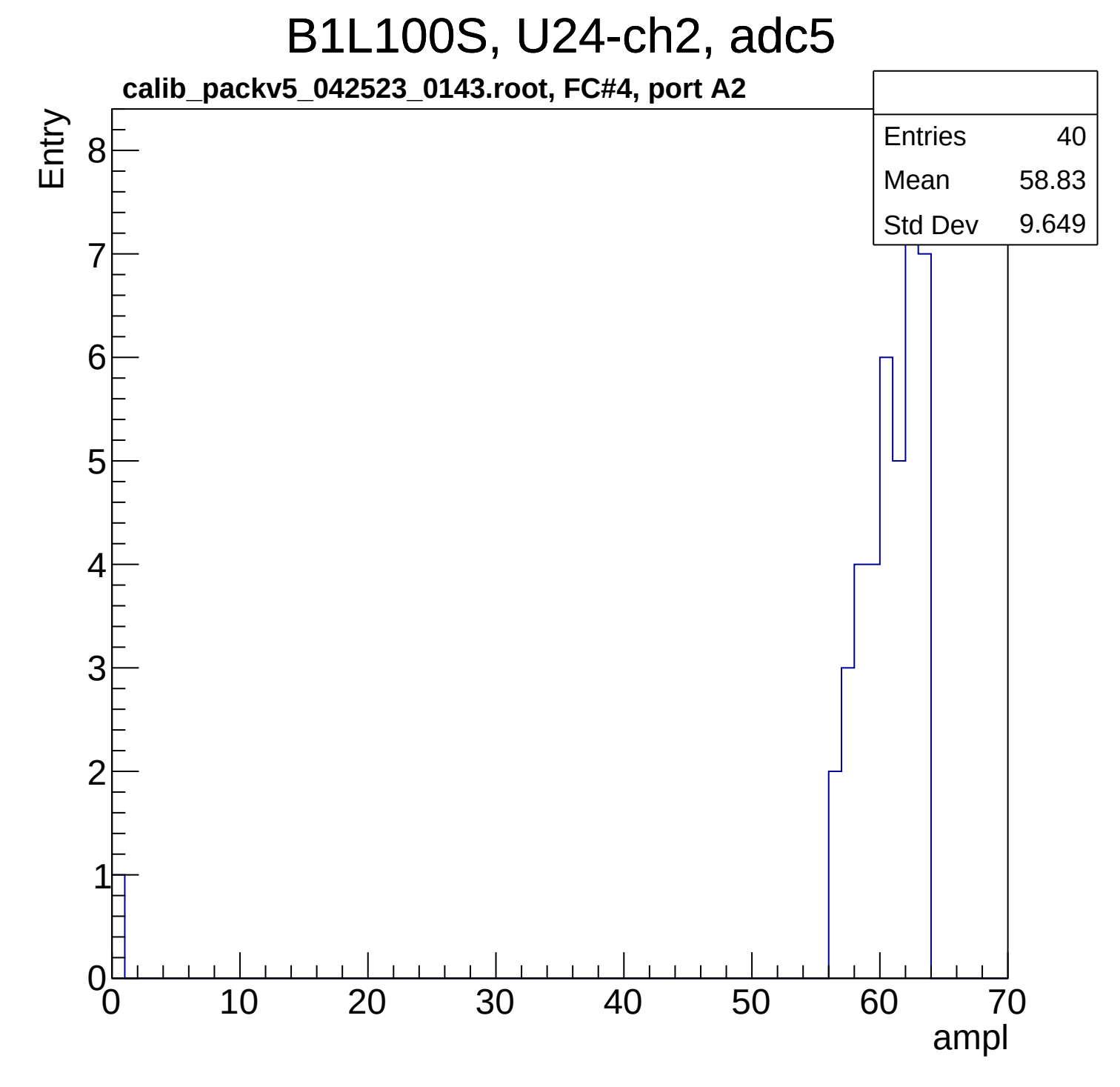
B1L100S, U24-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.83
Std Dev 9.649
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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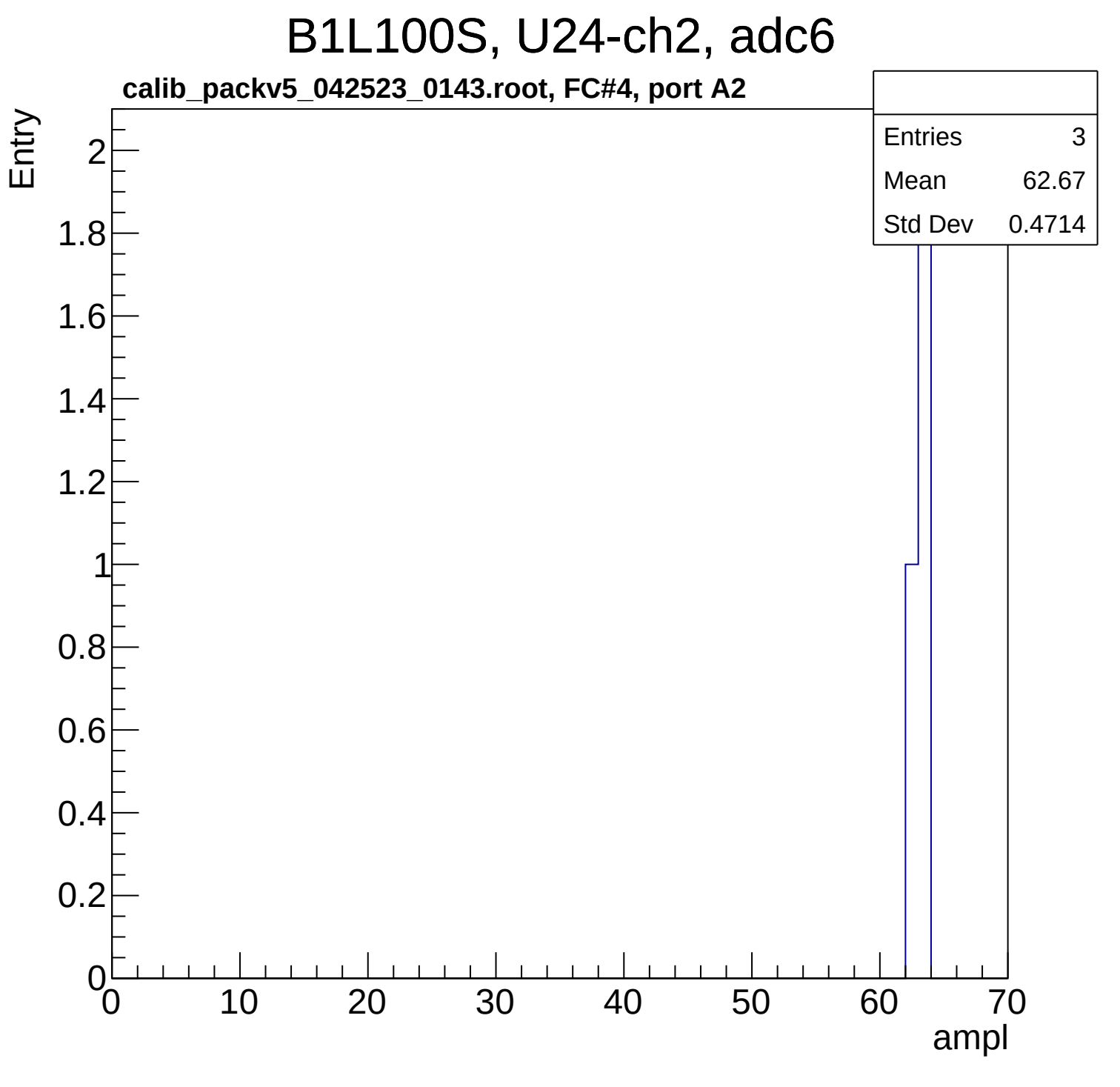
B1L100S, U24-ch2, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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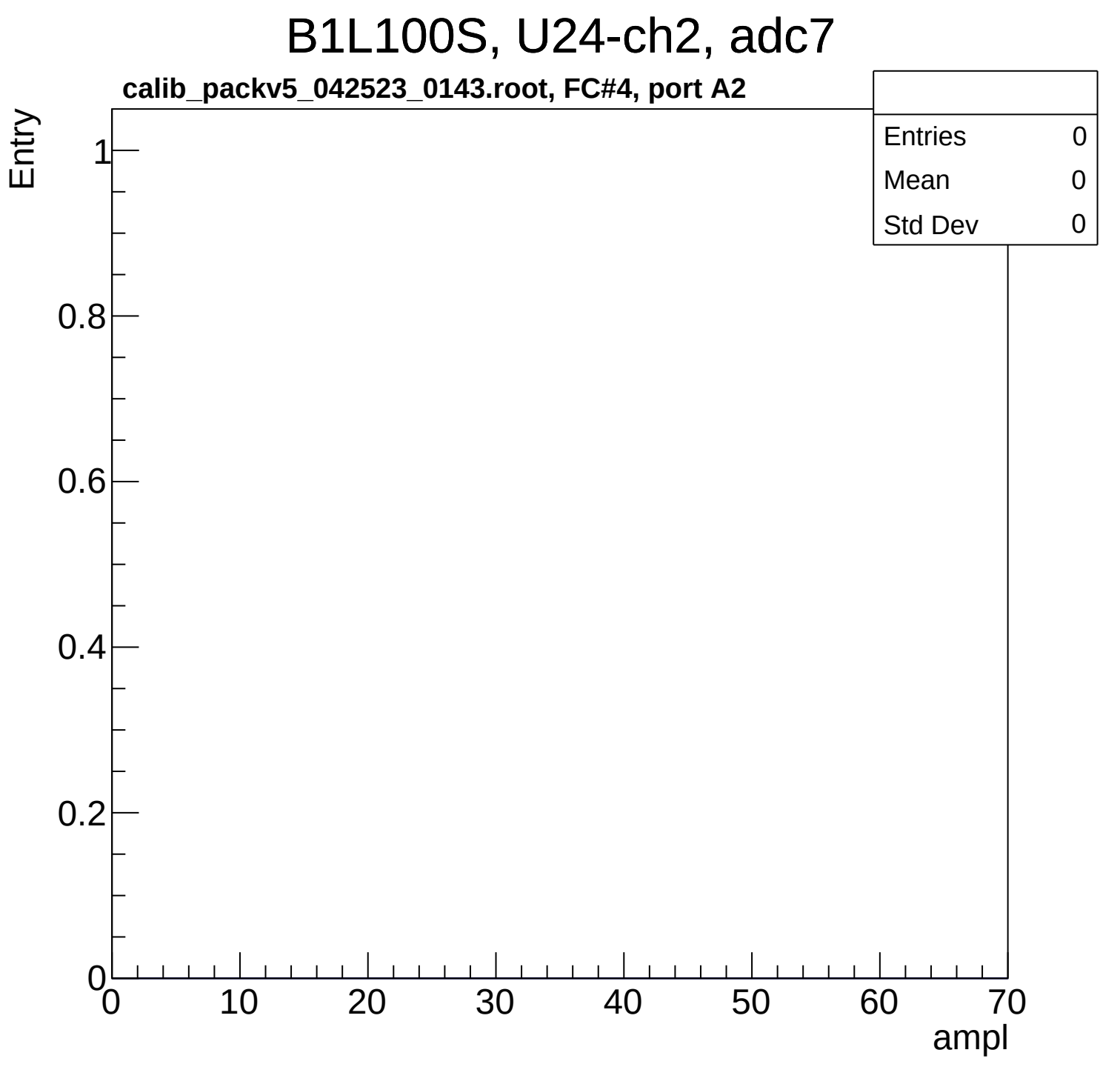
20
30
40
50
60
70


B1L100S, U24-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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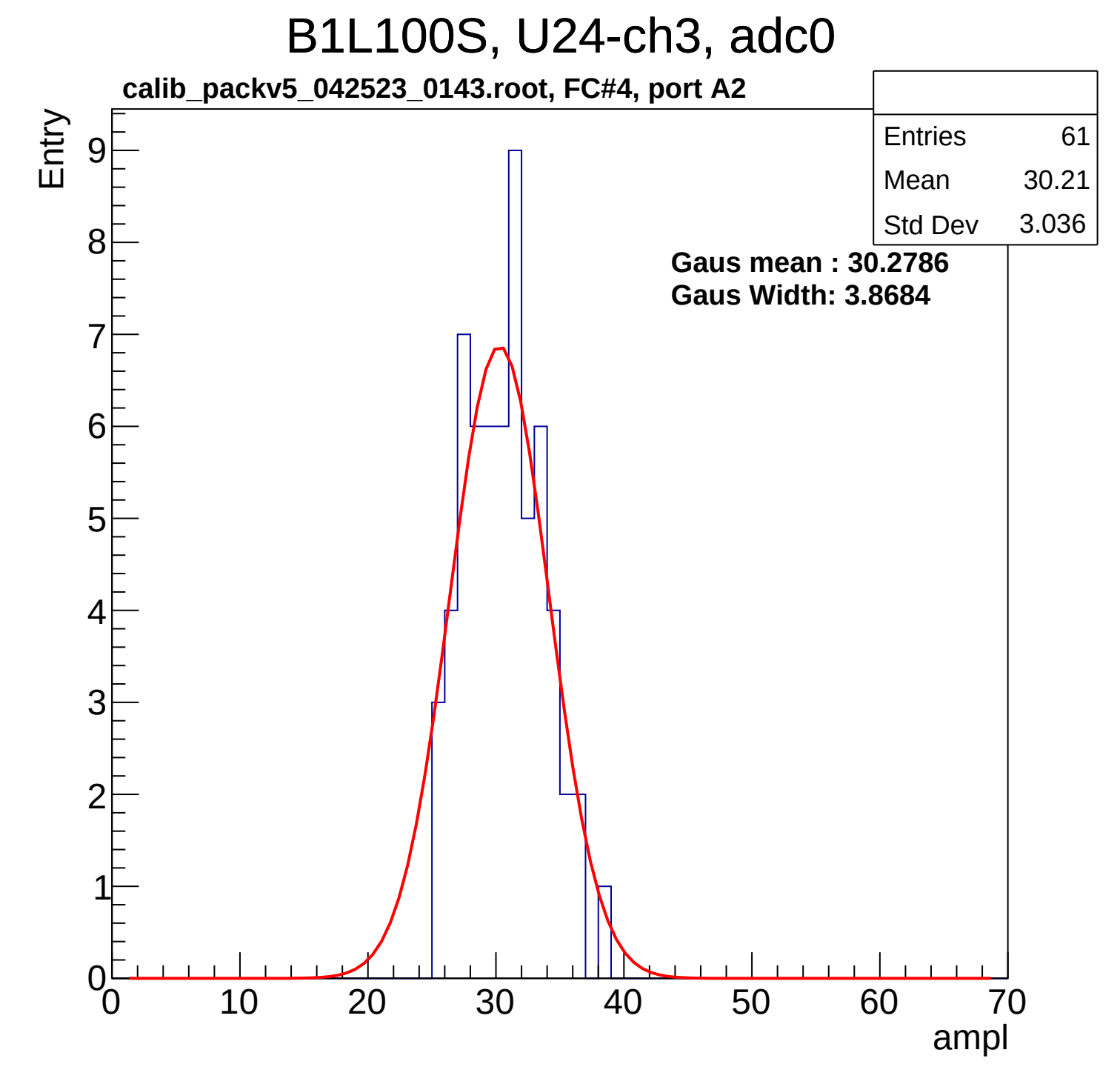
50
60
70
ampl


B1L100S, U24-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.21
Std Dev 3.036
Gaus mean : 30.2786
Gaus Width: 3.8684
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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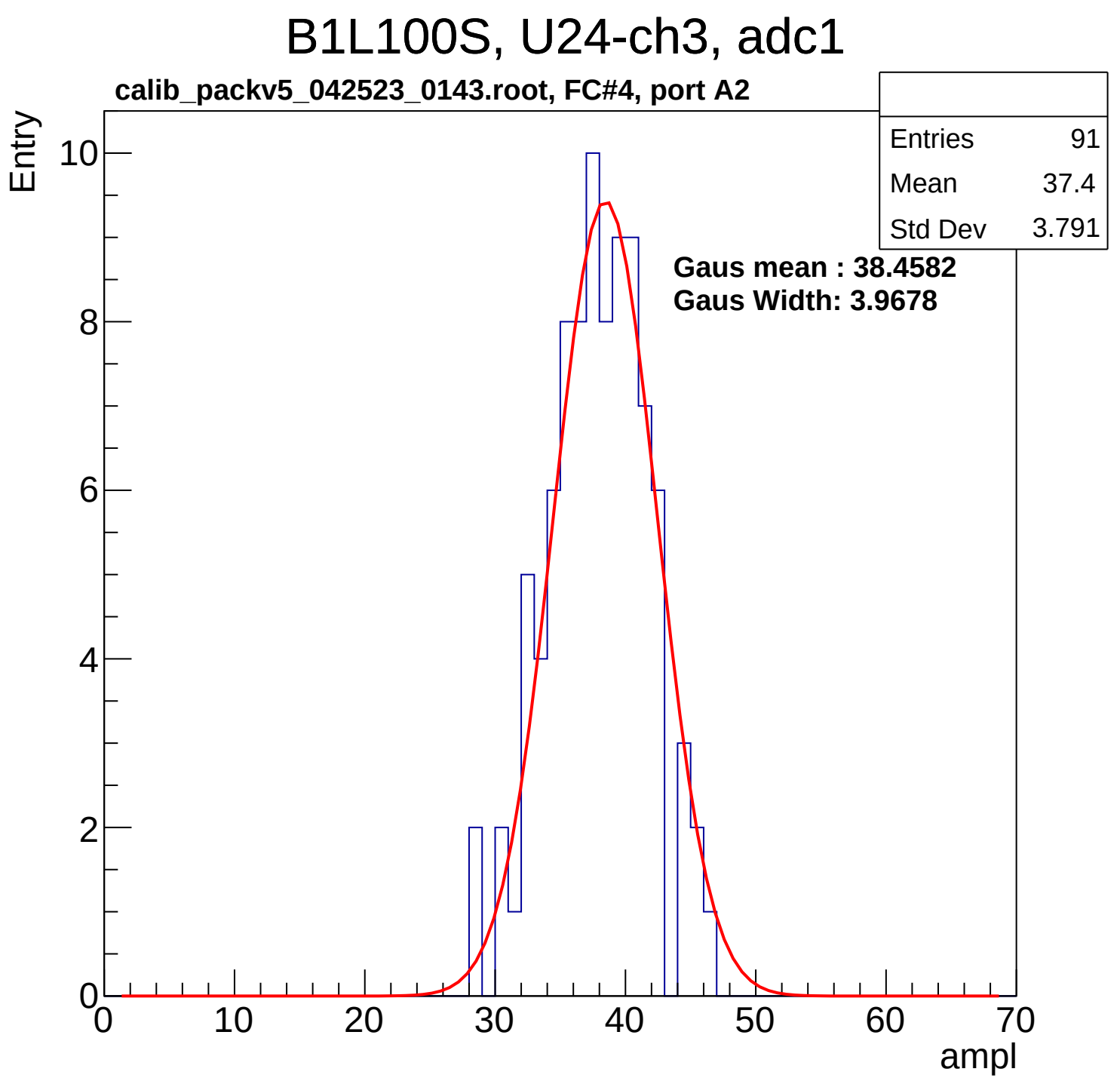
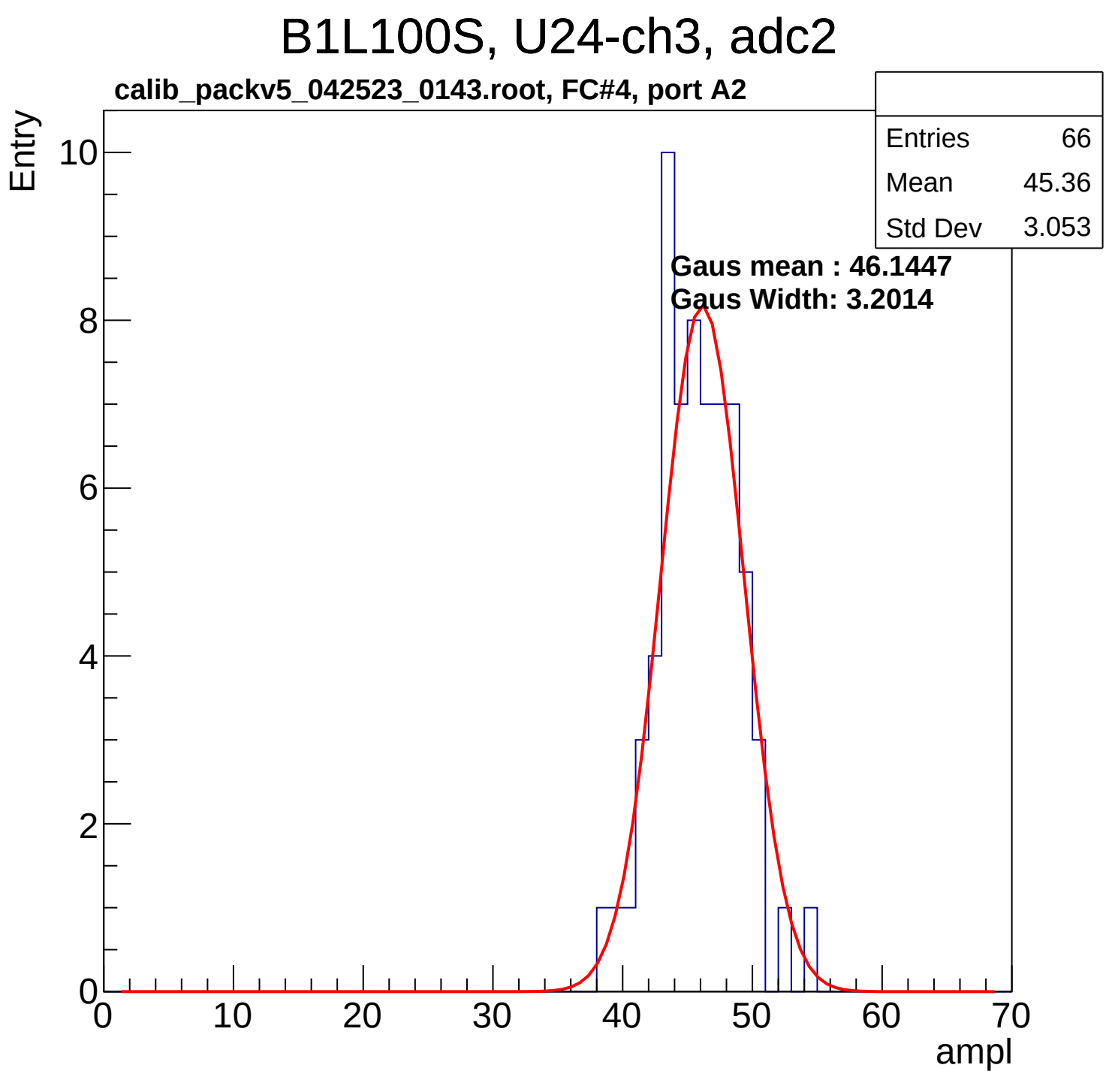
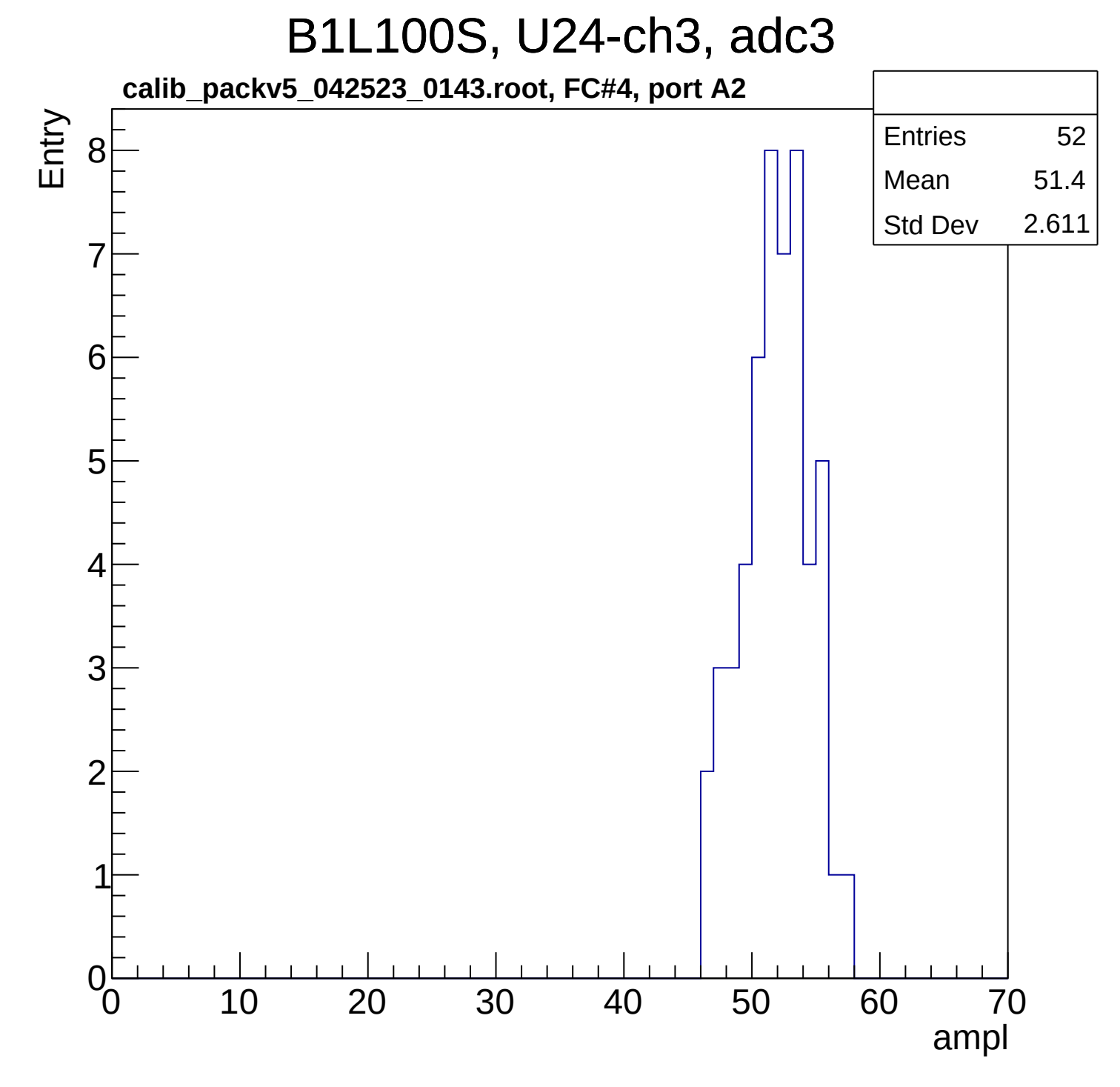

B1L100S, U24-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 37.4
Std Dev 3.791
Gaus mean : 38.4582
Gaus Width: 3.9678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 45.36
Std Dev 3.053
Gaus mean : 46.1447
Gaus Width: 3.2014
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 51.4
Std Dev 2.611
Entry
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2
3
4
5
6
7
8
0
10
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30
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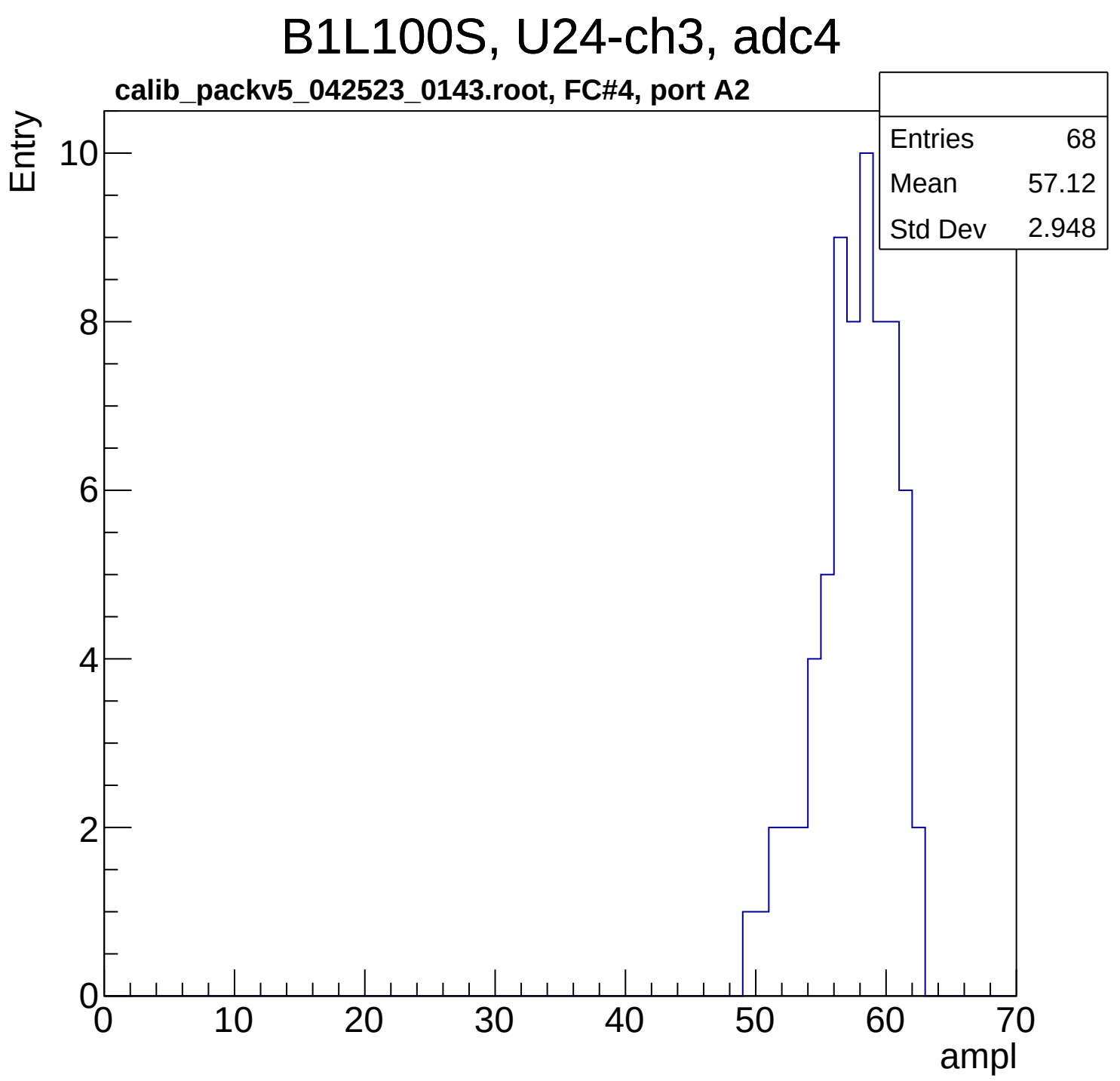
50
60
70
ampl


B1L100S, U24-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 57.12
Std Dev 2.948
Entry
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2
4
6
8
10
0
10
20
30
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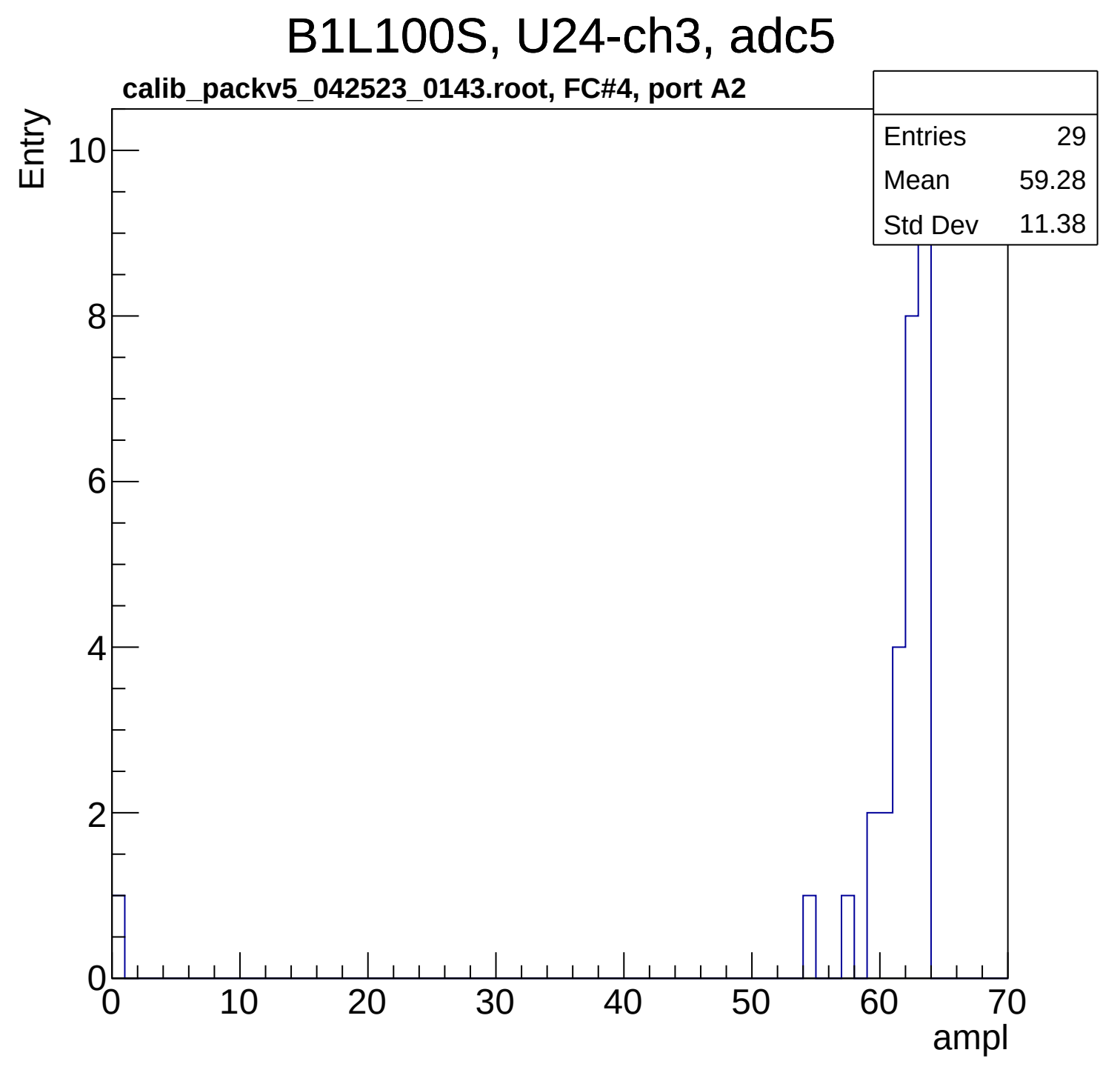
50
60
70
ampl


B1L100S, U24-ch3, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 59.28
Std Dev 11.38
Entry
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8
10
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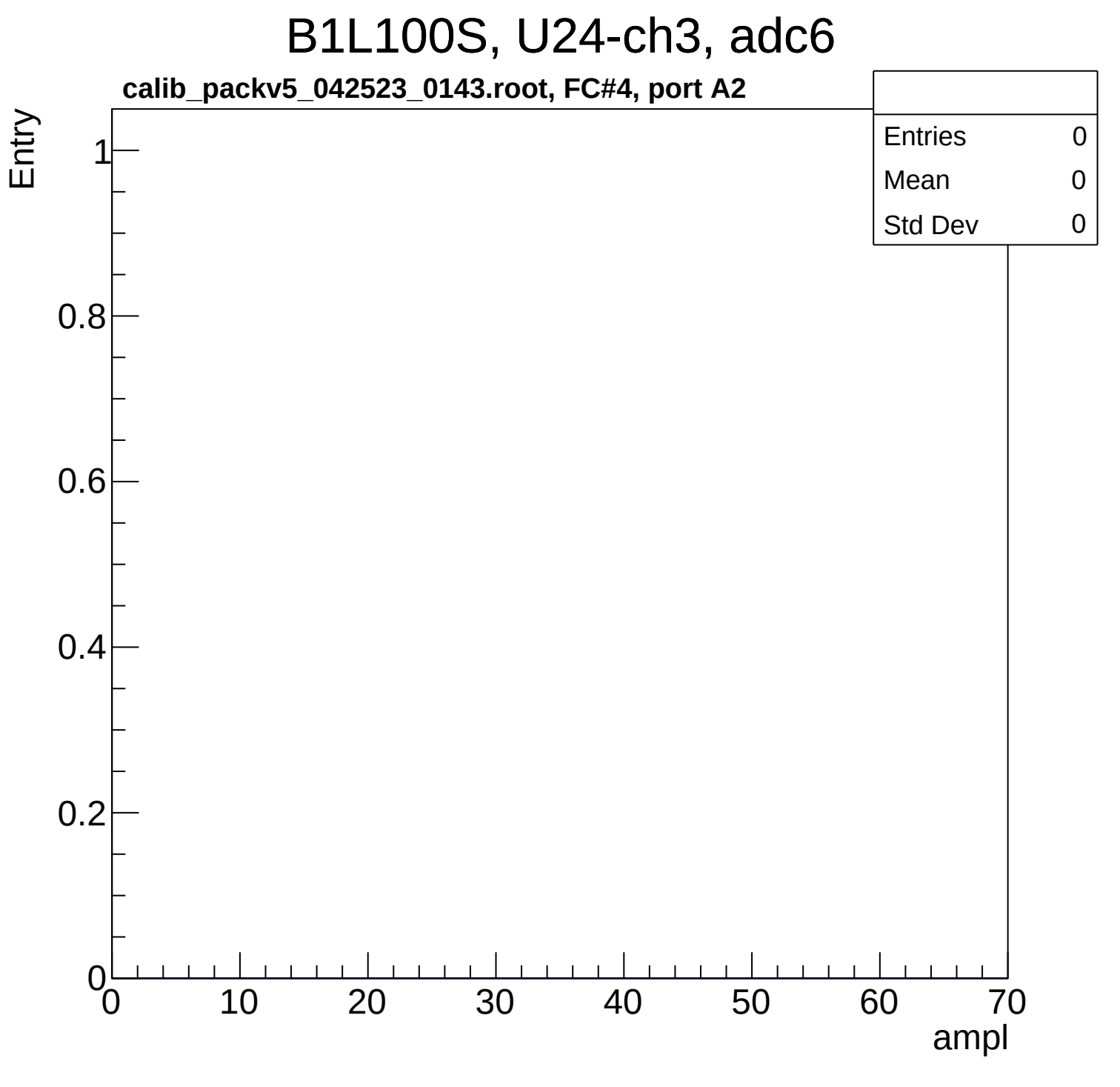
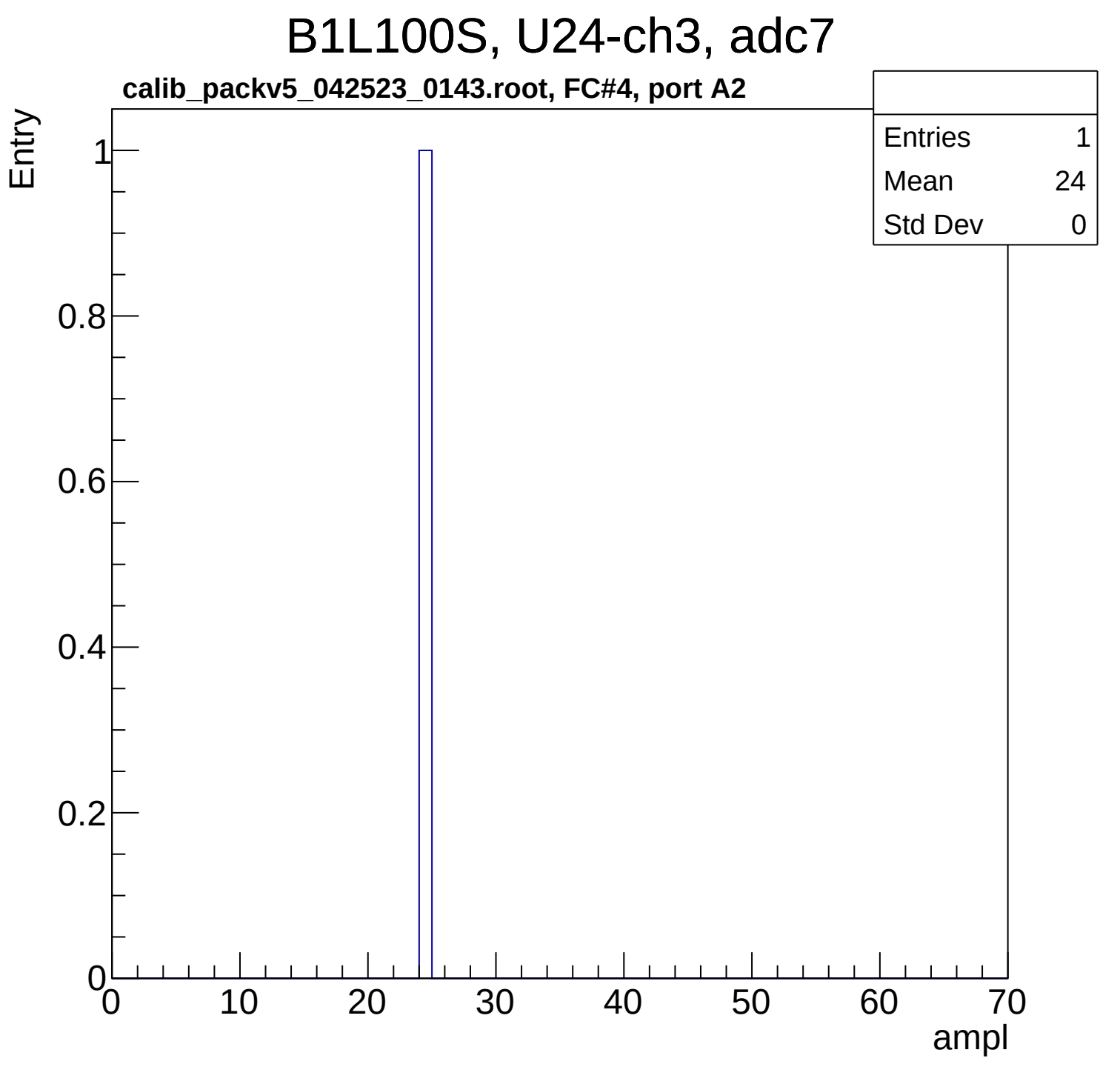
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch3, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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50
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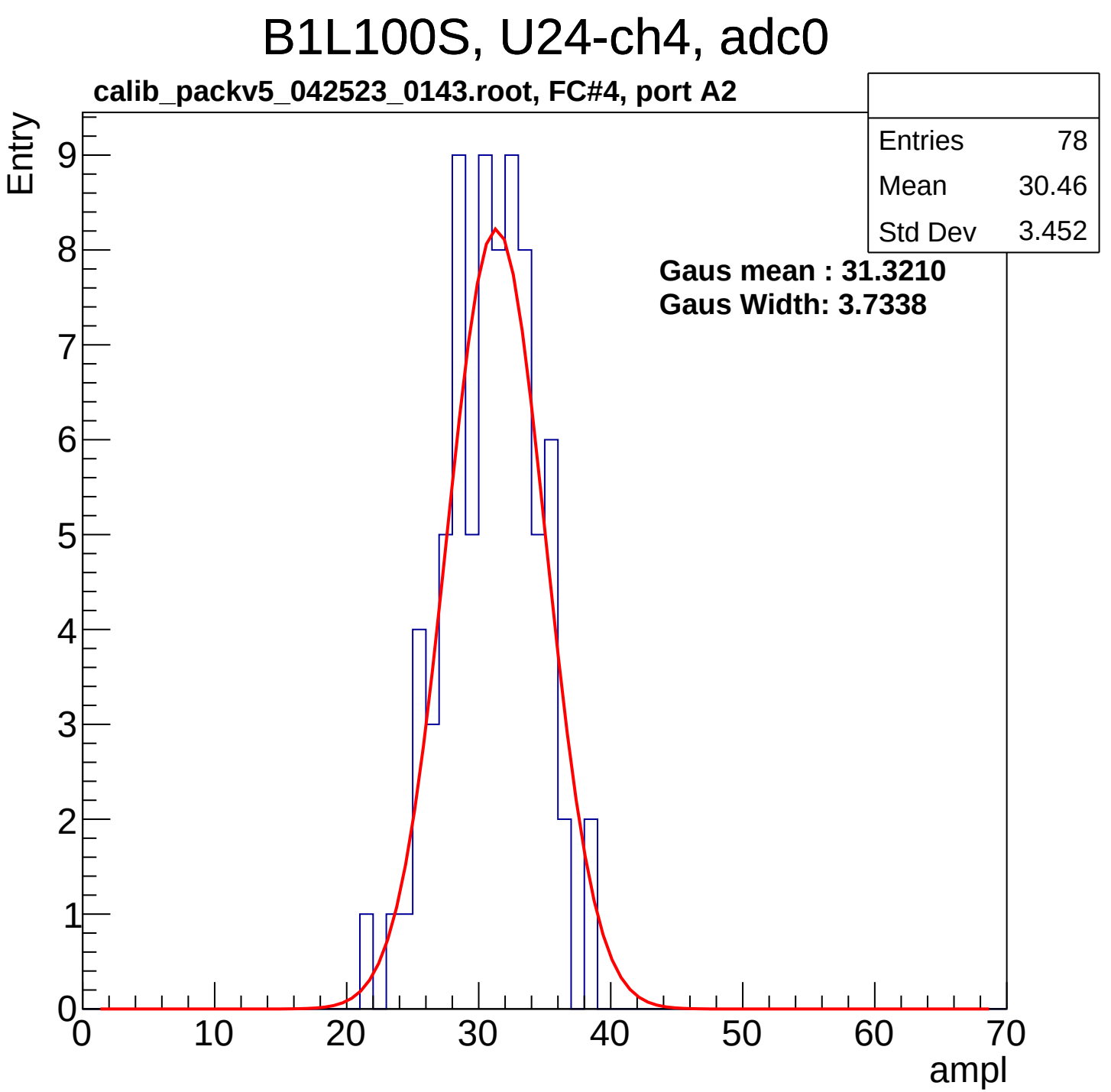
70
ampl


B1L100S, U24-ch4, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 30.46
Std Dev 3.452
Gaus mean : 31.3210
Gaus Width: 3.7338
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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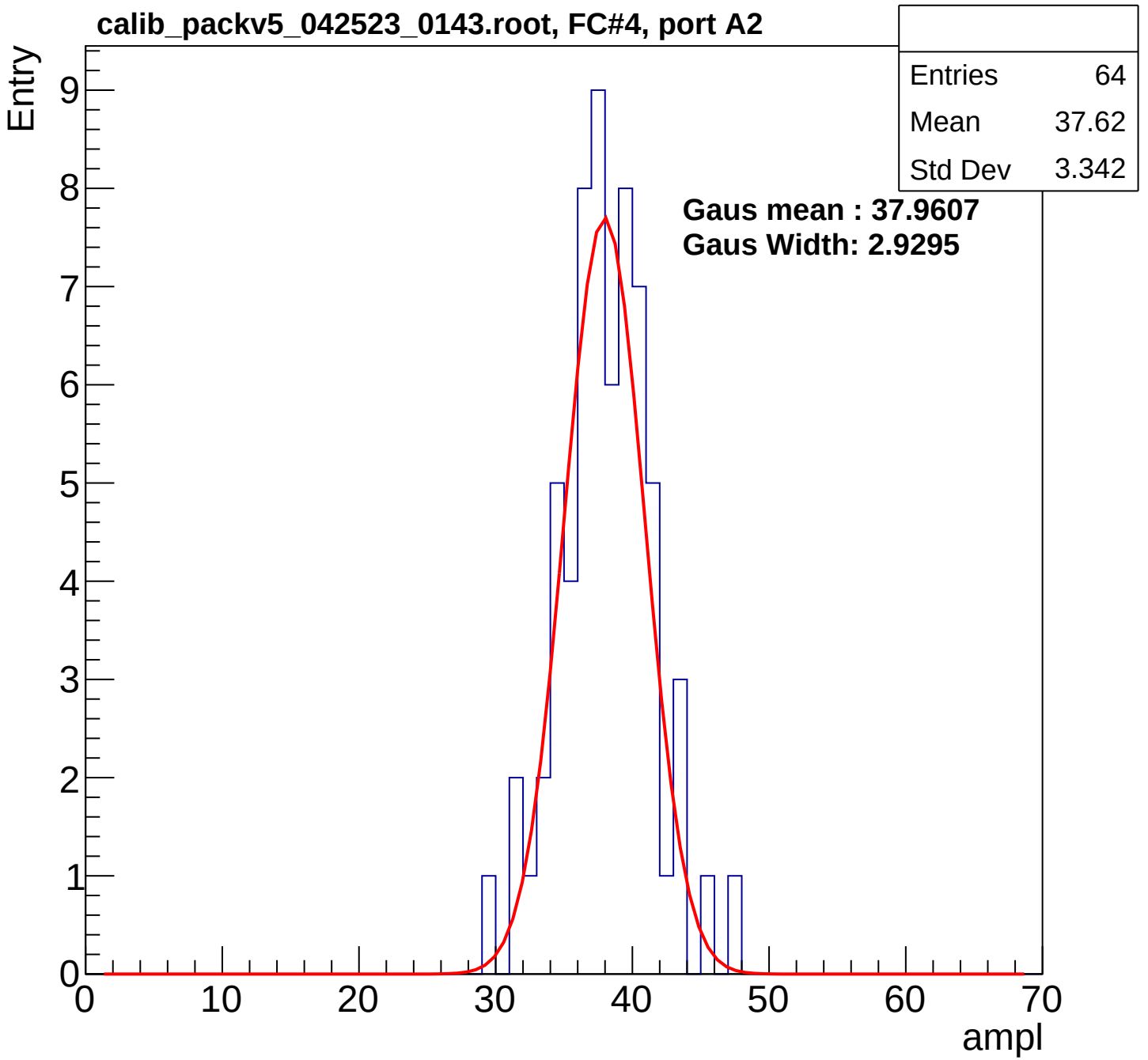
B1L100S, U24-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.62
Std Dev 3.342
Gaus mean : 37.9607
Gaus Width: 2.9295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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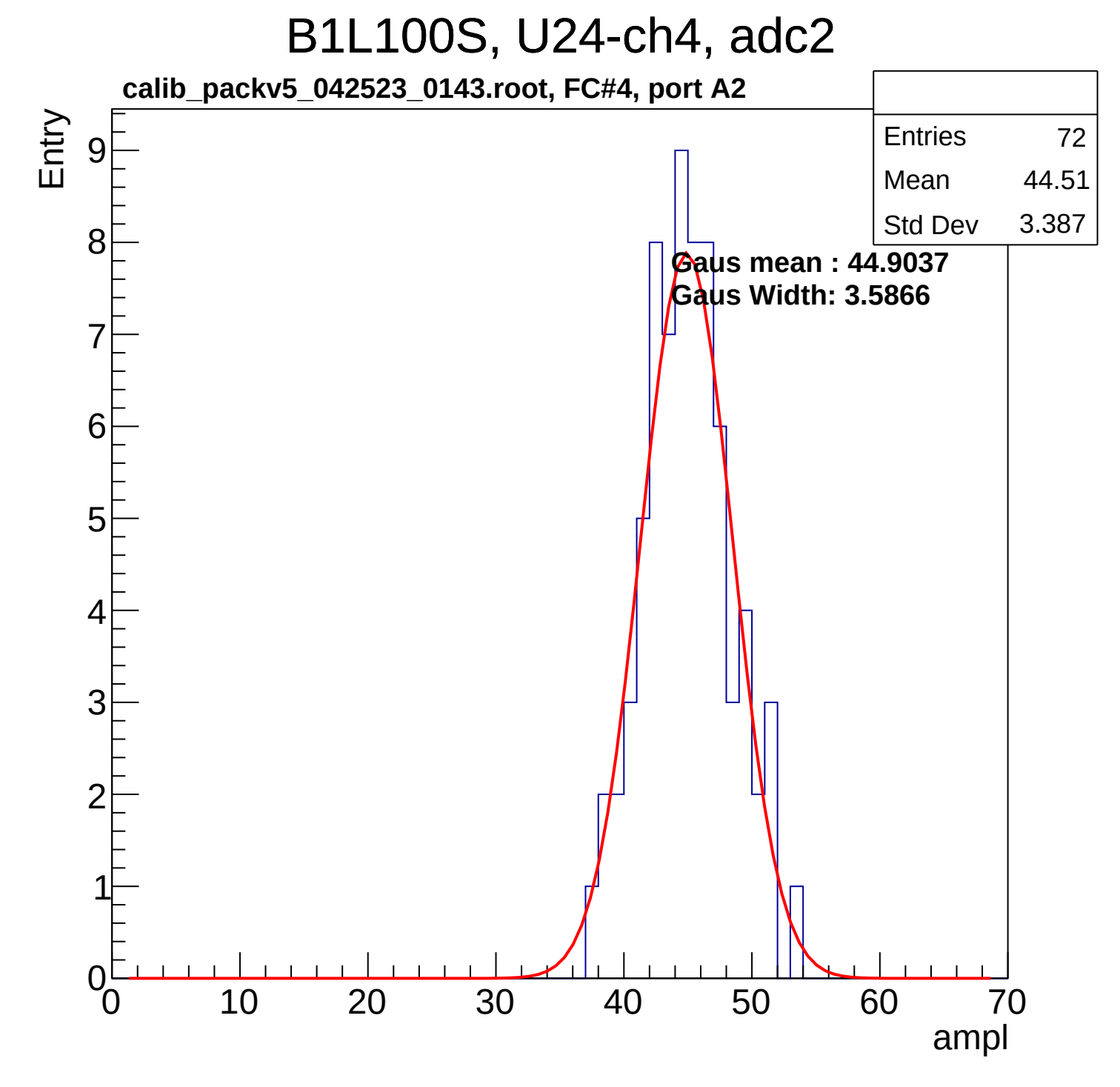
B1L100S, U24-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.51
Std Dev 3.387
Gaus mean : 44.9037
Gaus Width: 3.5866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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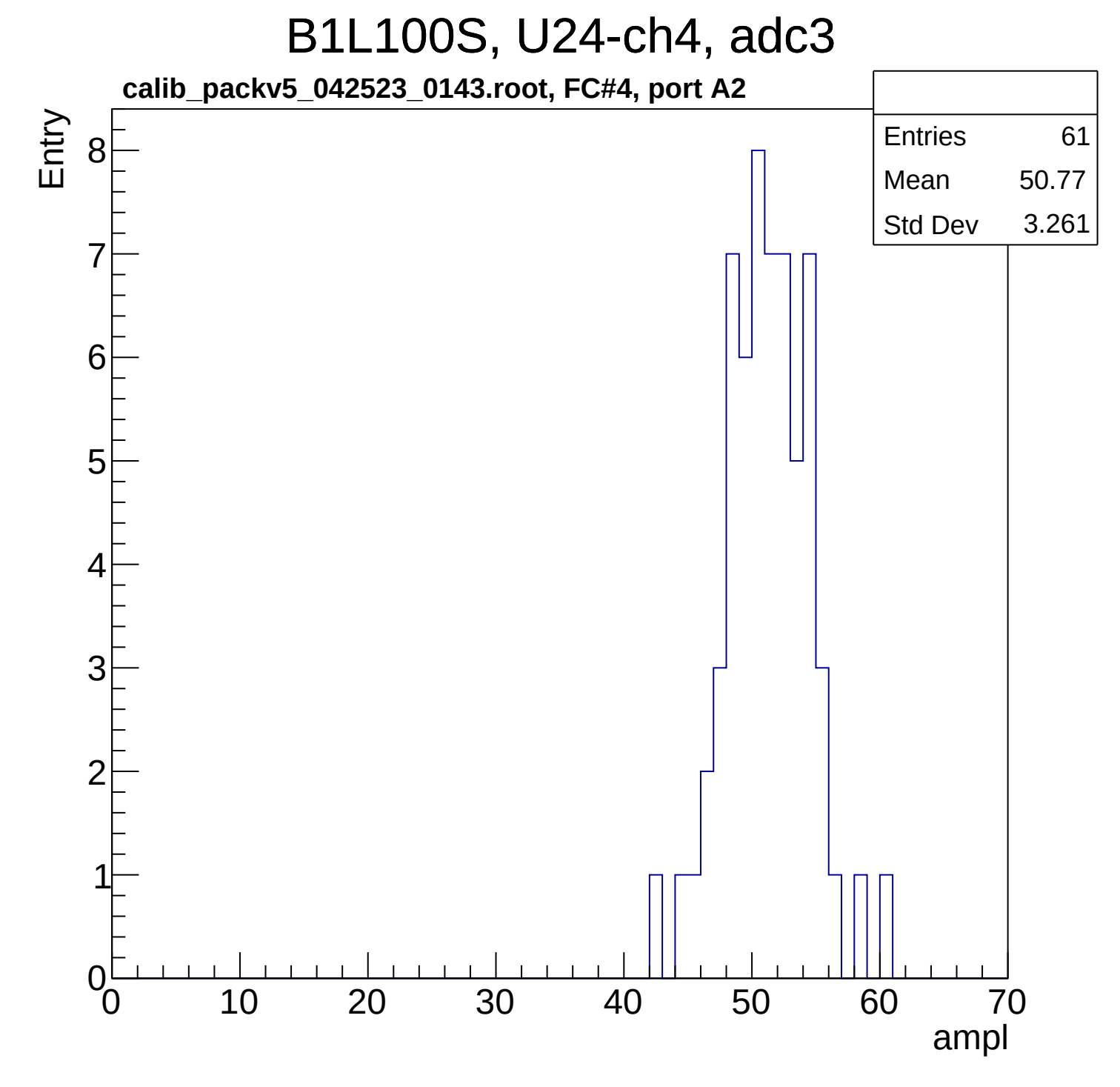
B1L100S, U24-ch4, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.77
Std Dev 3.261
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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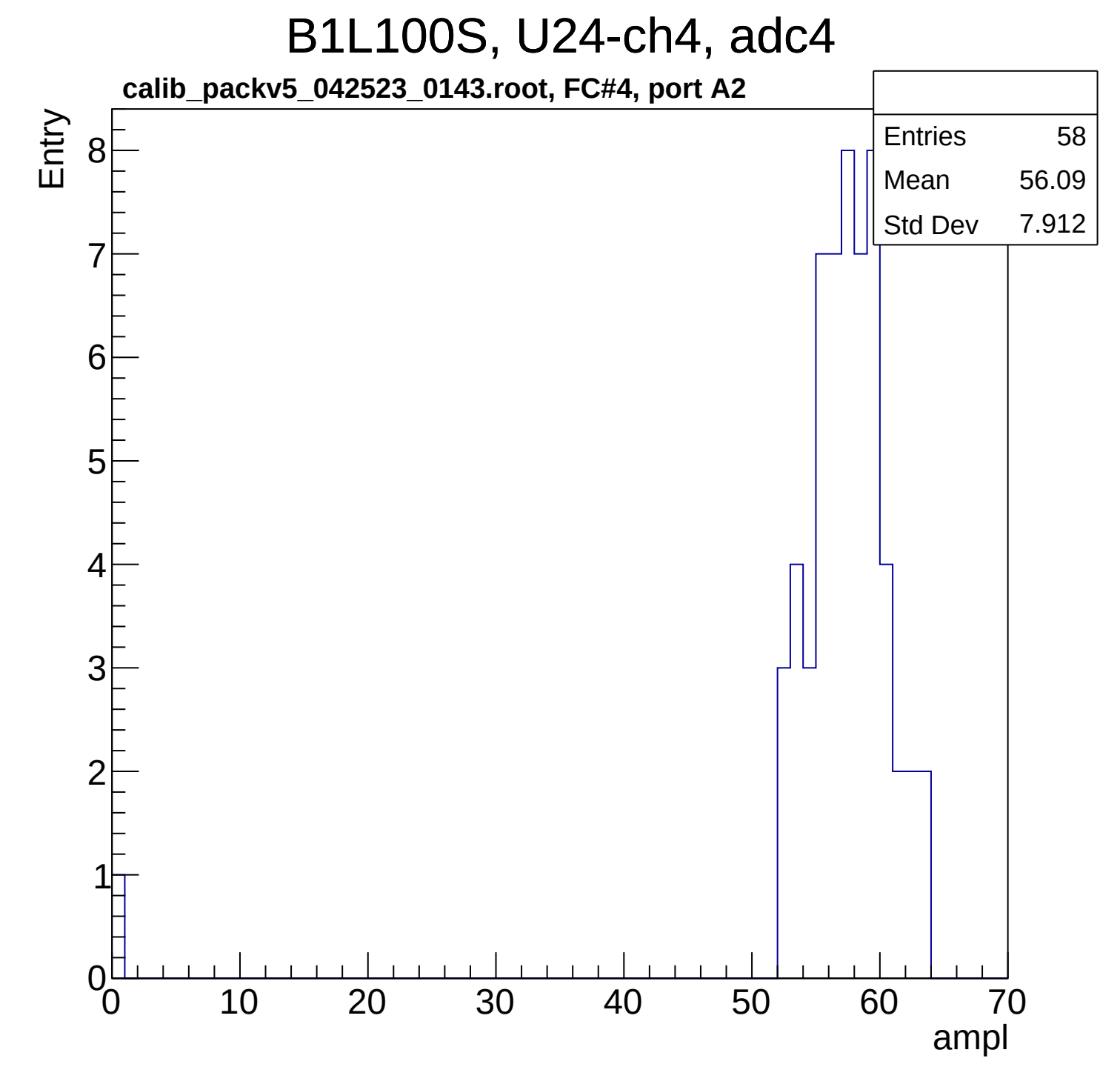
B1L100S, U24-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.09
Std Dev 7.912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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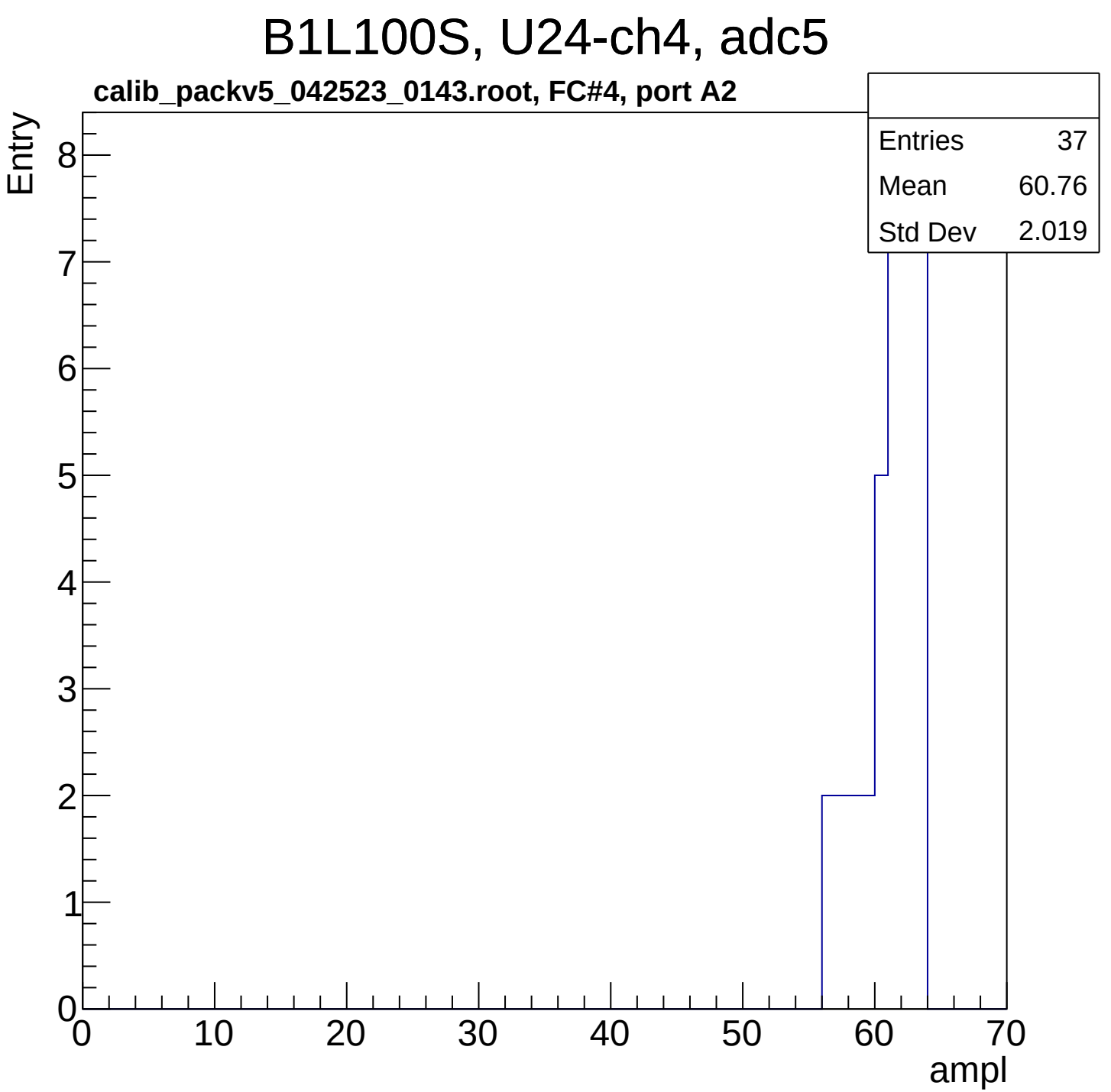
B1L100S, U24-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.76
Std Dev 2.019
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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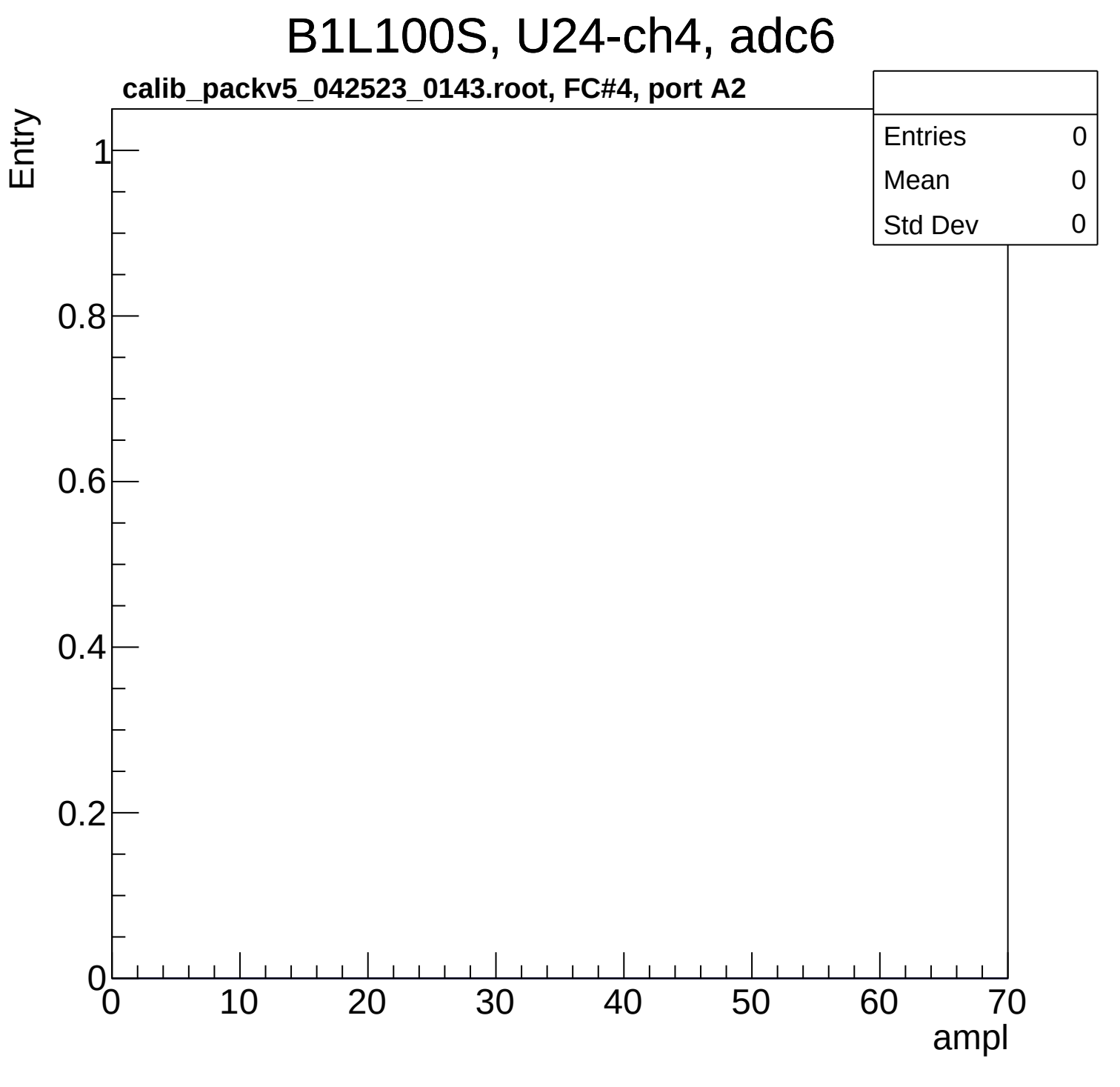
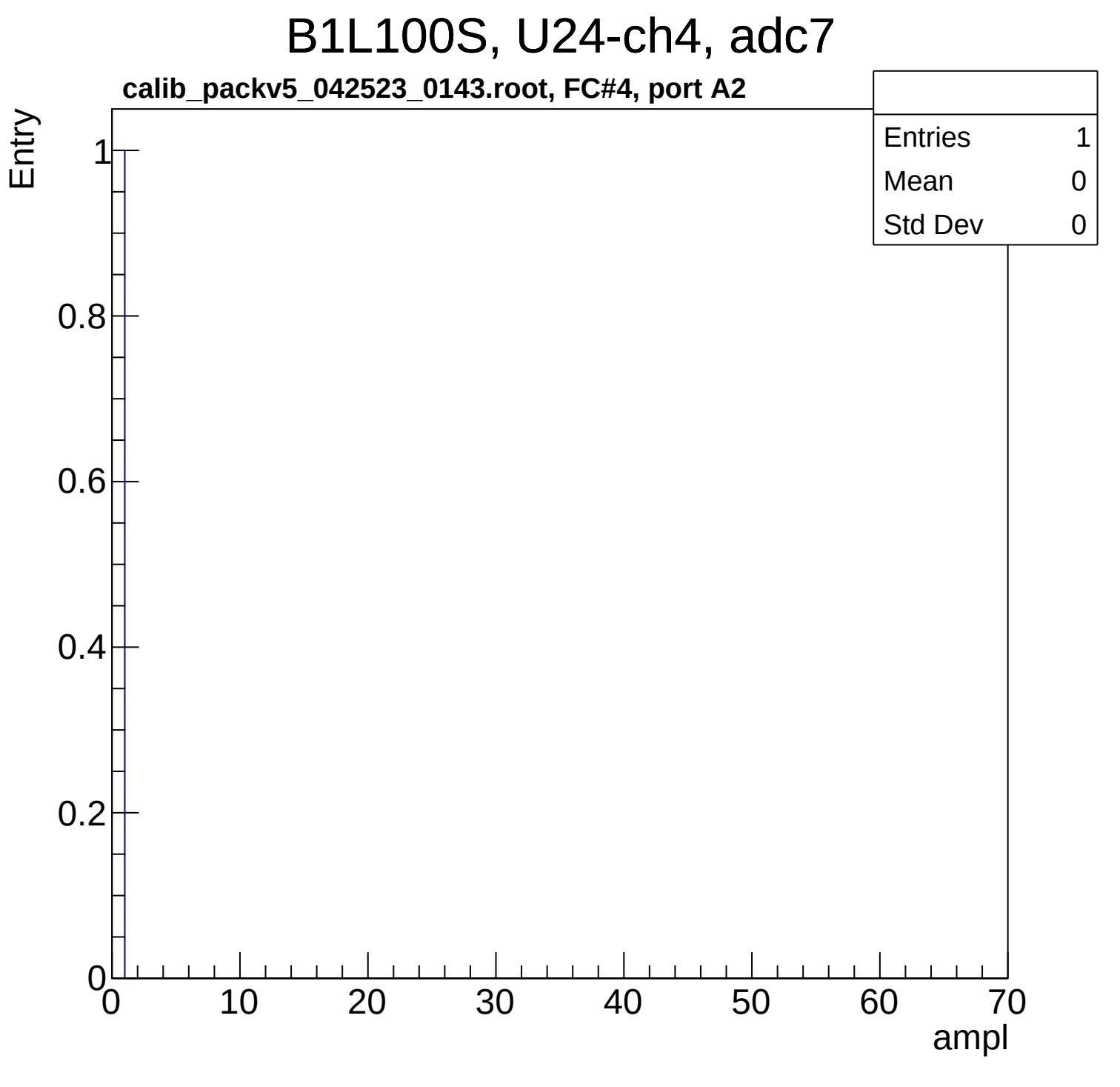
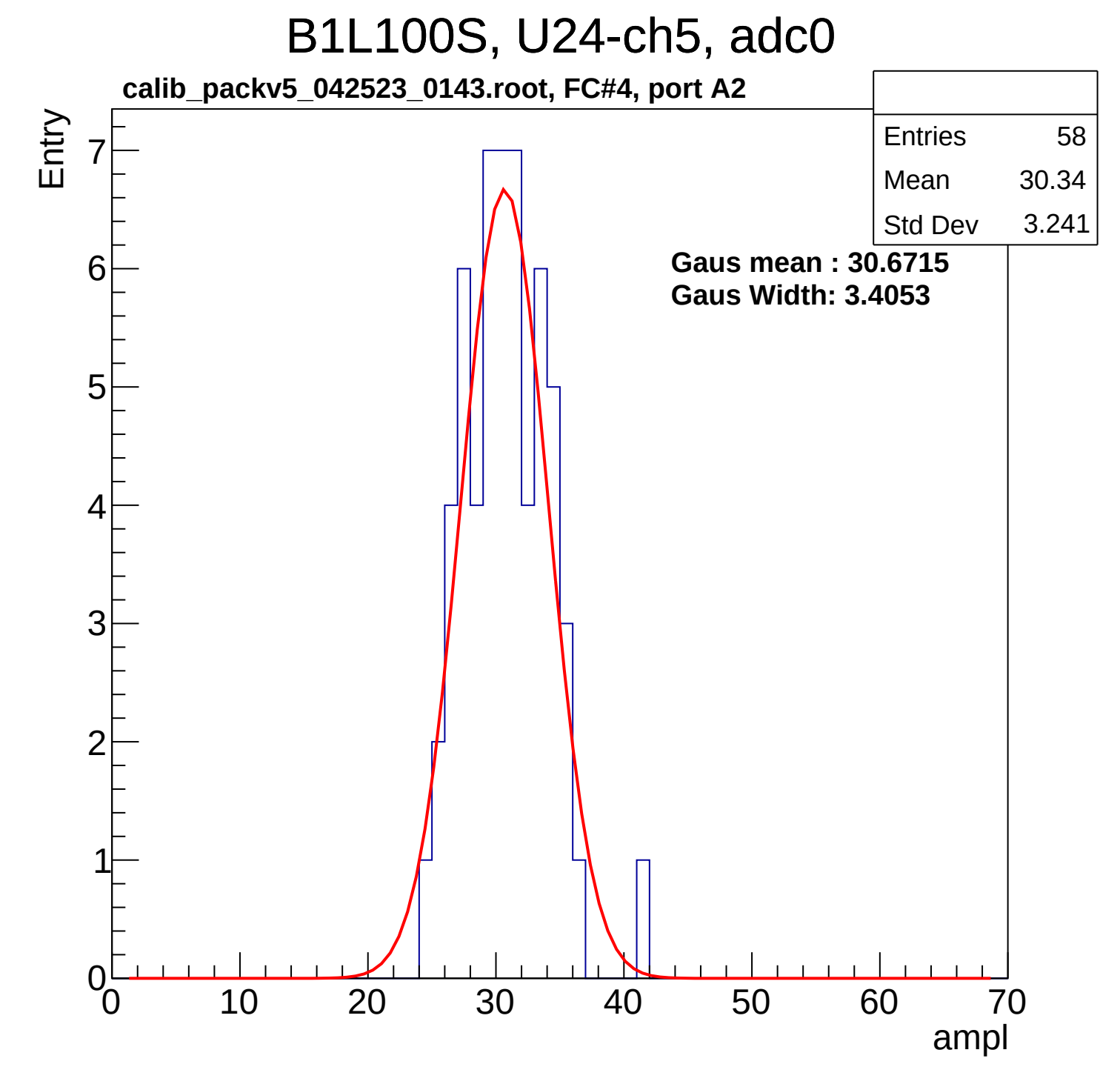
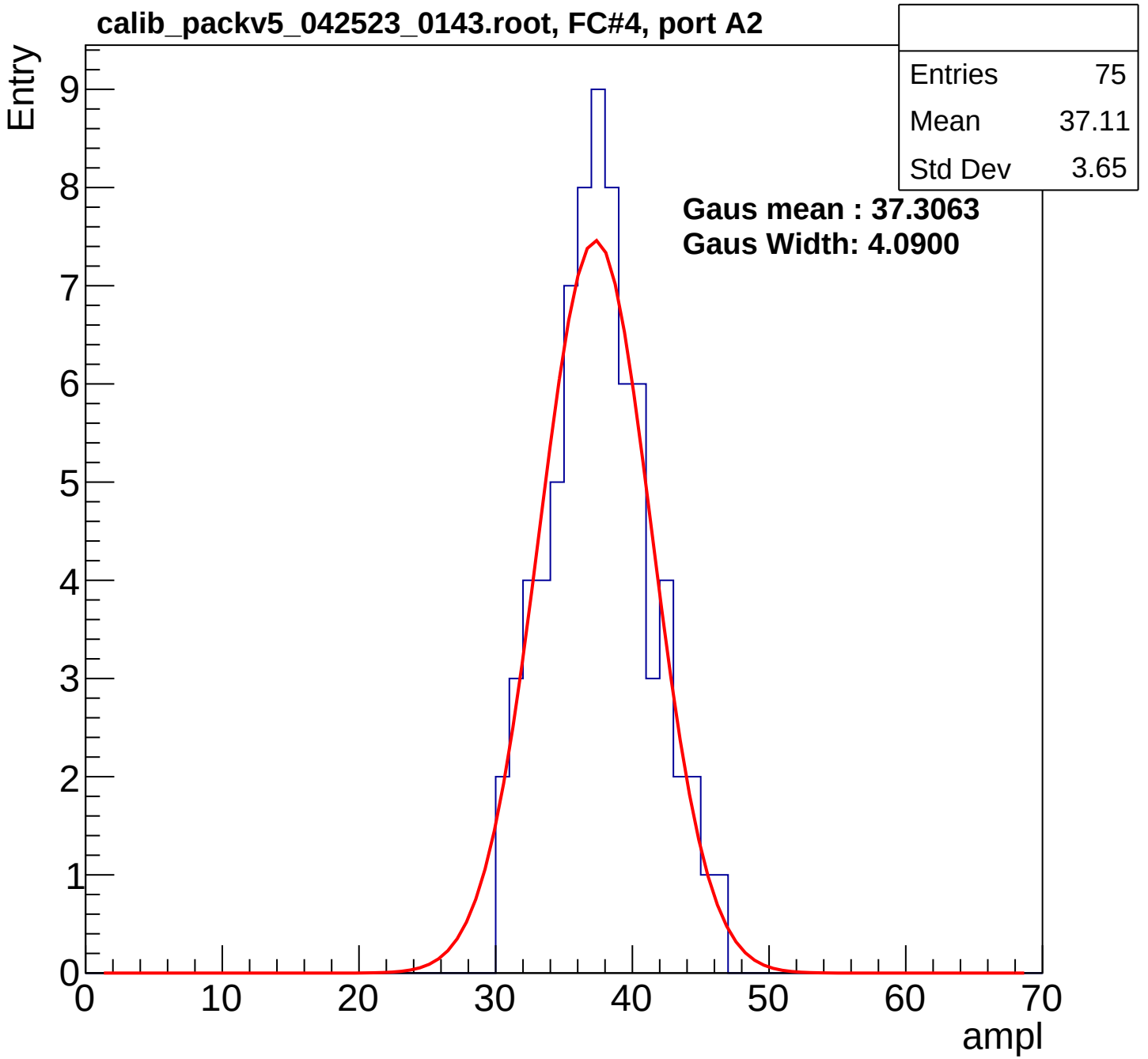

B1L100S, U24-ch4, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 30.34
Std Dev 3.241
Gaus mean : 30.6715
Gaus Width: 3.4053
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.11
Std Dev 3.65
Gaus mean : 37.3063
Gaus Width: 4.0900
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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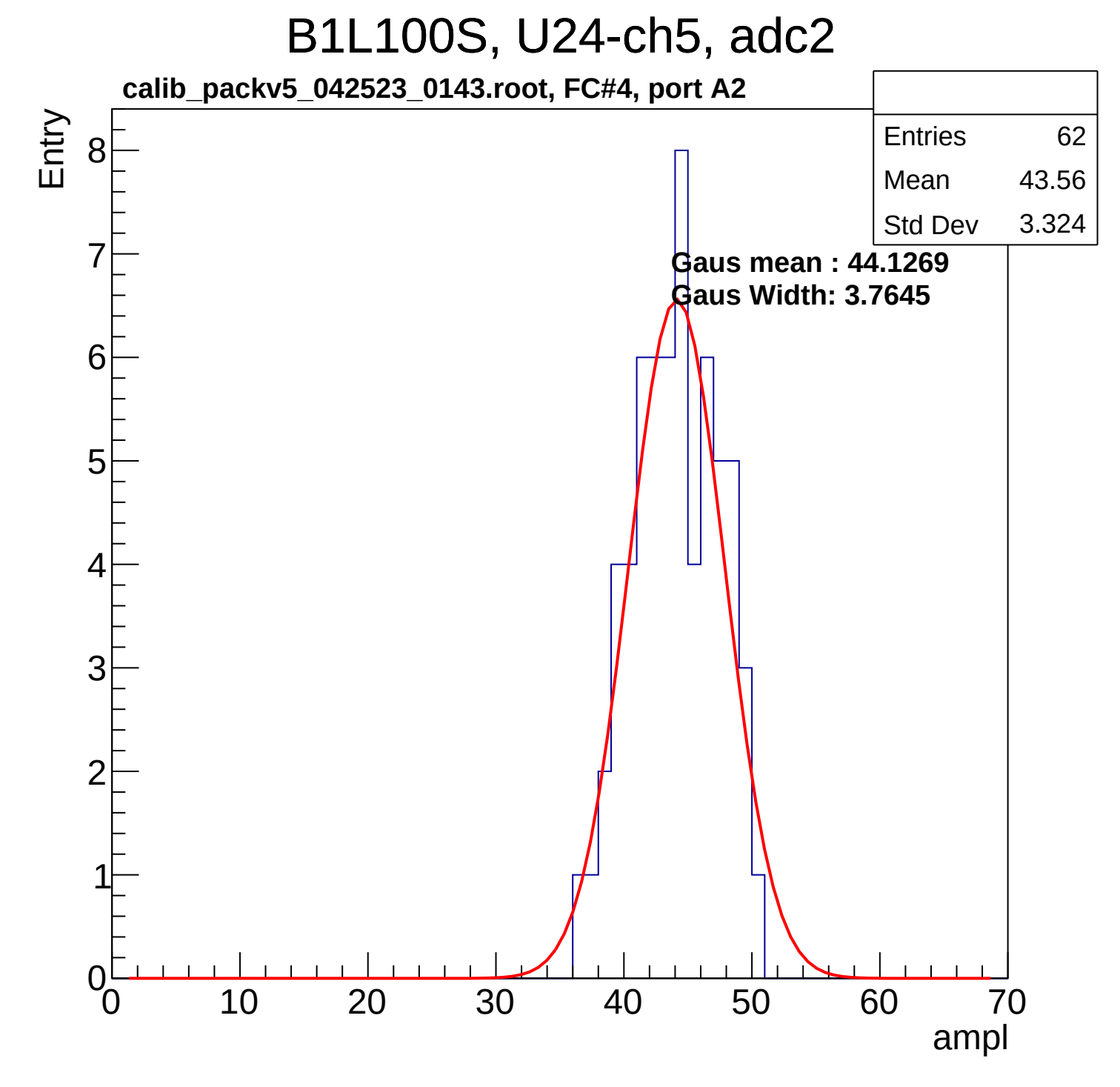
B1L100S, U24-ch5, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.56
Std Dev 3.324
Gaus mean : 44.1269
Gaus Width: 3.7645
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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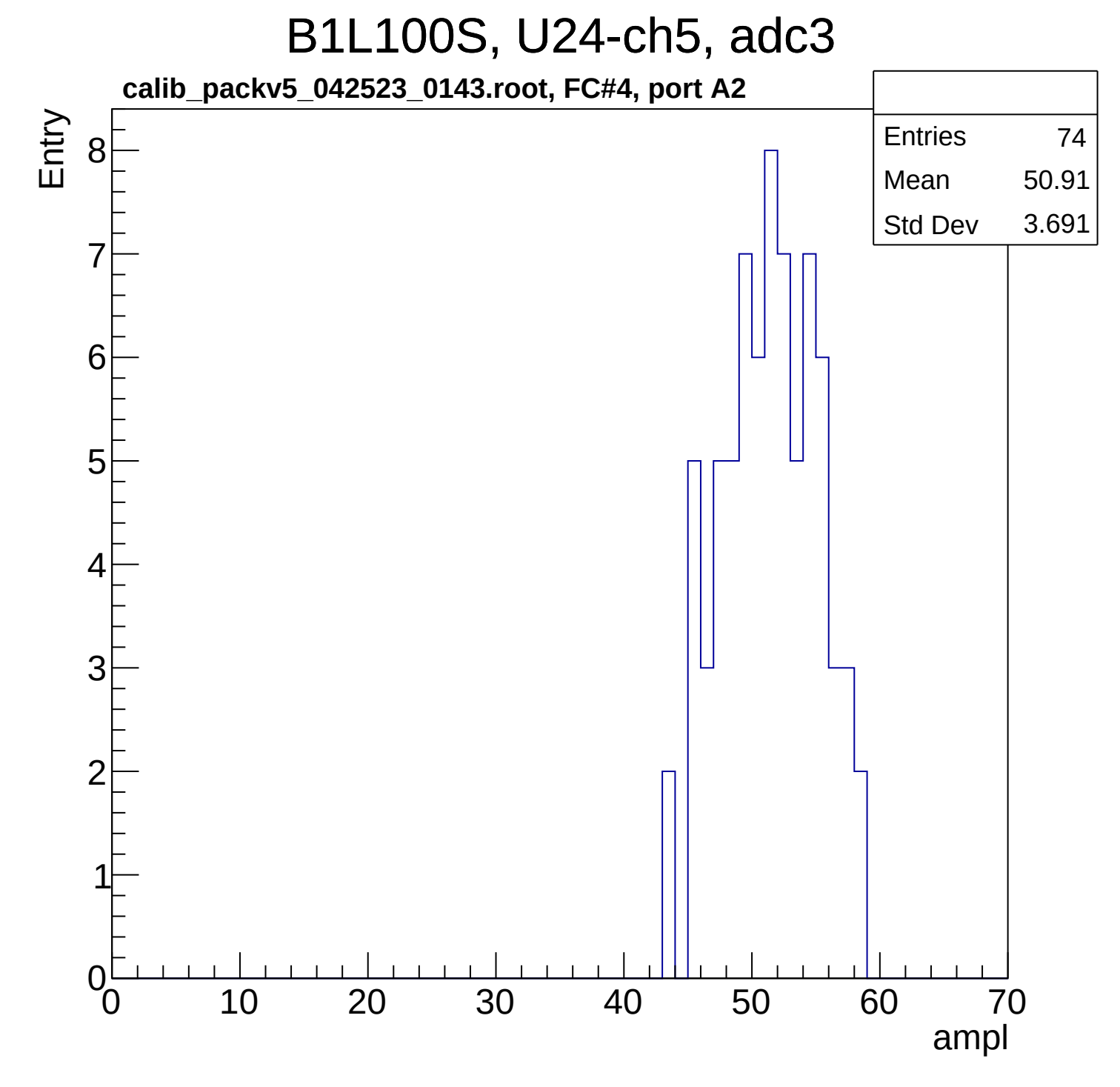
B1L100S, U24-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.91
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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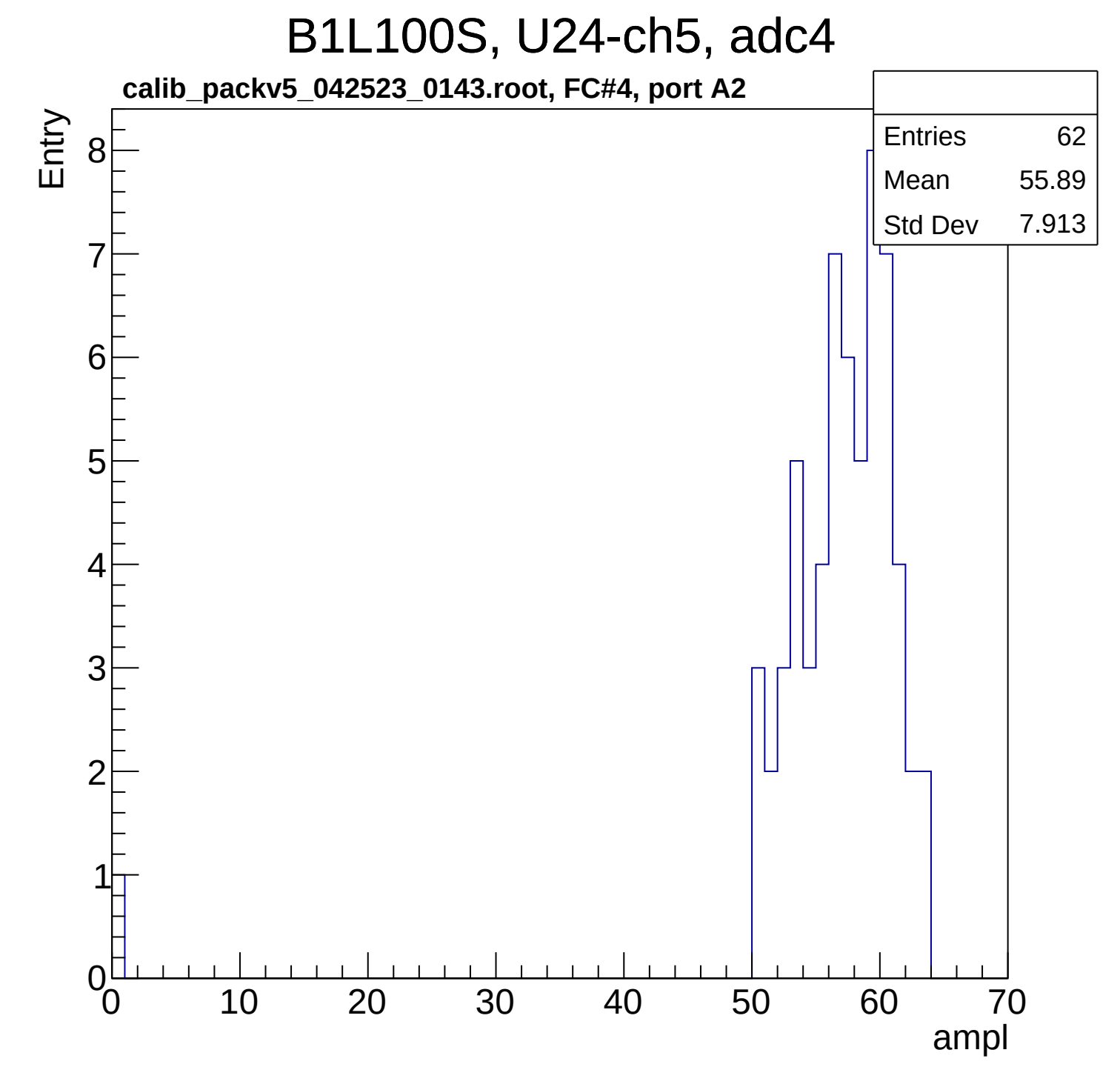
B1L100S, U24-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.89
Std Dev 7.913
Entry
ampl
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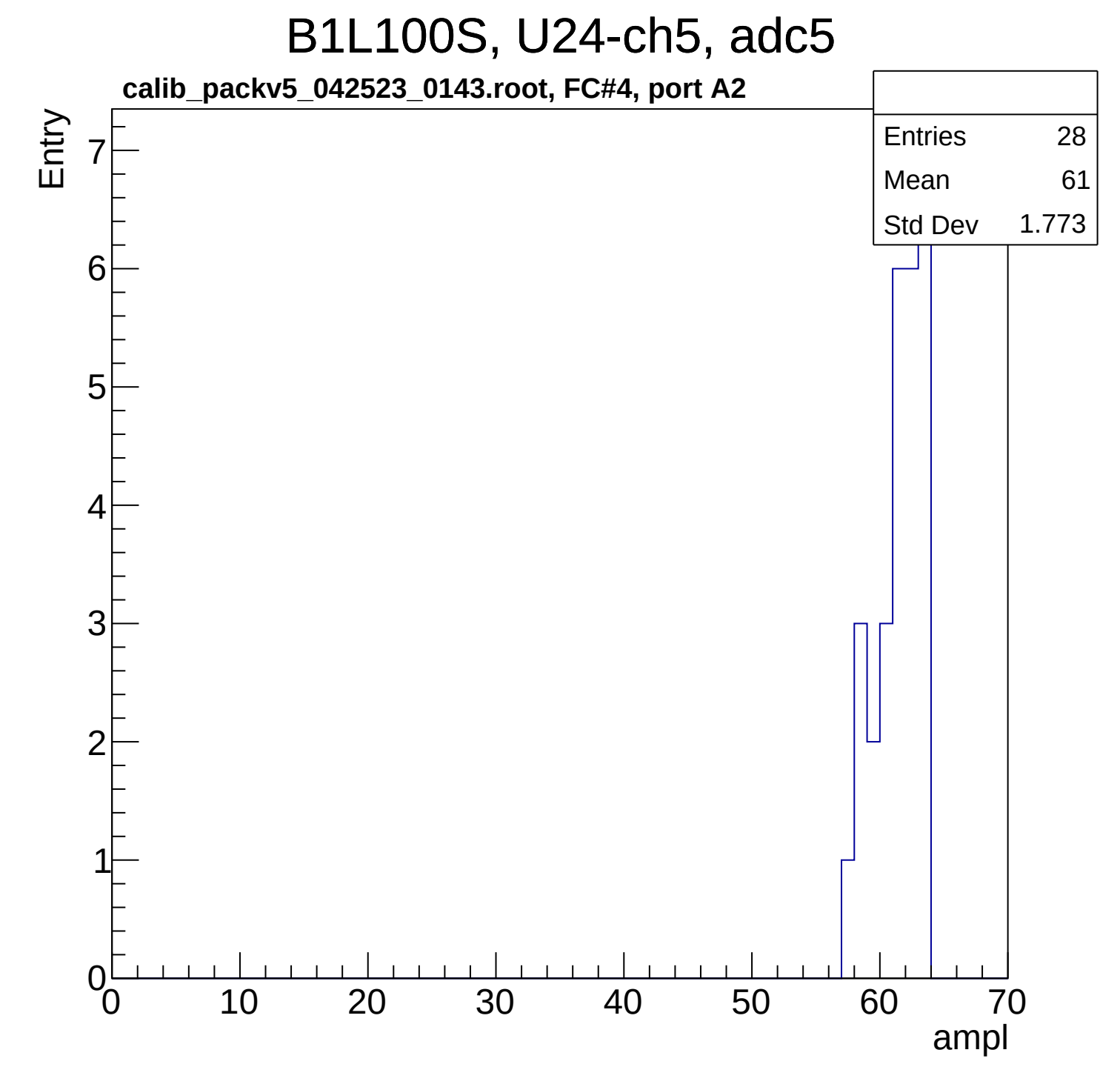
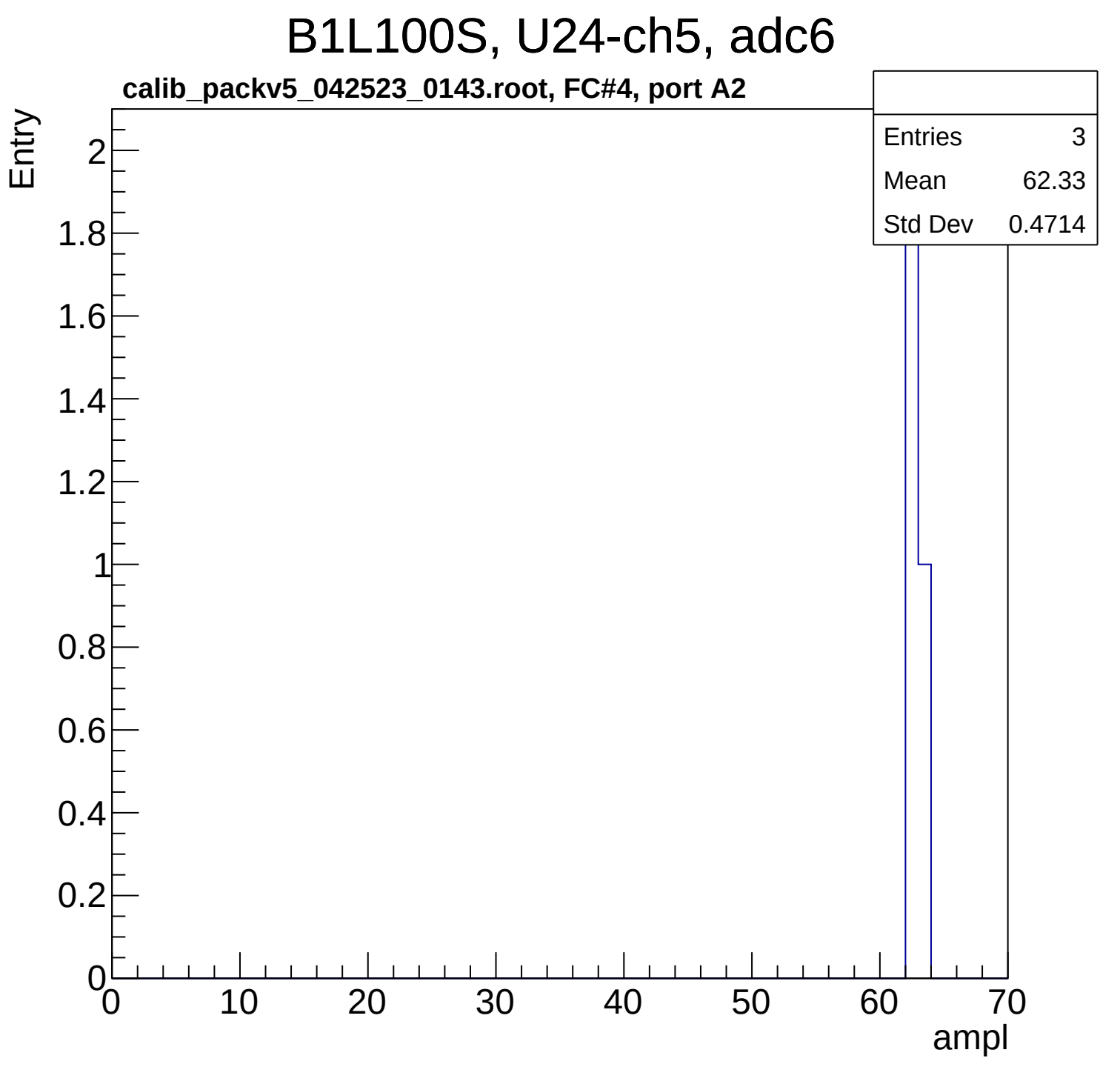
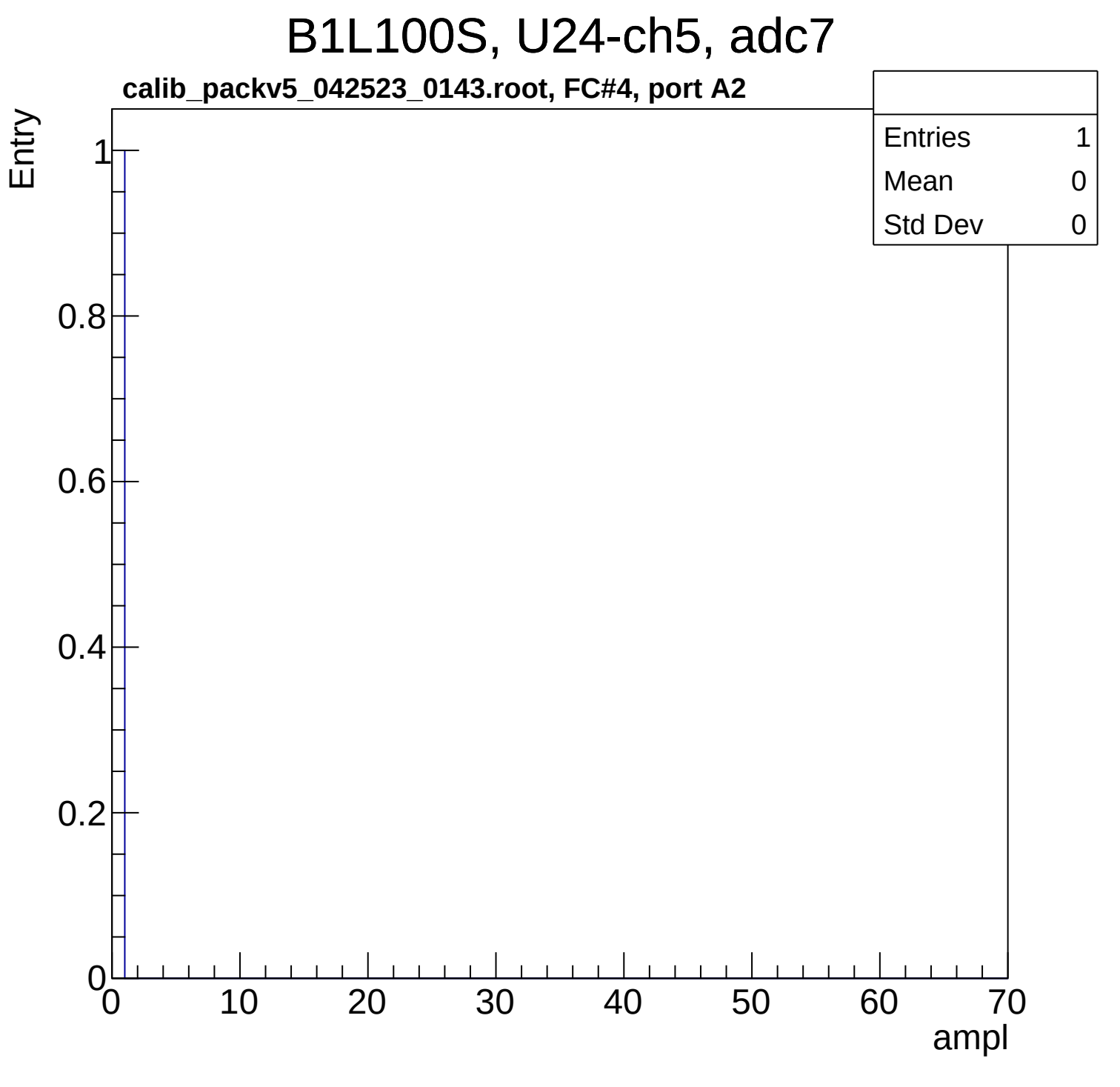
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch5, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 61
Std Dev 1.773
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch5, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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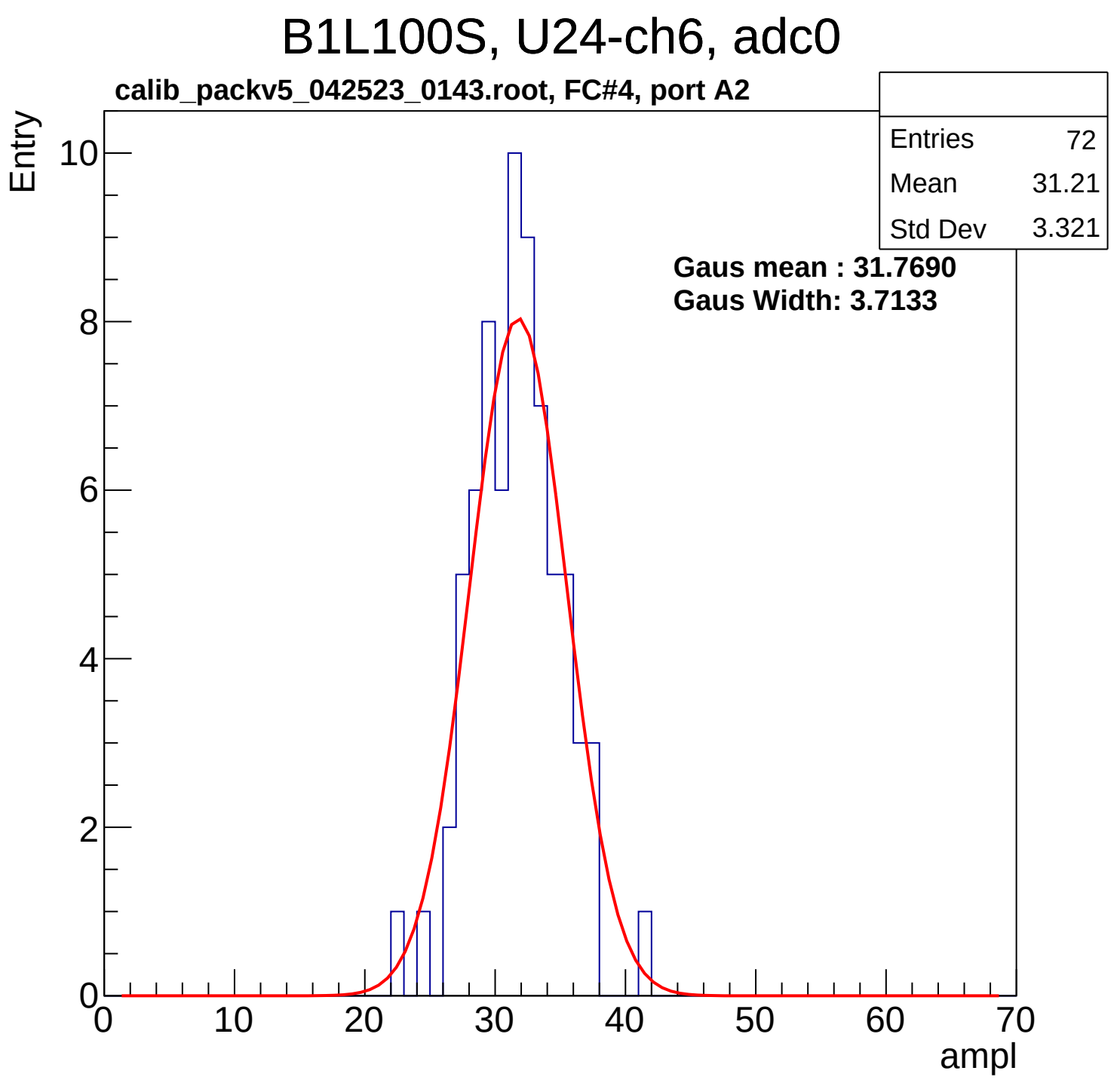
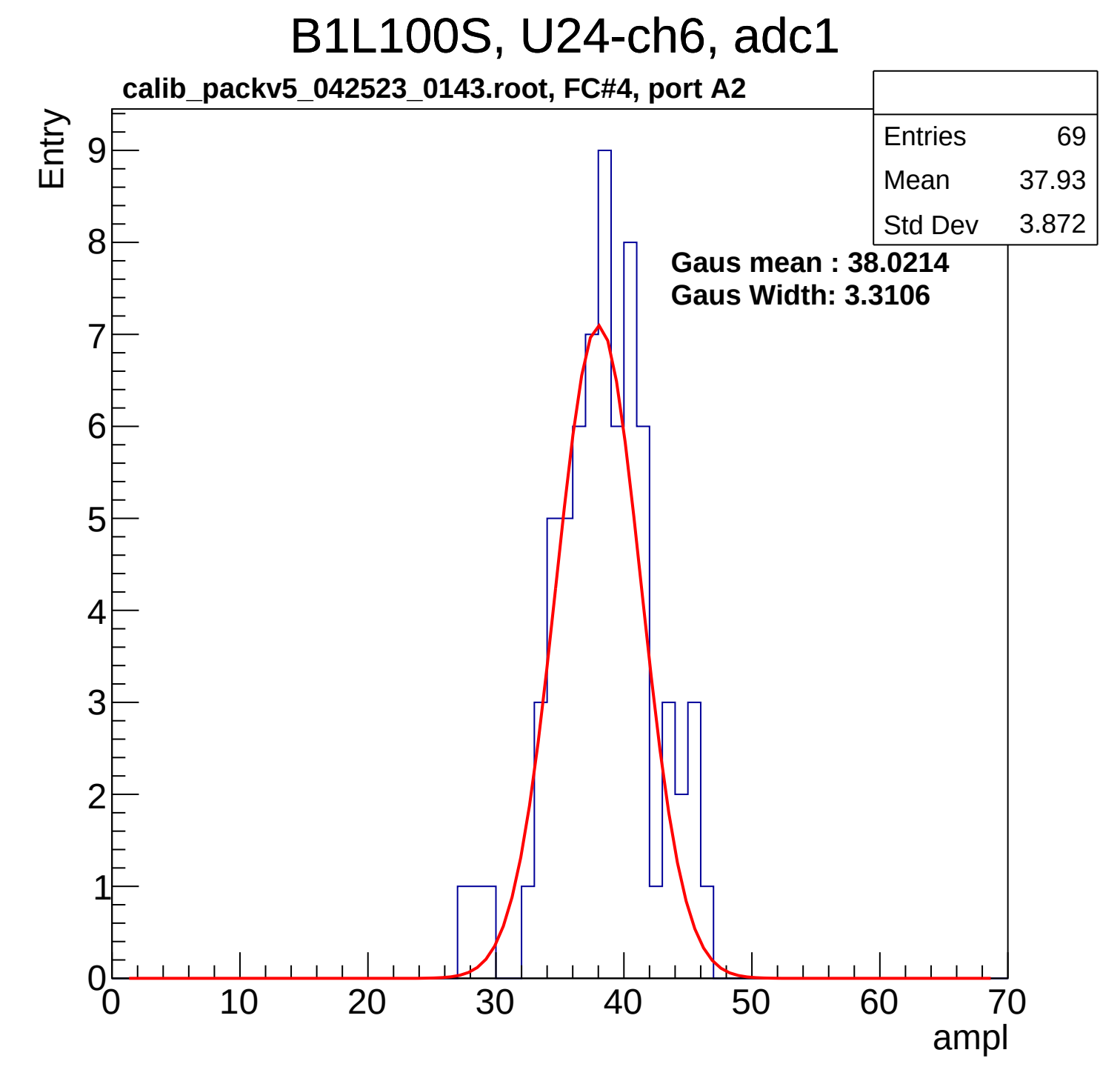
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 31.21
Std Dev 3.321
Gaus mean : 31.7690
Gaus Width: 3.7133
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.93
Std Dev 3.872
Gaus mean : 38.0214
Gaus Width: 3.3106
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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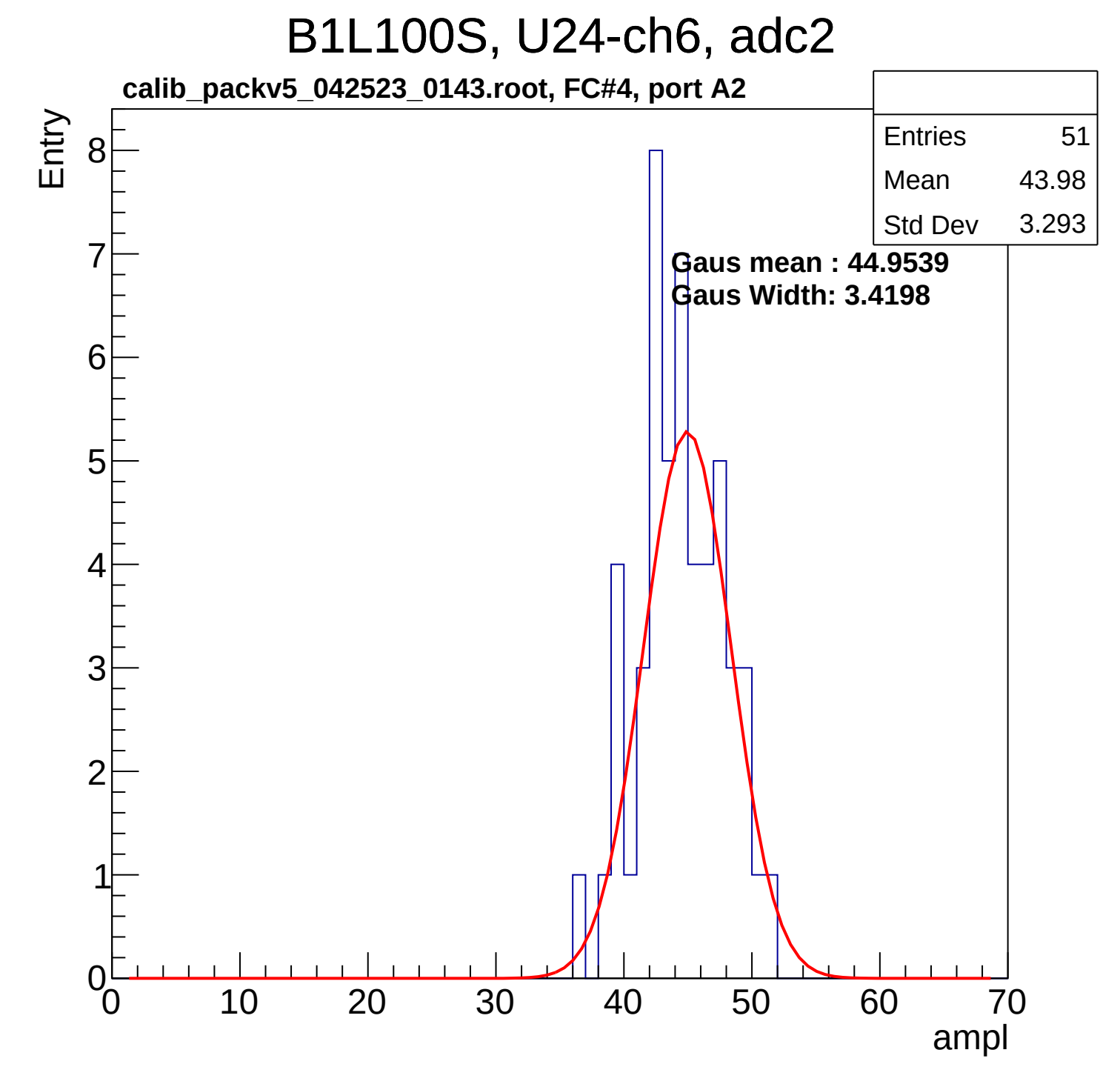
B1L100S, U24-ch6, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 43.98
Std Dev 3.293
Gaus mean : 44.9539
Gaus Width: 3.4198
Entry
ampl
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2
3
4
5
6
7
8
0
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40
50
60
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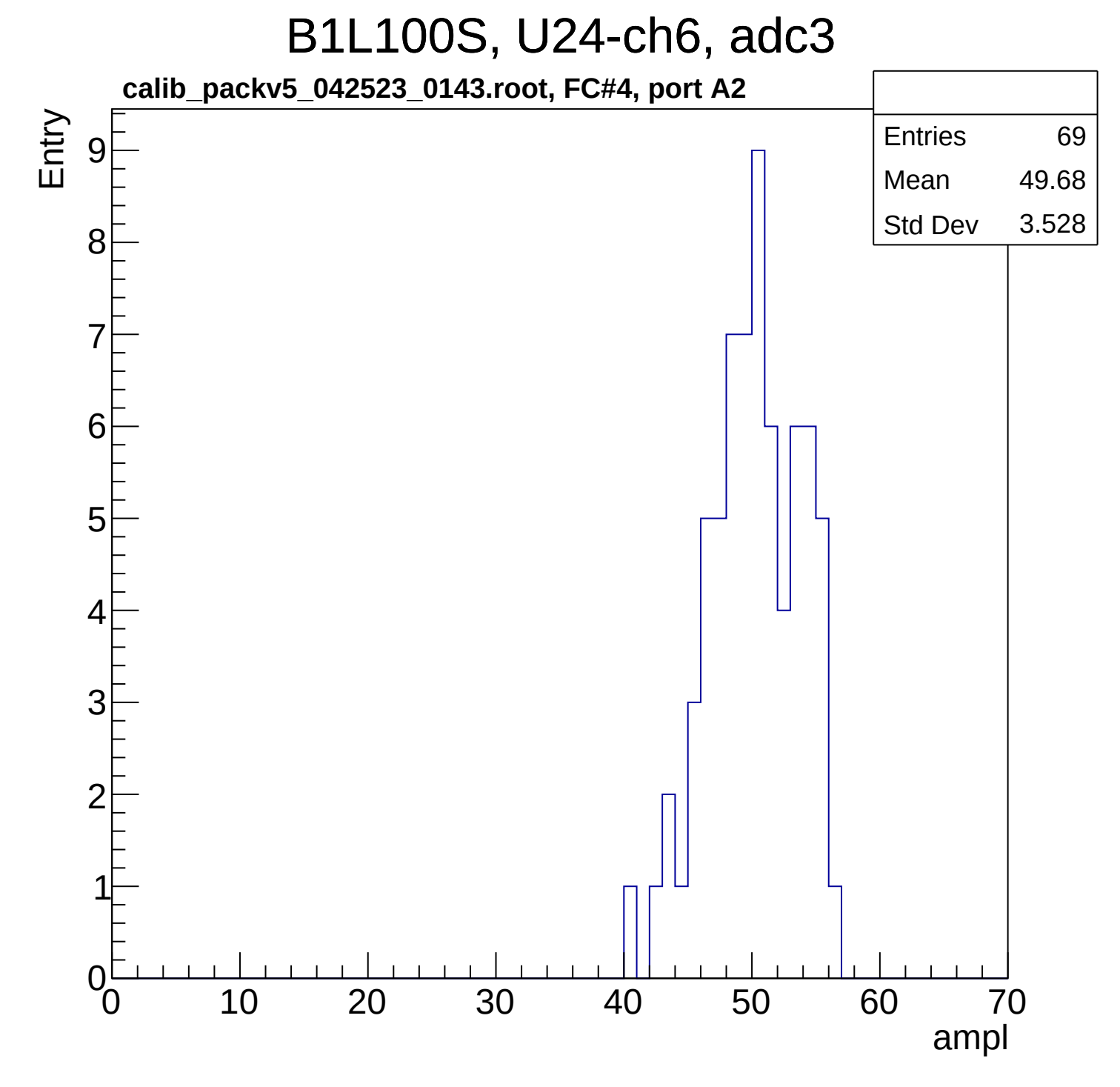
B1L100S, U24-ch6, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.68
Std Dev 3.528
Entry
ampl
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3
4
5
6
7
8
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0
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60
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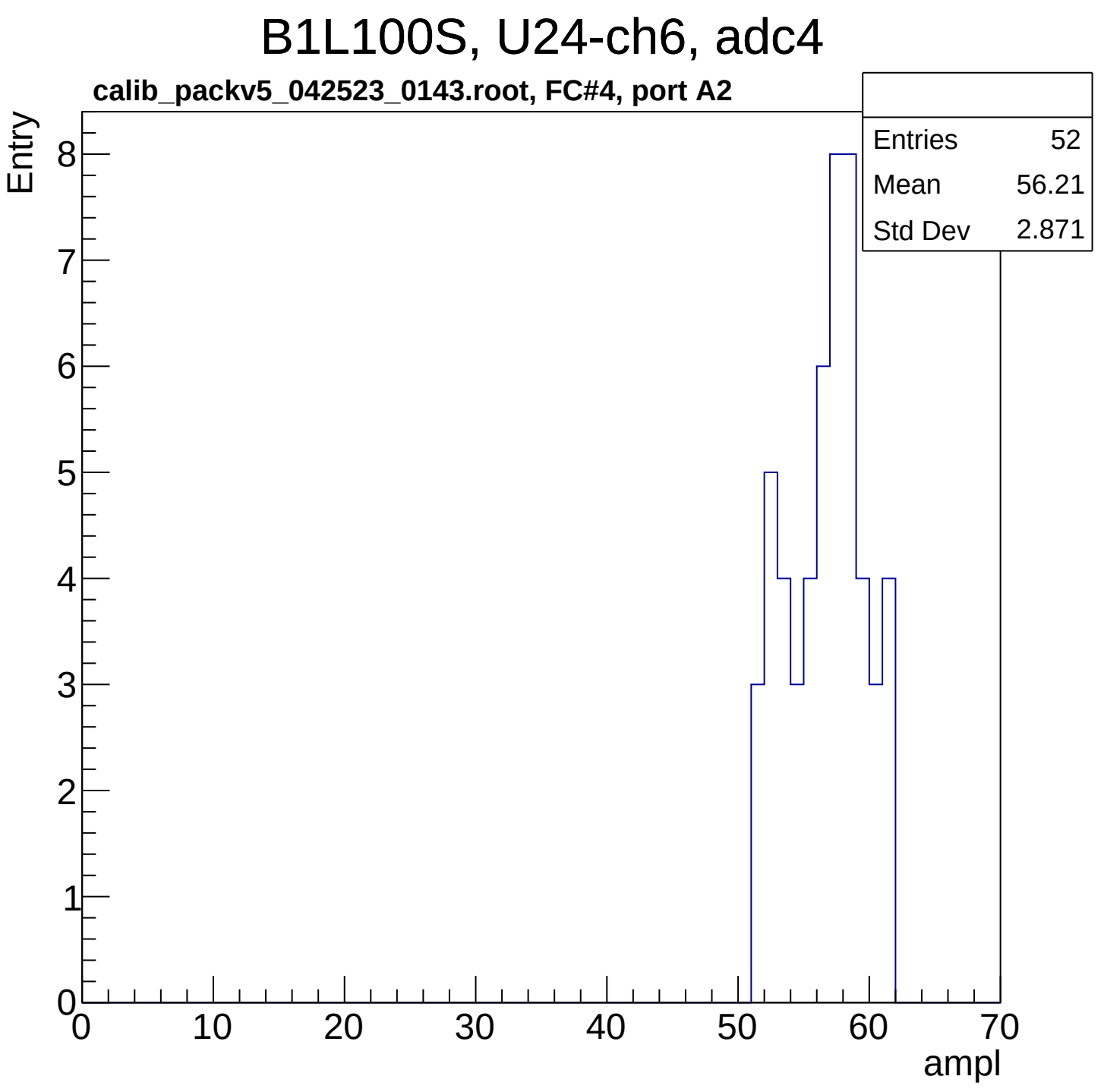
B1L100S, U24-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.21
Std Dev 2.871
Entry
ampl
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0
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60
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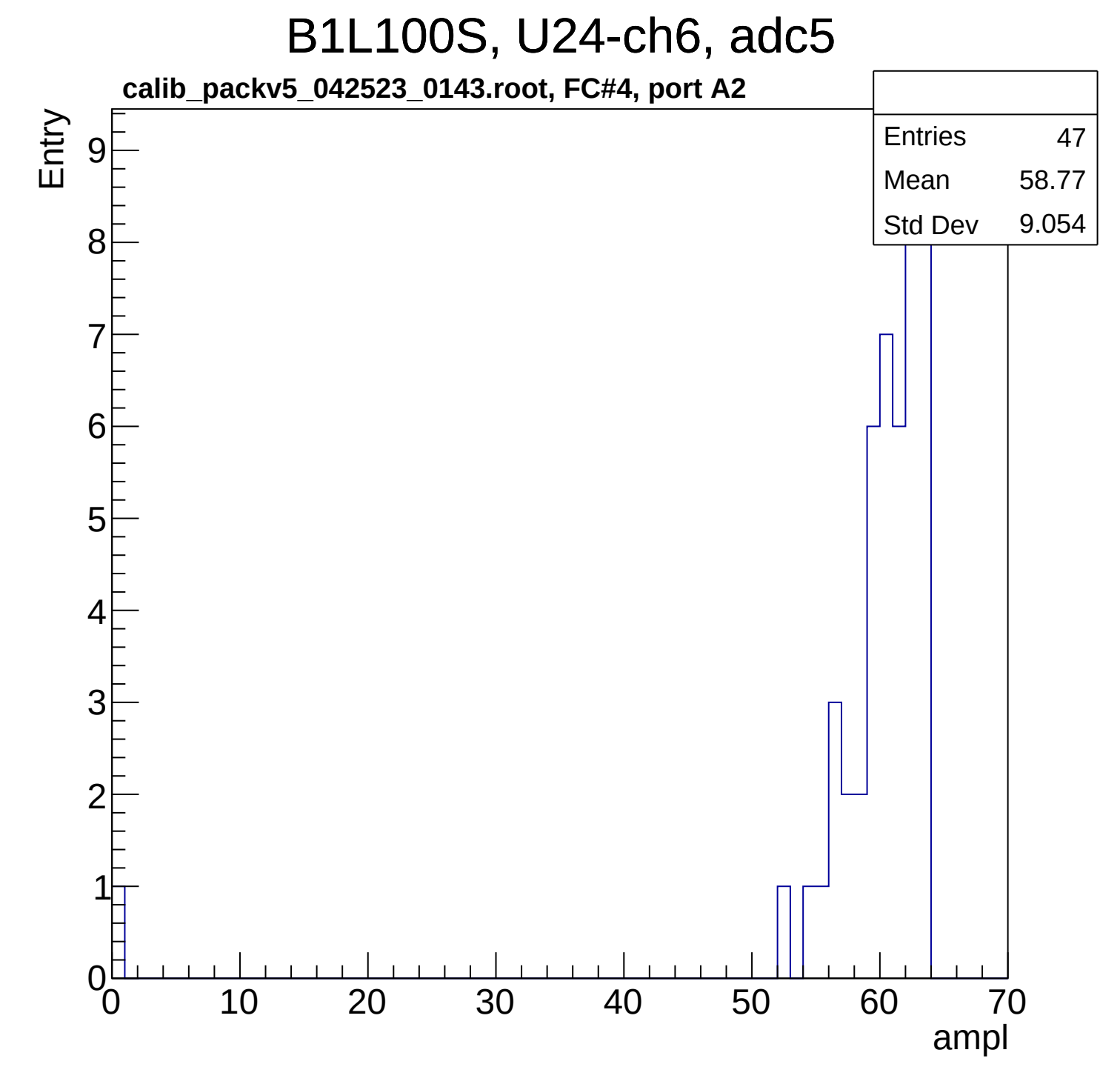
B1L100S, U24-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.77
Std Dev 9.054
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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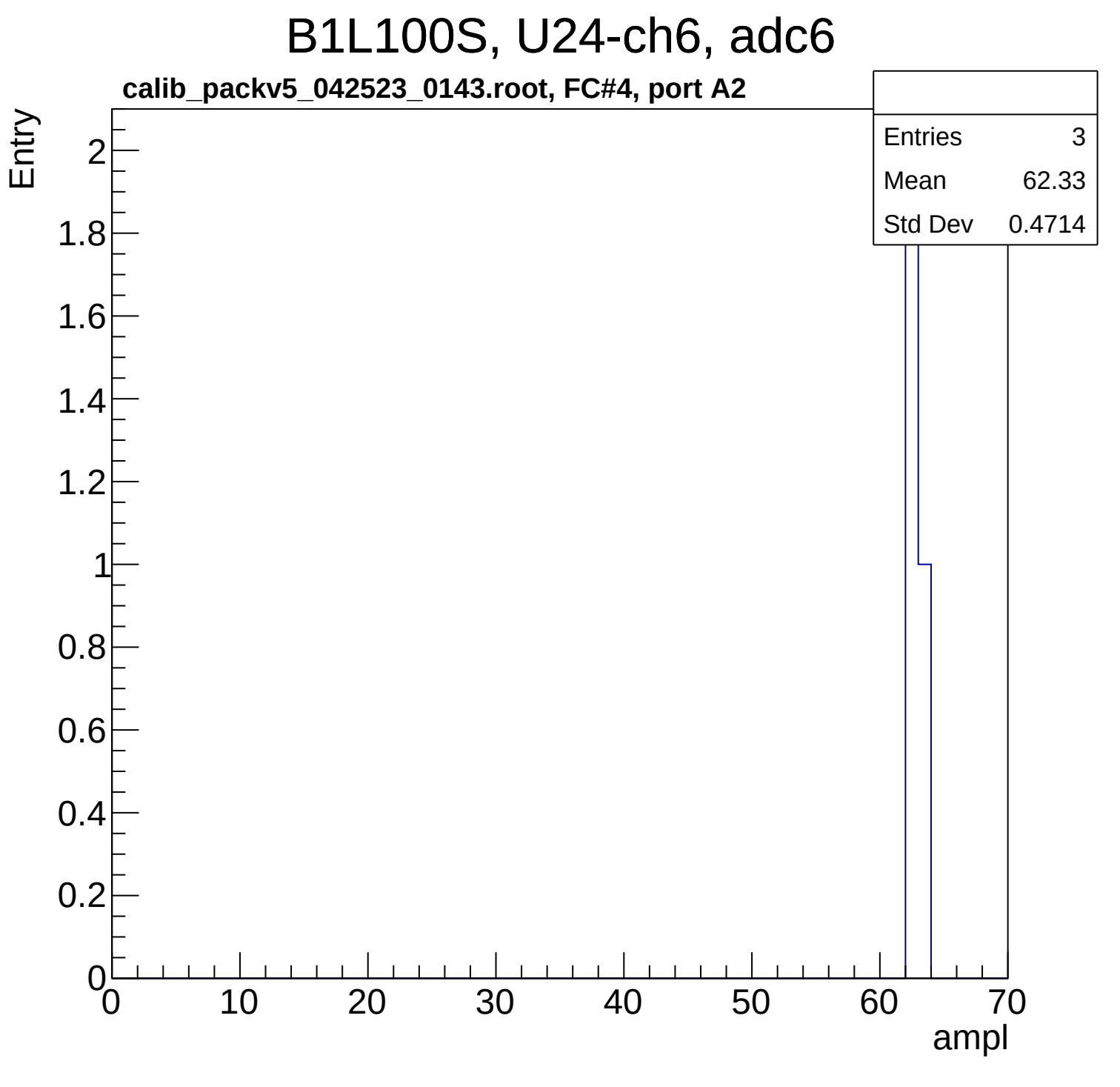
B1L100S, U24-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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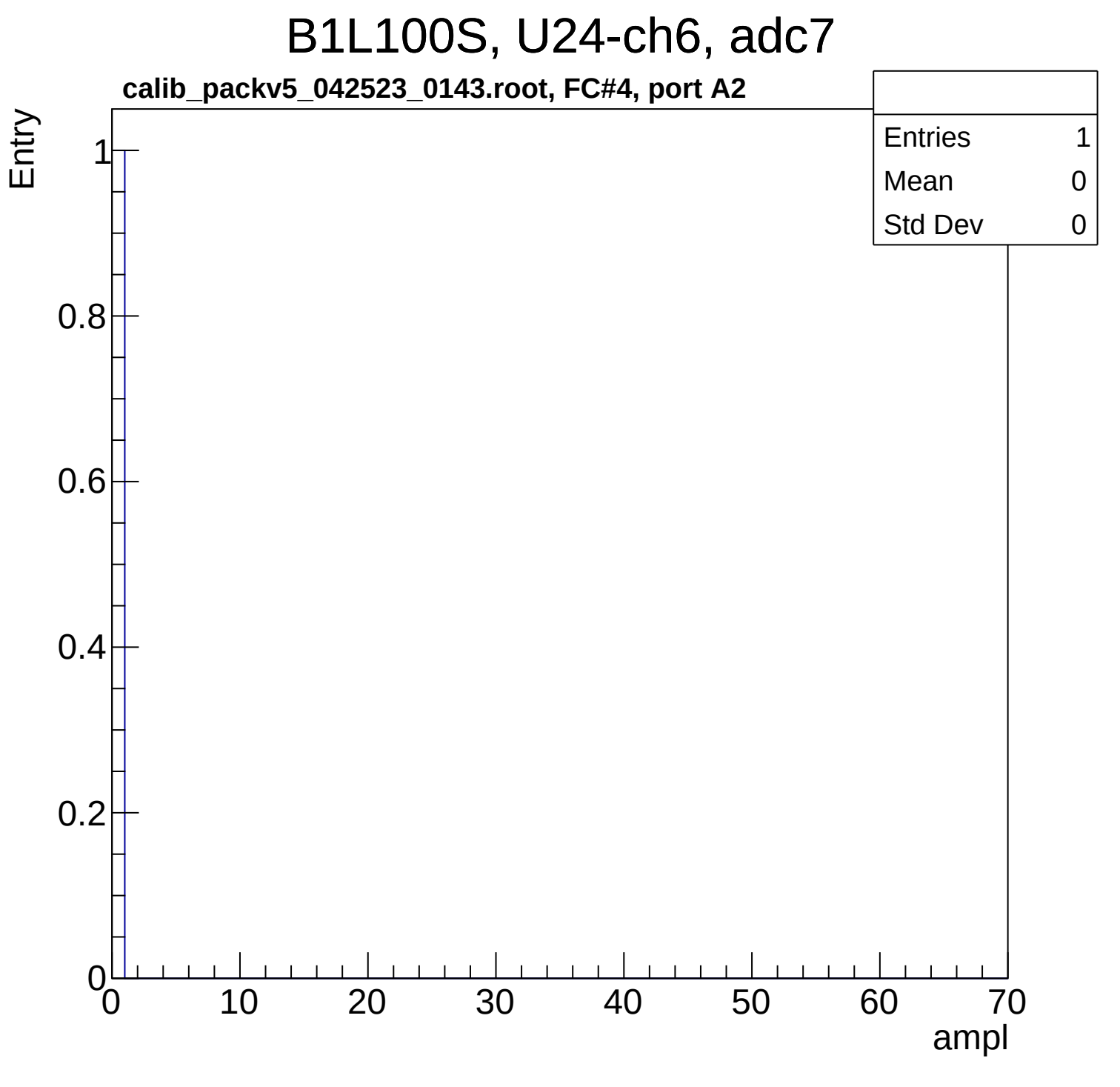
B1L100S, U24-ch6, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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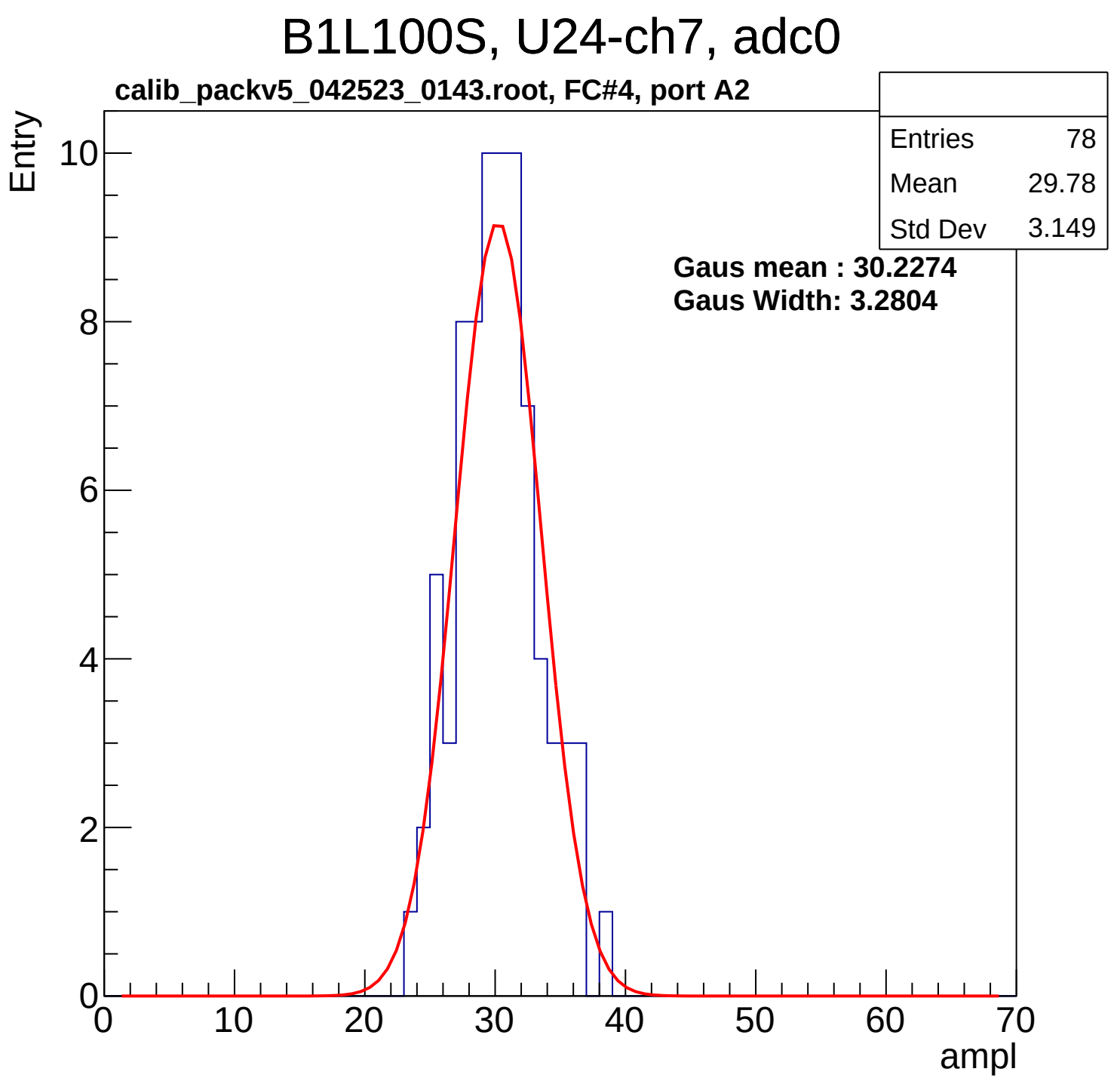
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.78
Std Dev 3.149
Gaus mean : 30.2274
Gaus Width: 3.2804
Entry
ampl
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6
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0
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40
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60
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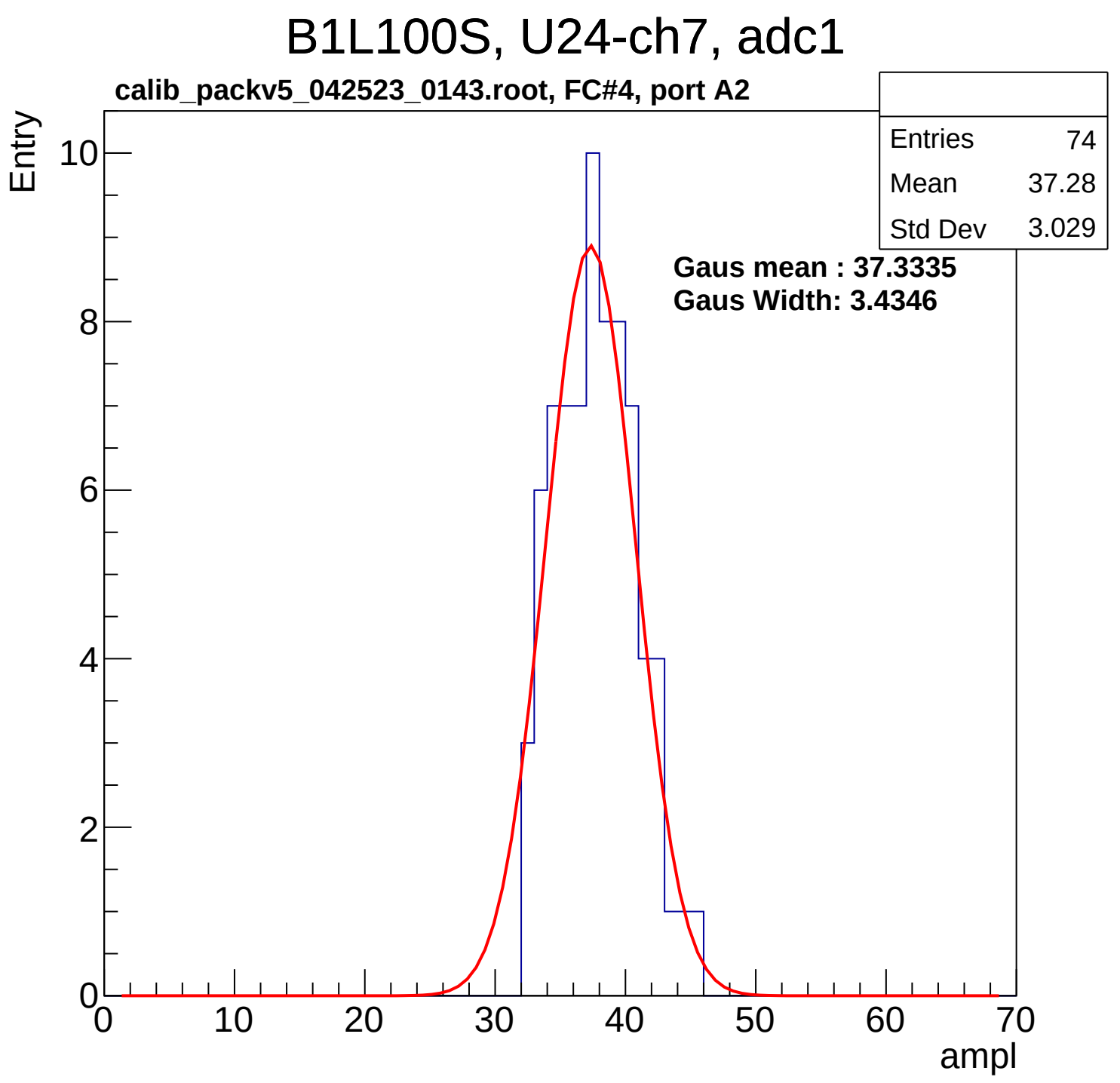
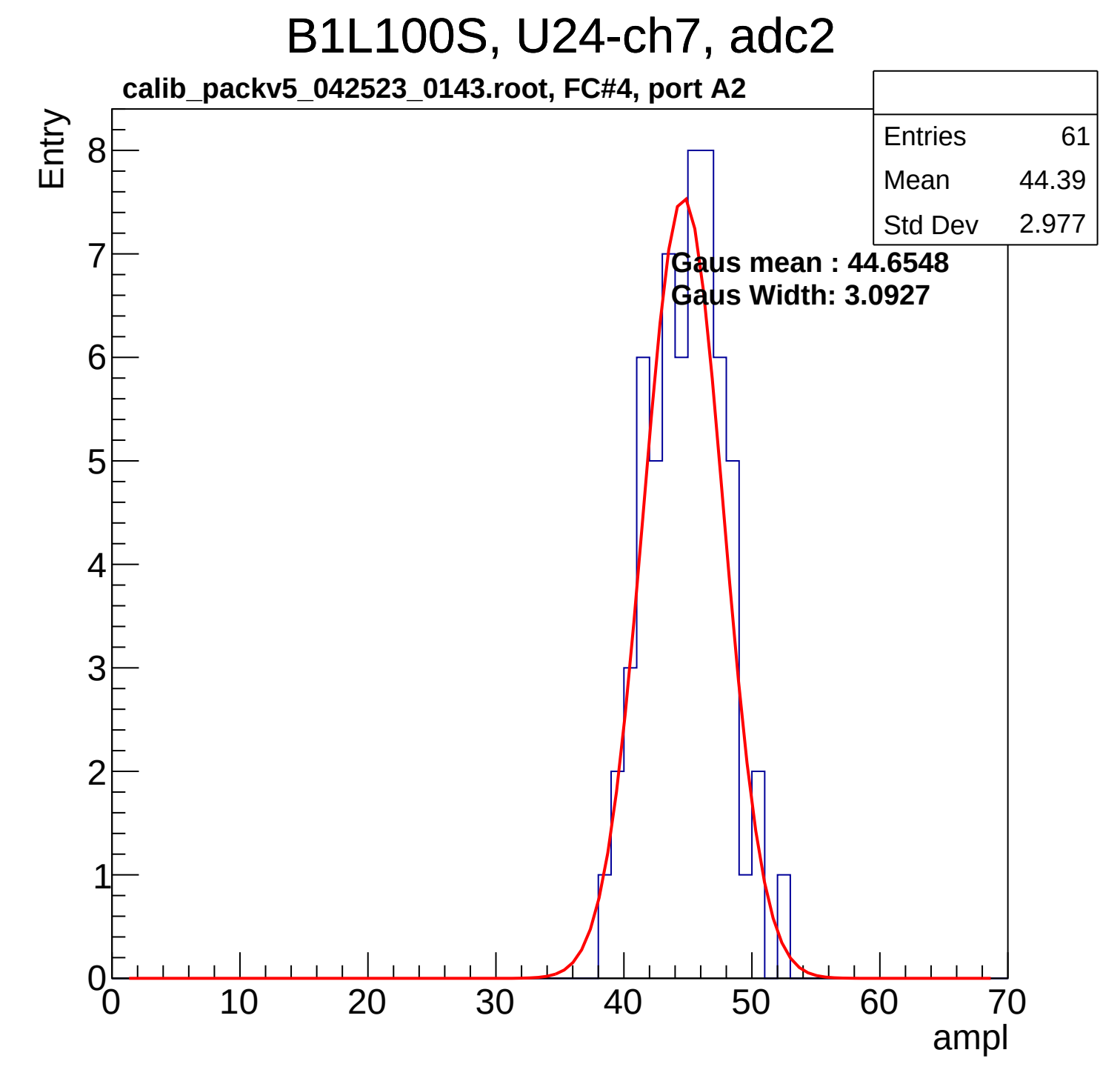

B1L100S, U24-ch7, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.28
Std Dev 3.029
Gaus mean : 37.3335
Gaus Width: 3.4346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 44.39
Std Dev 2.977
Gaus mean : 44.6548
Gaus Width: 3.0927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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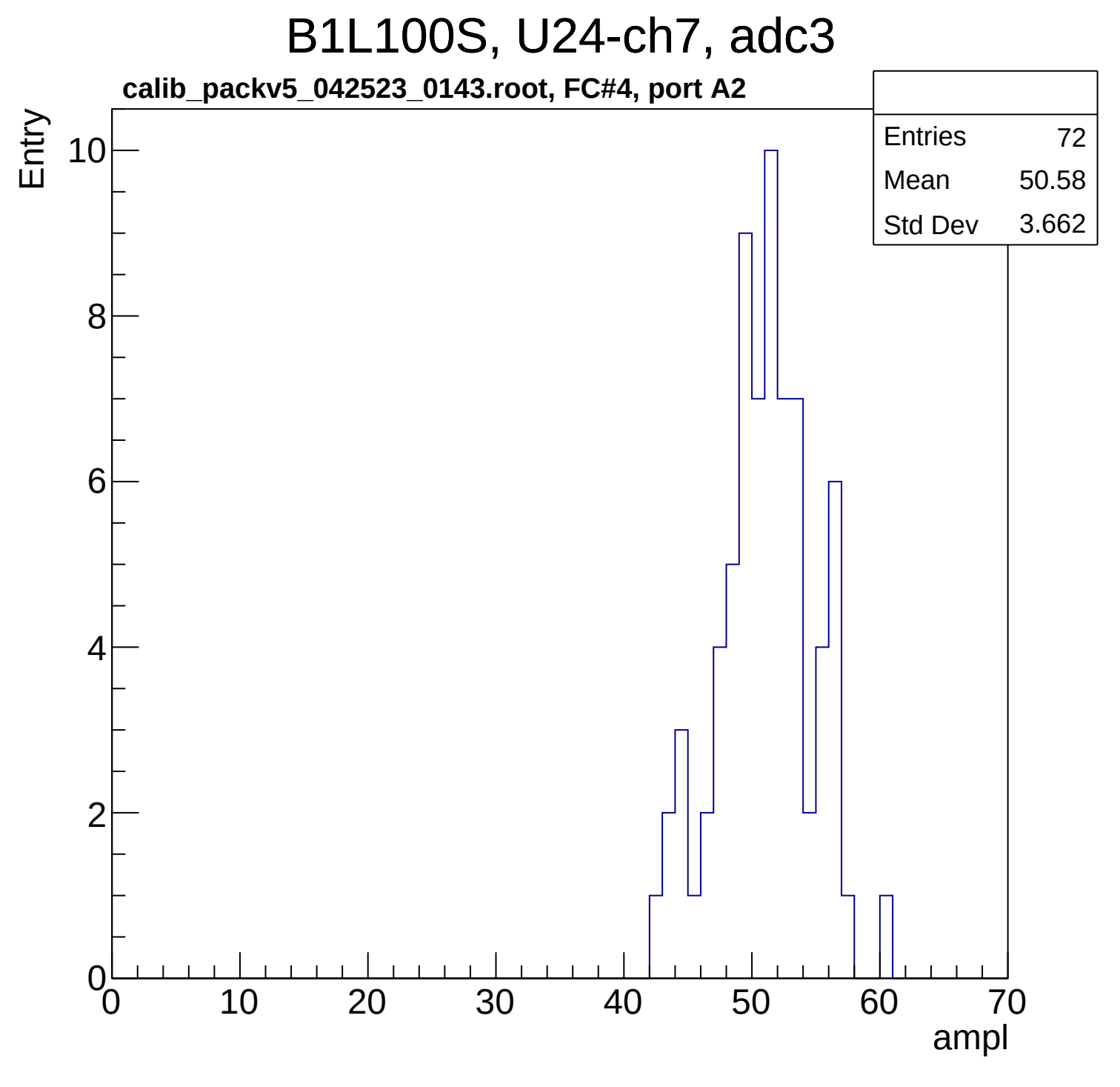
30
40
50
60
70


B1L100S, U24-ch7, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.58
Std Dev 3.662
Entry
ampl
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4
6
8
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0
10
20
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40
50
60
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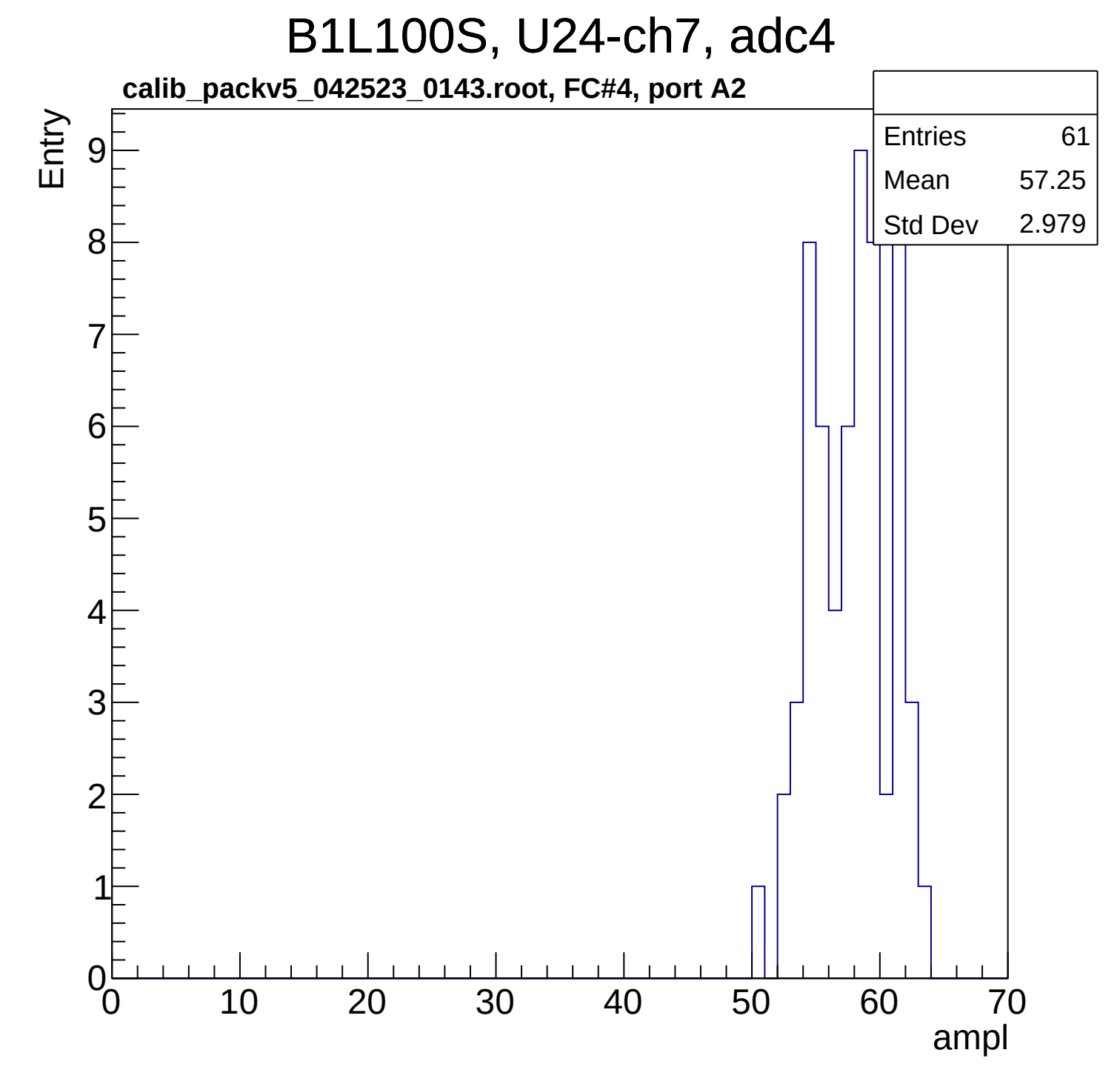
B1L100S, U24-ch7, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.25
Std Dev 2.979
Entry
ampl
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3
4
5
6
7
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0
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60
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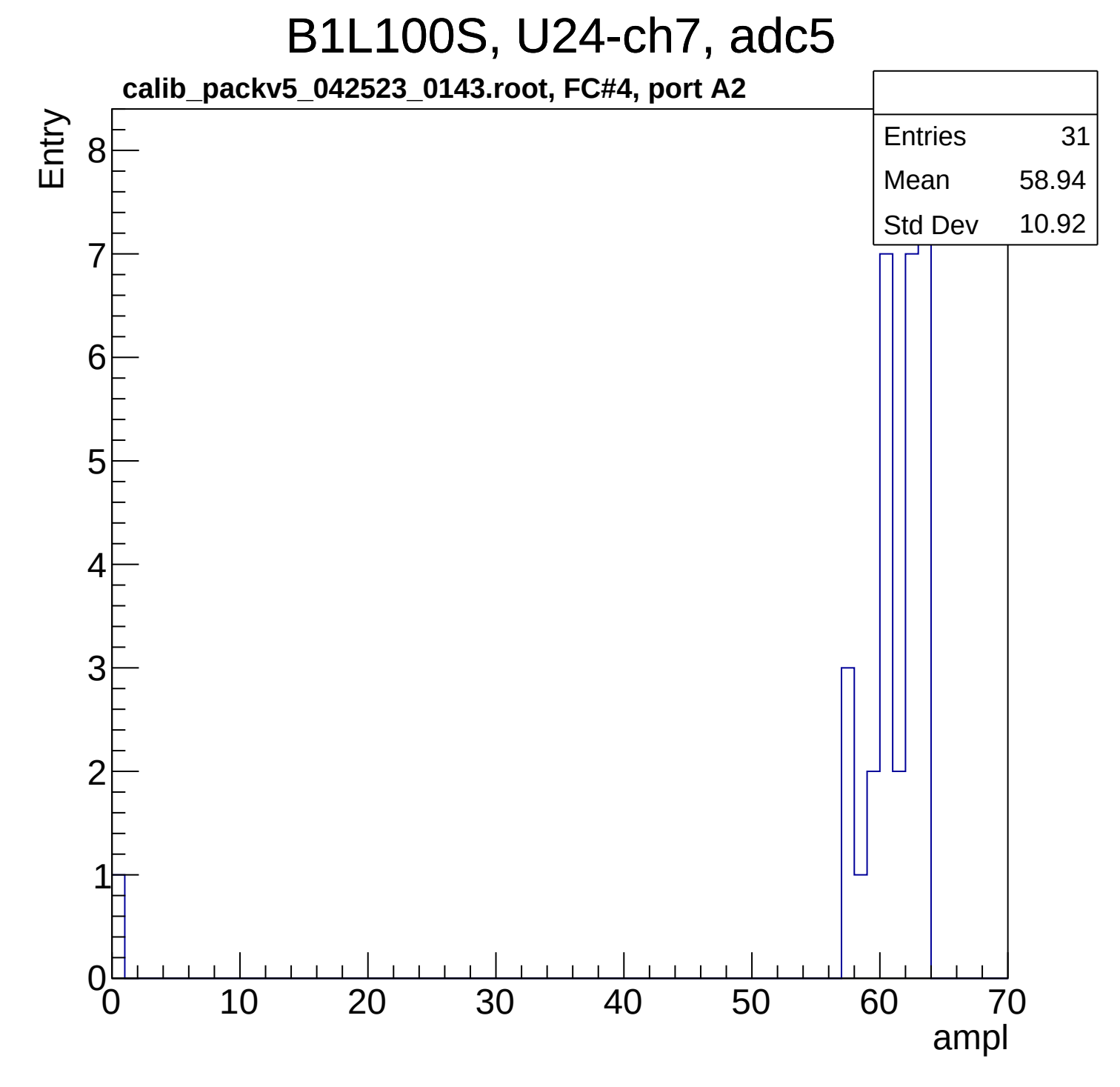
B1L100S, U24-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.94
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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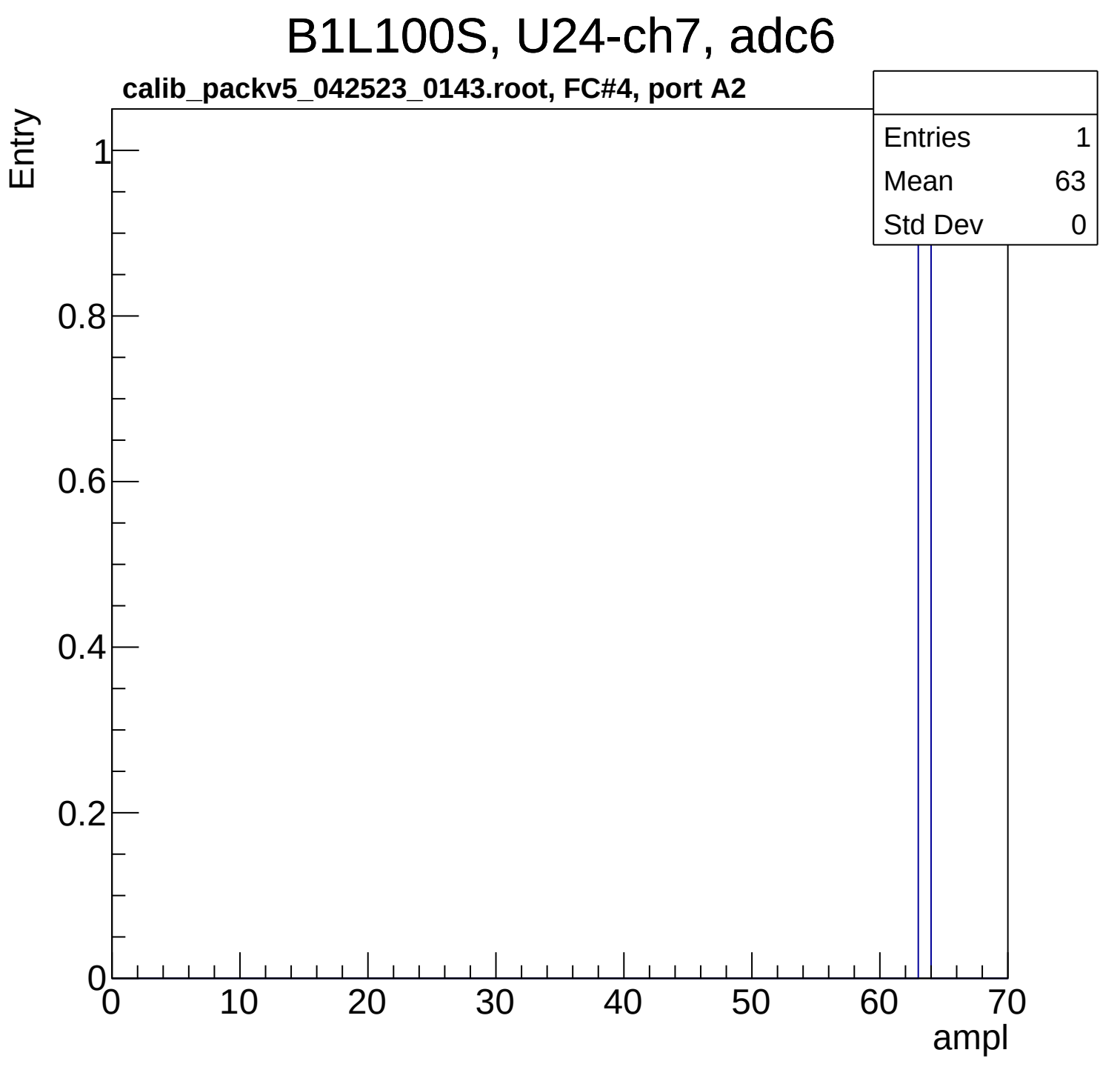
B1L100S, U24-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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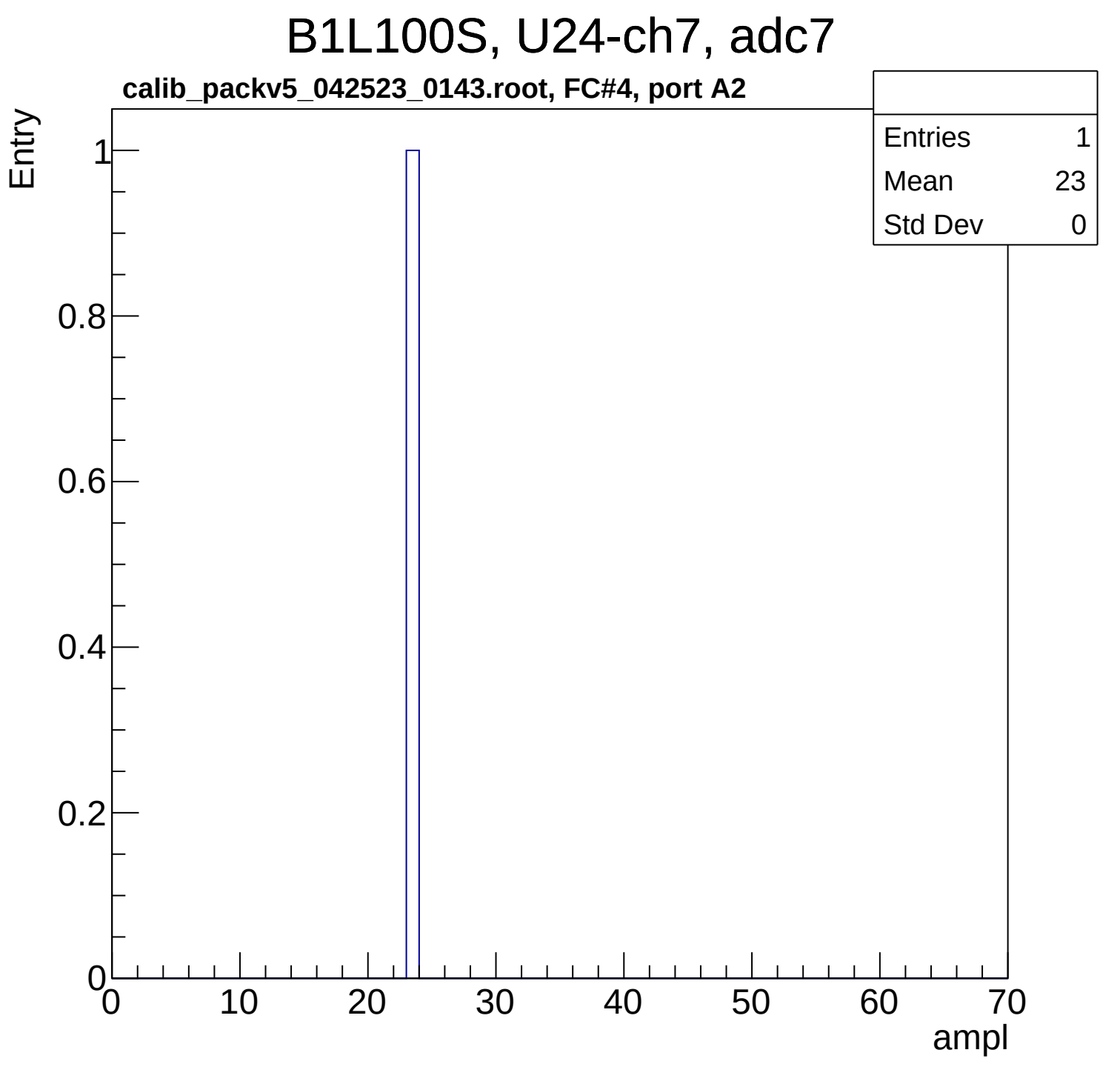
70
ampl


B1L100S, U24-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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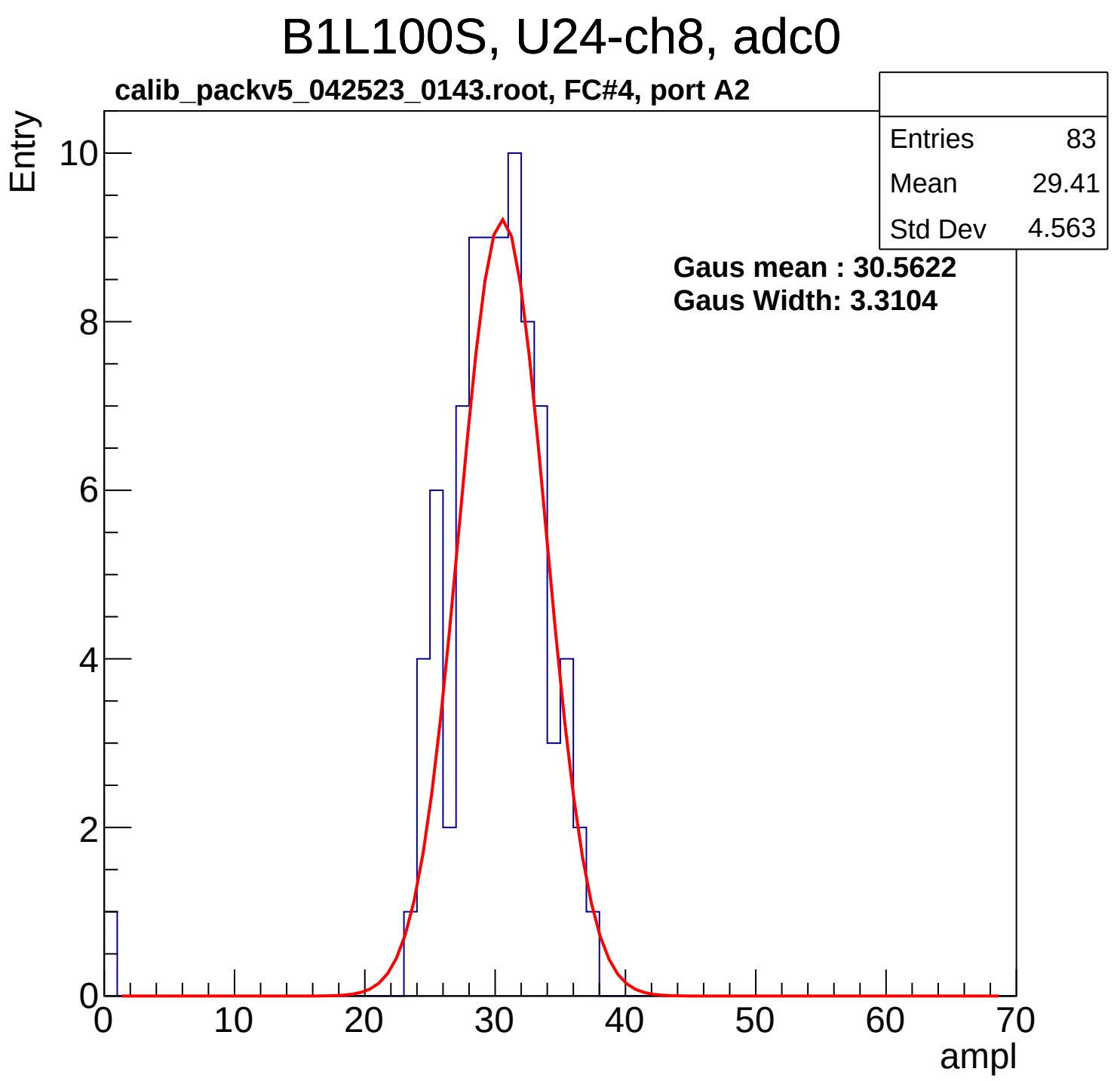
ampl


B1L100S, U24-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 29.41
Std Dev 4.563
Gaus mean : 30.5622
Gaus Width: 3.3104
Entry
ampl
0
2
4
6
8
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0
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40
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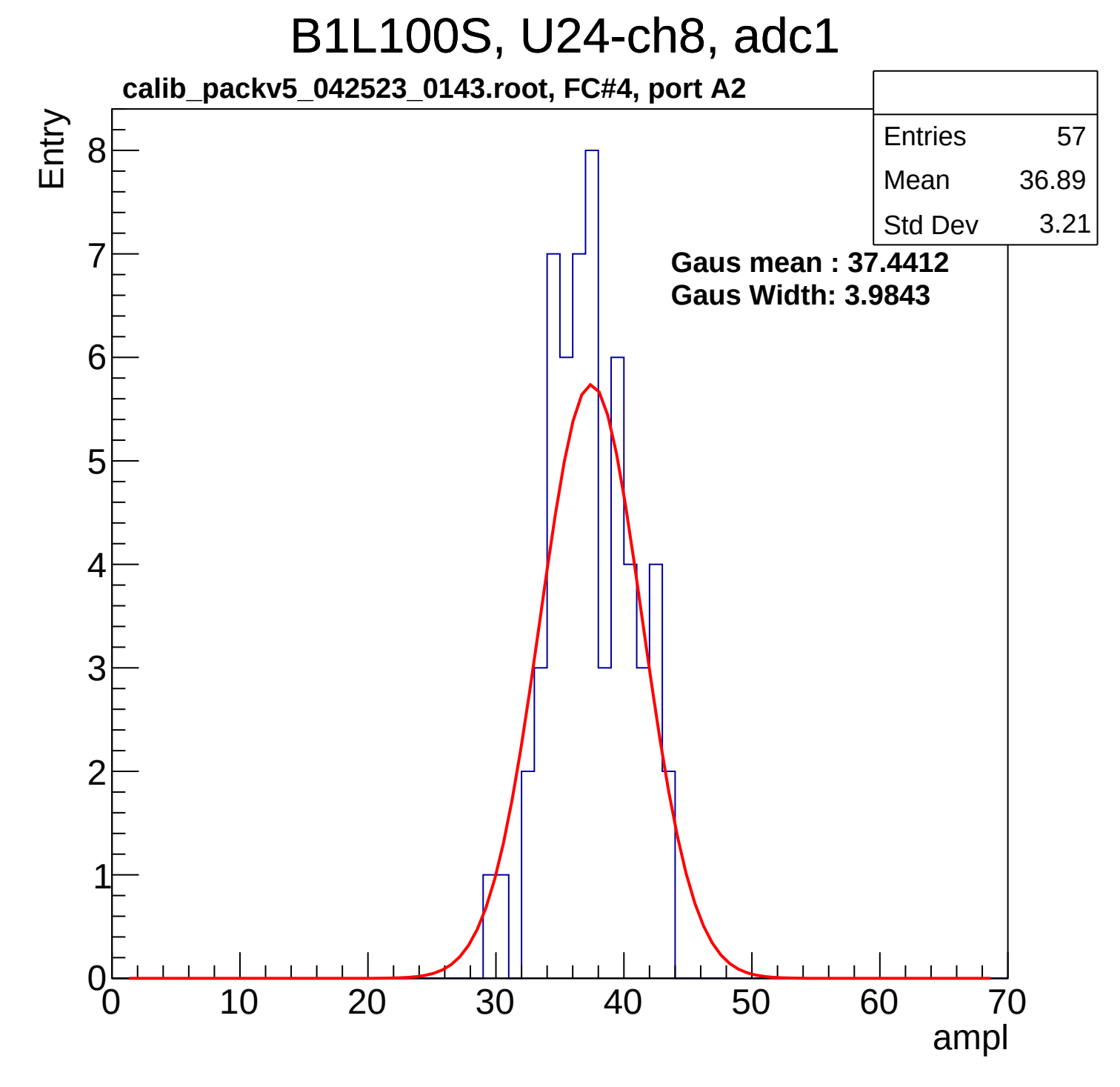
B1L100S, U24-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 36.89
Std Dev 3.21
Gaus mean : 37.4412
Gaus Width: 3.9843
Entry
ampl
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1
2
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4
5
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0
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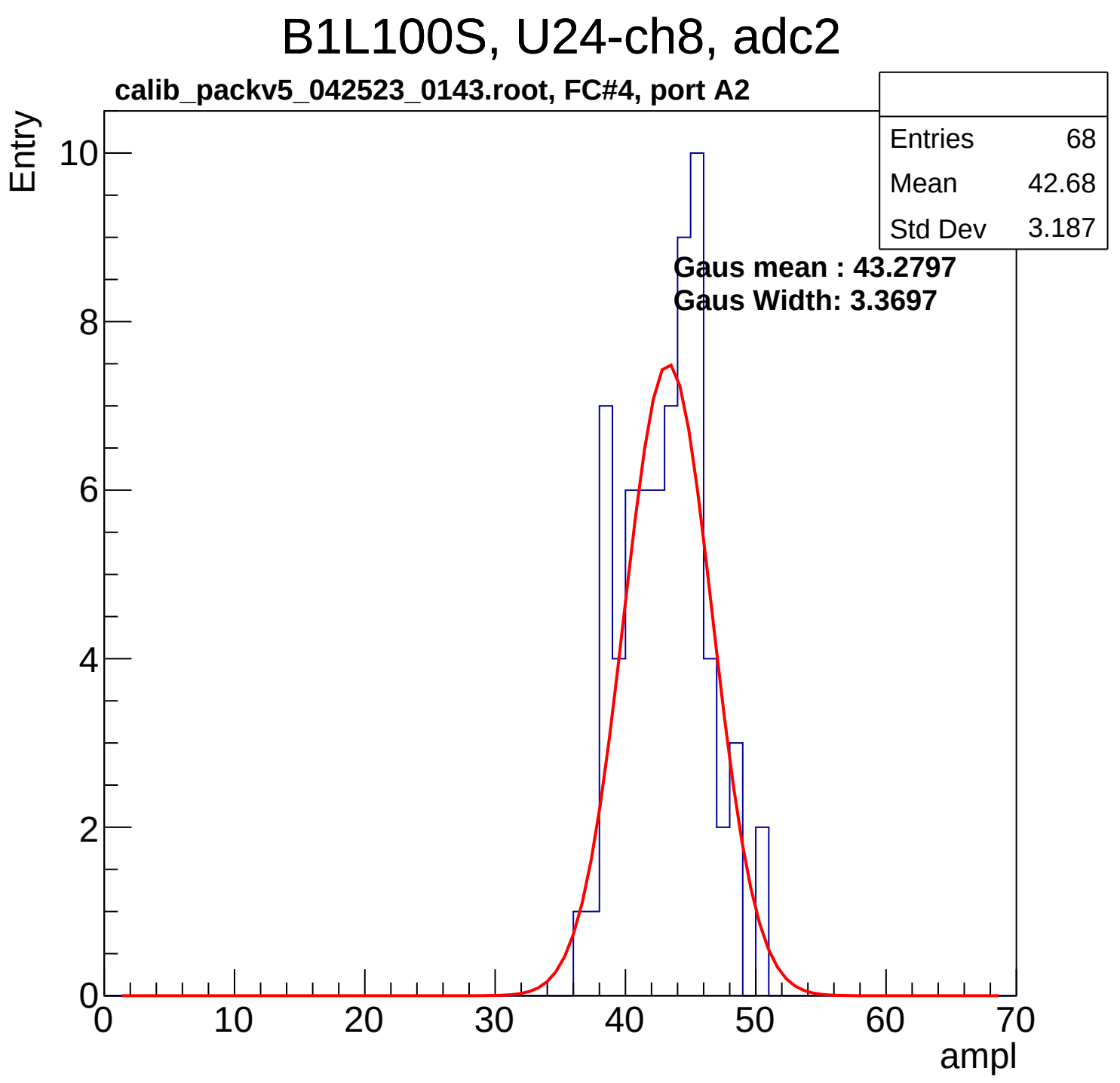
B1L100S, U24-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.68
Std Dev 3.187
Gaus mean : 43.2797
Gaus Width: 3.3697
Entry
ampl
0
2
4
6
8
10
0
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40
50
60
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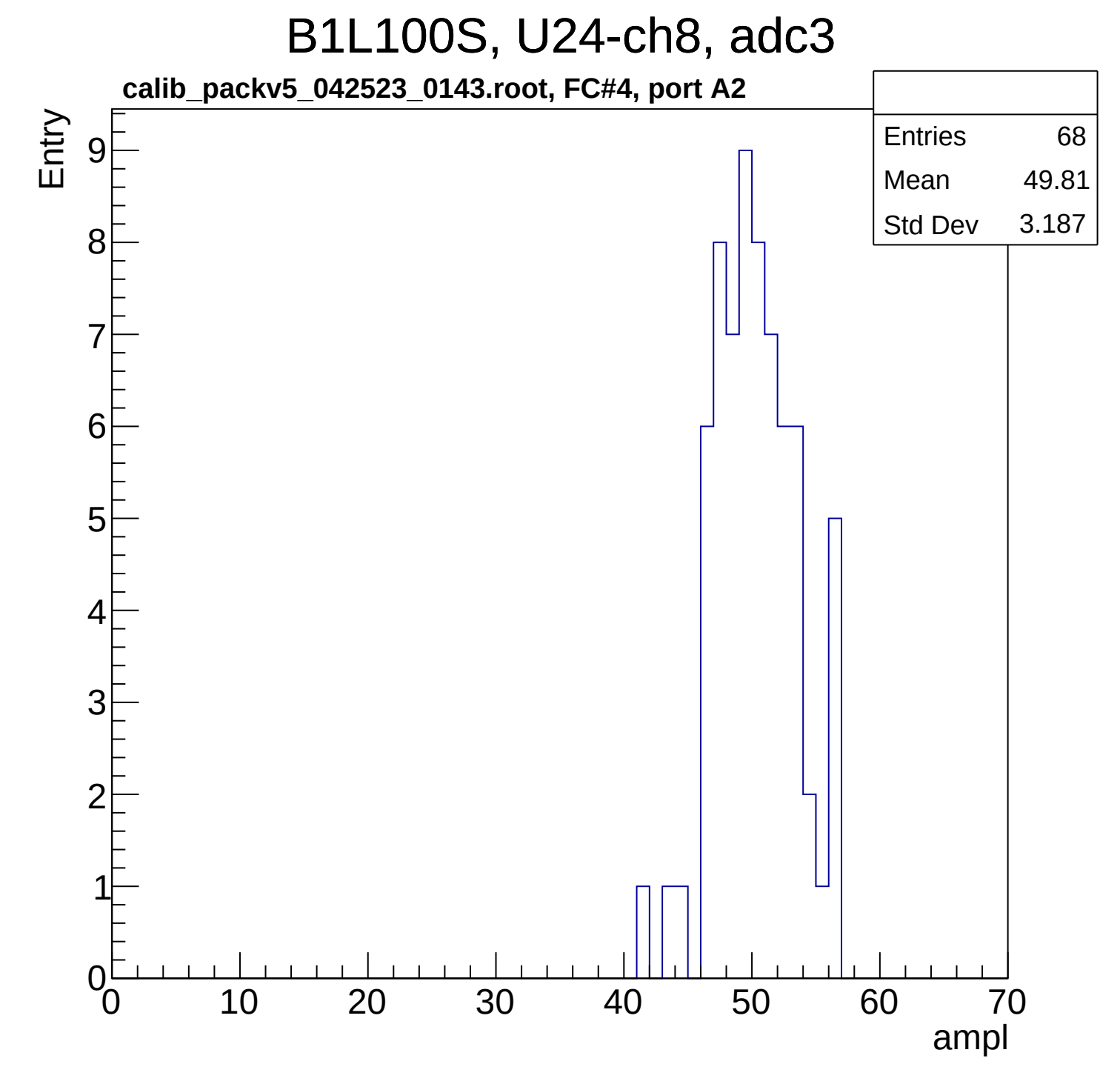
B1L100S, U24-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.81
Std Dev 3.187
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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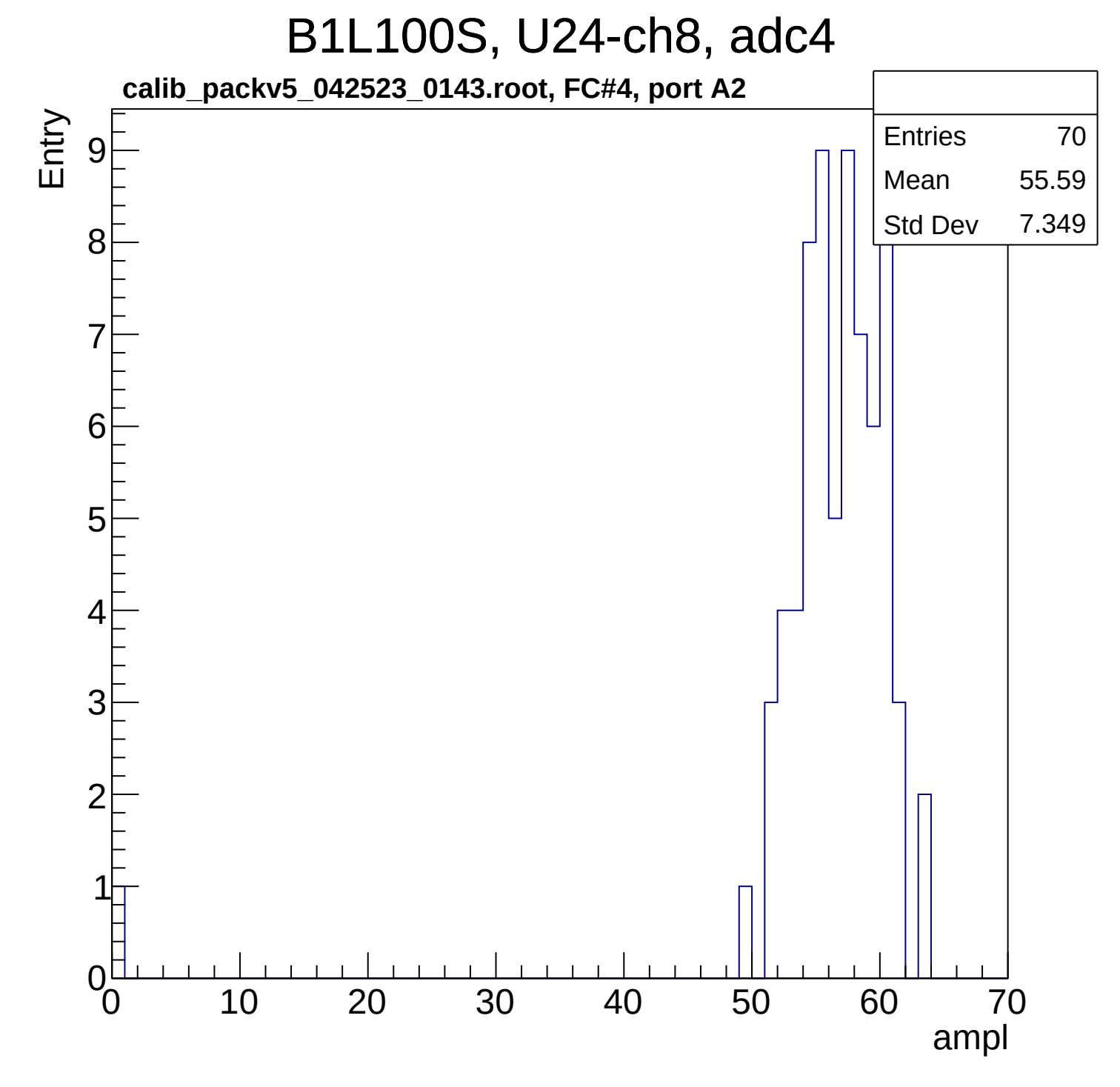
B1L100S, U24-ch8, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 55.59
Std Dev 7.349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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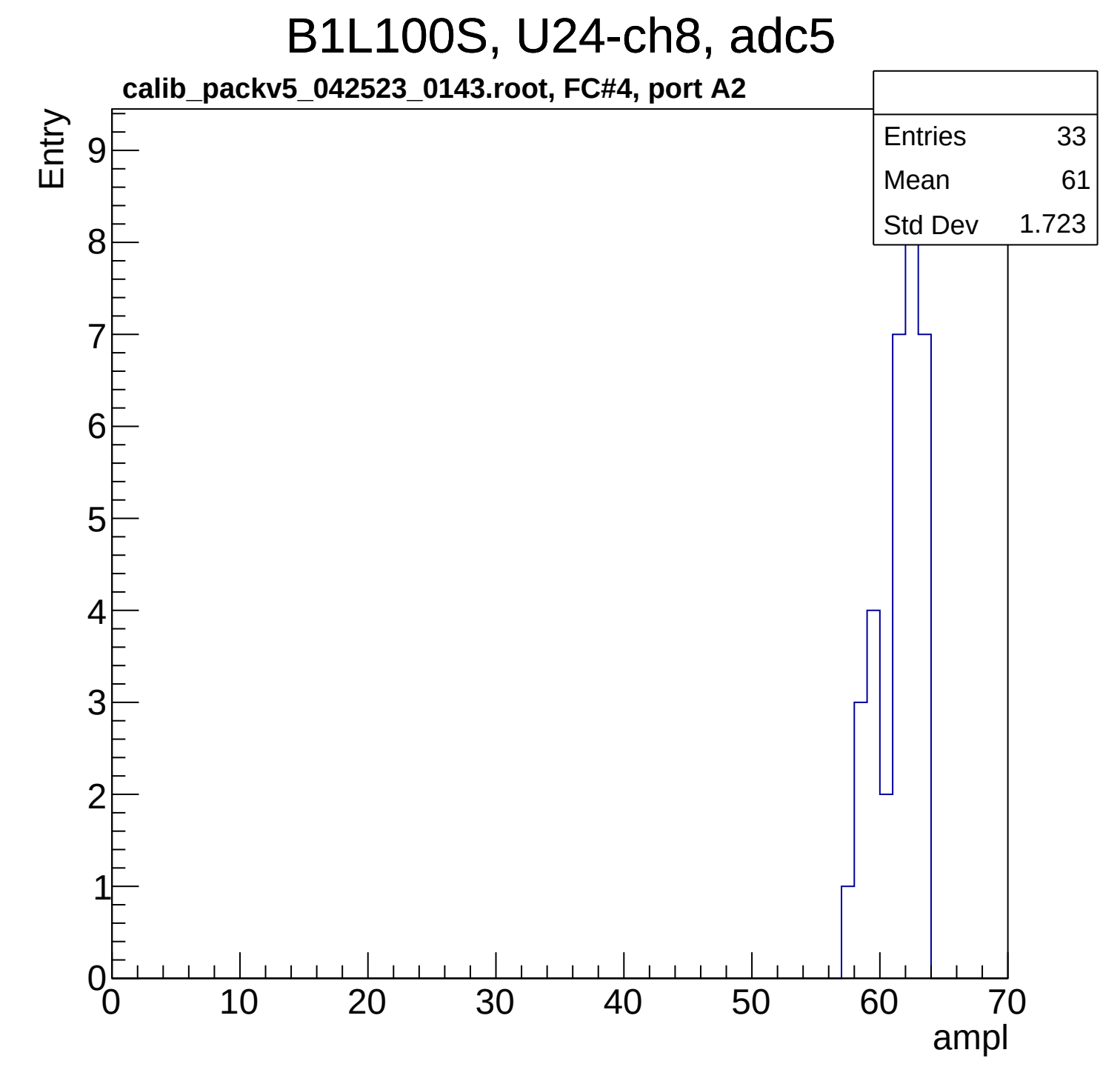
B1L100S, U24-ch8, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 61
Std Dev 1.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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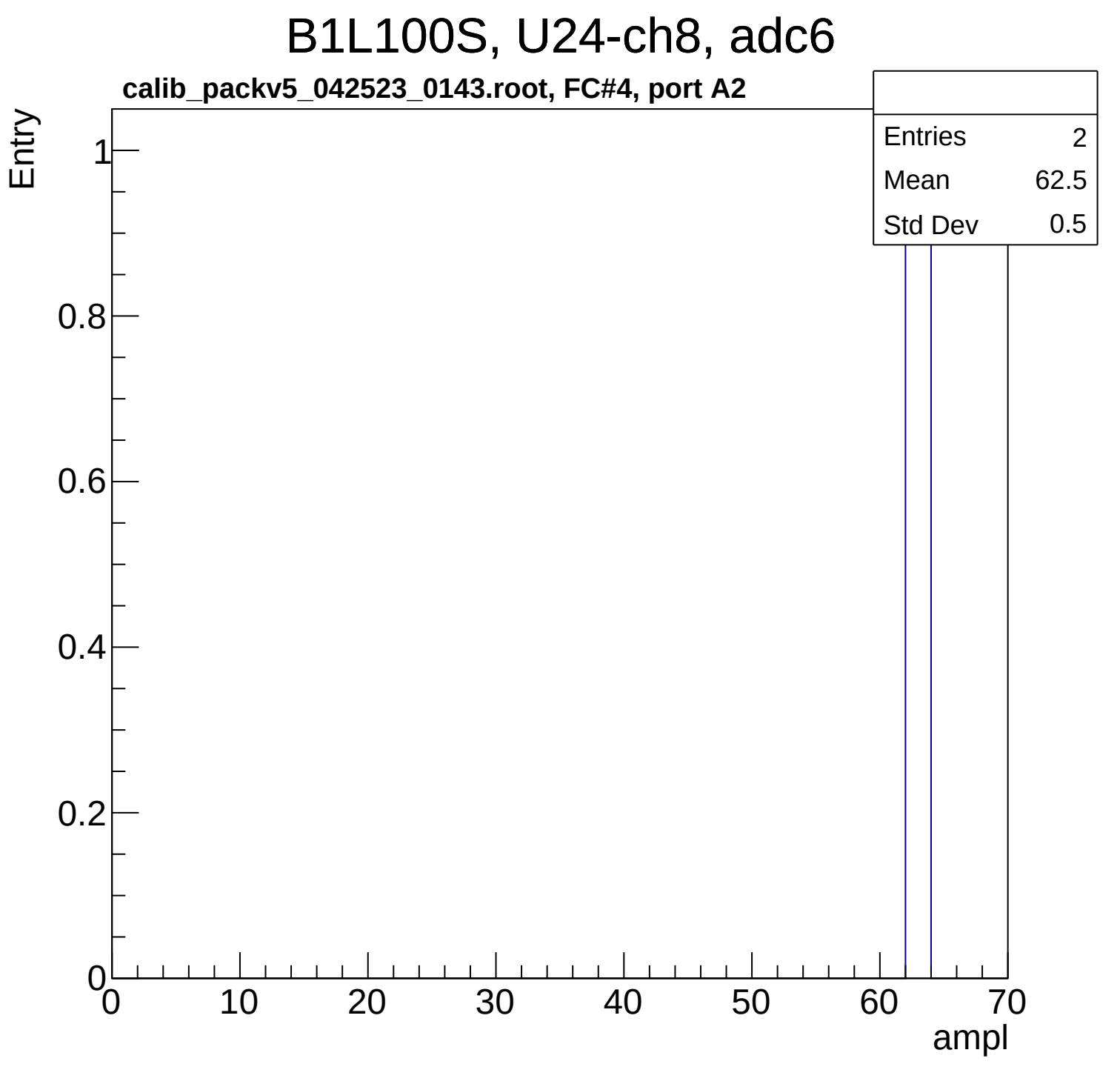
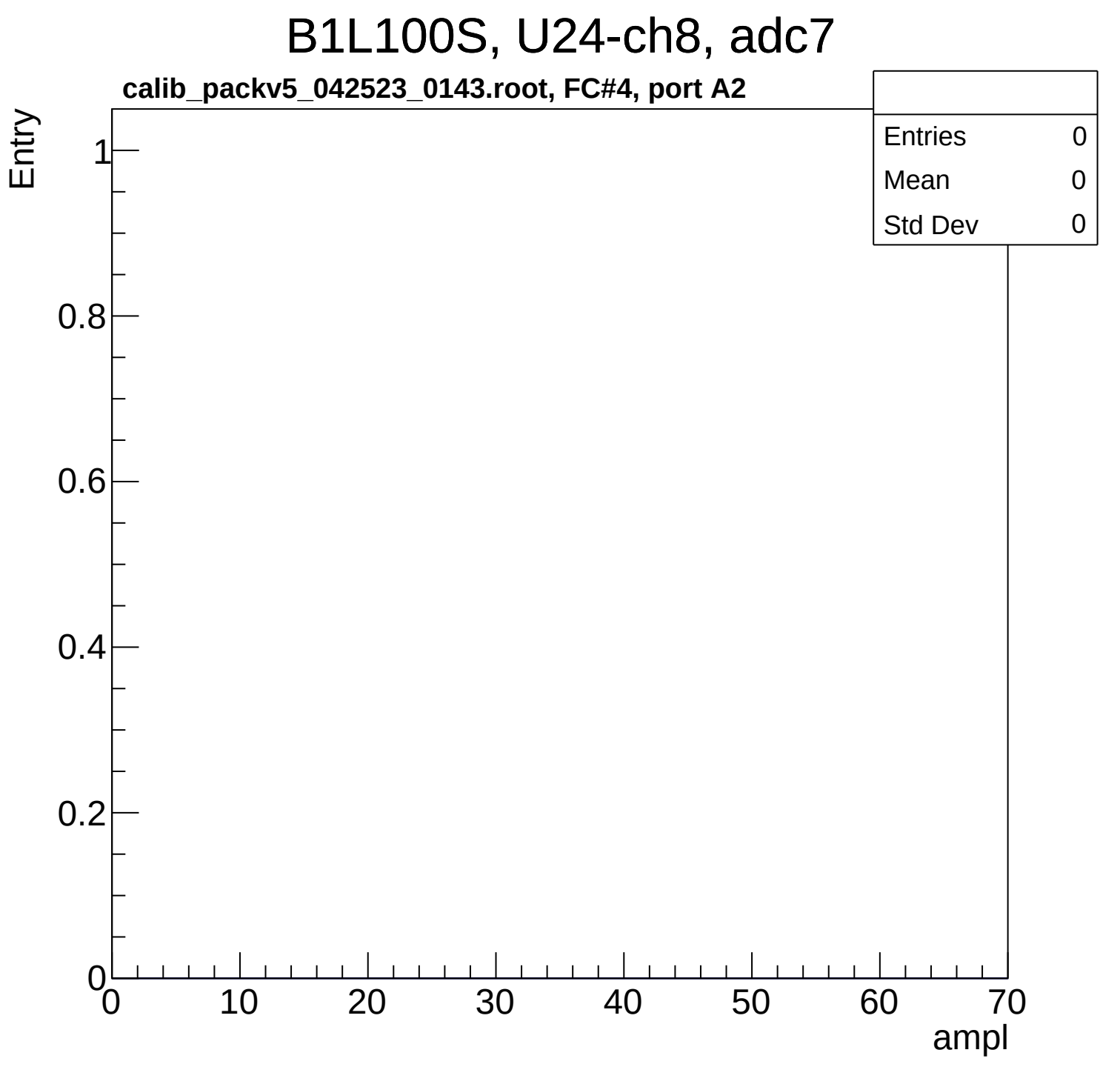
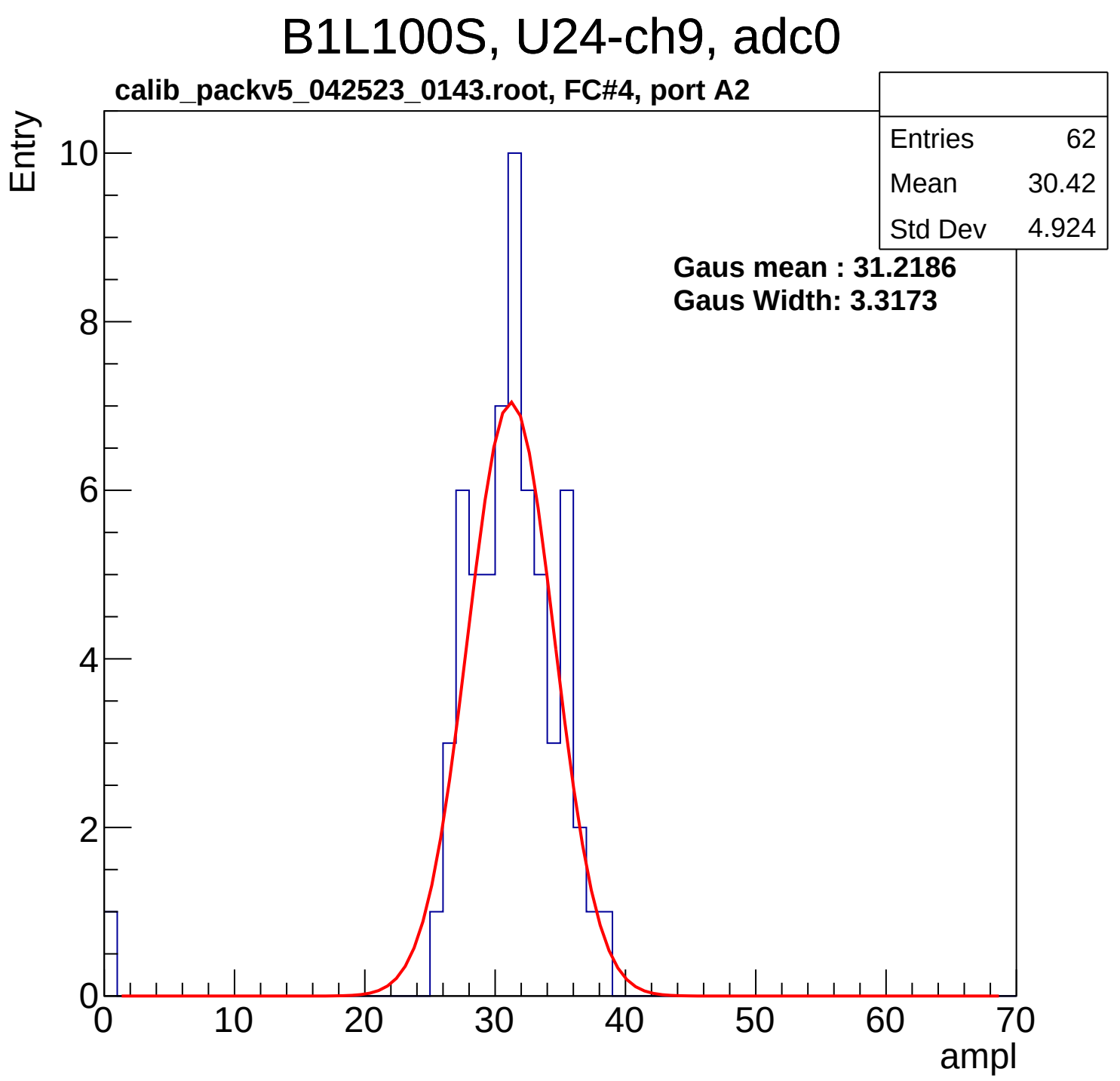
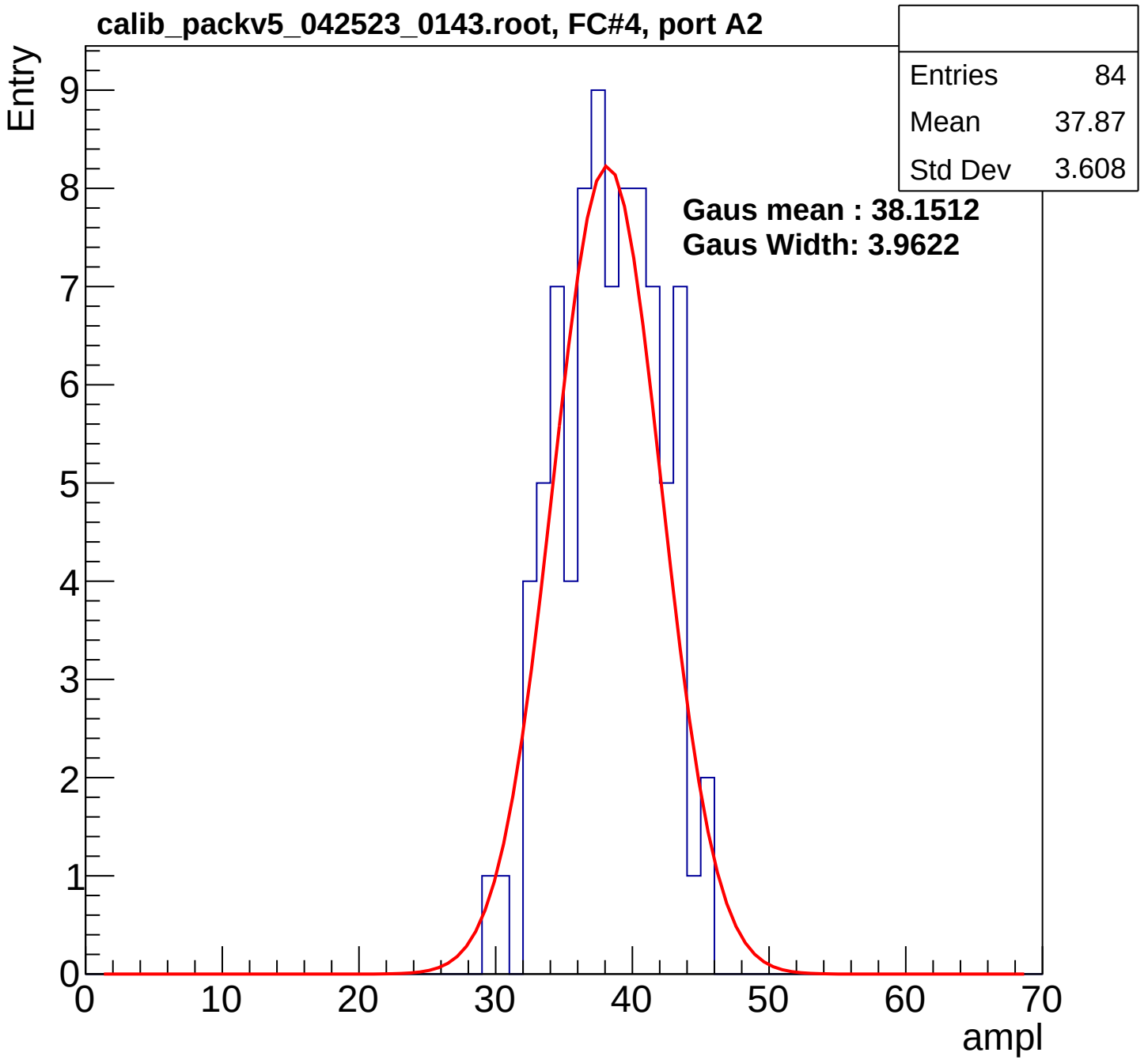

B1L100S, U24-ch8, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30.42
Std Dev 4.924
Gaus mean : 31.2186
Gaus Width: 3.3173
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.87
Std Dev 3.608
Gaus mean : 38.1512
Gaus Width: 3.9622
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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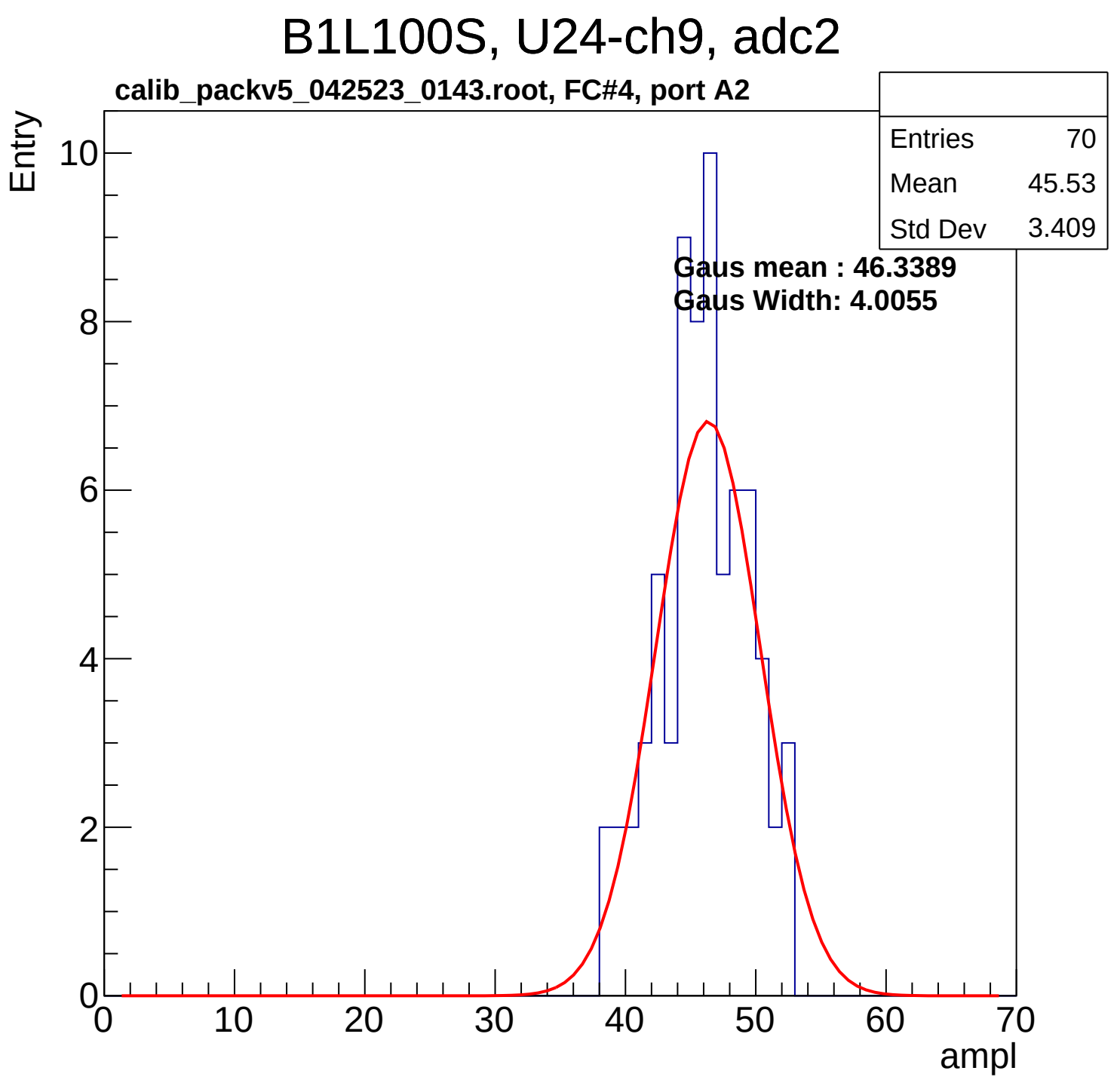
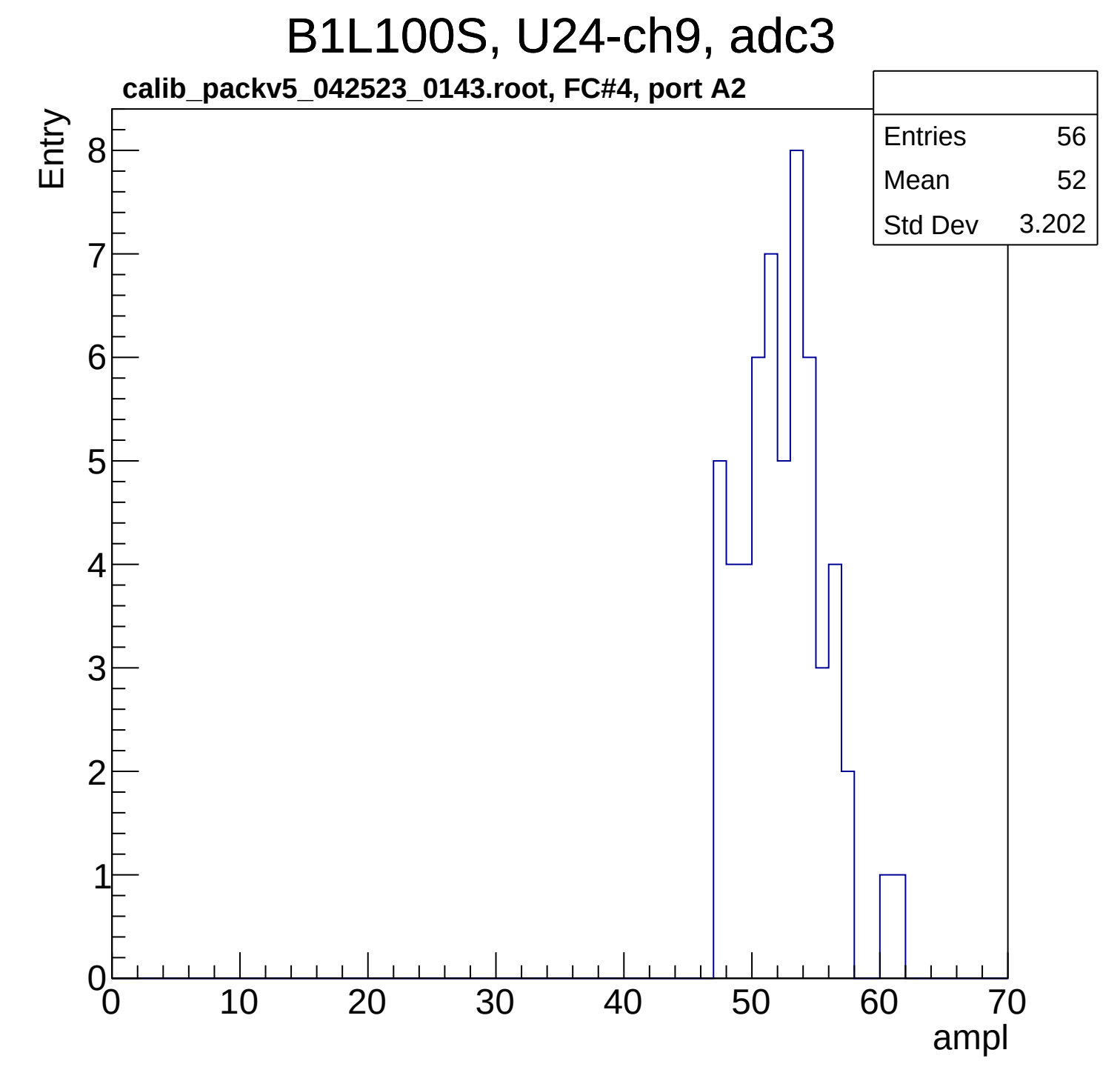

B1L100S, U24-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 45.53
Std Dev 3.409
Gaus mean : 46.3389
Gaus Width: 4.0055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch9, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 52
Std Dev 3.202
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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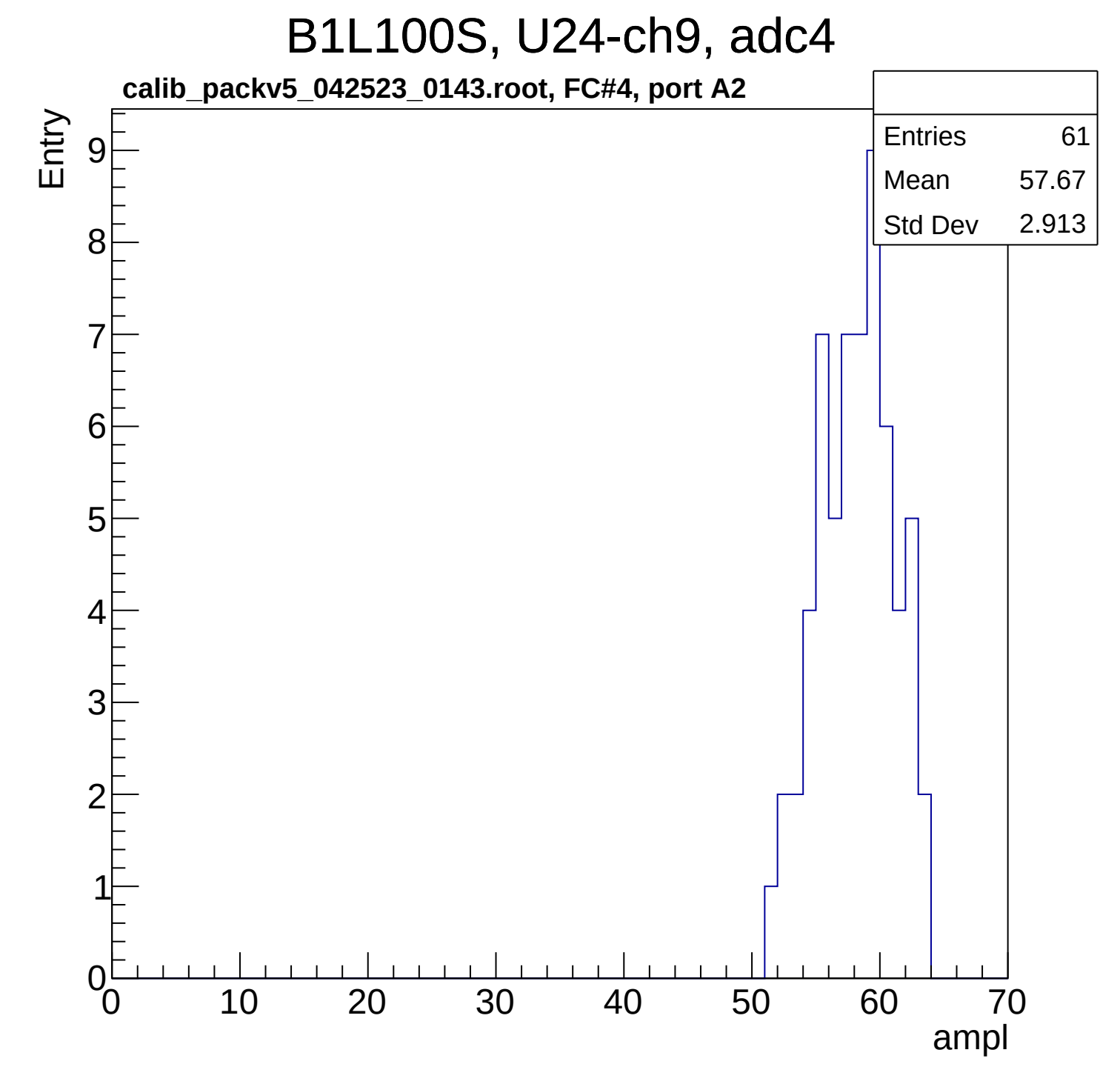
B1L100S, U24-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.67
Std Dev 2.913
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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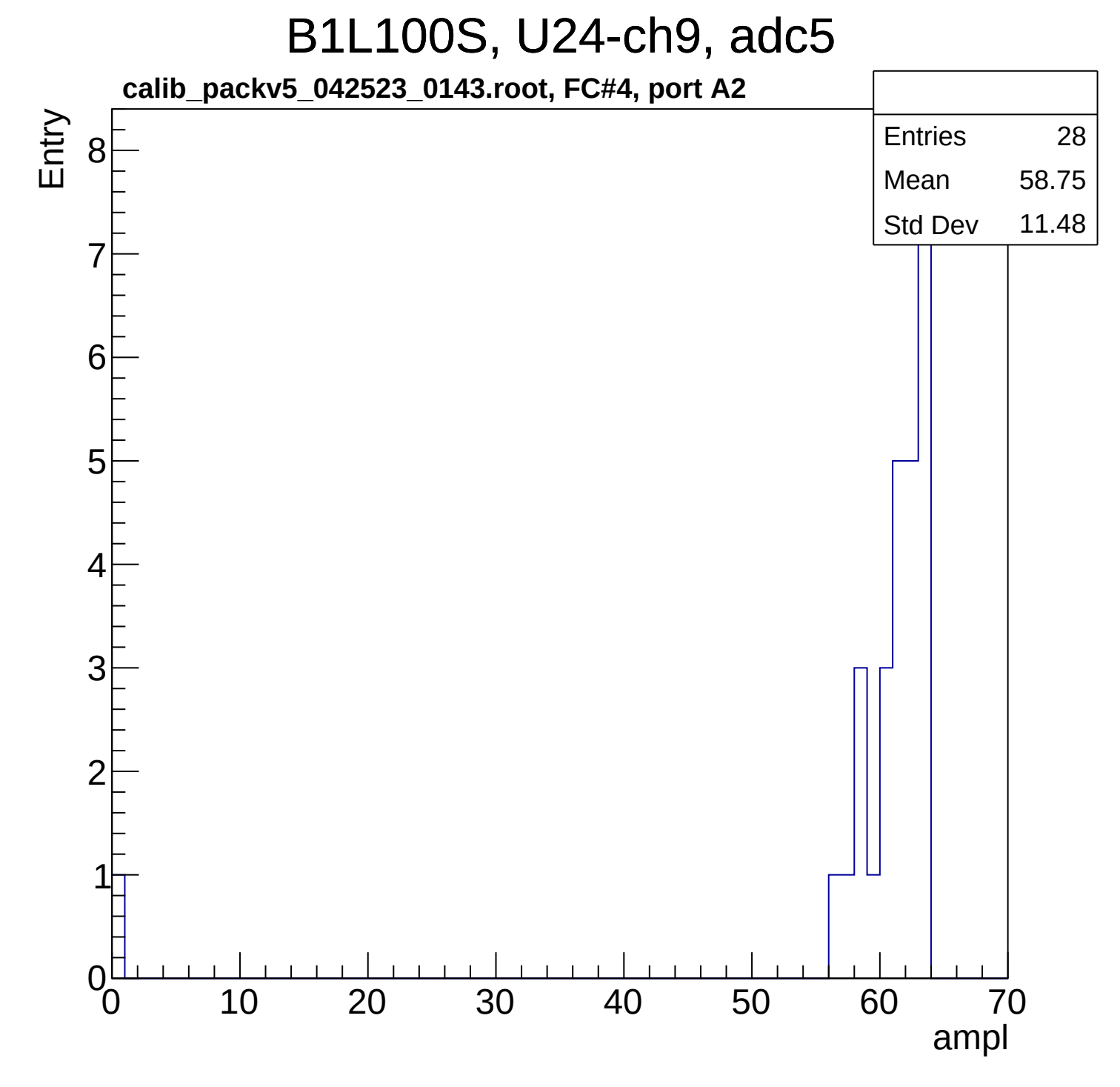
B1L100S, U24-ch9, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.75
Std Dev 11.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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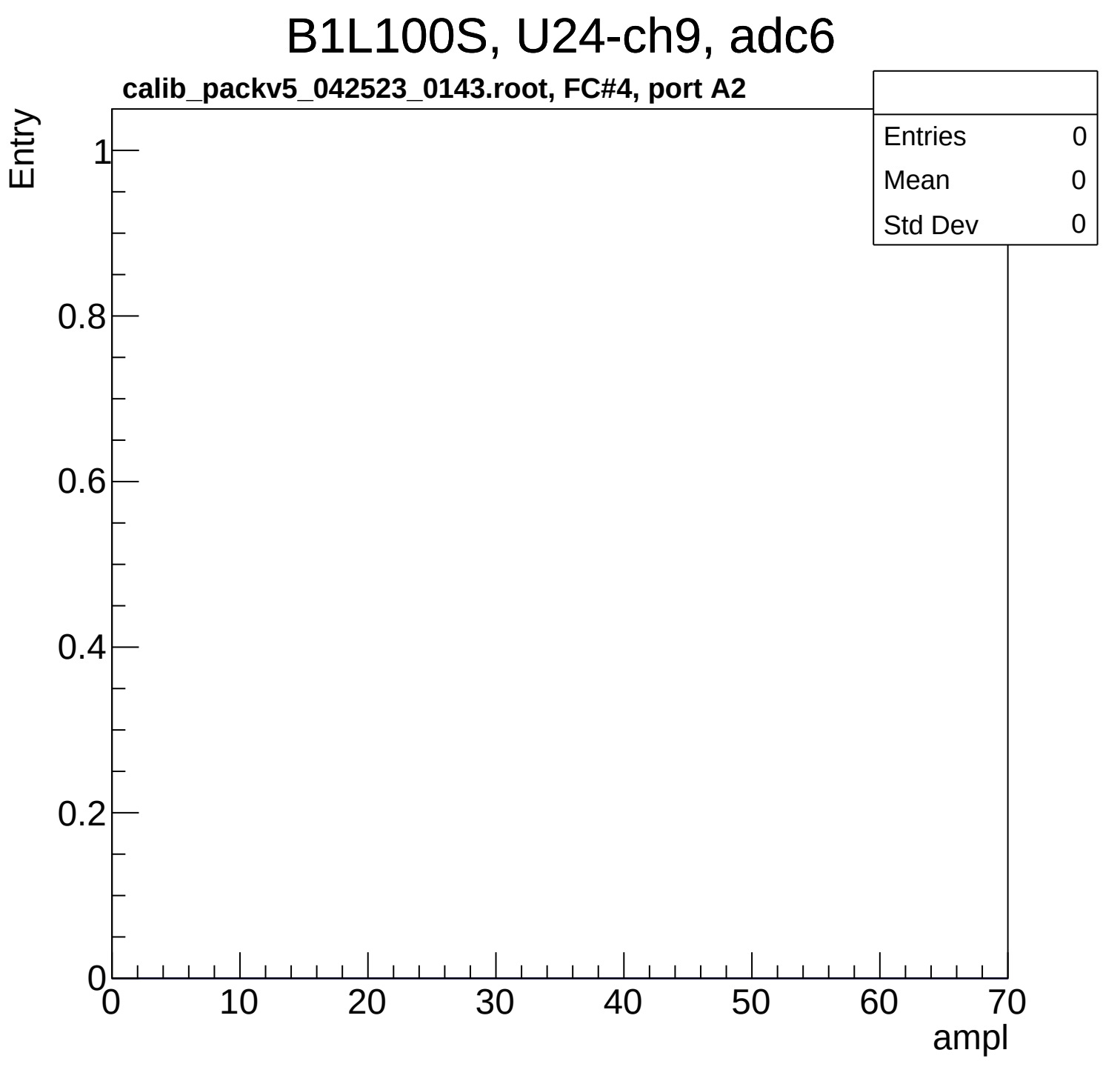
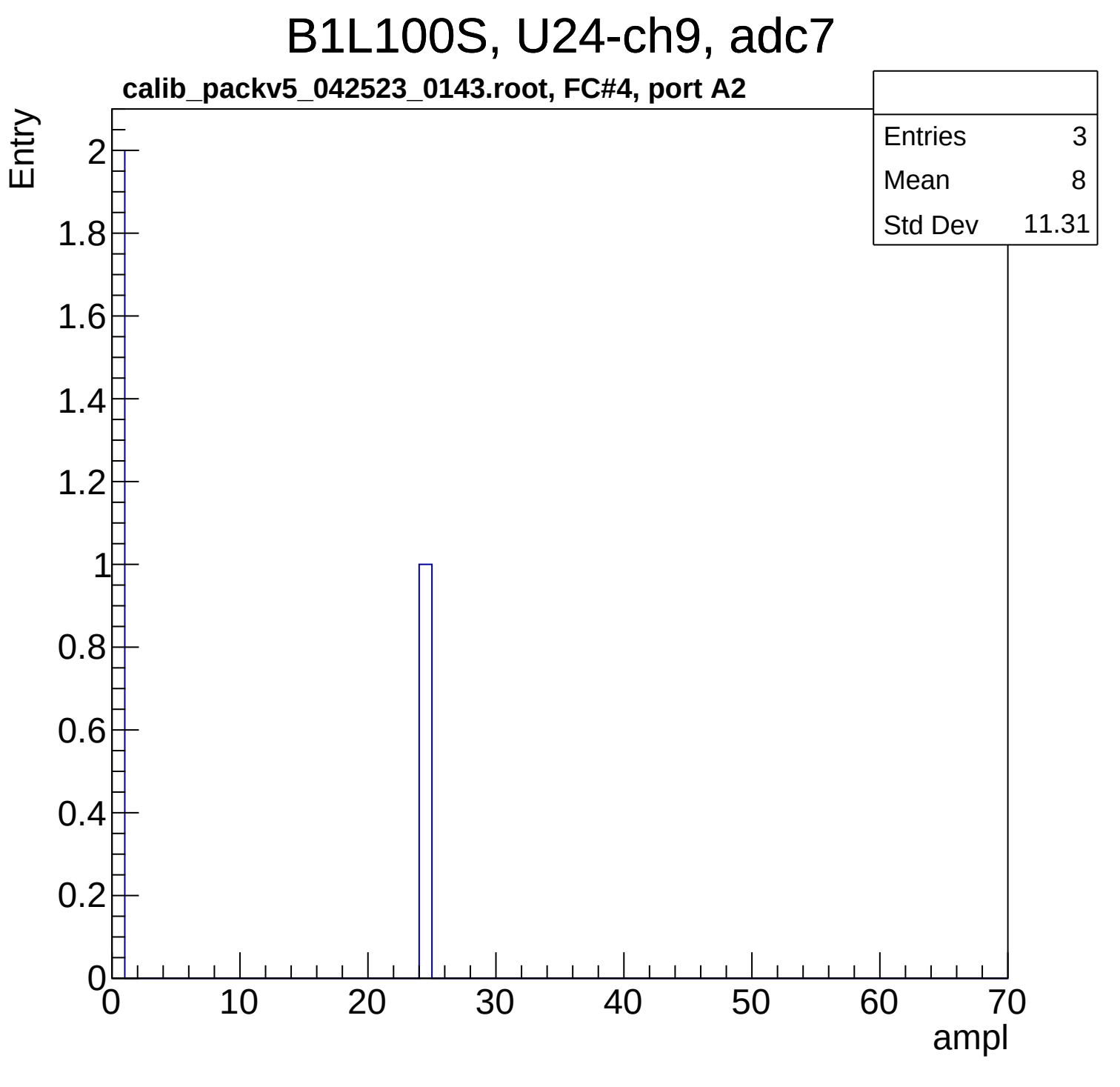

B1L100S, U24-ch9, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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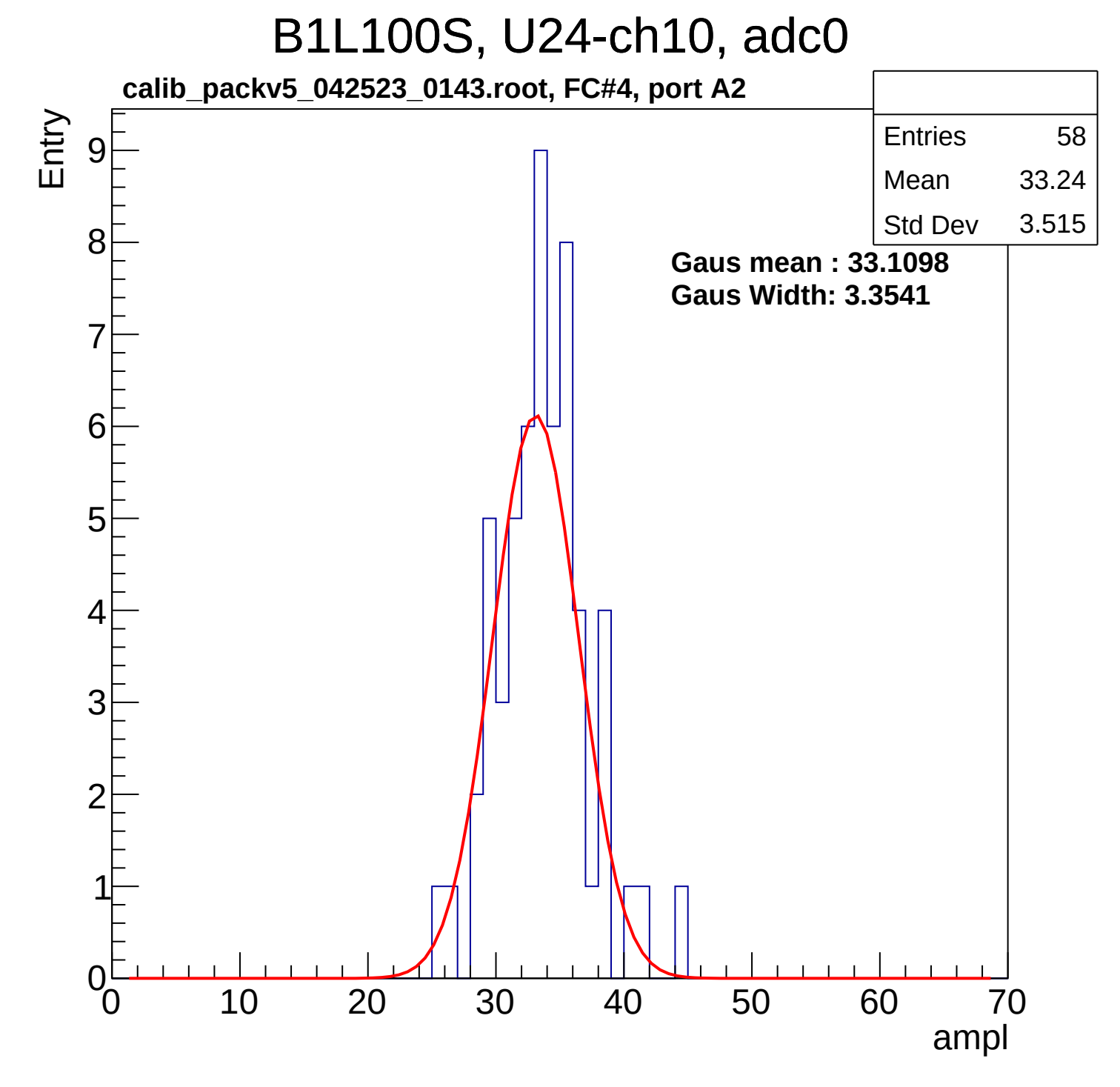
B1L100S, U24-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 33.24
Std Dev 3.515
Gaus mean : 33.1098
Gaus Width: 3.3541
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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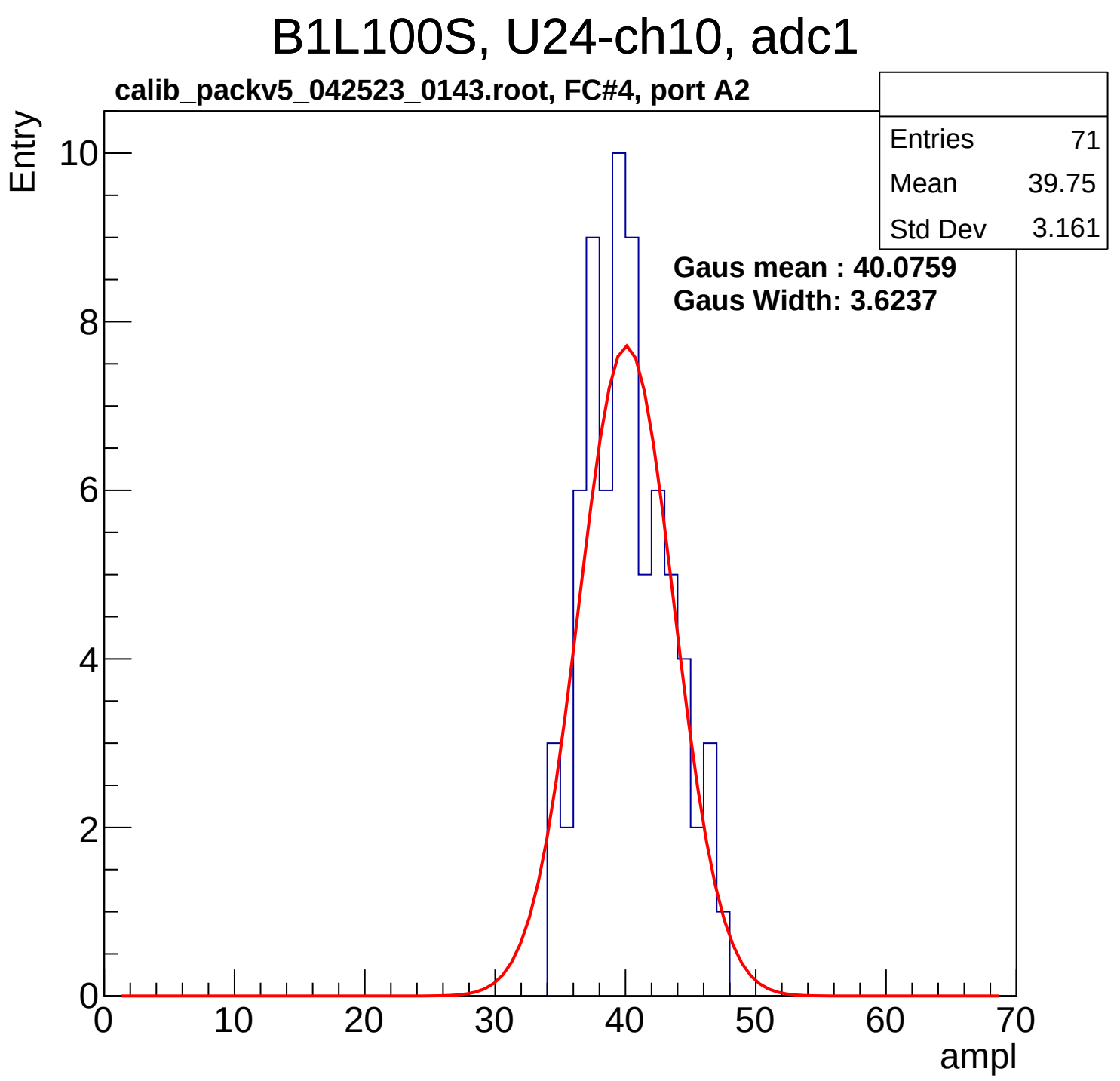
B1L100S, U24-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 39.75
Std Dev 3.161
Gaus mean : 40.0759
Gaus Width: 3.6237
Entry
ampl
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8
10
0
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20
30
40
50
60
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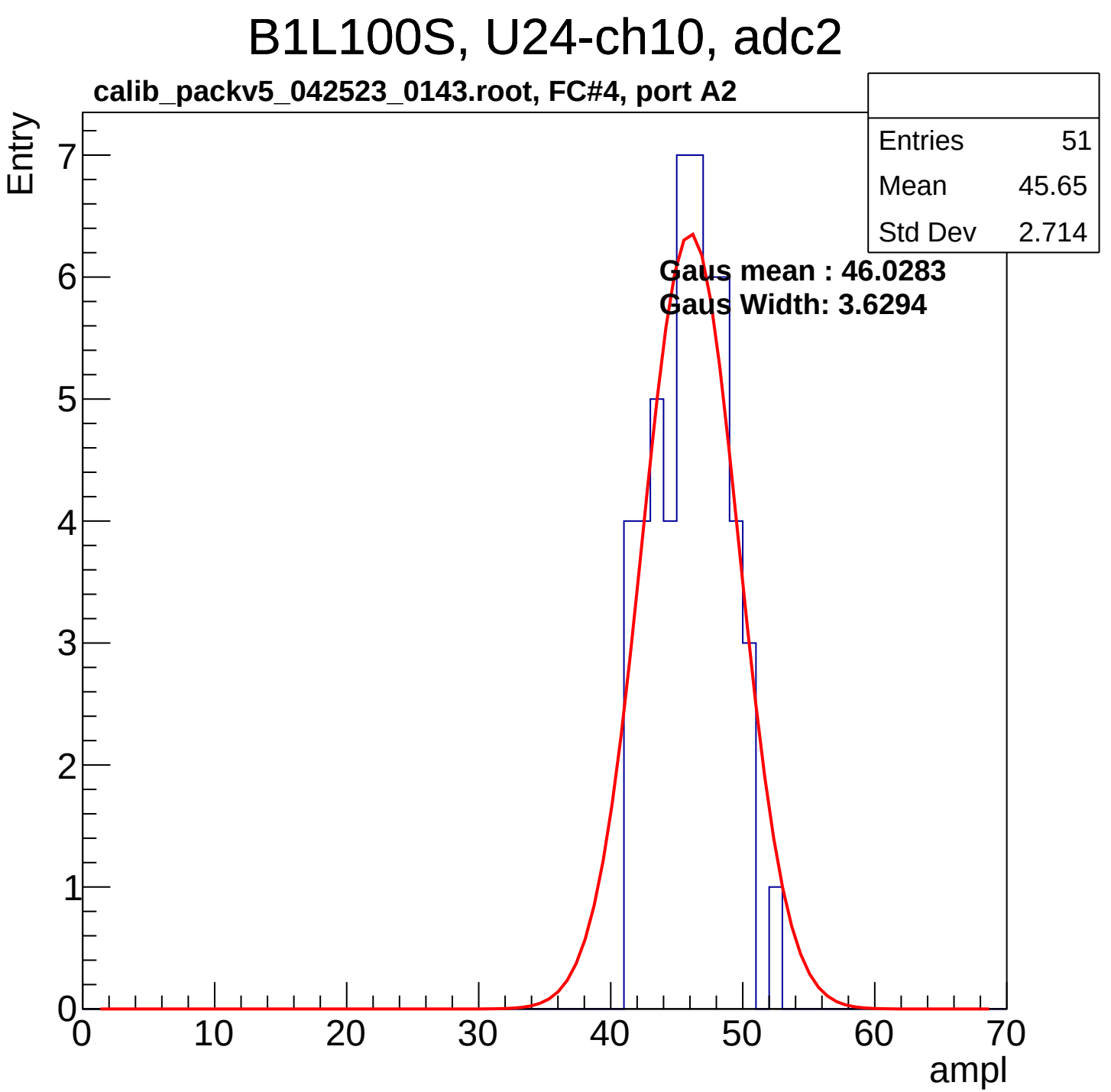
B1L100S, U24-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 45.65
Std Dev 2.714
Gaus mean : 46.0283
Gaus Width: 3.6294
Entry
ampl
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3
4
5
6
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0
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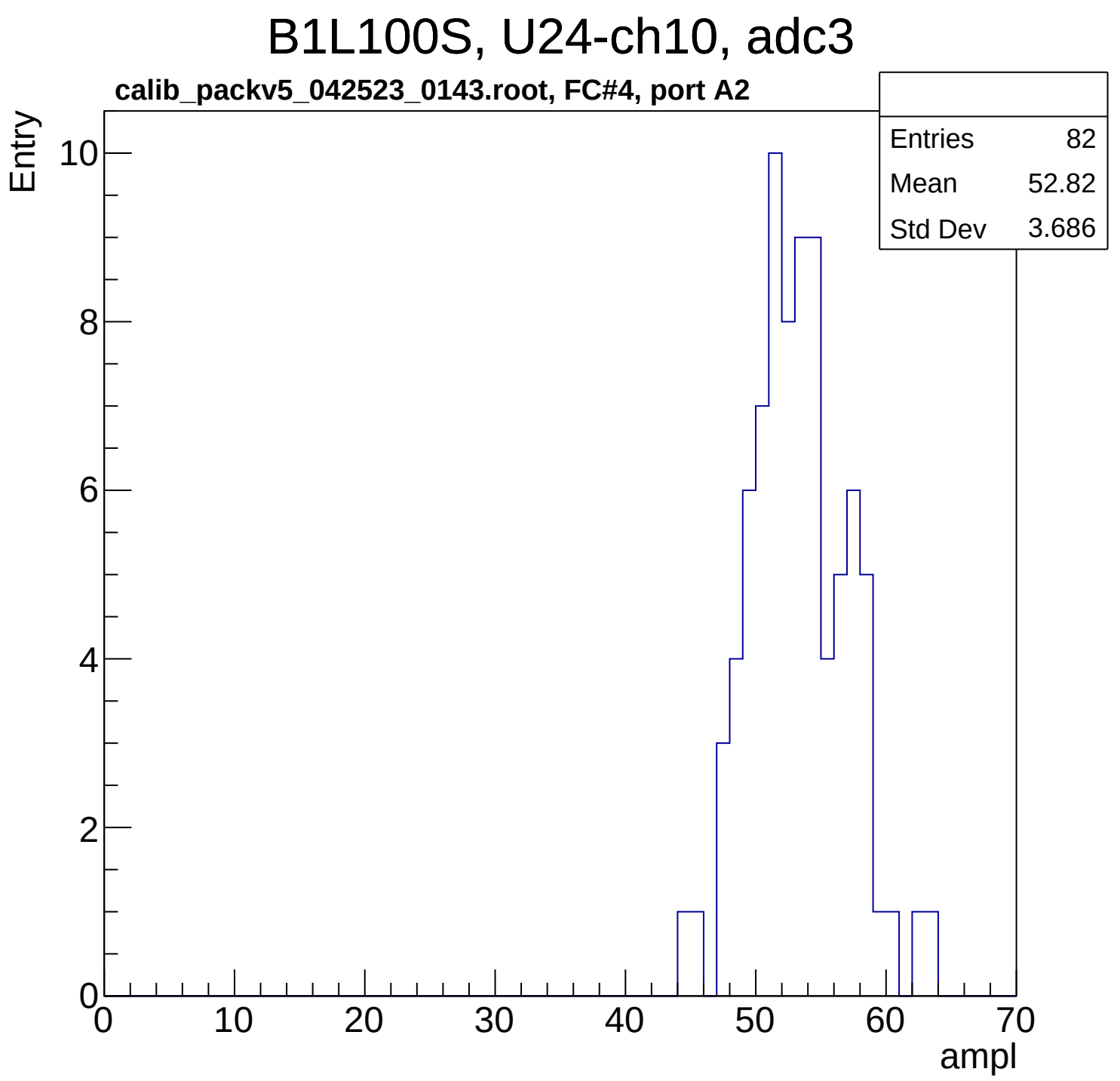
60
70


B1L100S, U24-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 52.82
Std Dev 3.686
Entry
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6
8
10
0
10
20
30
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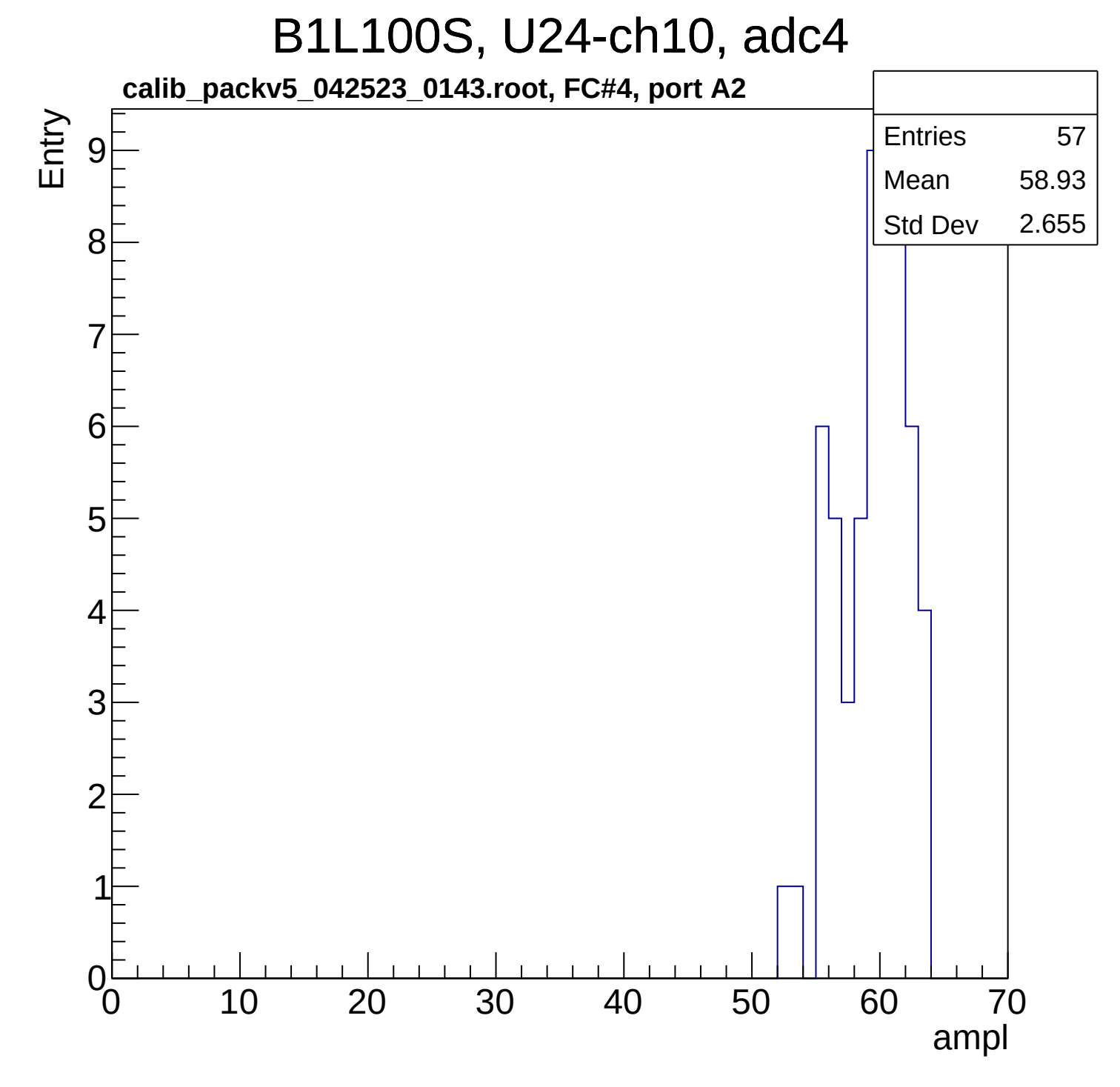
50
60
70
ampl


B1L100S, U24-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 58.93
Std Dev 2.655
Entry
ampl
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3
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6
7
8
9
0
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20
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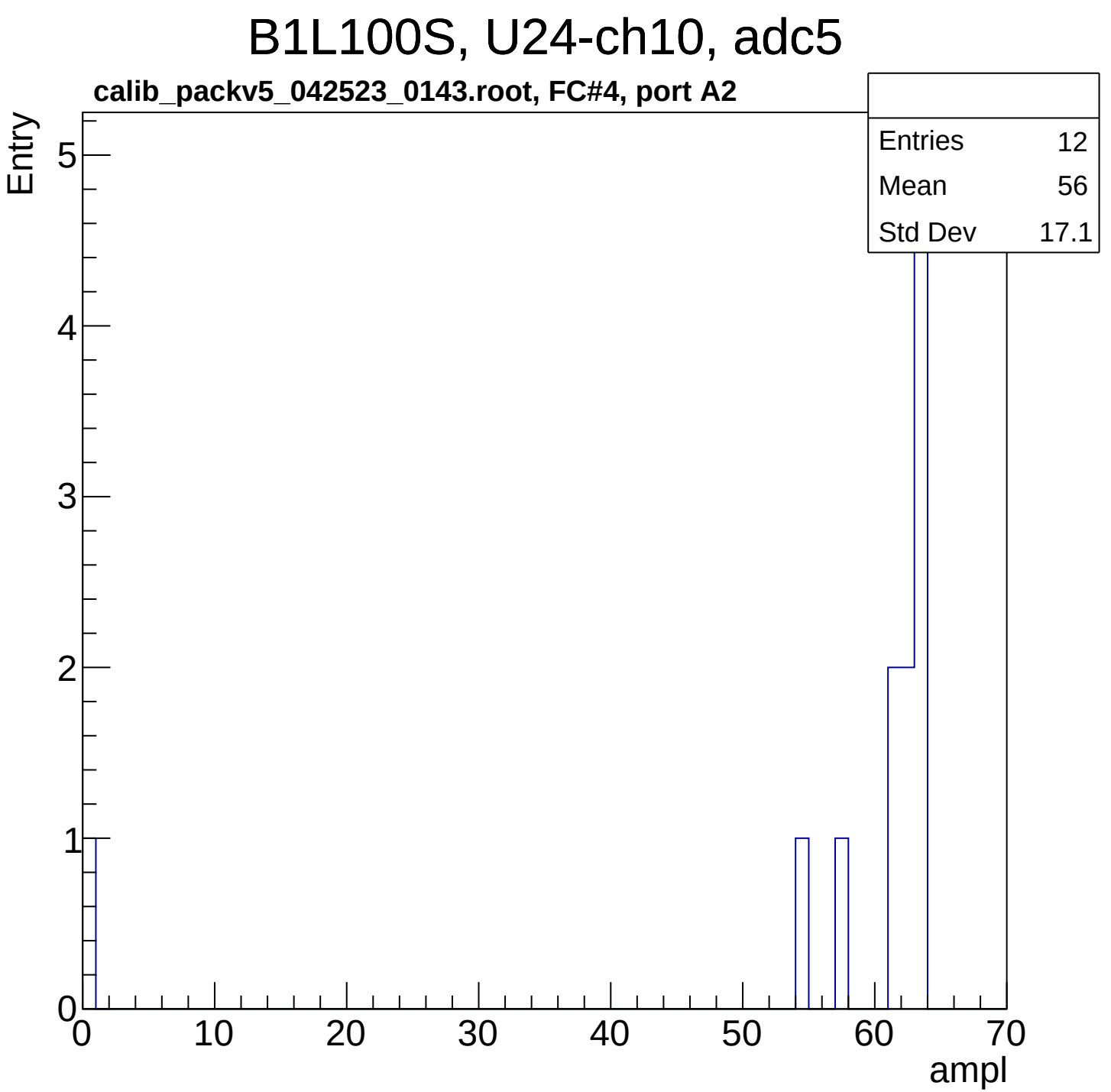
B1L100S, U24-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56
Std Dev 17.1
Entry
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3
4
5
0
10
20
30
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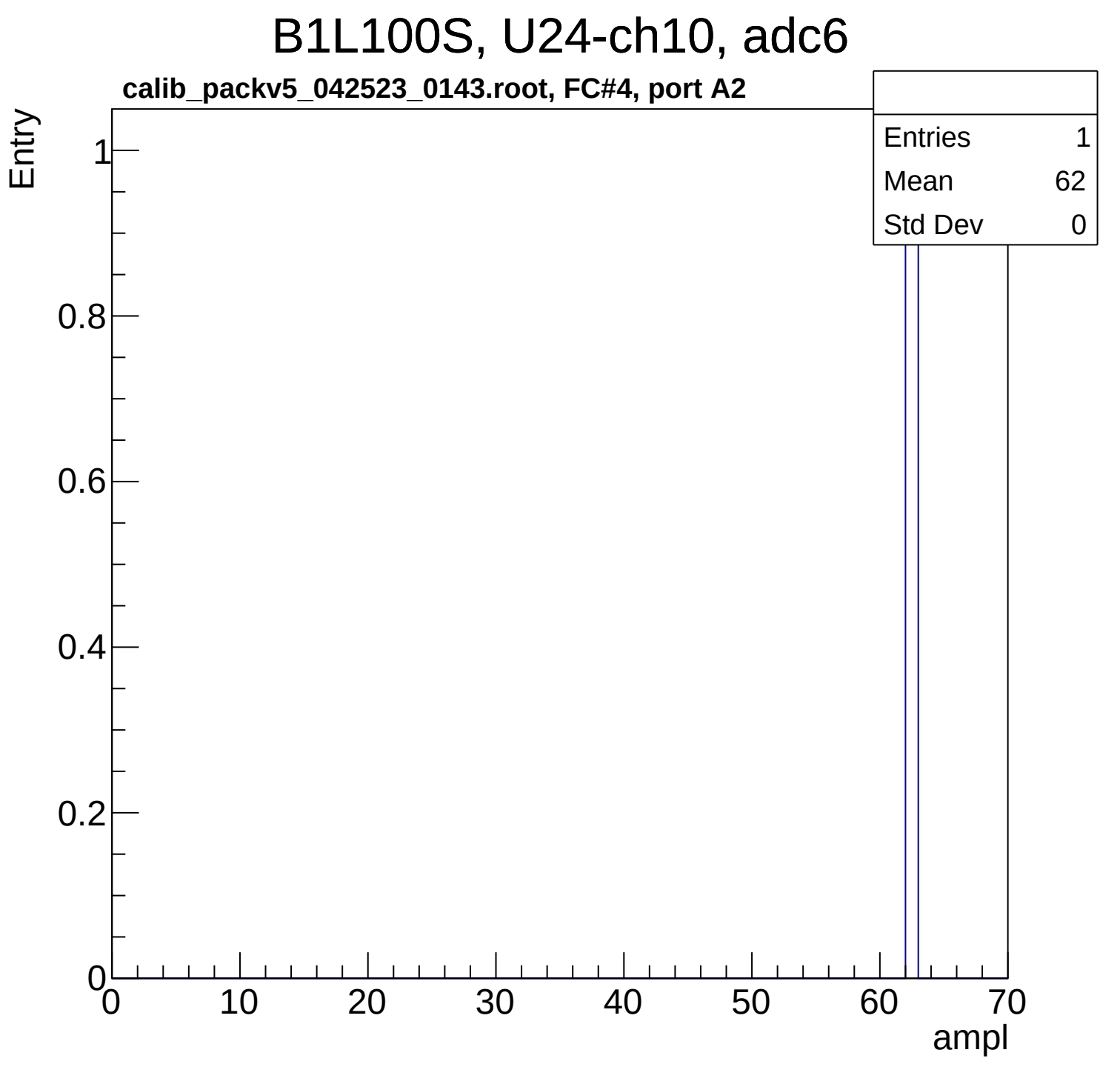
50
60
70
ampl


B1L100S, U24-ch10, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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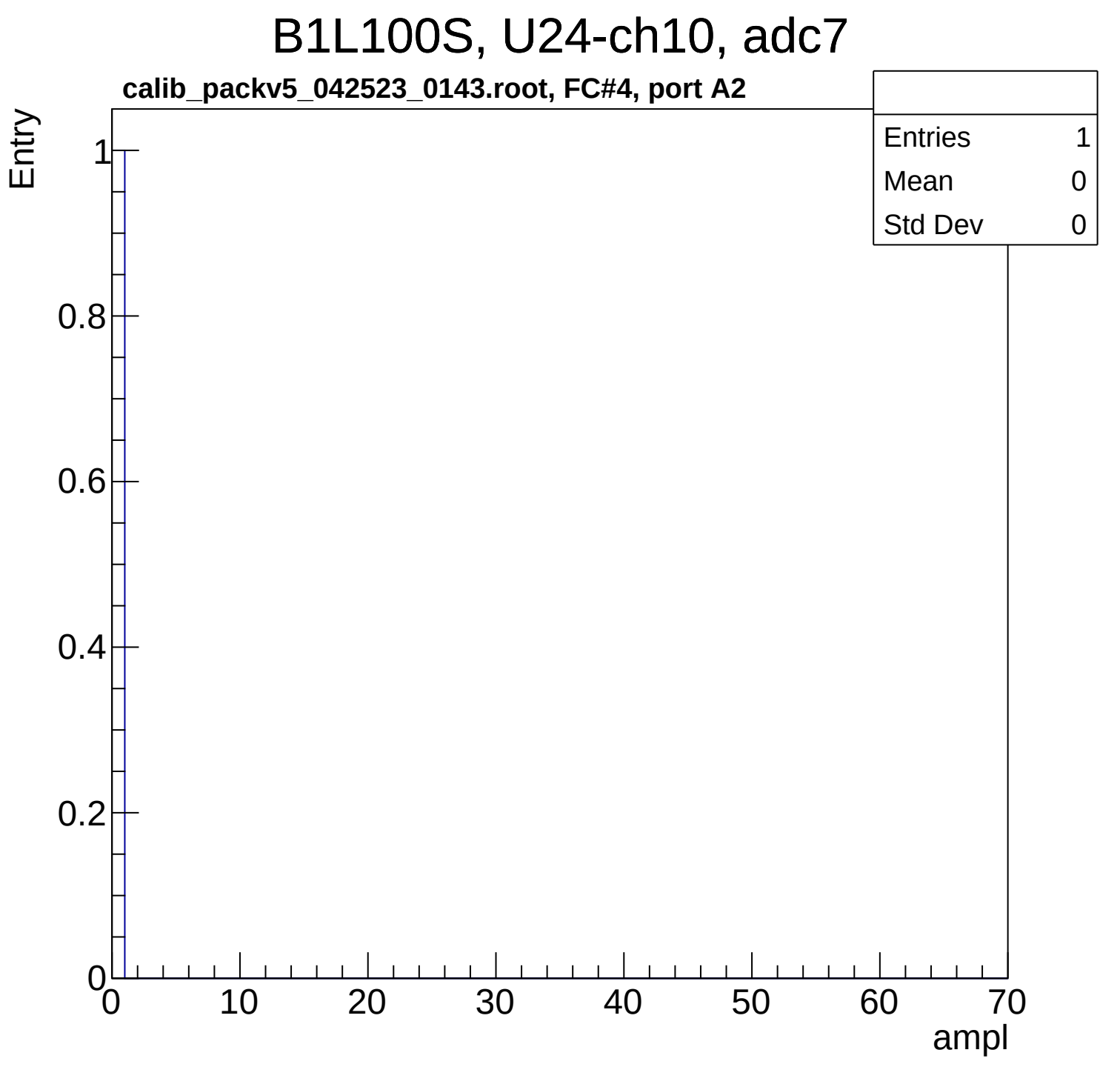
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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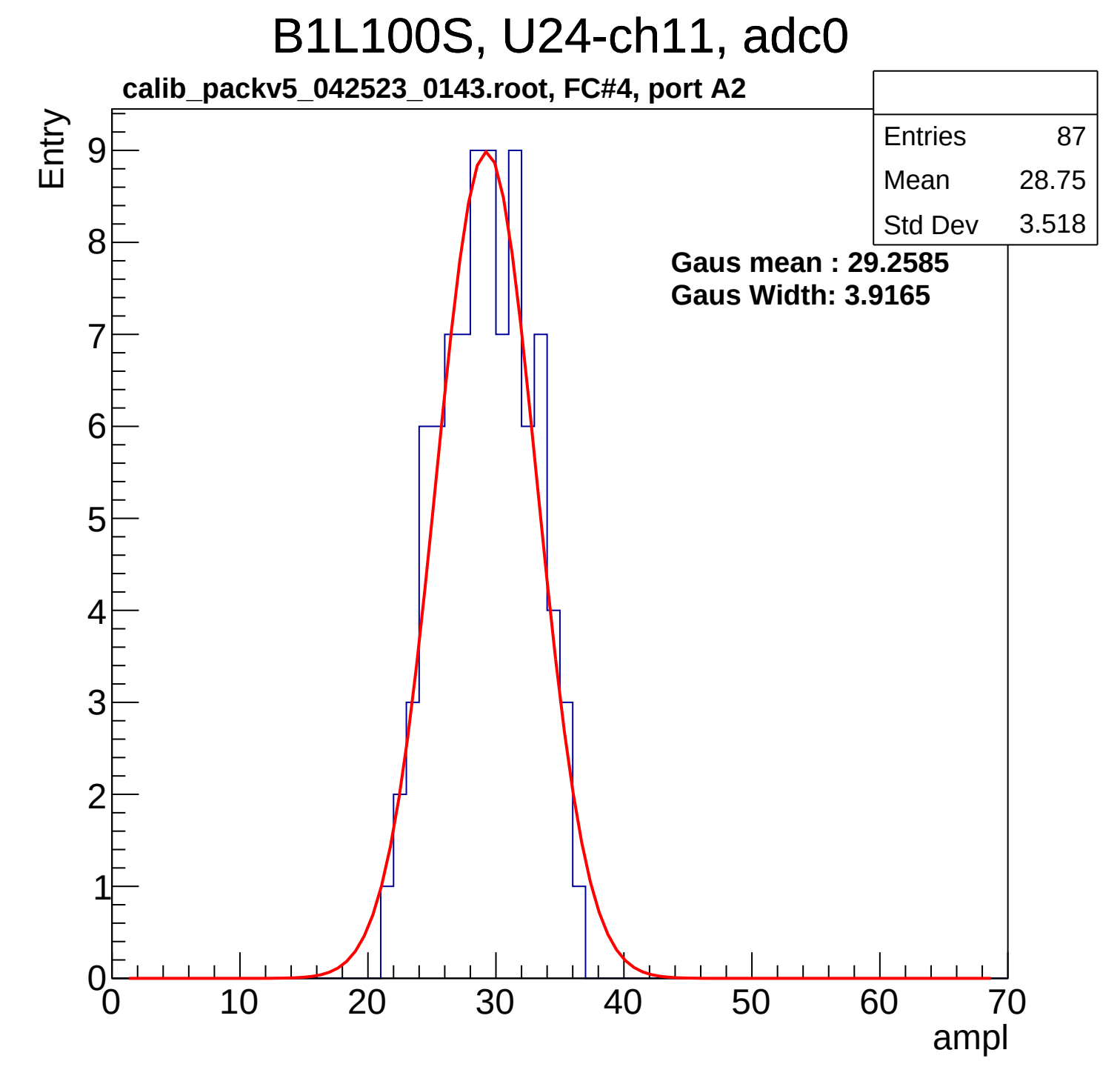
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 28.75
Std Dev 3.518
Gaus mean : 29.2585
Gaus Width: 3.9165
Entry
ampl
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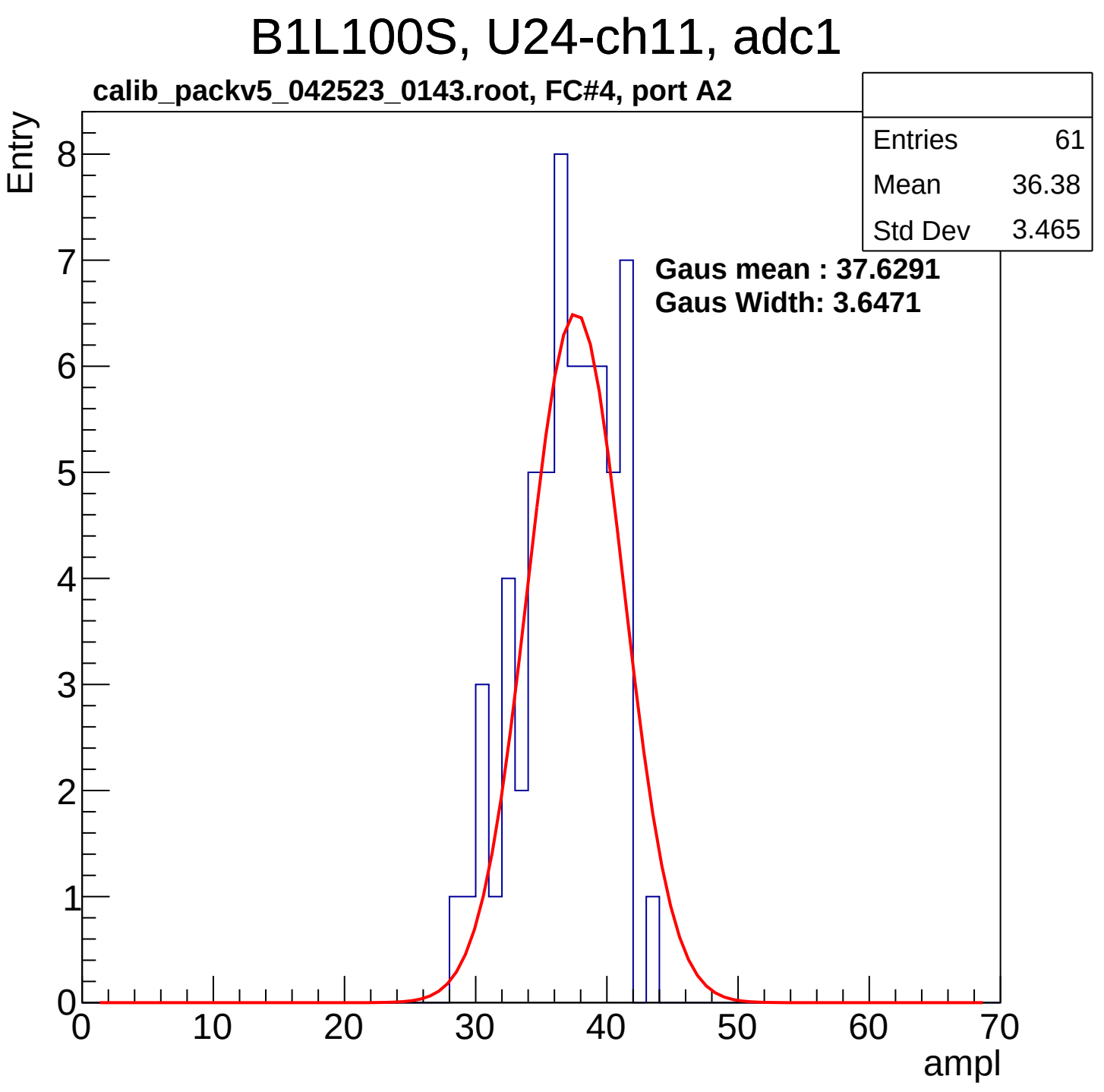
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U24-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 36.38
Std Dev 3.465
Gaus mean : 37.6291
Gaus Width: 3.6471
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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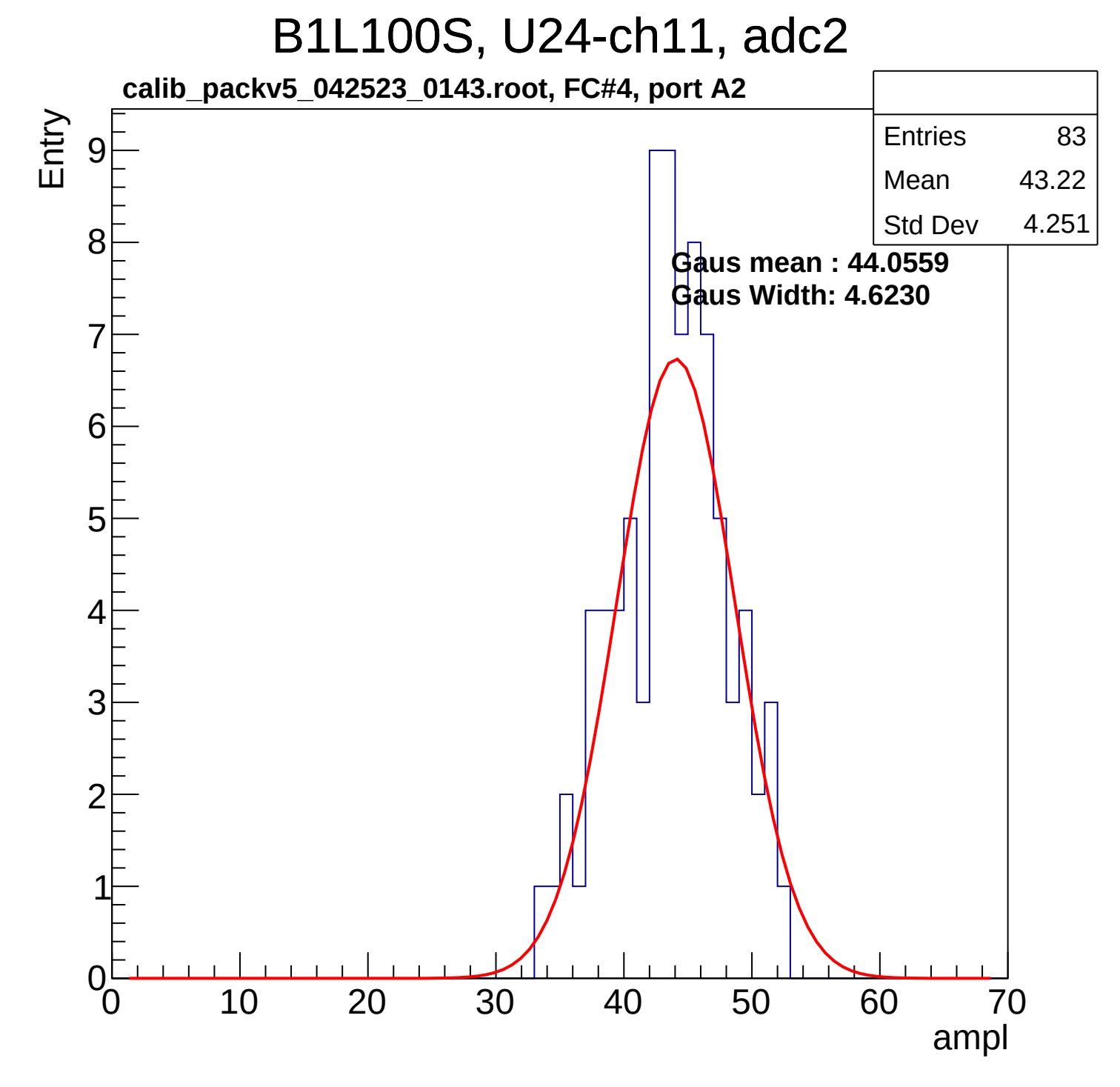
B1L100S, U24-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 43.22
Std Dev 4.251
Gaus mean : 44.0559
Gaus Width: 4.6230
Entry
ampl
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2
3
4
5
6
7
8
9
0
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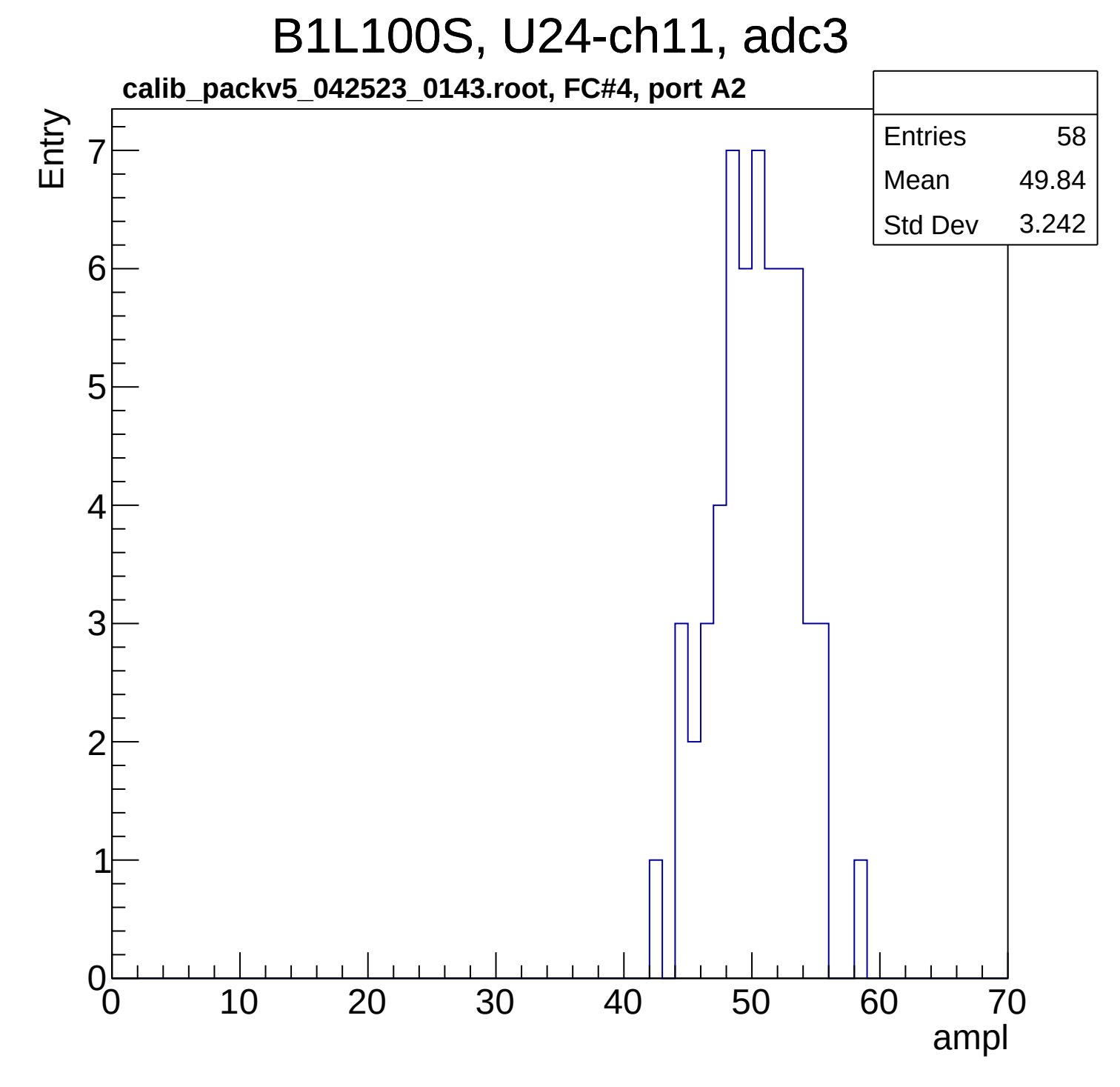
B1L100S, U24-ch11, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.84
Std Dev 3.242
Entry
ampl
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2
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4
5
6
7
0
10
20
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40
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60
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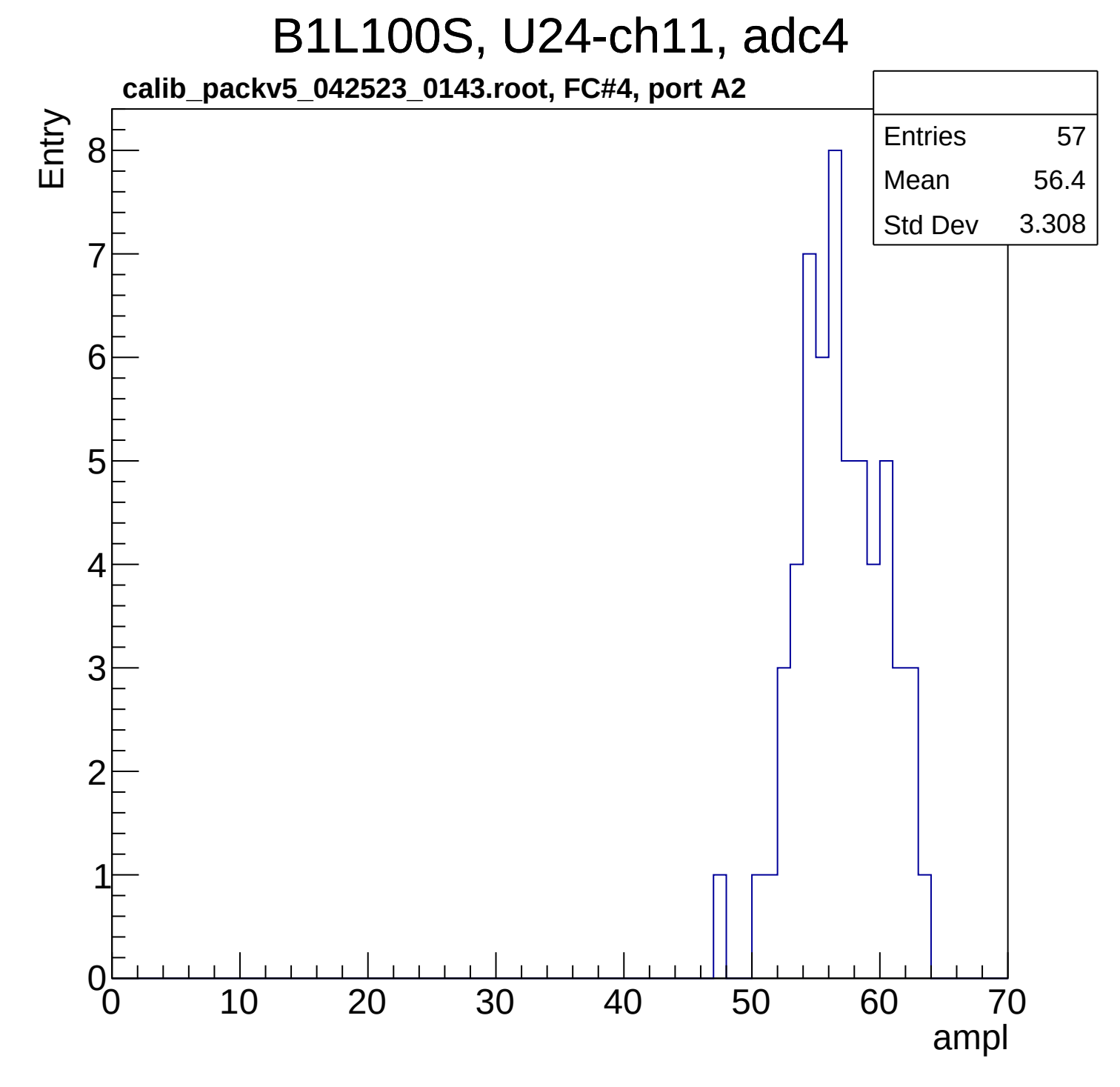
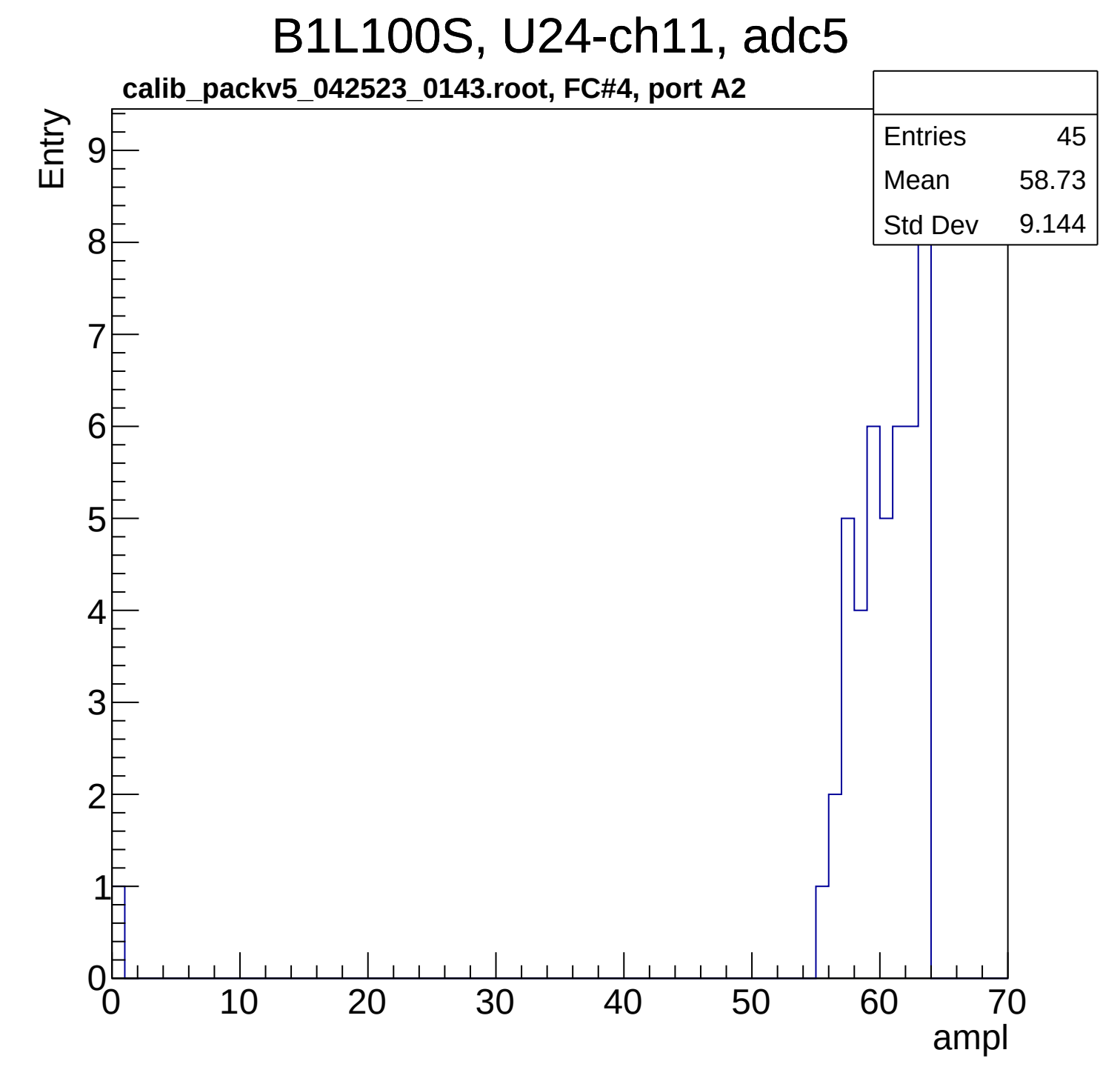

B1L100S, U24-ch11, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.4
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch11, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.73
Std Dev 9.144
Entry
ampl
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2
3
4
5
6
7
8
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0
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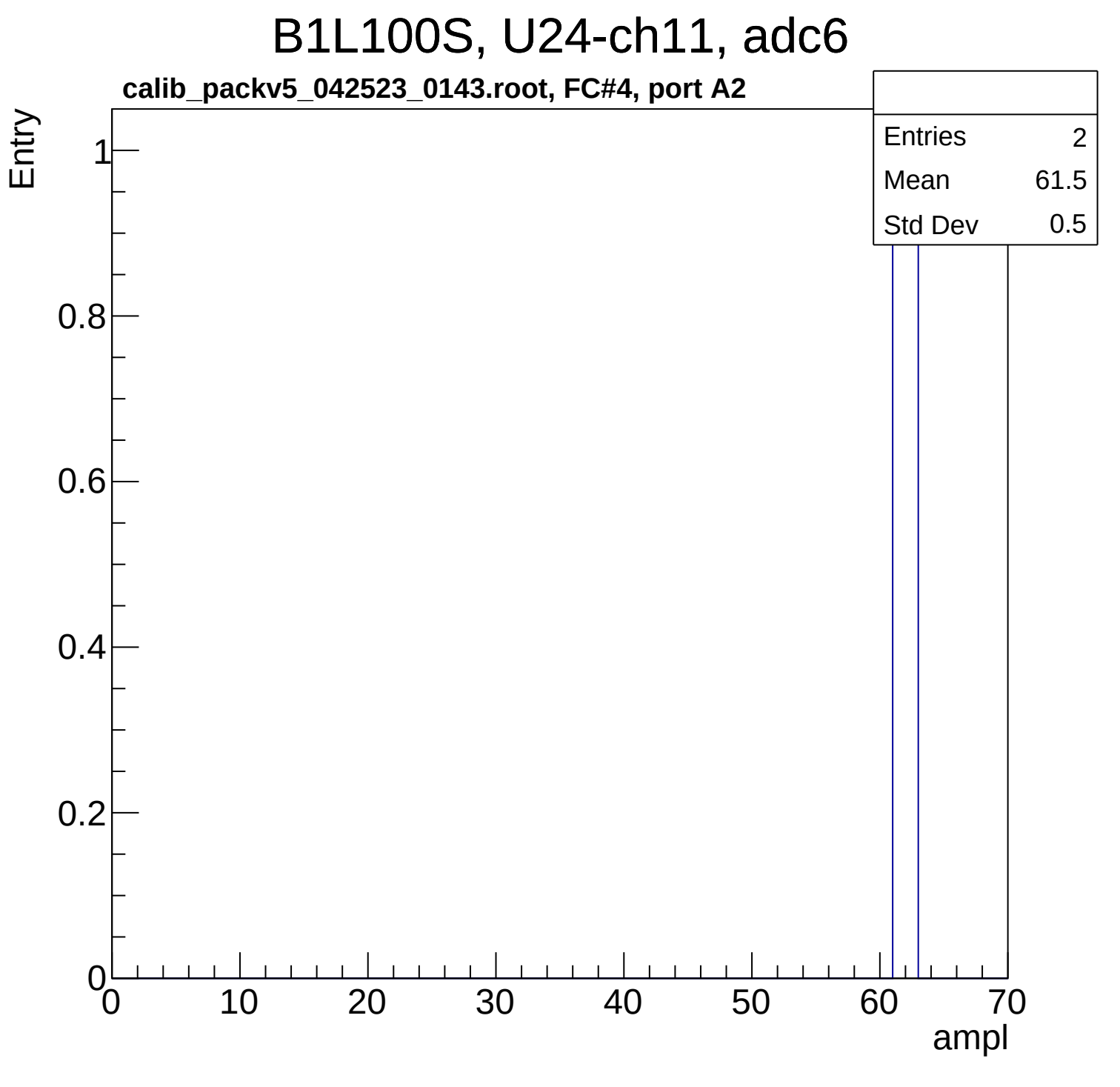
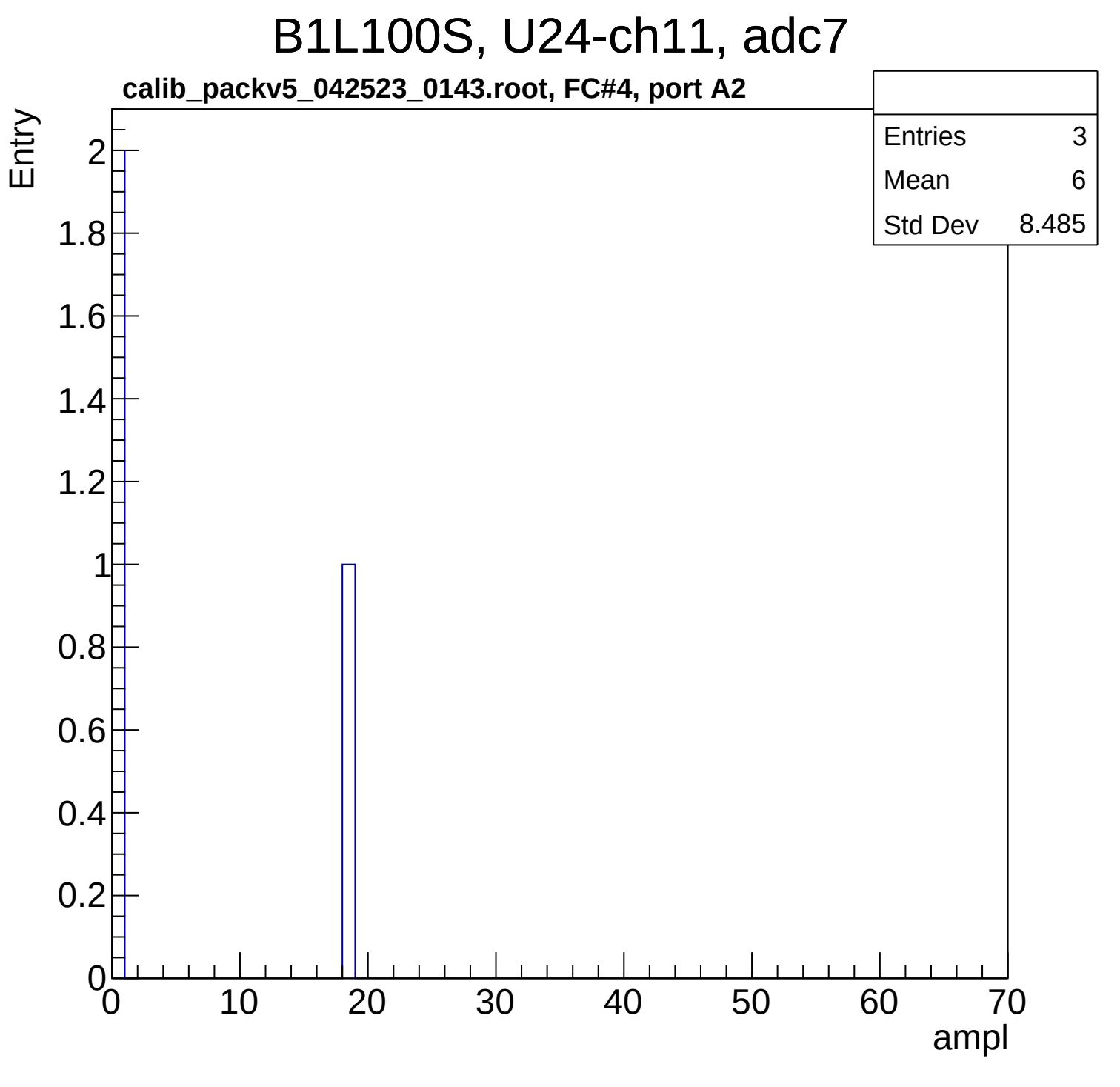

B1L100S, U24-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 6
Std Dev 8.485
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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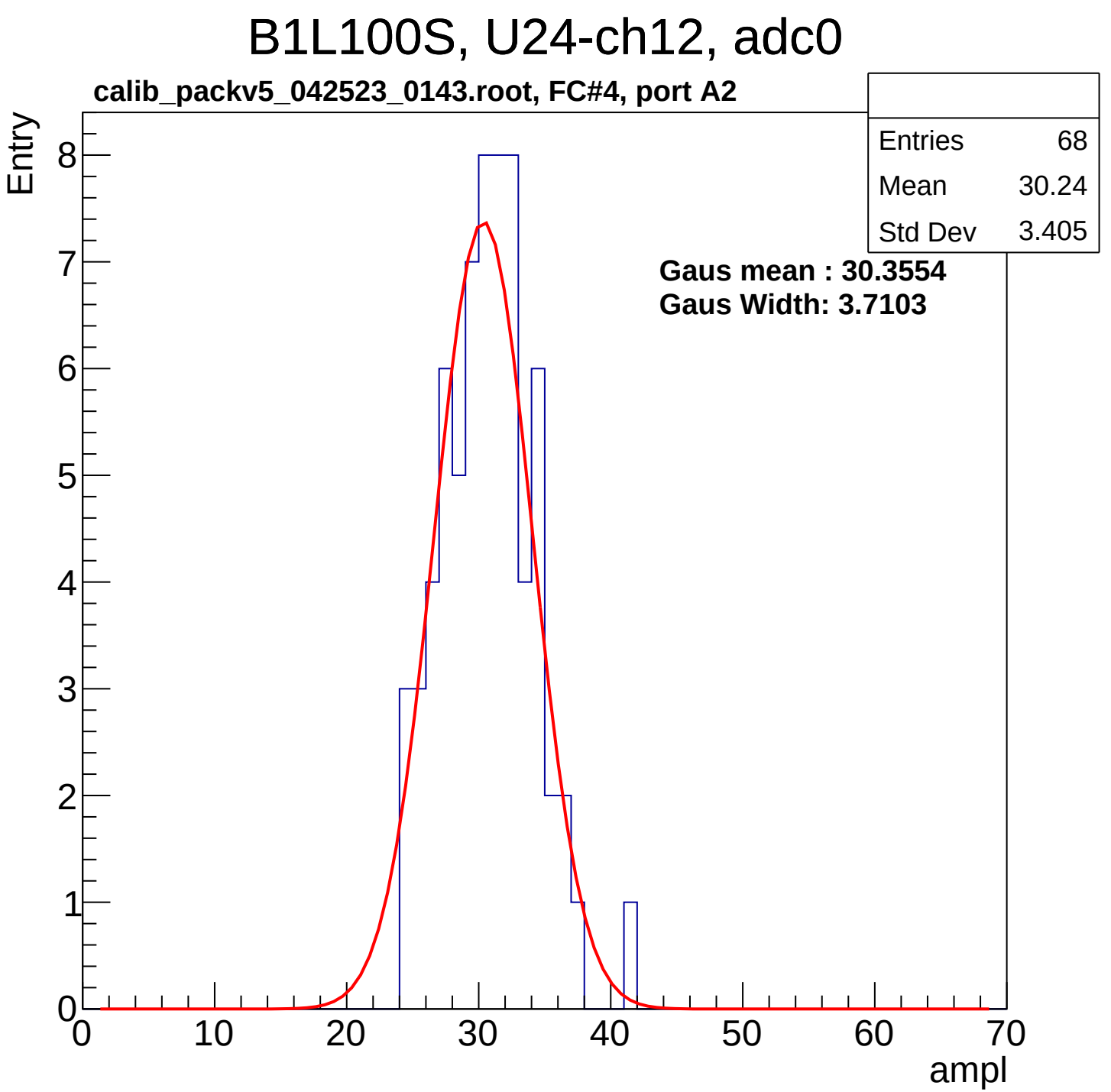
B1L100S, U24-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 30.24
Std Dev 3.405
Gaus mean : 30.3554
Gaus Width: 3.7103
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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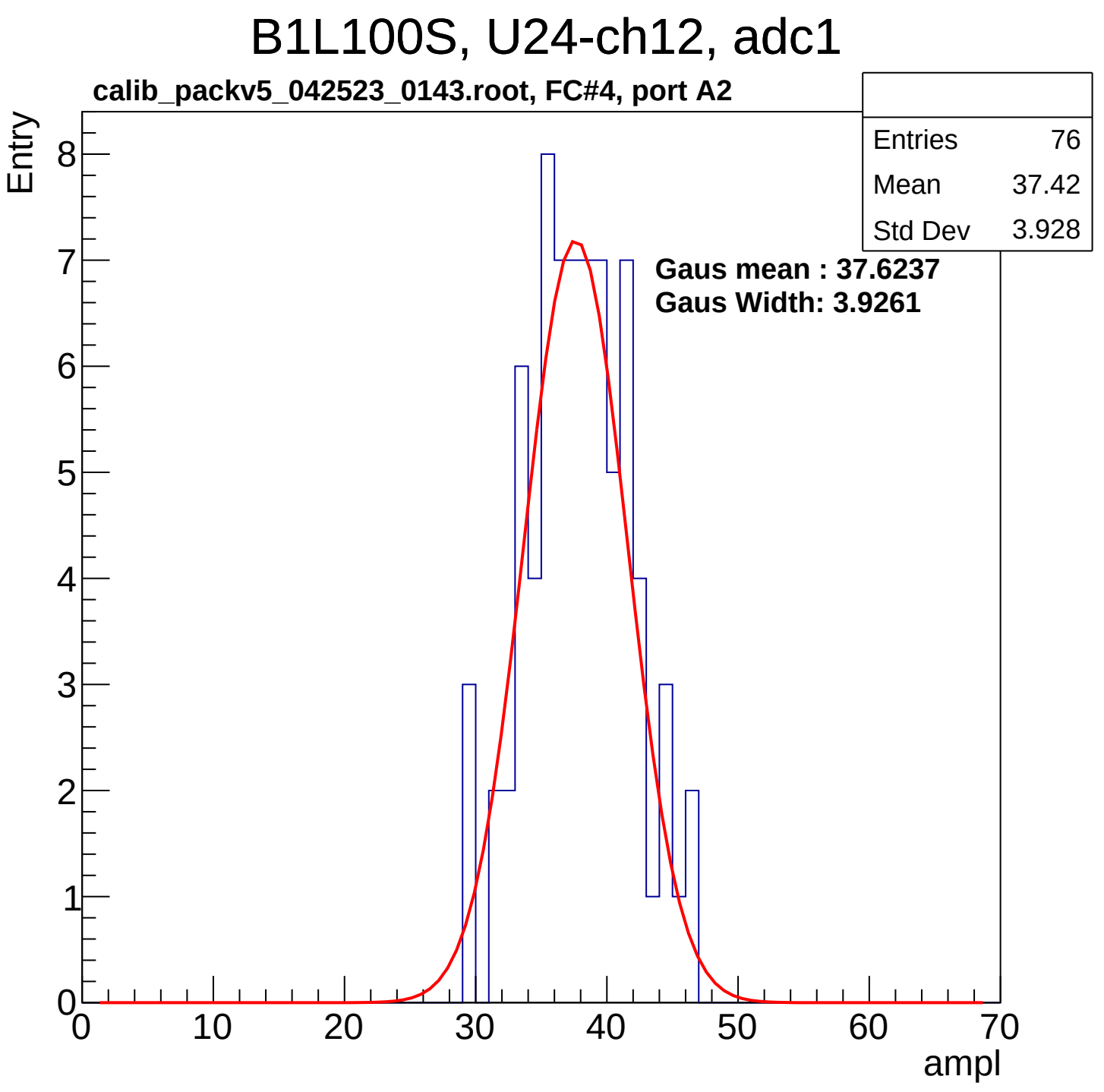
B1L100S, U24-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.42
Std Dev 3.928
Gaus mean : 37.6237
Gaus Width: 3.9261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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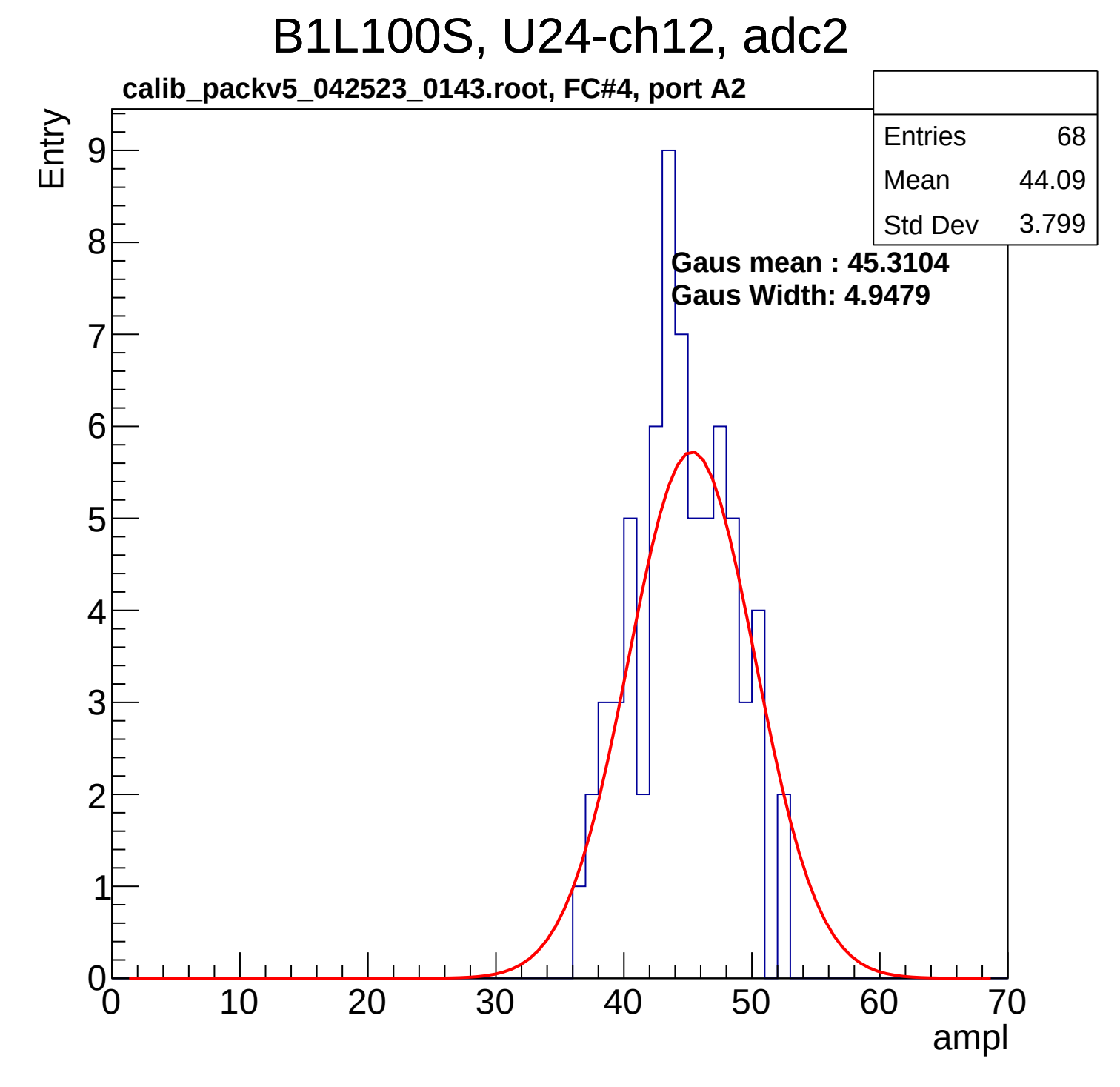
B1L100S, U24-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 44.09
Std Dev 3.799
Gaus mean : 45.3104
Gaus Width: 4.9479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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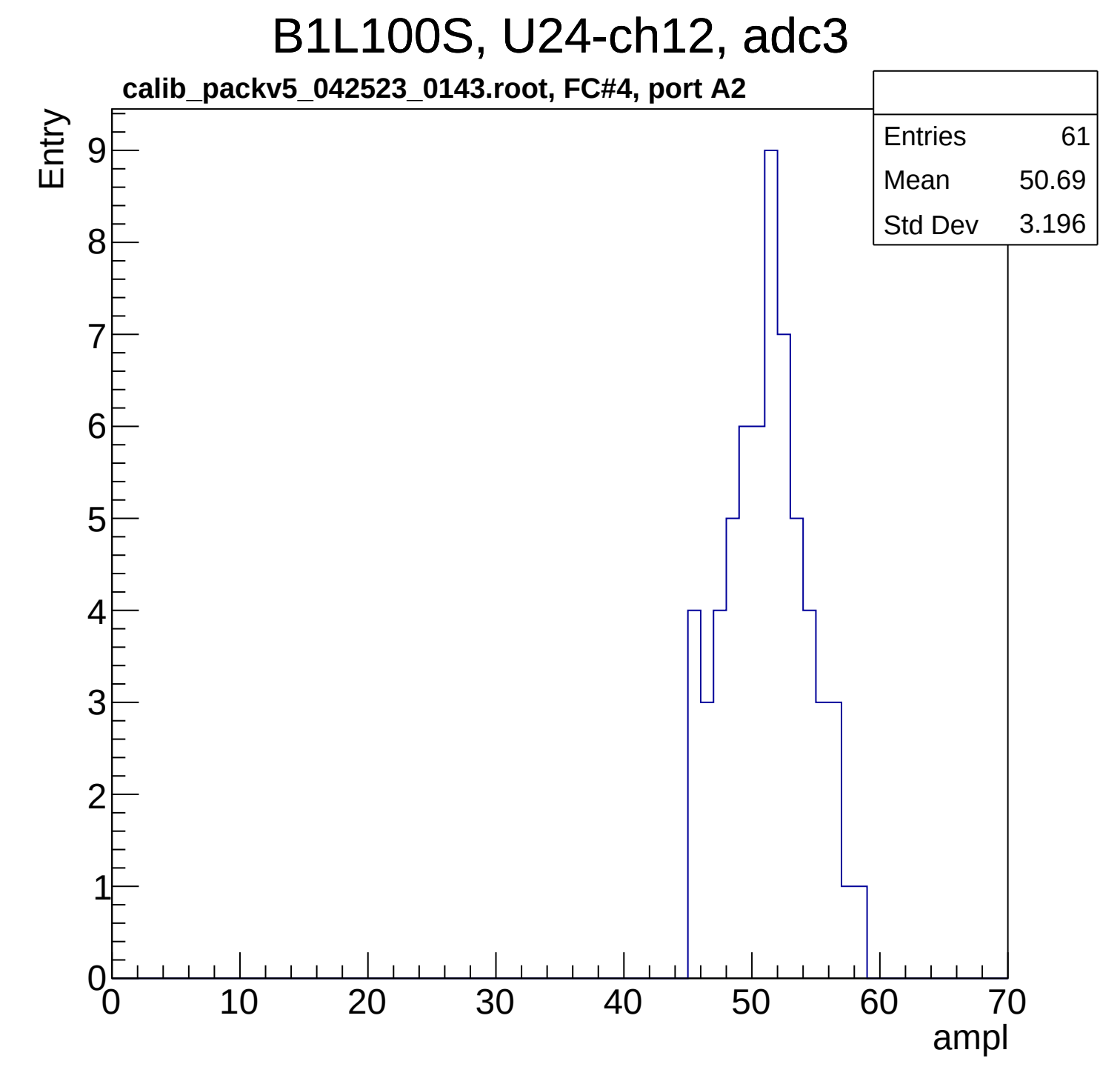
B1L100S, U24-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.69
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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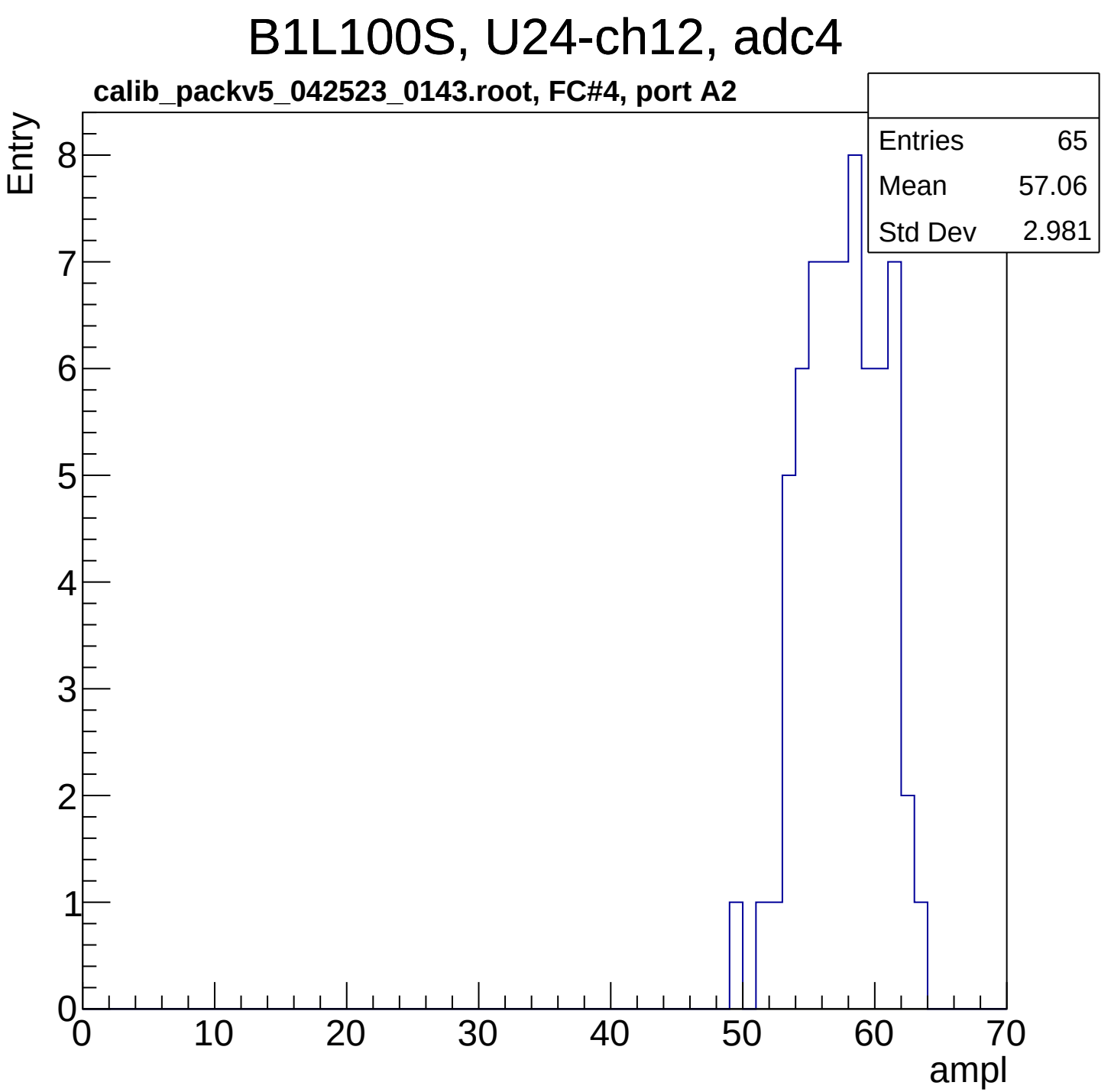
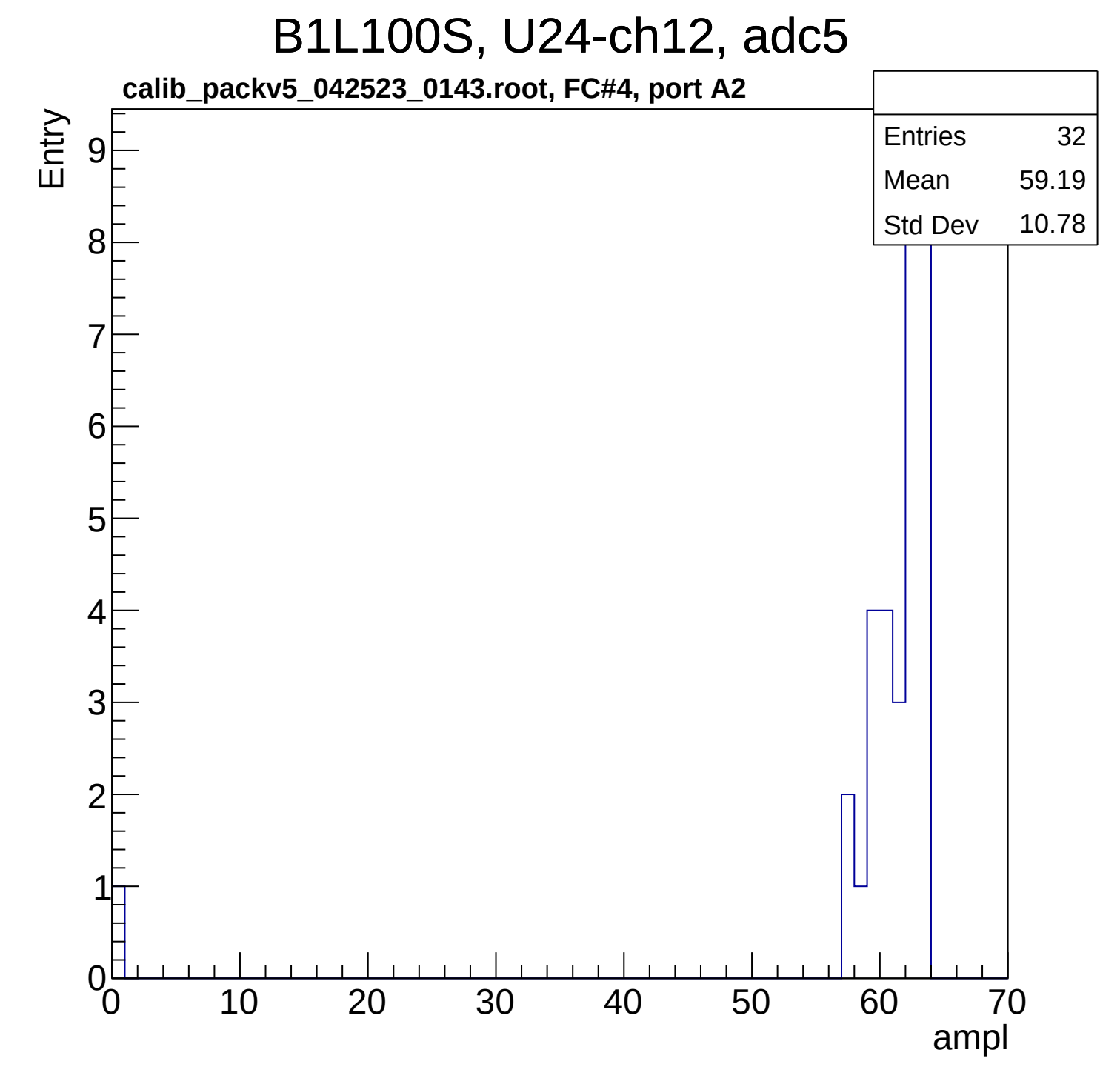

B1L100S, U24-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 57.06
Std Dev 2.981
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.19
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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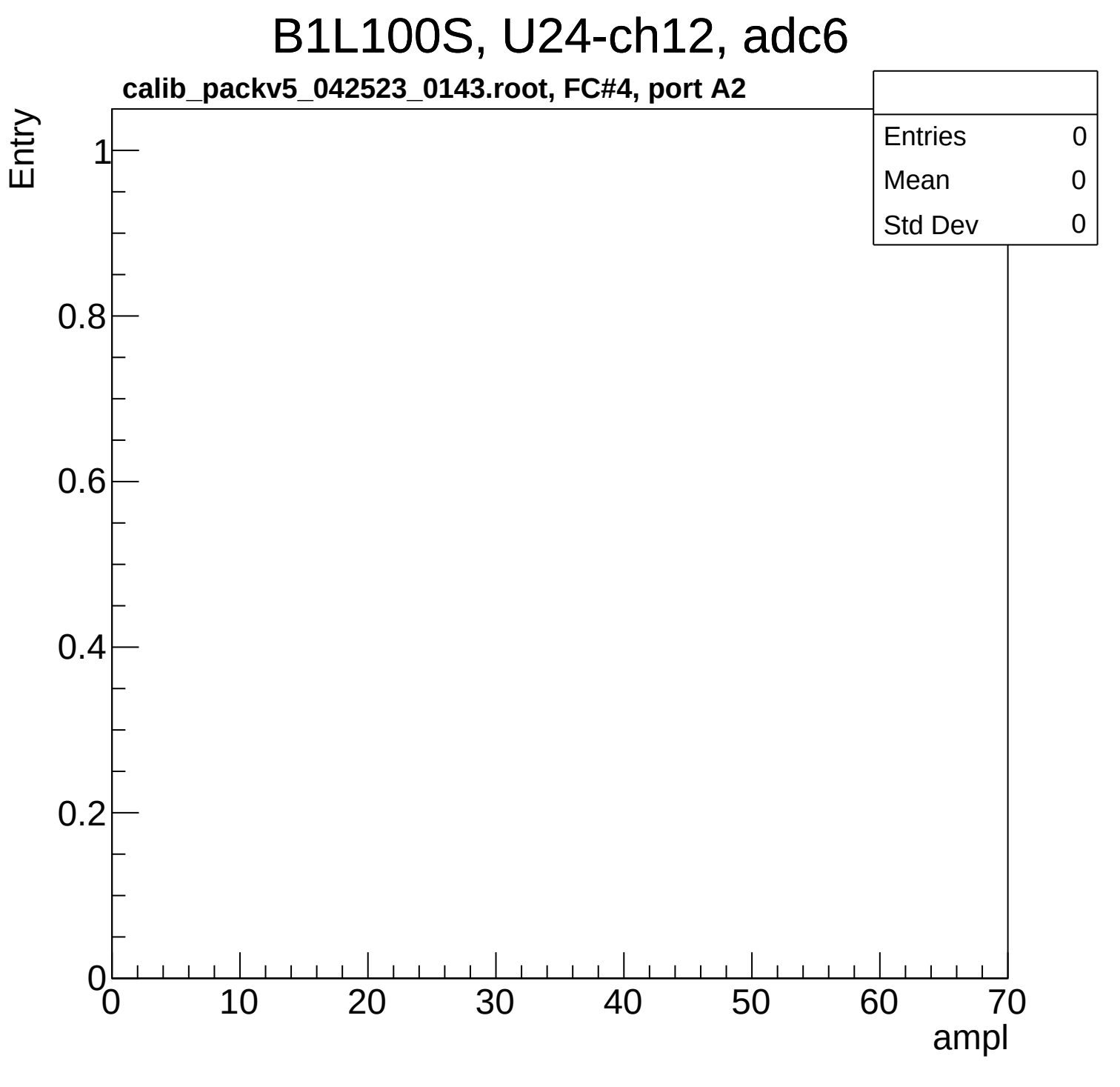
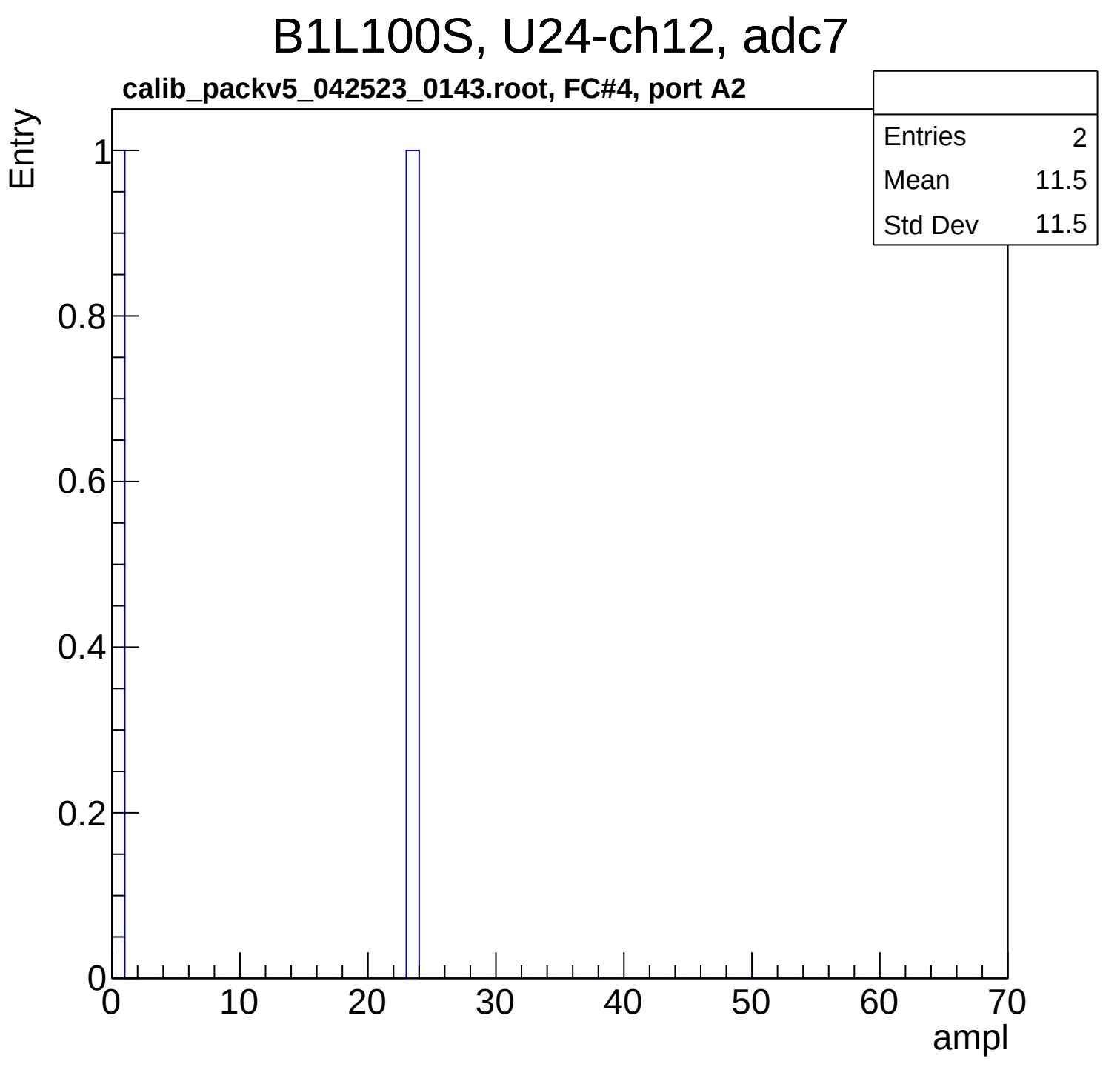
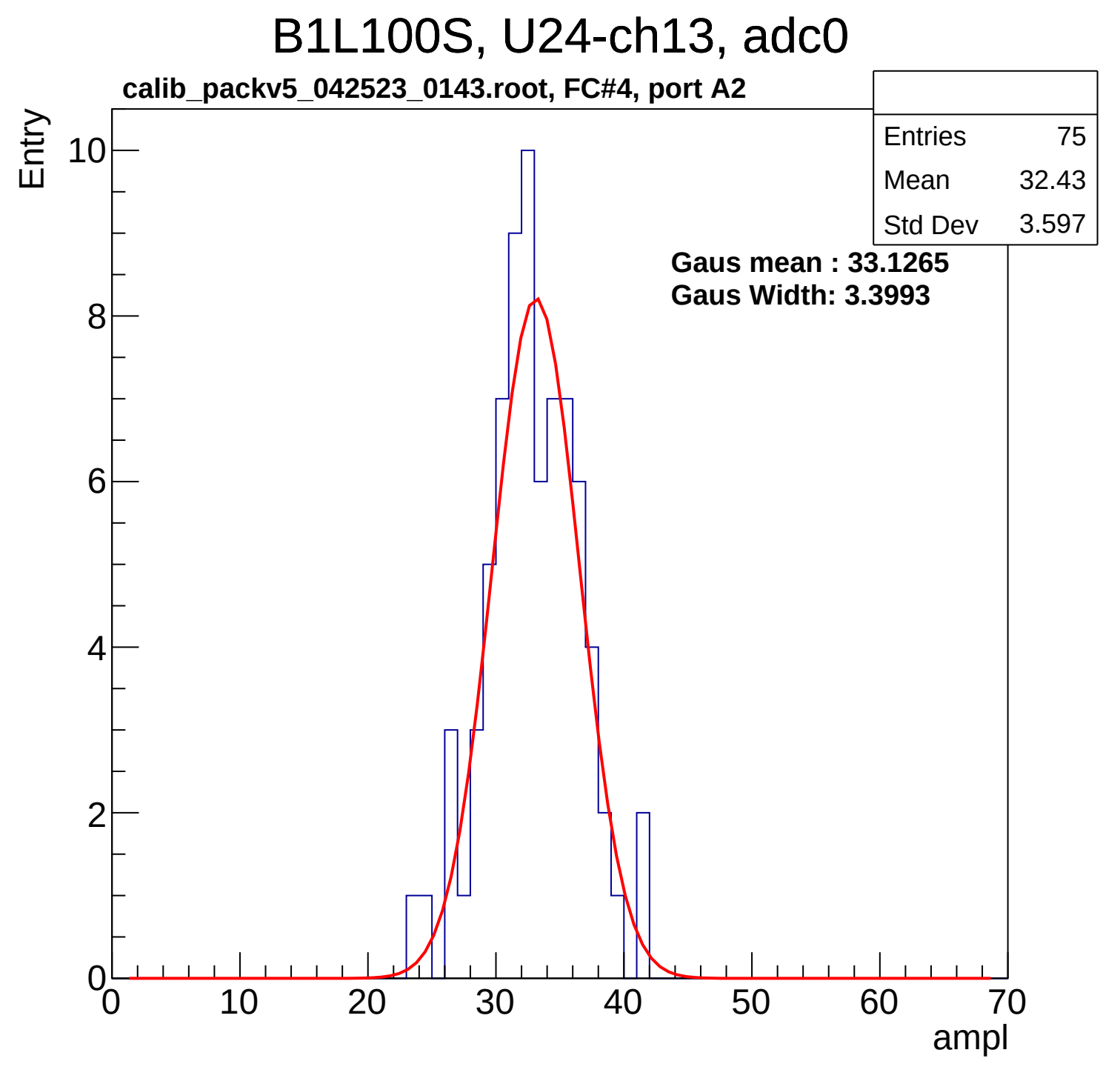
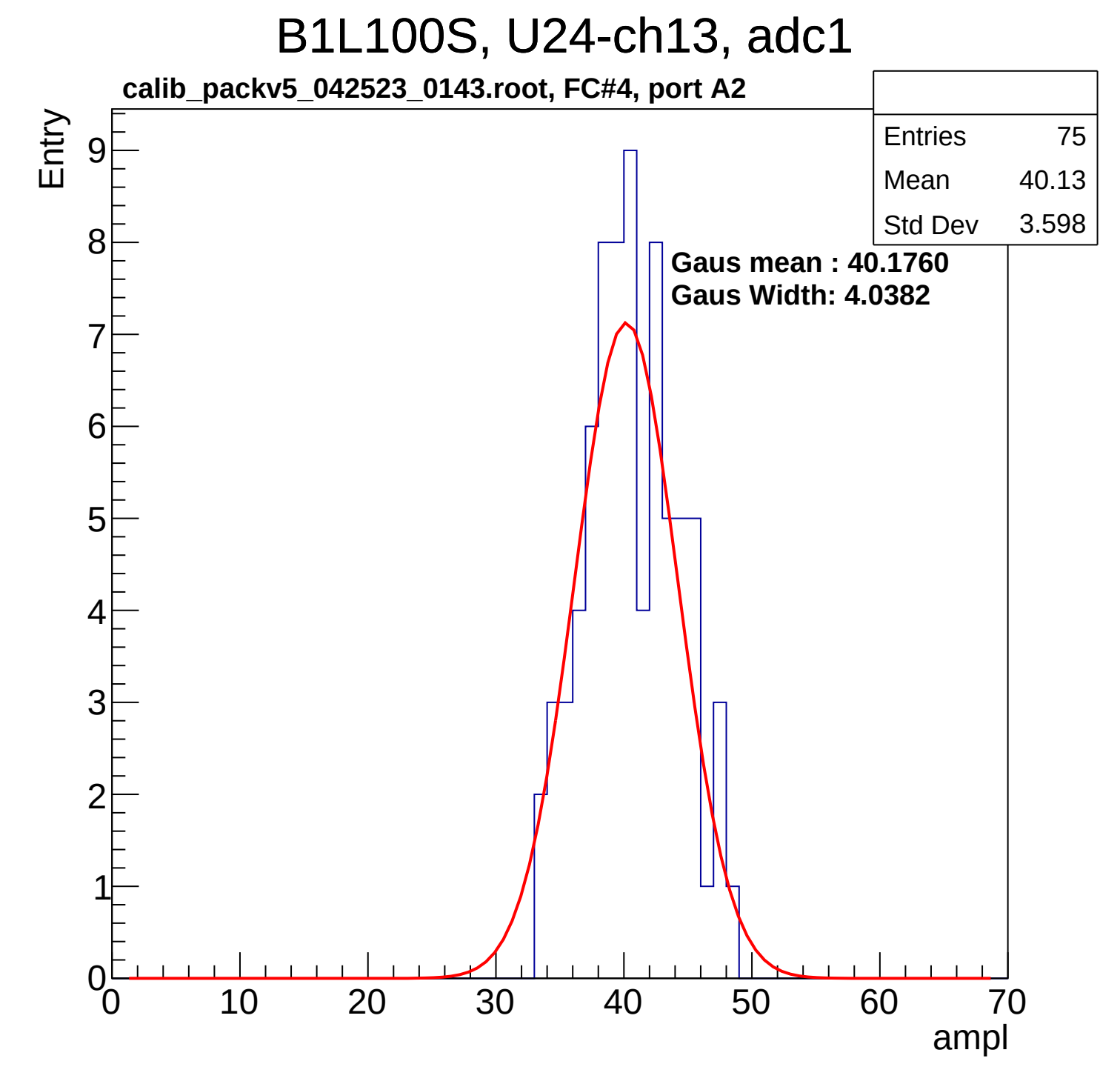
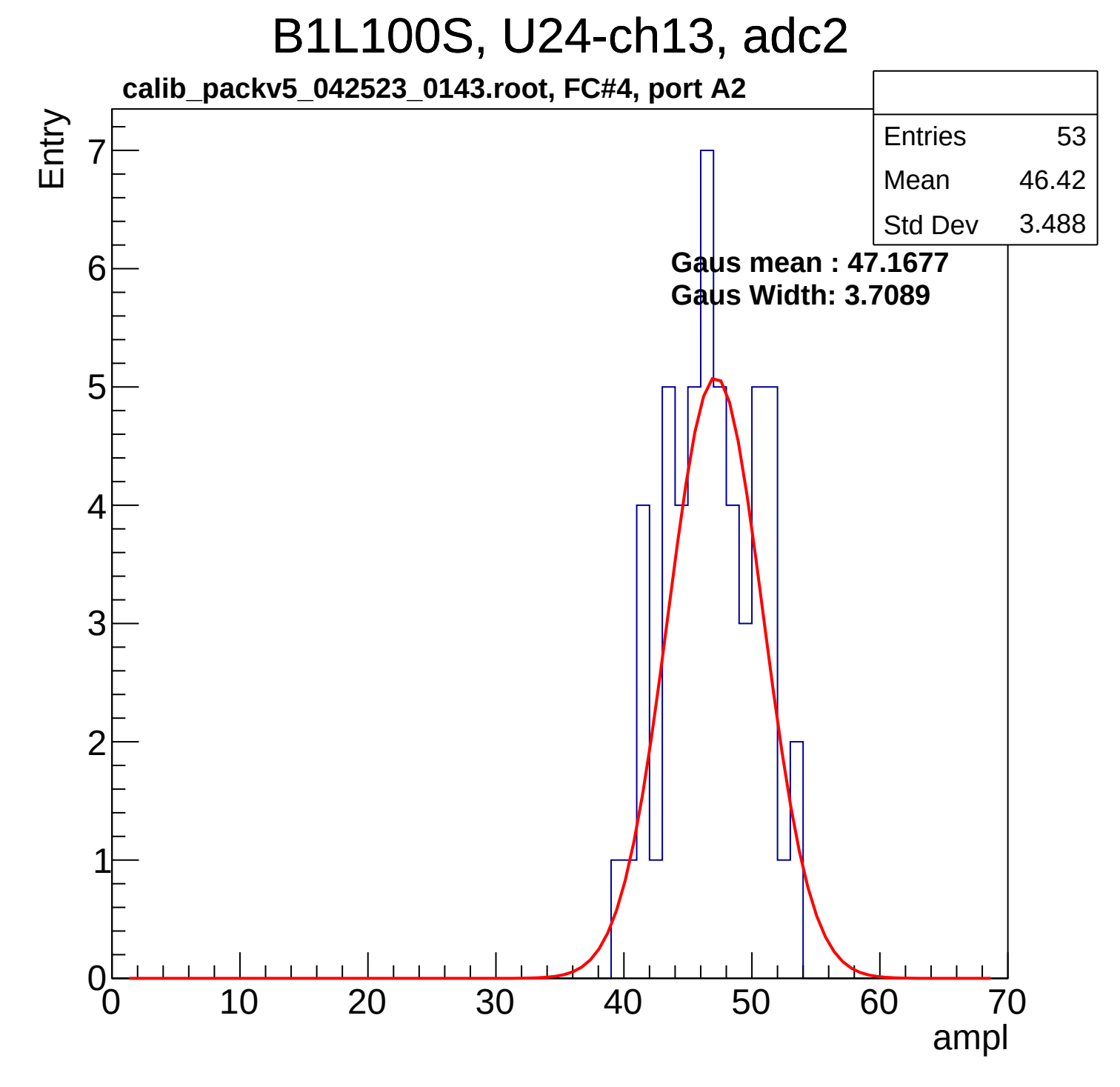

B1L100S, U24-ch12, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U24-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 32.43
Std Dev 3.597
Gaus mean : 33.1265
Gaus Width: 3.3993
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 40.13
Std Dev 3.598
Gaus mean : 40.1760
Gaus Width: 4.0382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 46.42
Std Dev 3.488
Gaus mean : 47.1677
Gaus Width: 3.7089
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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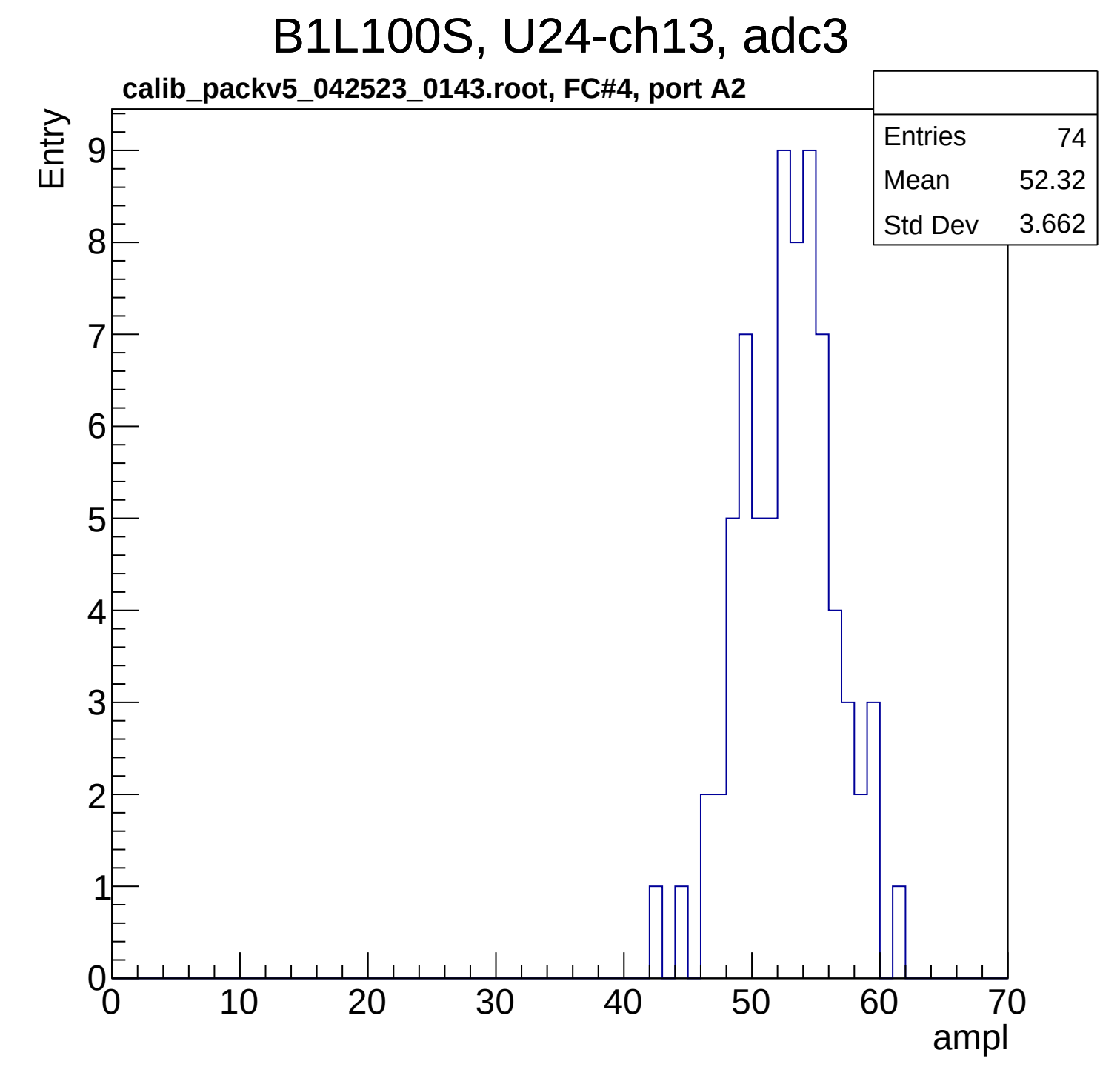
B1L100S, U24-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 52.32
Std Dev 3.662
Entry
ampl
0
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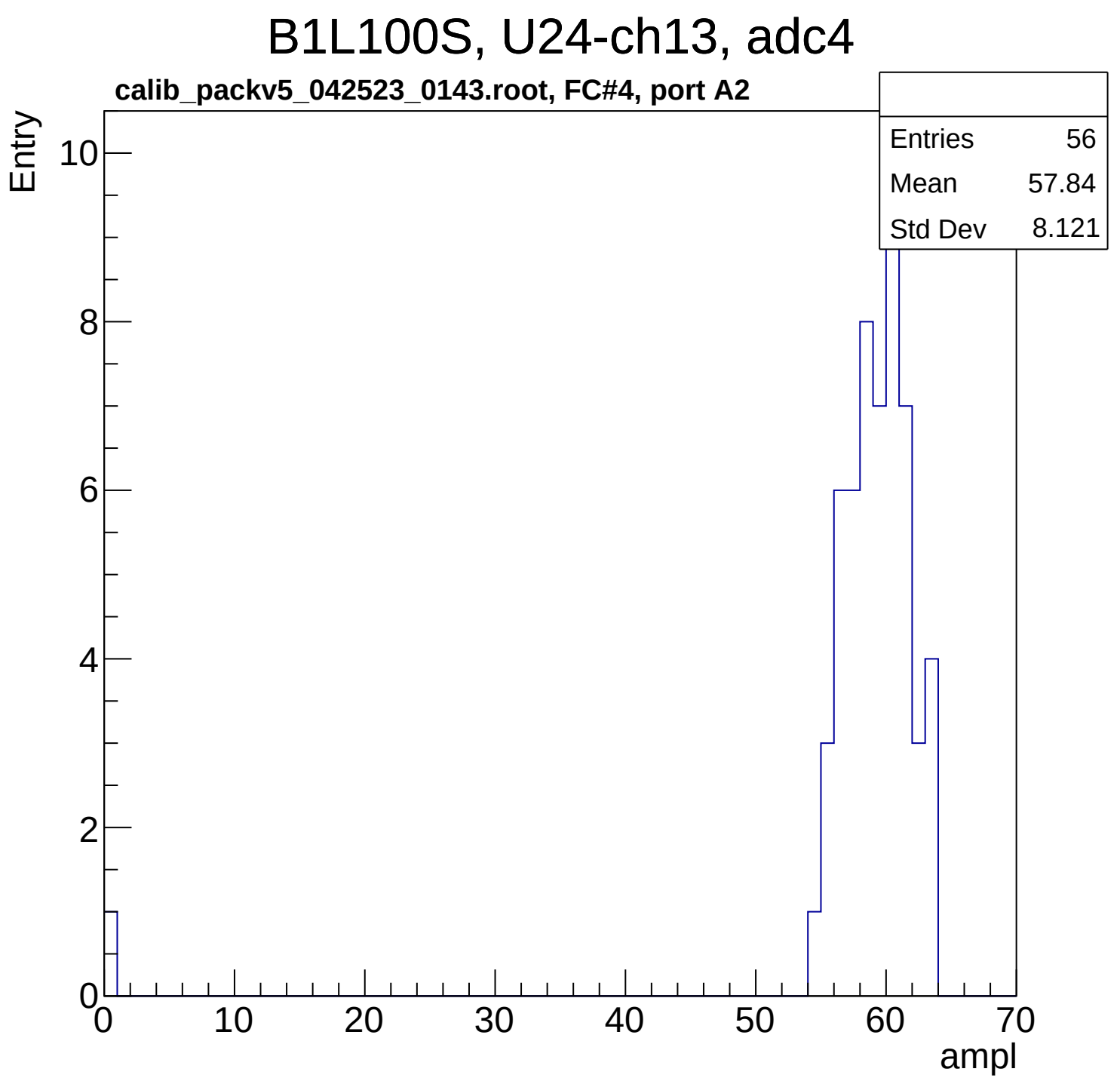
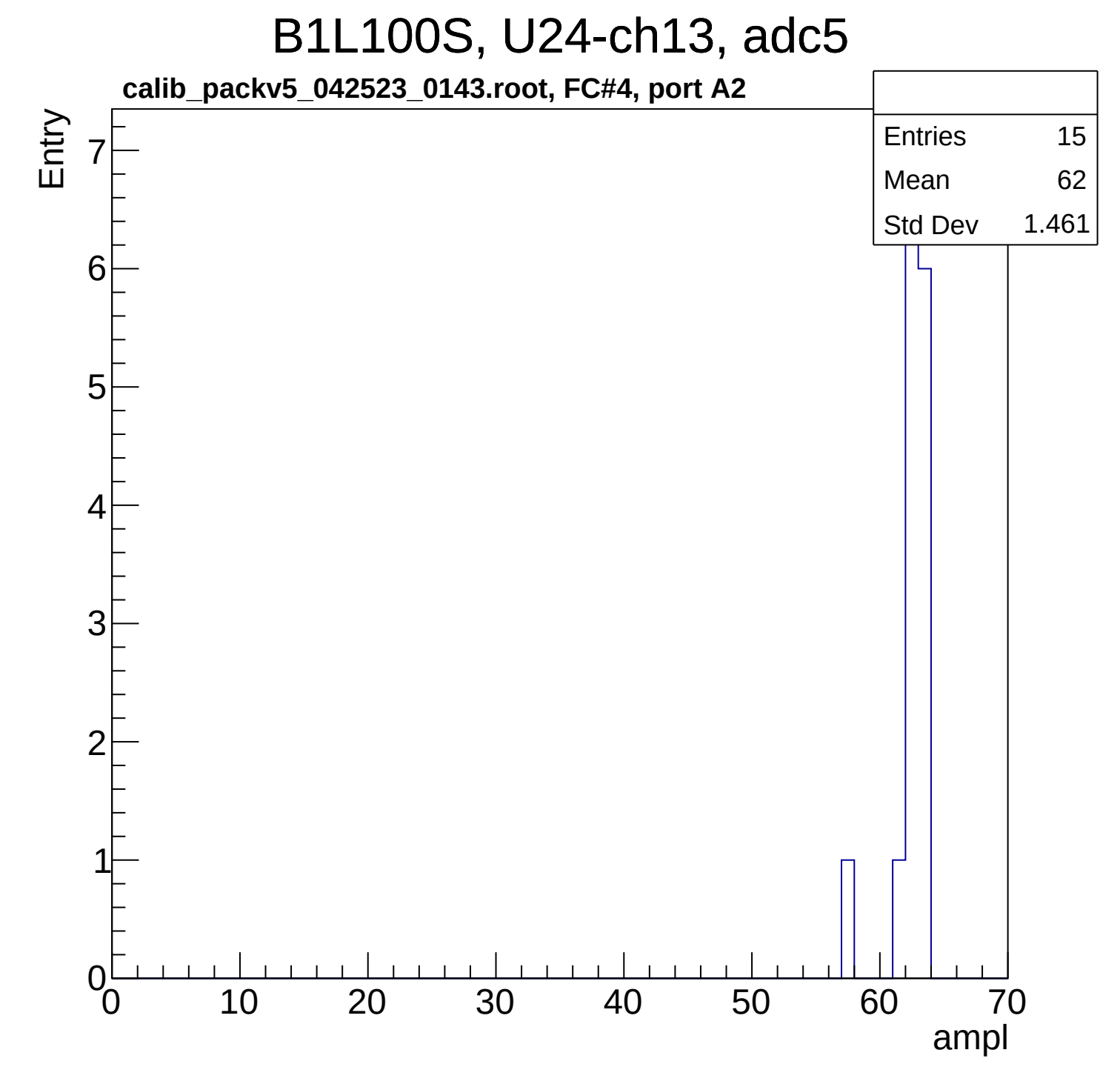
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.84
Std Dev 8.121
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 15
Mean 62
Std Dev 1.461
Entry
ampl
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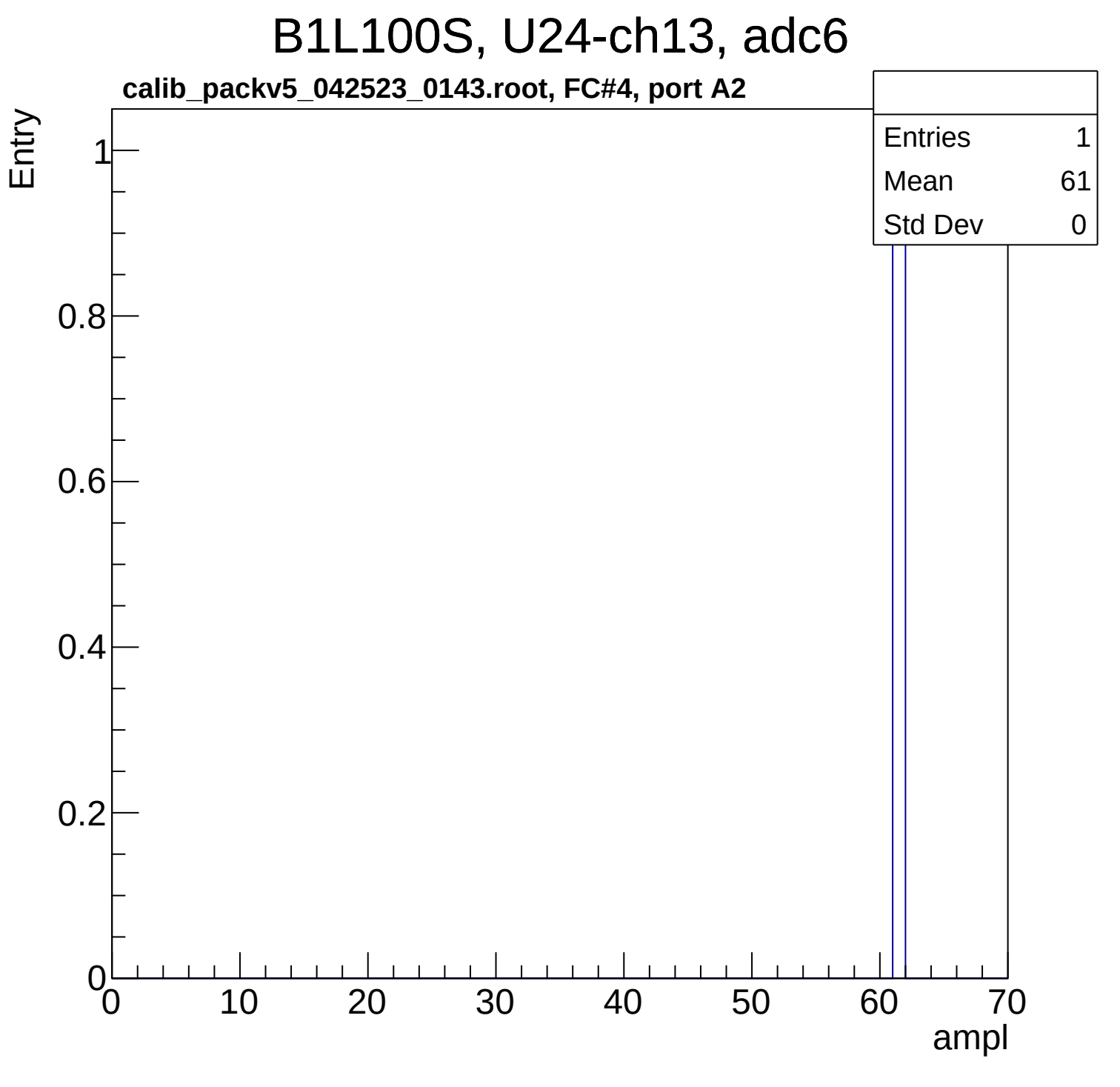
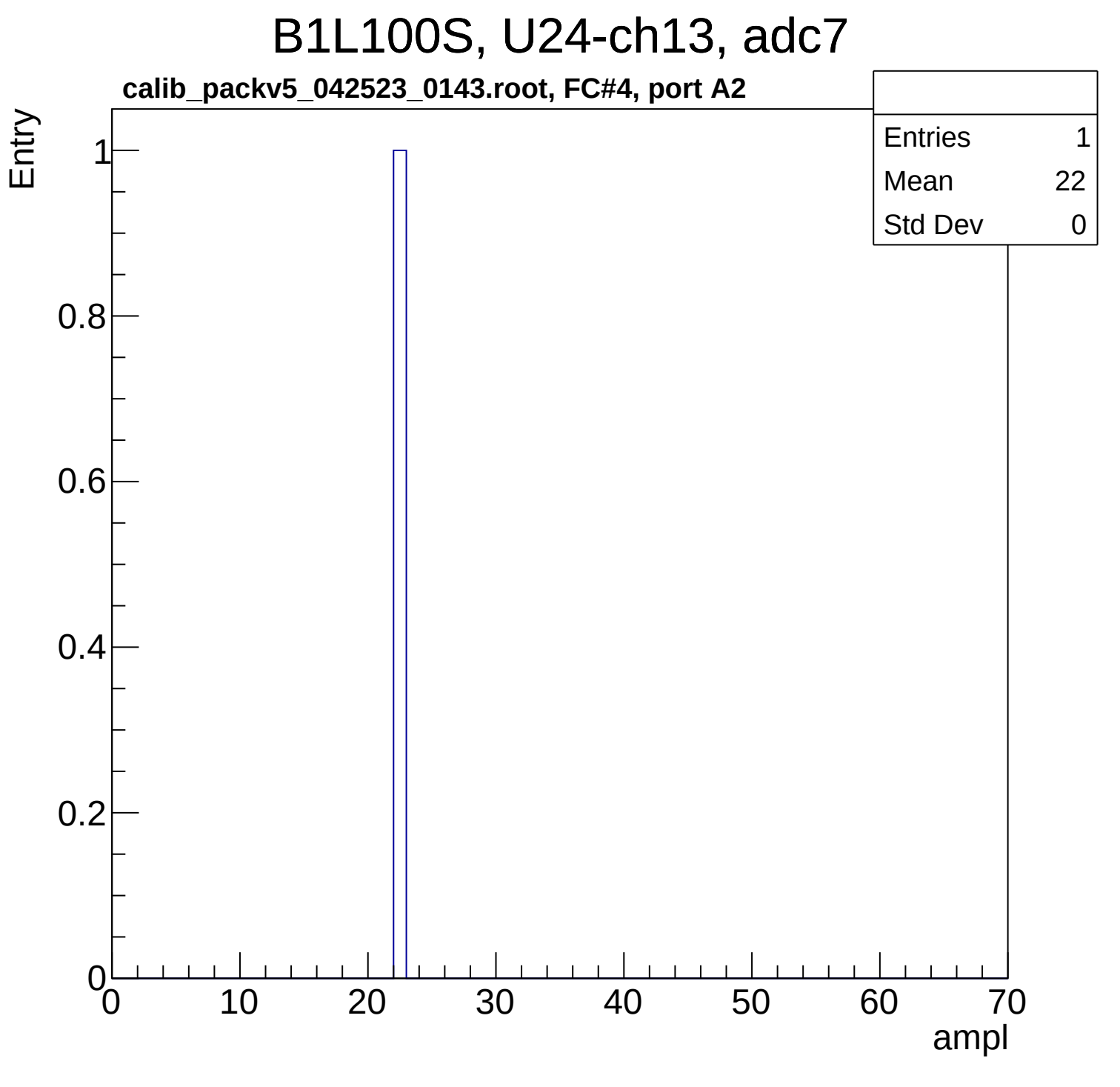
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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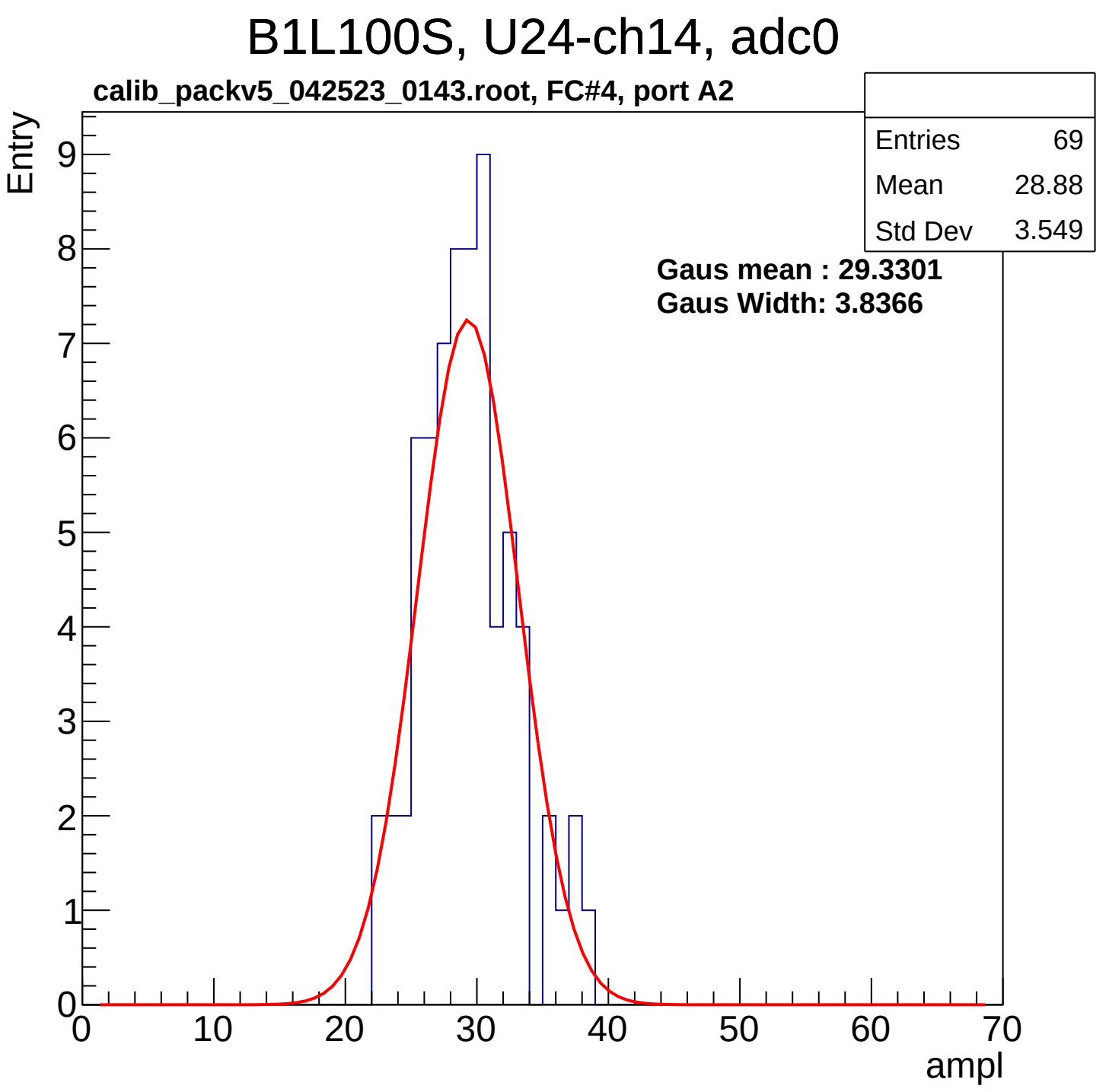
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.88
Std Dev 3.549
Gaus mean : 29.3301
Gaus Width: 3.8366
Entry
ampl
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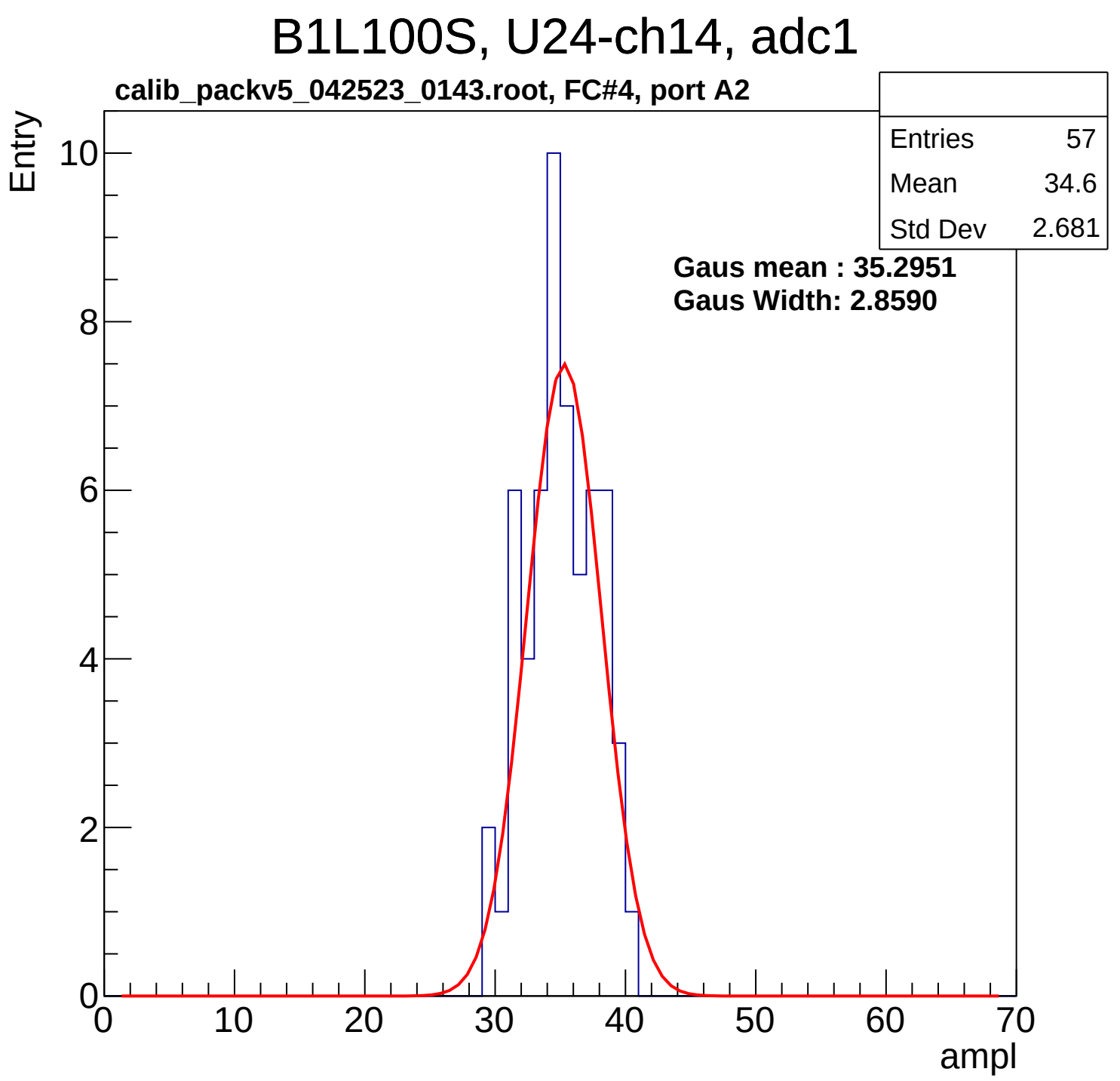
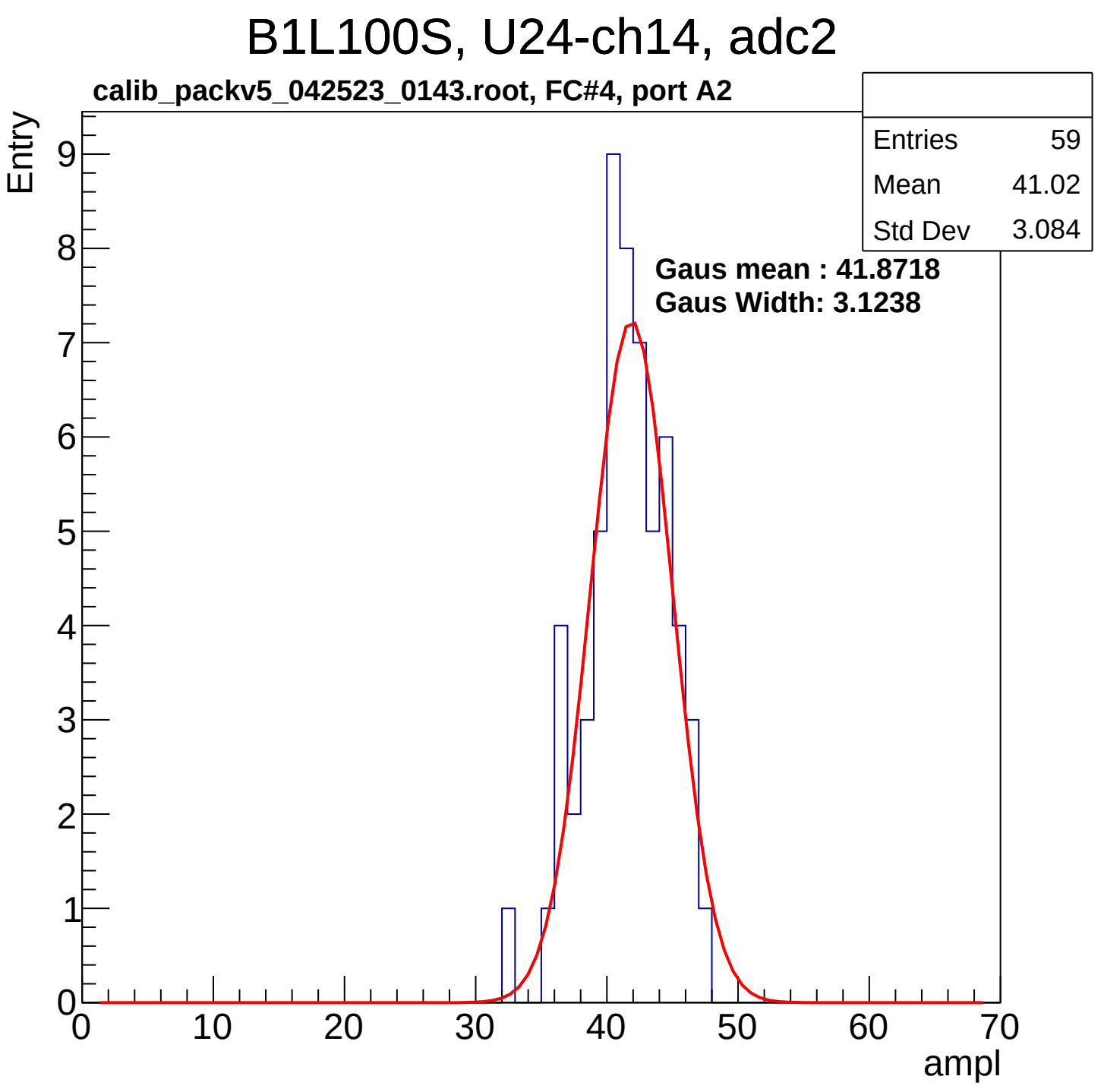
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 34.6
Std Dev 2.681
Gaus mean : 35.2951
Gaus Width: 2.8590
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 41.02
Std Dev 3.084
Gaus mean : 41.8718
Gaus Width: 3.1238
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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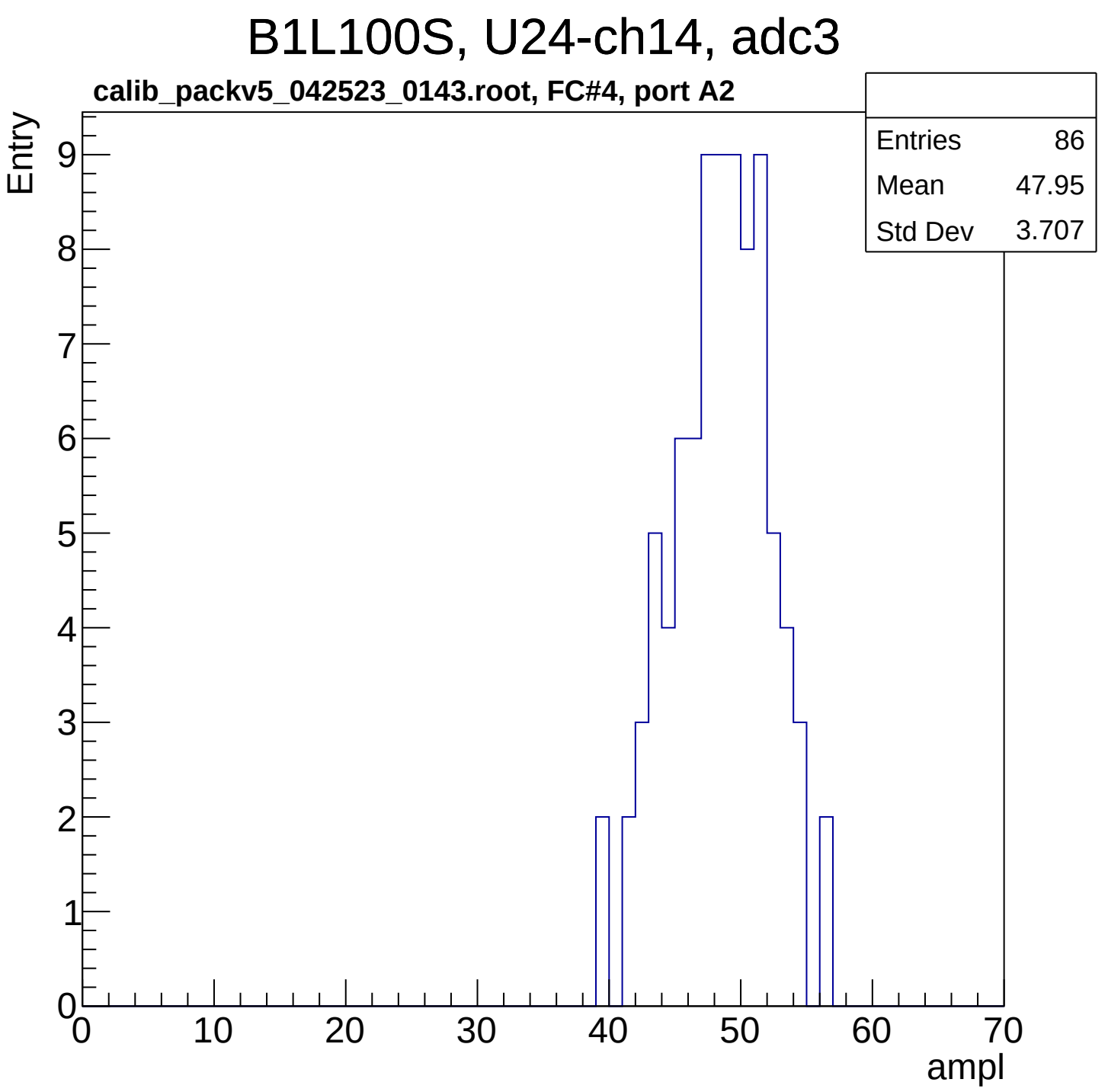
60
70


B1L100S, U24-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 47.95
Std Dev 3.707
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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40
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60
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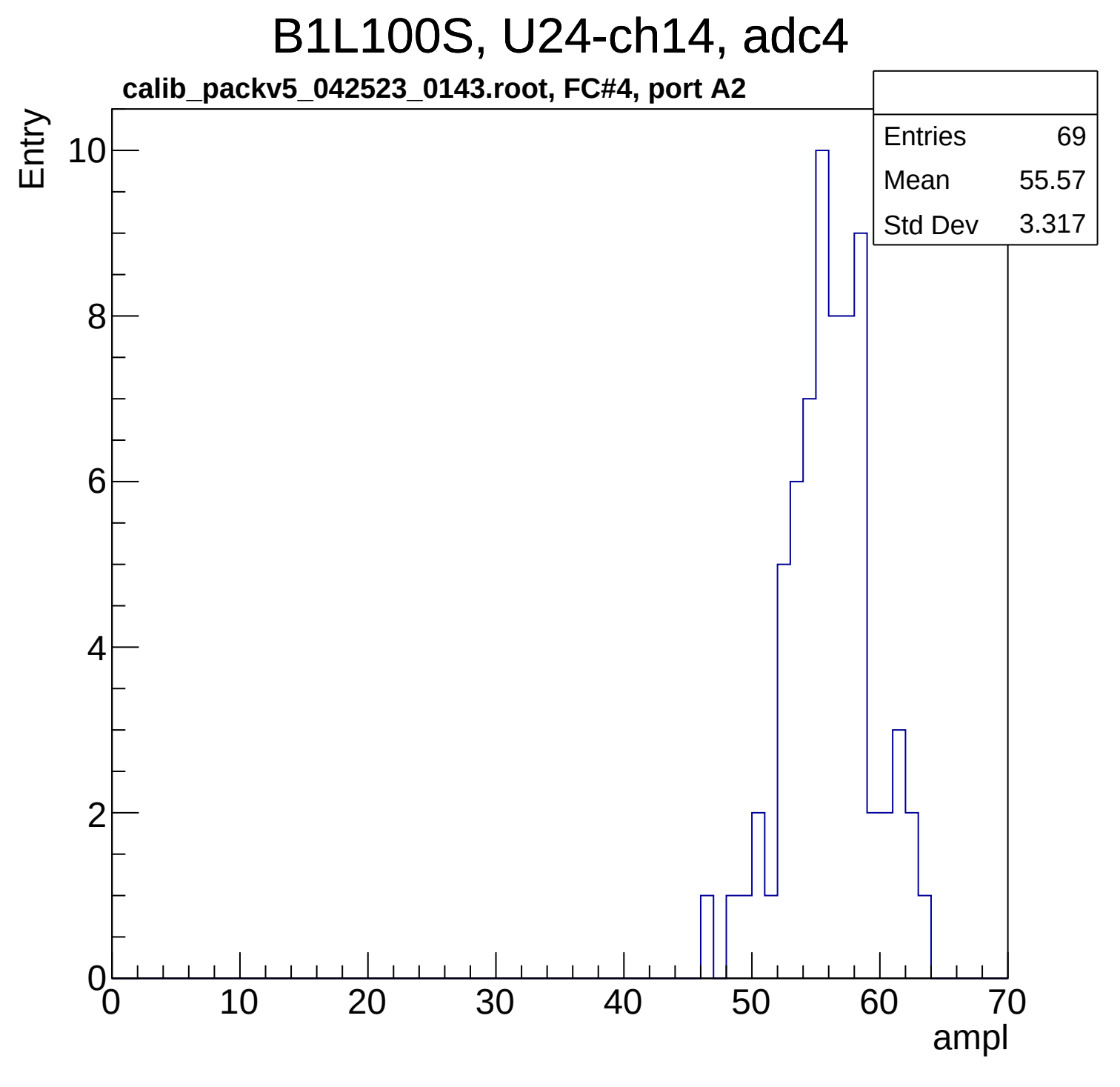
B1L100S, U24-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.57
Std Dev 3.317
Entry
ampl
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0
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60
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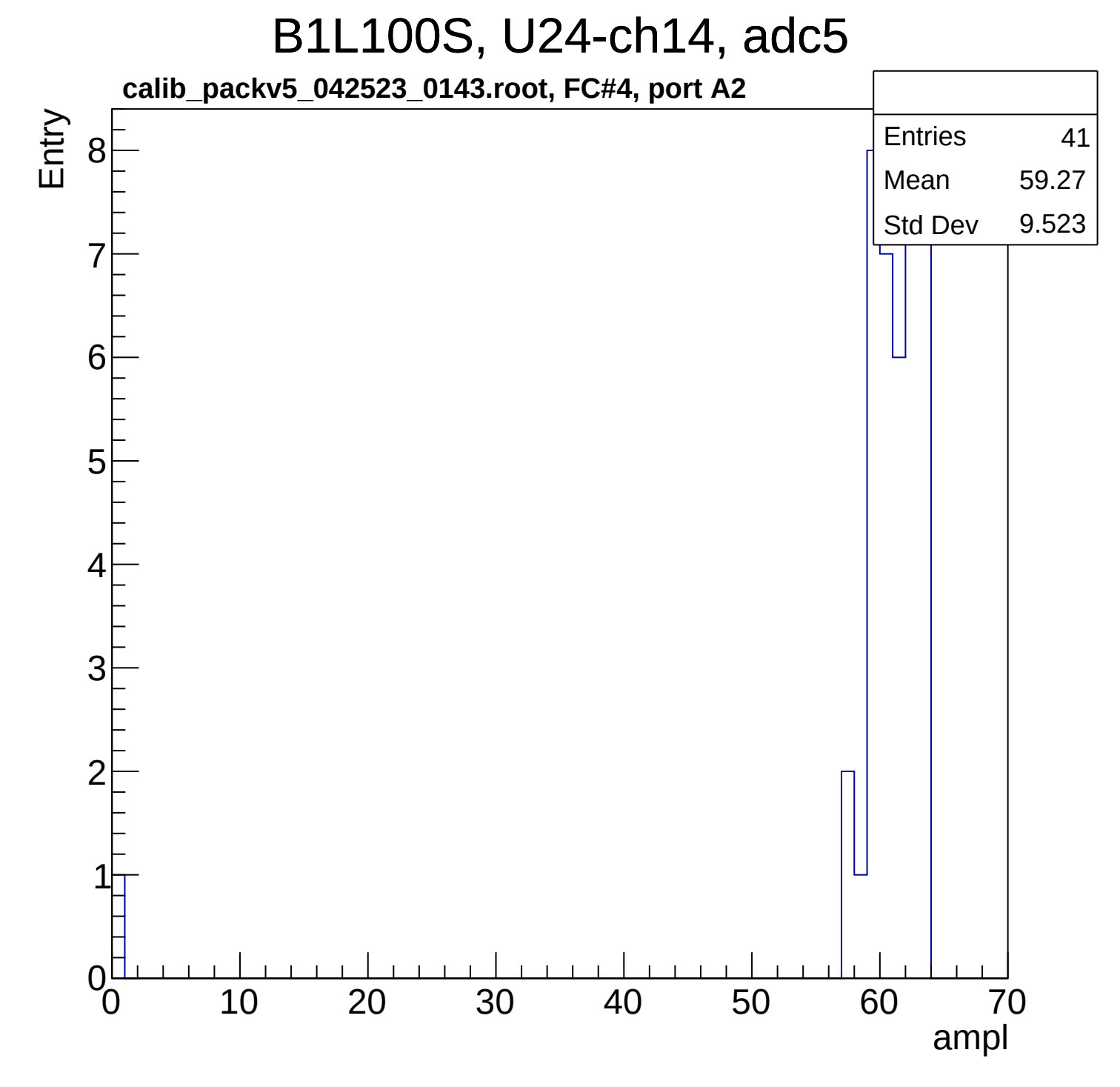
B1L100S, U24-ch14, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.27
Std Dev 9.523
Entry
ampl
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6
7
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0
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60
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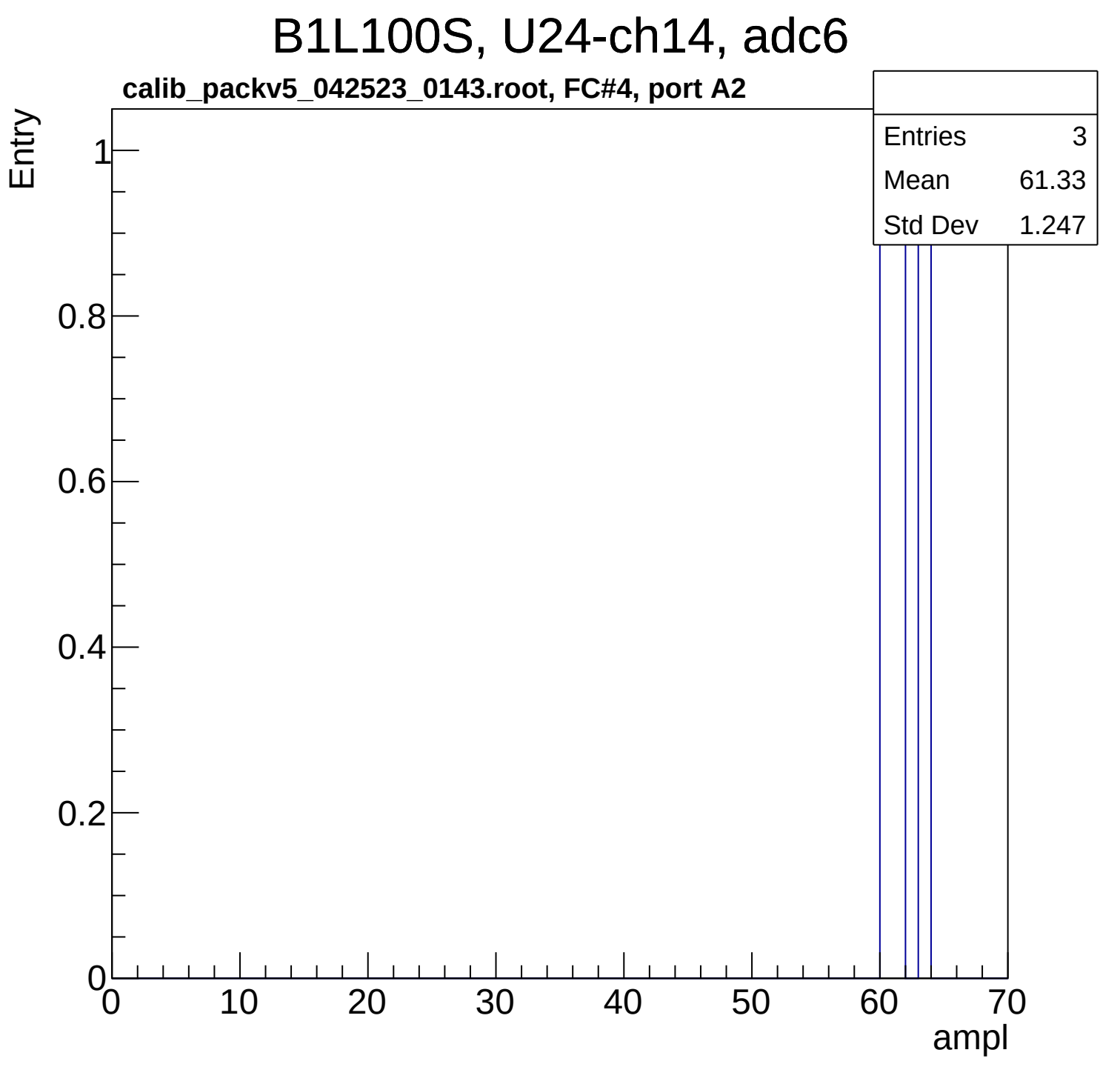
B1L100S, U24-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.247
Entry
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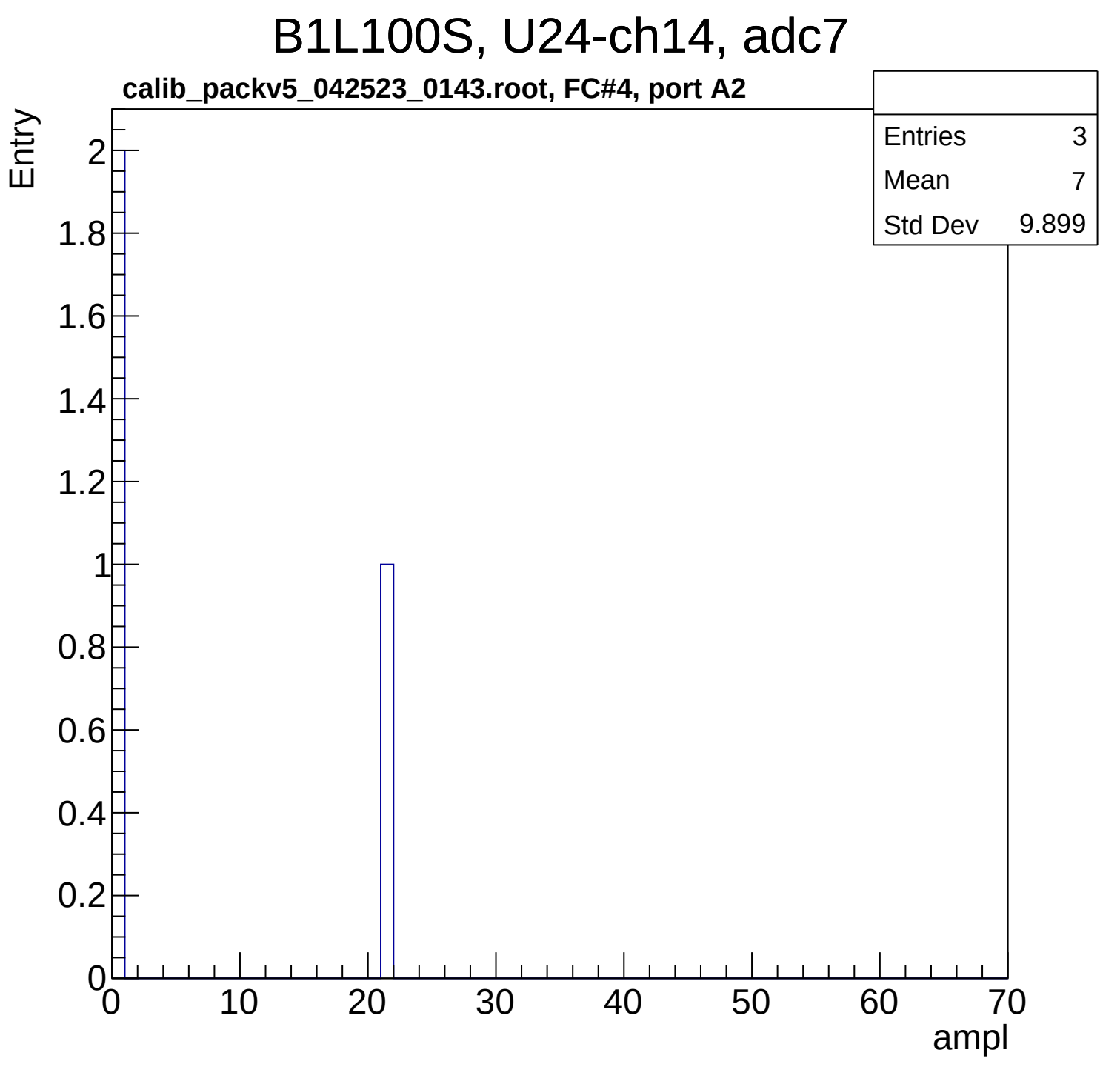
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch14, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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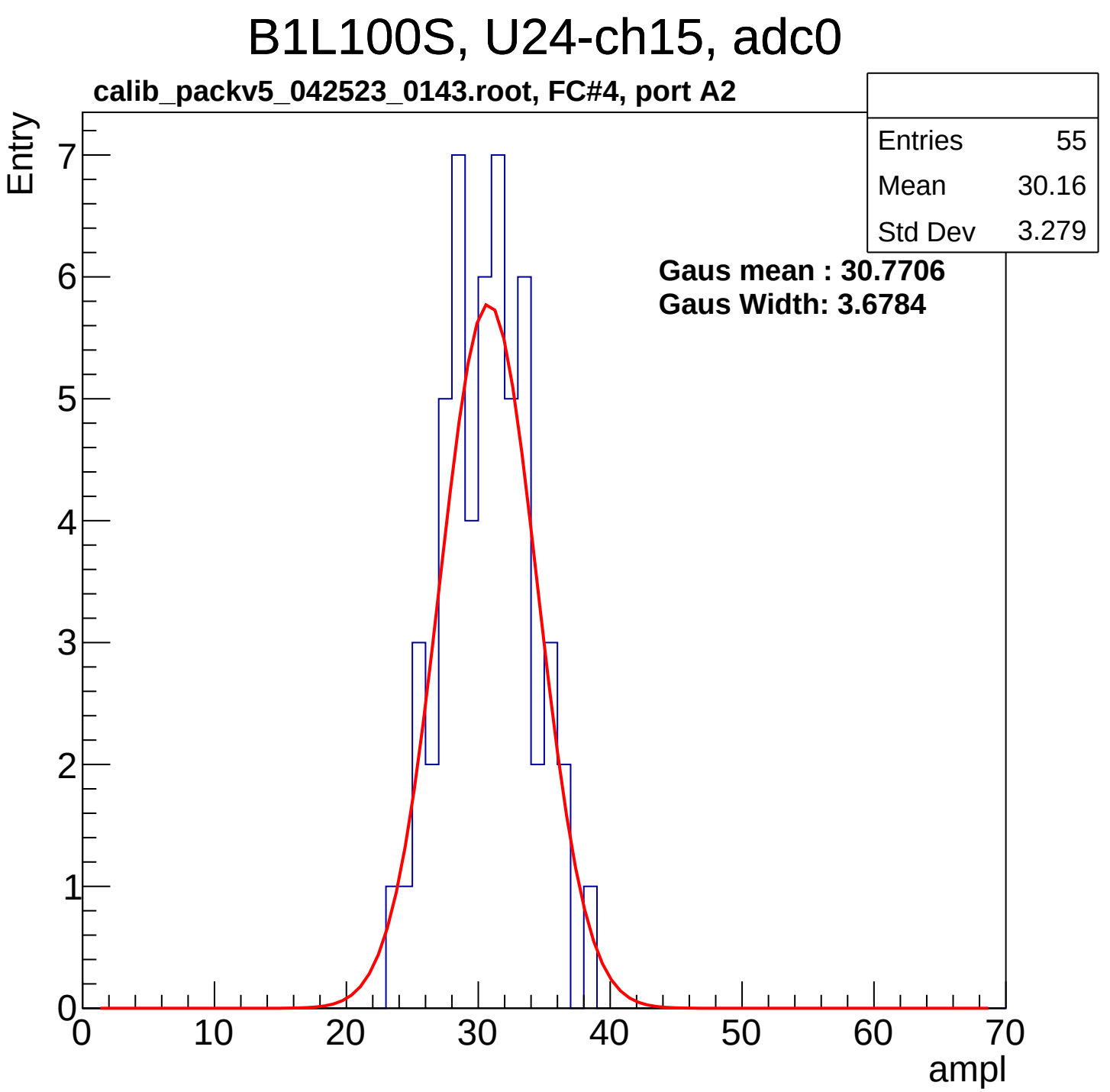
B1L100S, U24-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 30.16
Std Dev 3.279
Gaus mean : 30.7706
Gaus Width: 3.6784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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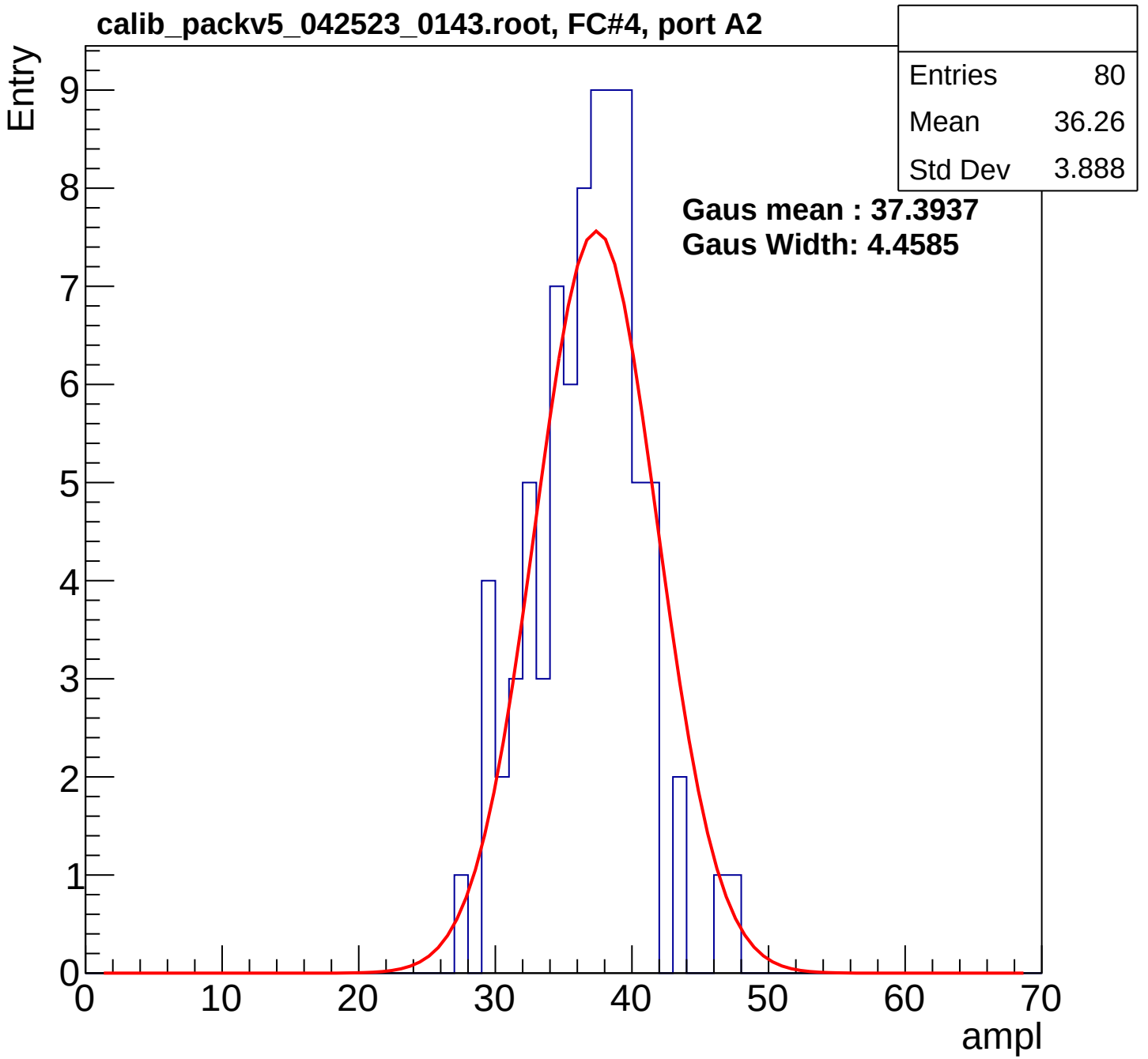
B1L100S, U24-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.26
Std Dev 3.888
Gaus mean : 37.3937
Gaus Width: 4.4585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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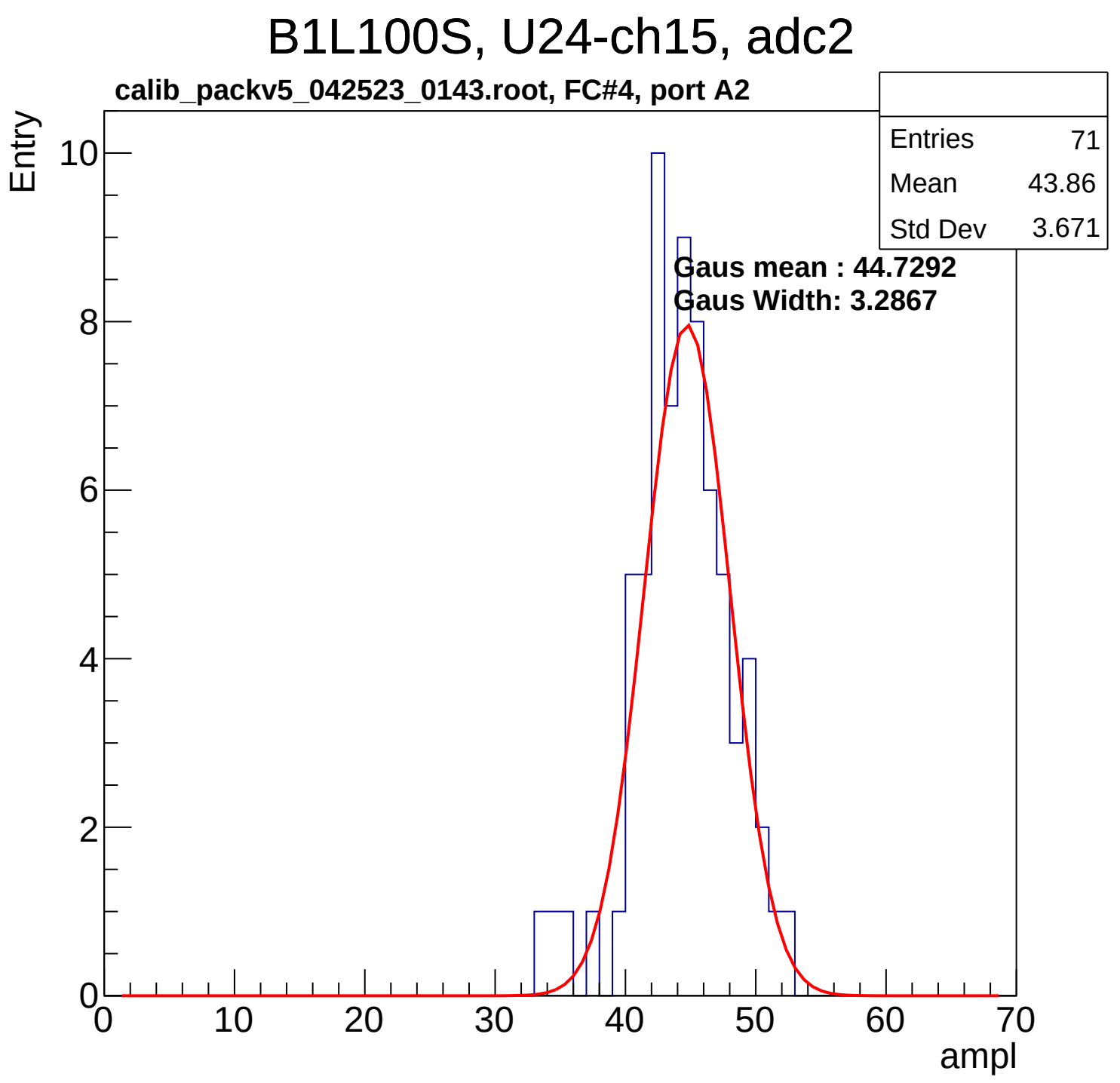
B1L100S, U24-ch15, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 43.86
Std Dev 3.671
Gaus mean : 44.7292
Gaus Width: 3.2867
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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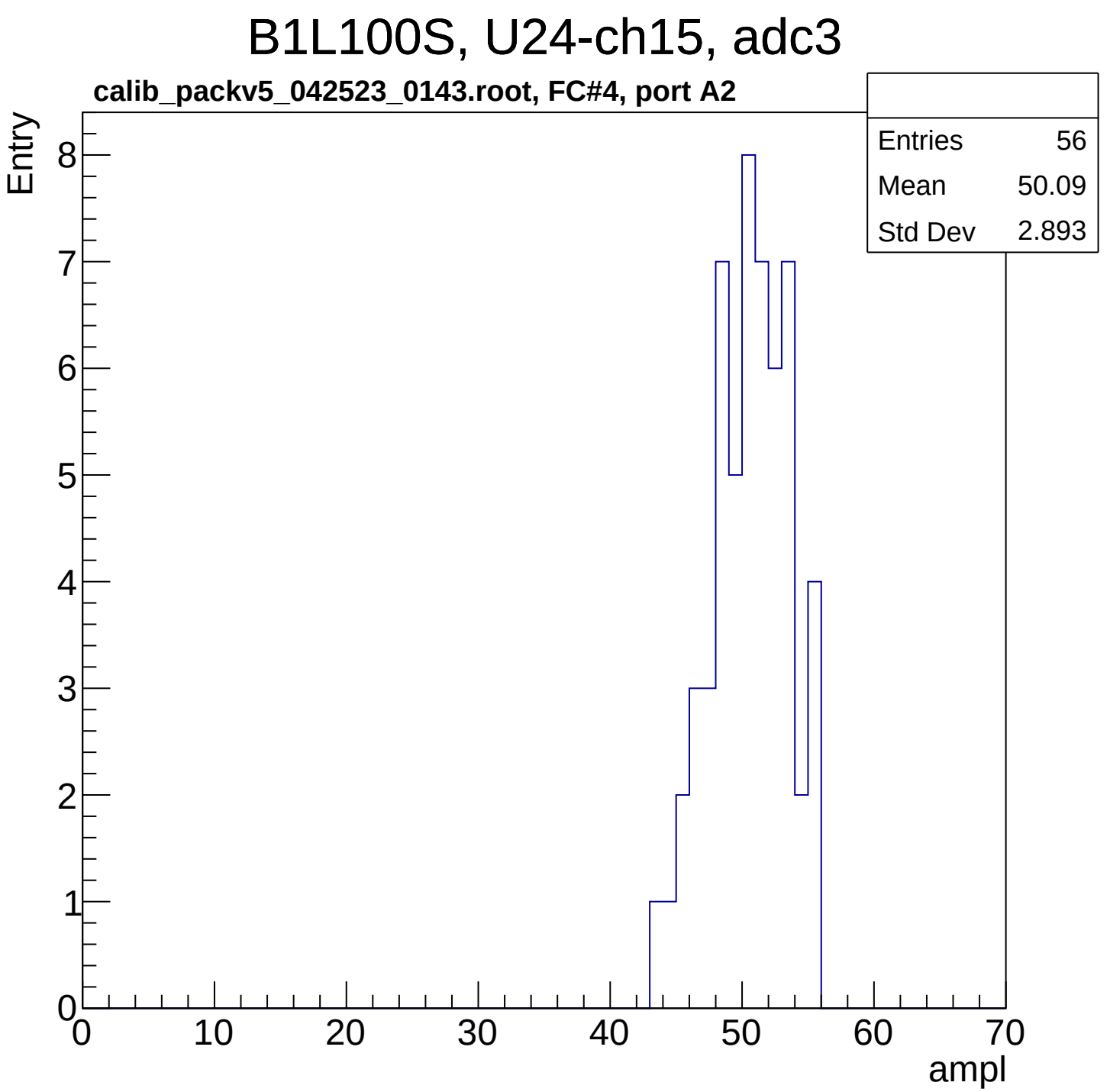
B1L100S, U24-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.09
Std Dev 2.893
Entry
ampl
0
1
2
3
4
5
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60
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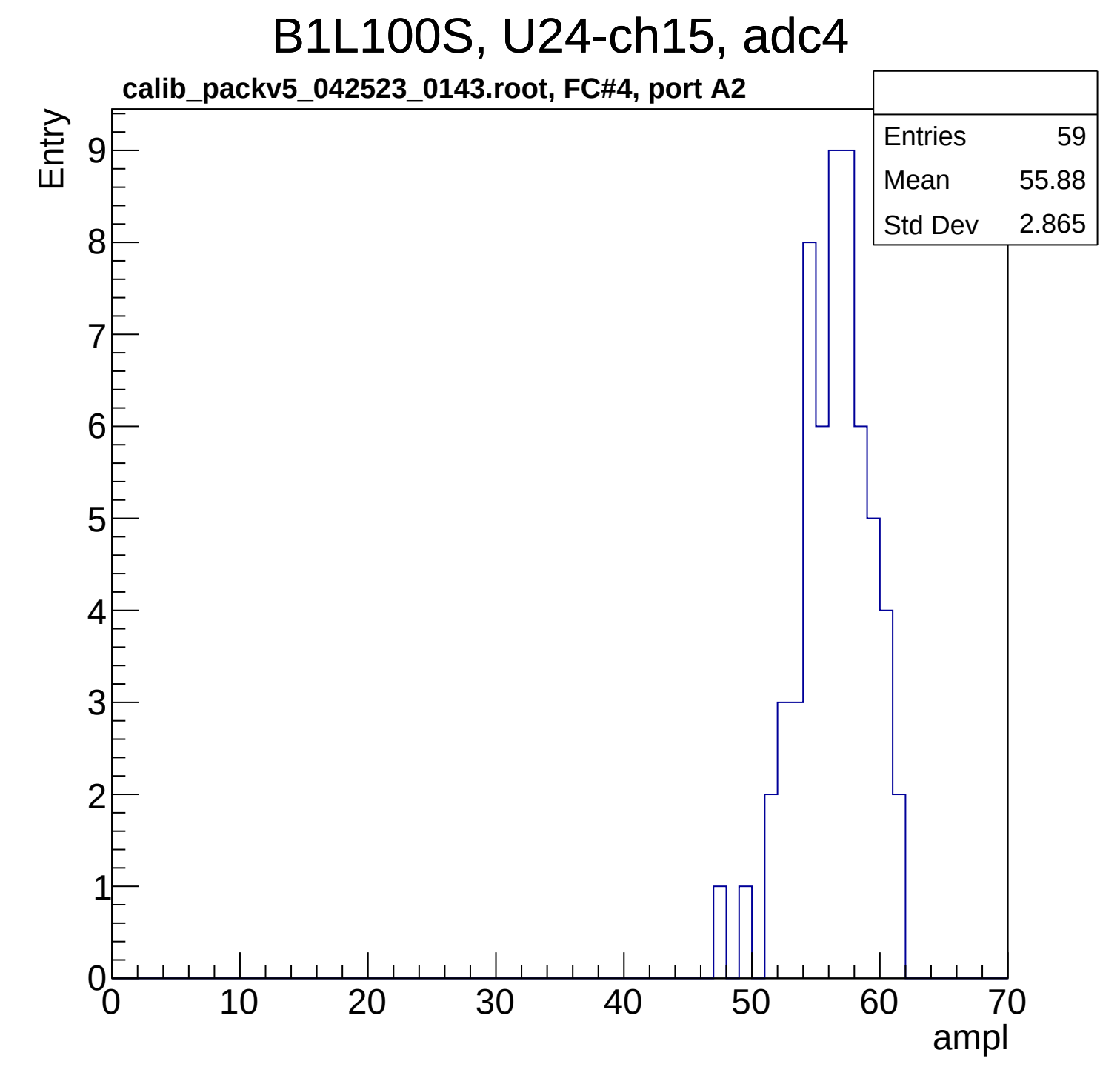
B1L100S, U24-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.88
Std Dev 2.865
Entry
ampl
0
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2
3
4
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6
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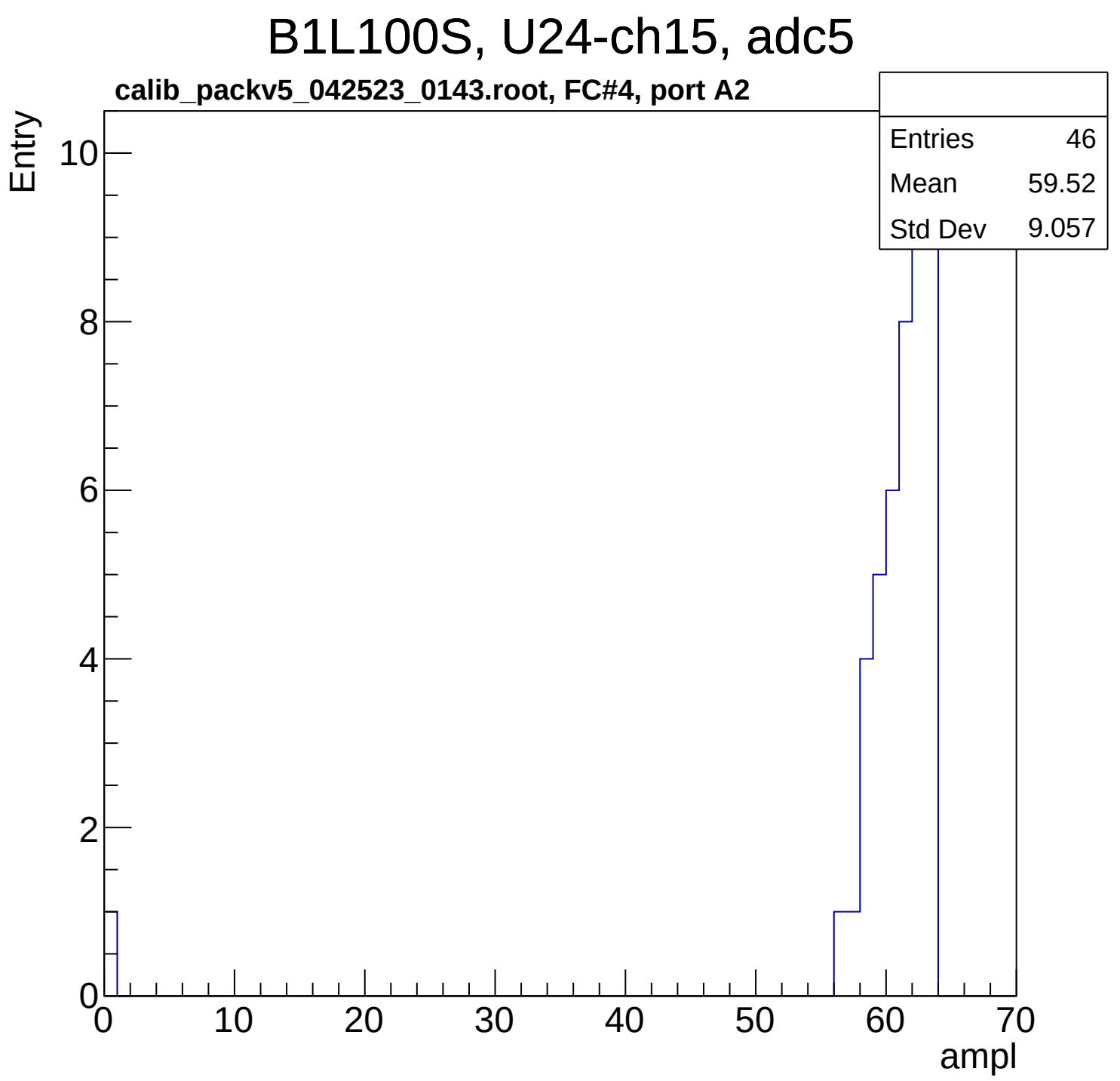
B1L100S, U24-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 59.52
Std Dev 9.057
Entry
0
2
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6
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50
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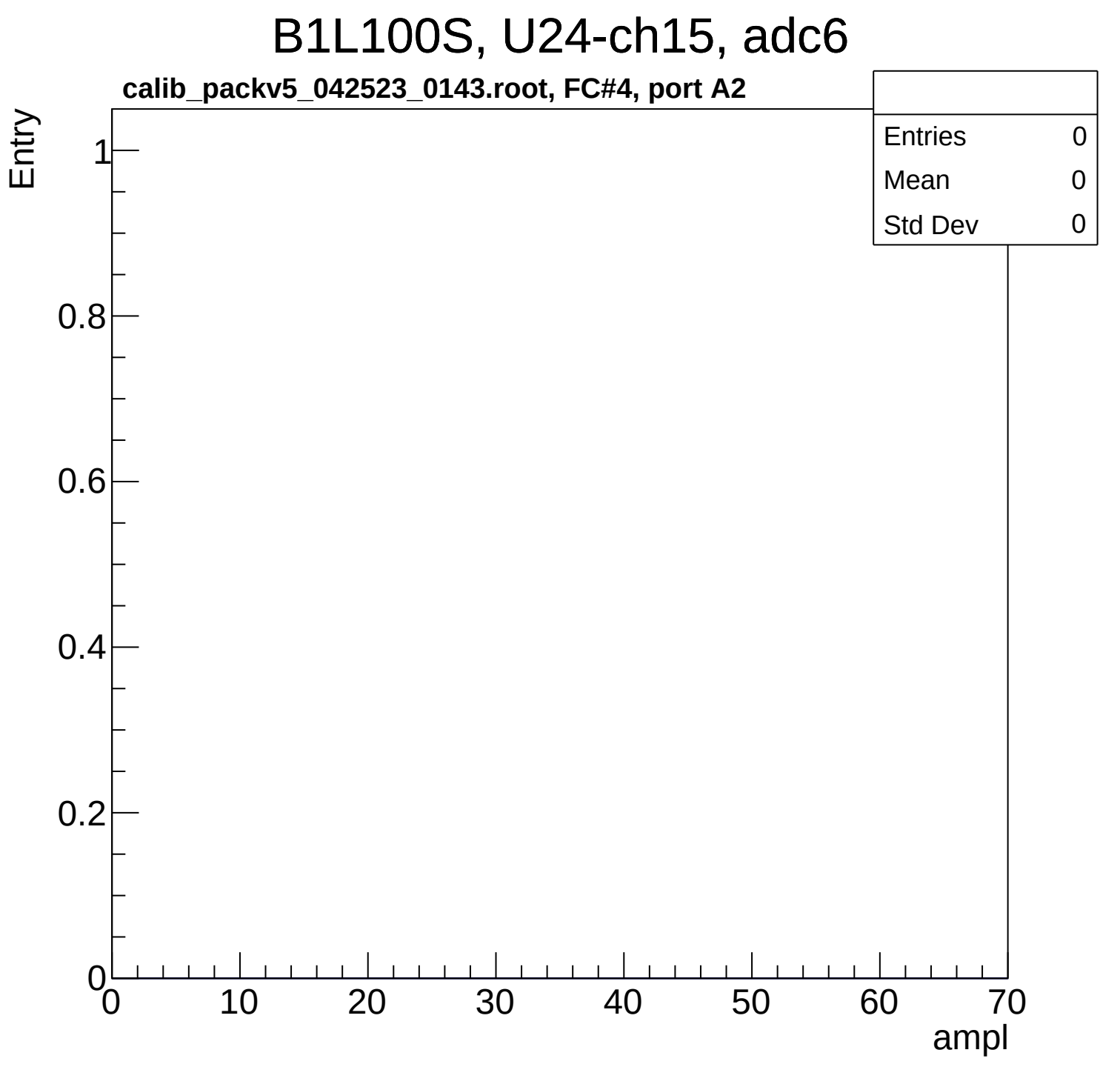
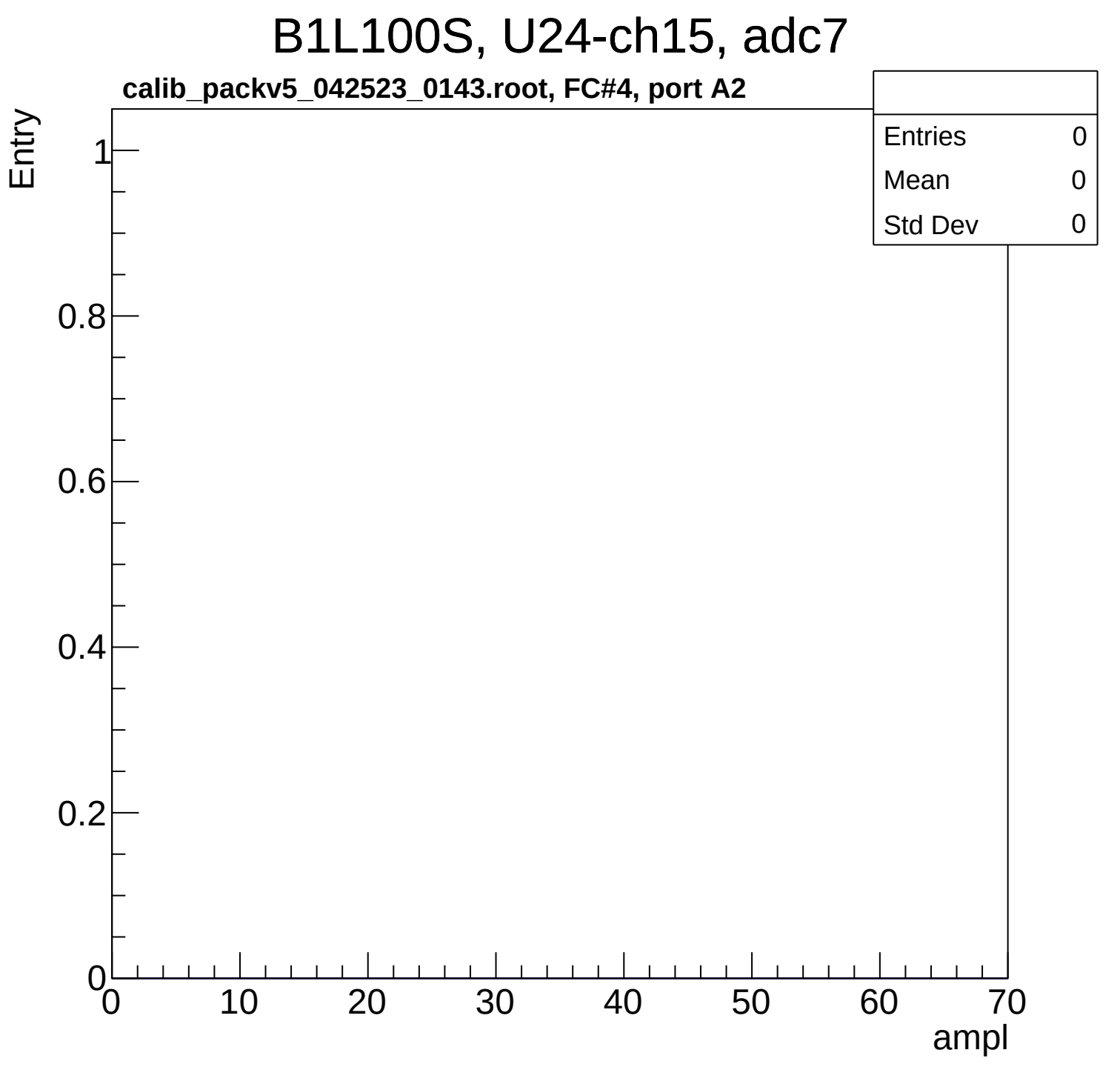
70
ampl


B1L100S, U24-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch15, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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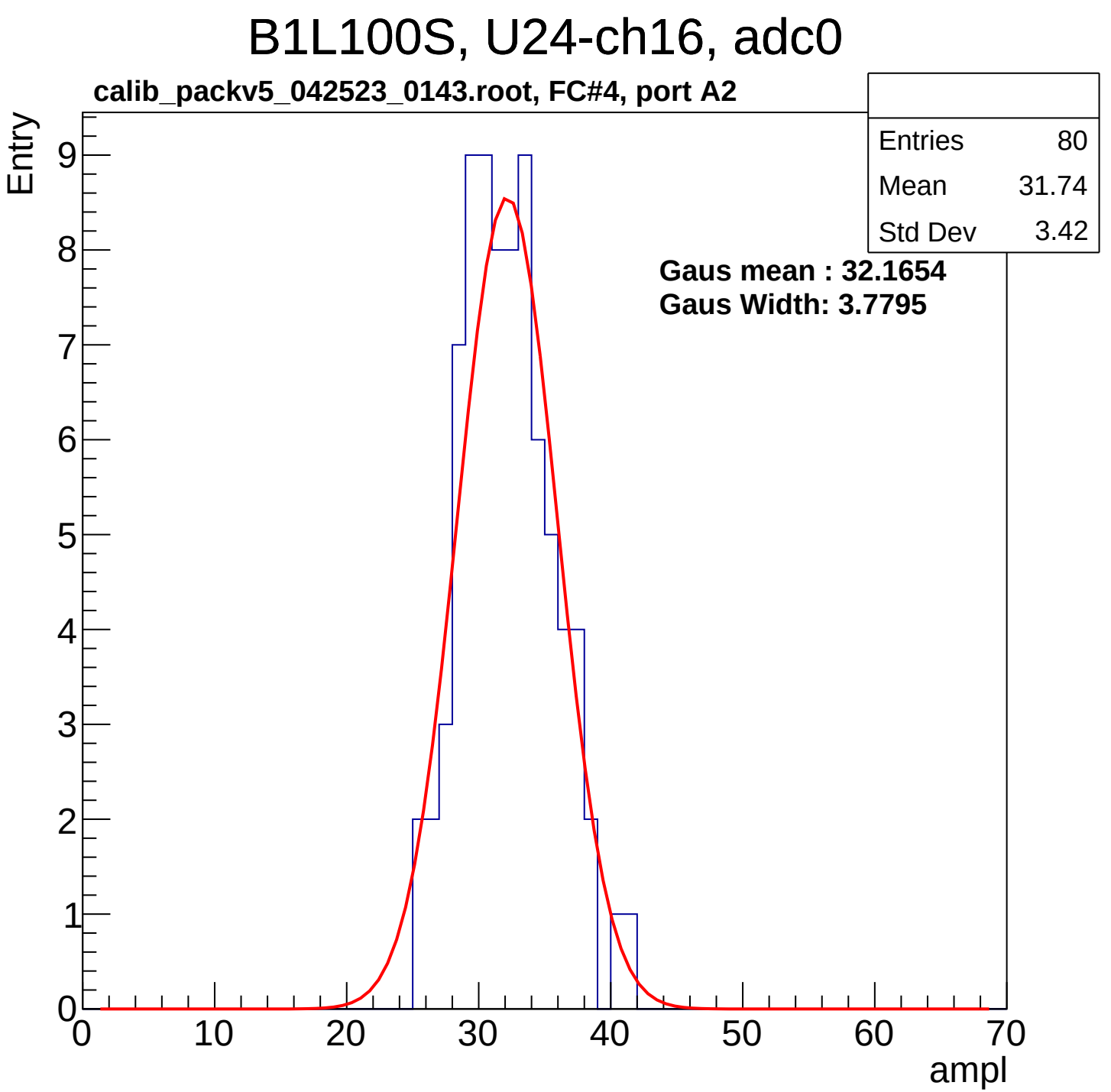
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch16, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 31.74
Std Dev 3.42
Gaus mean : 32.1654
Gaus Width: 3.7795
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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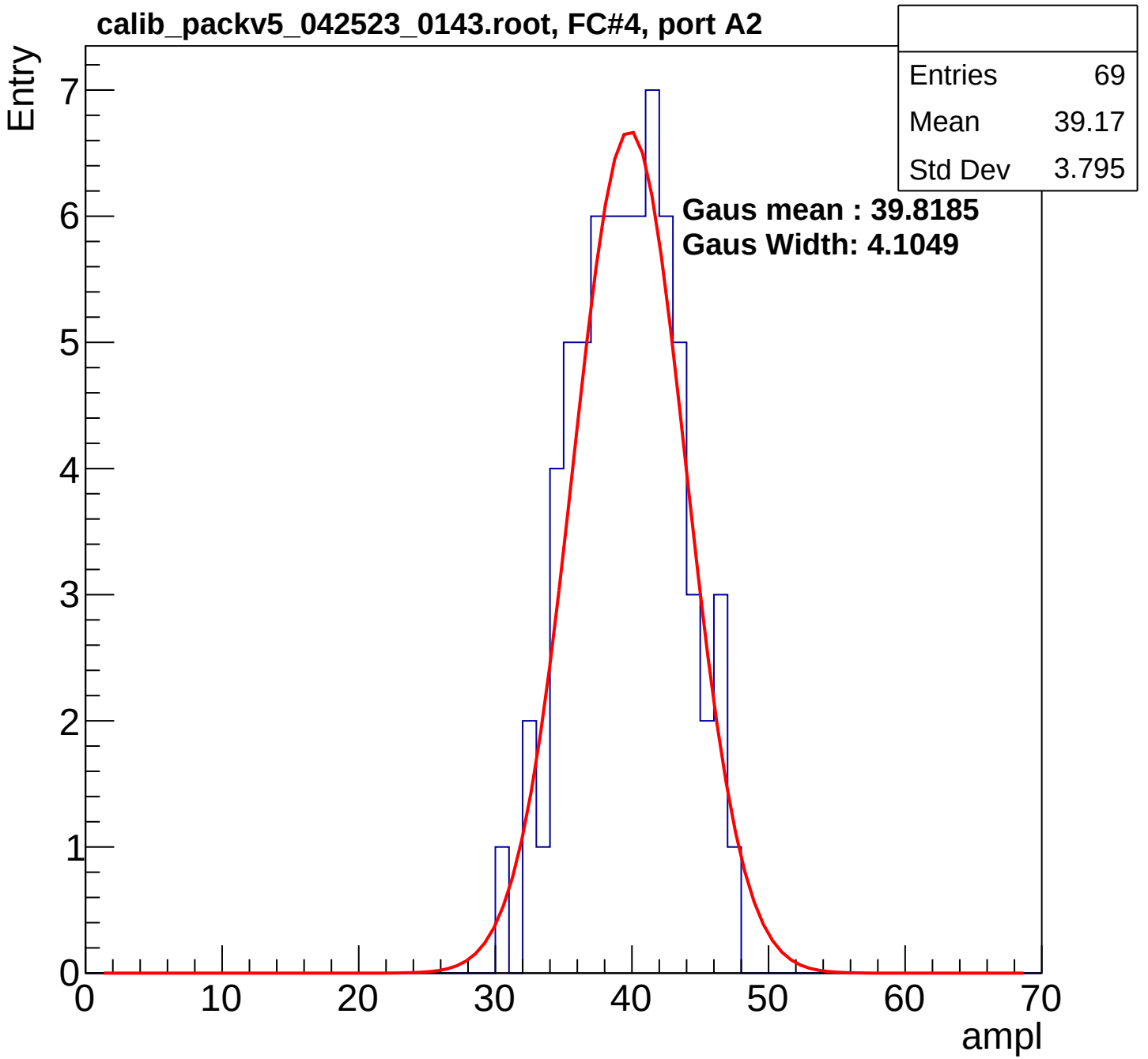
B1L100S, U24-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 39.17
Std Dev 3.795
Gaus mean : 39.8185
Gaus Width: 4.1049
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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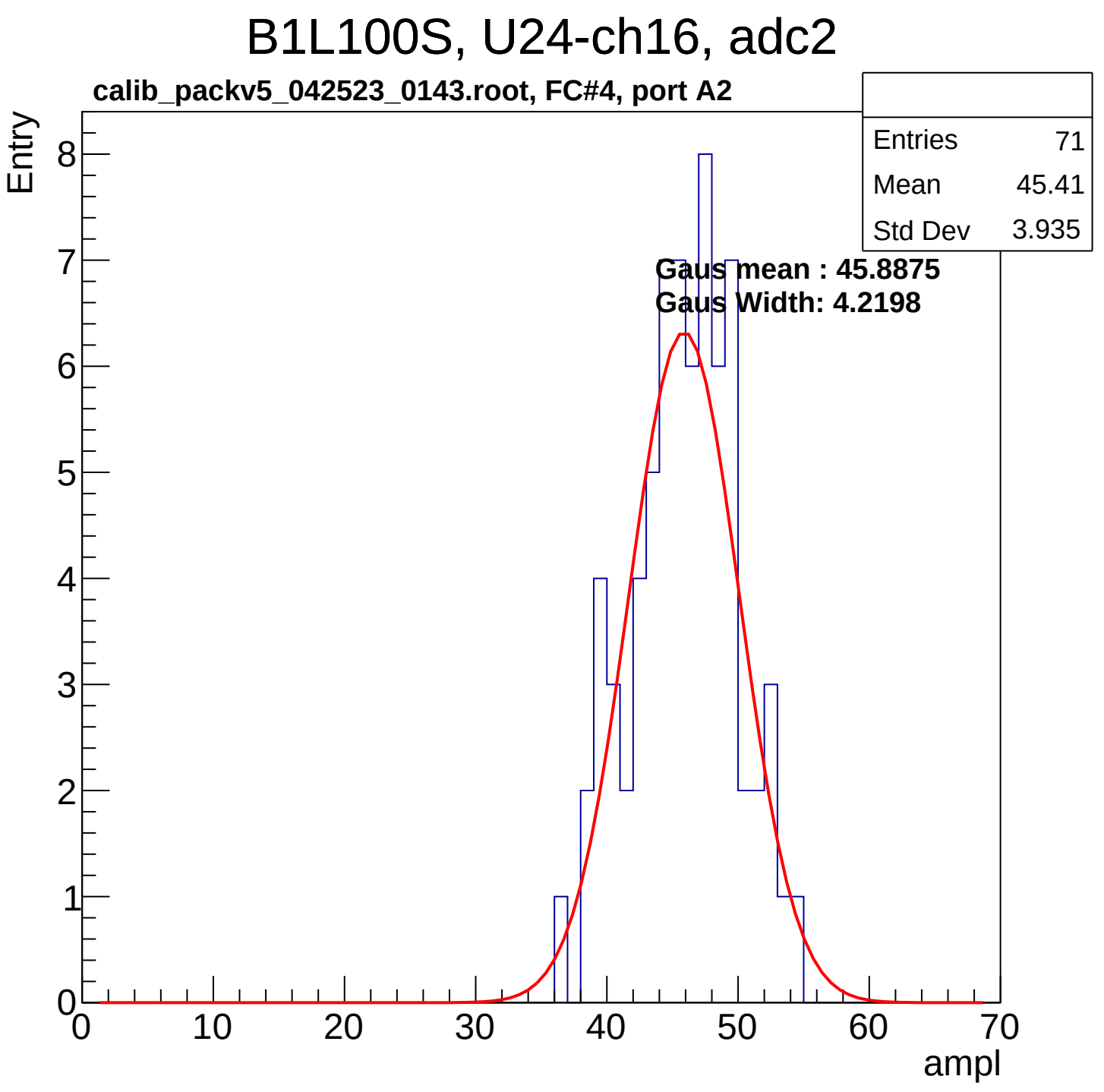
B1L100S, U24-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 45.41
Std Dev 3.935
Gaus mean : 45.8875
Gaus Width: 4.2198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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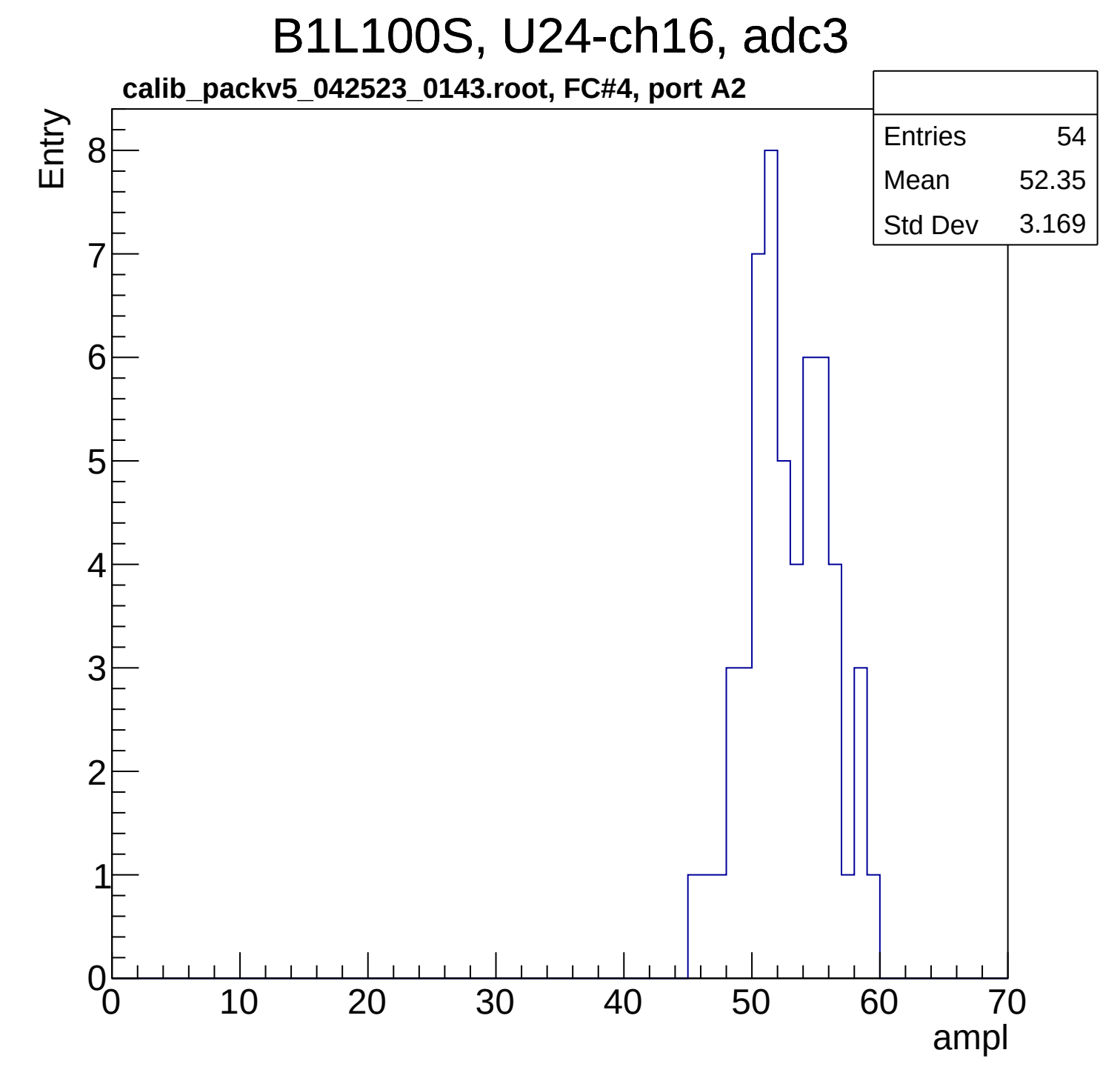
B1L100S, U24-ch16, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 52.35
Std Dev 3.169
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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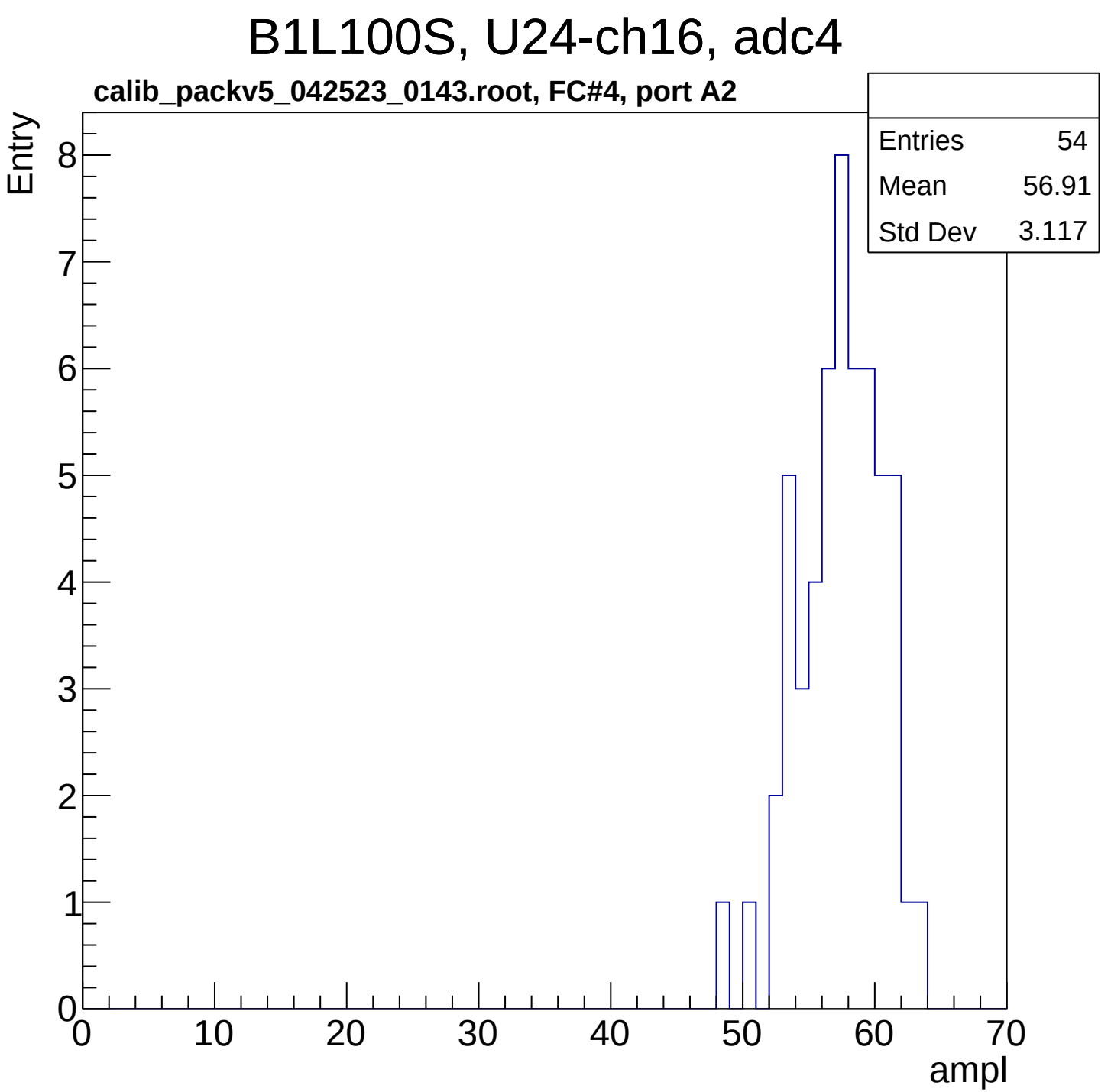
B1L100S, U24-ch16, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.91
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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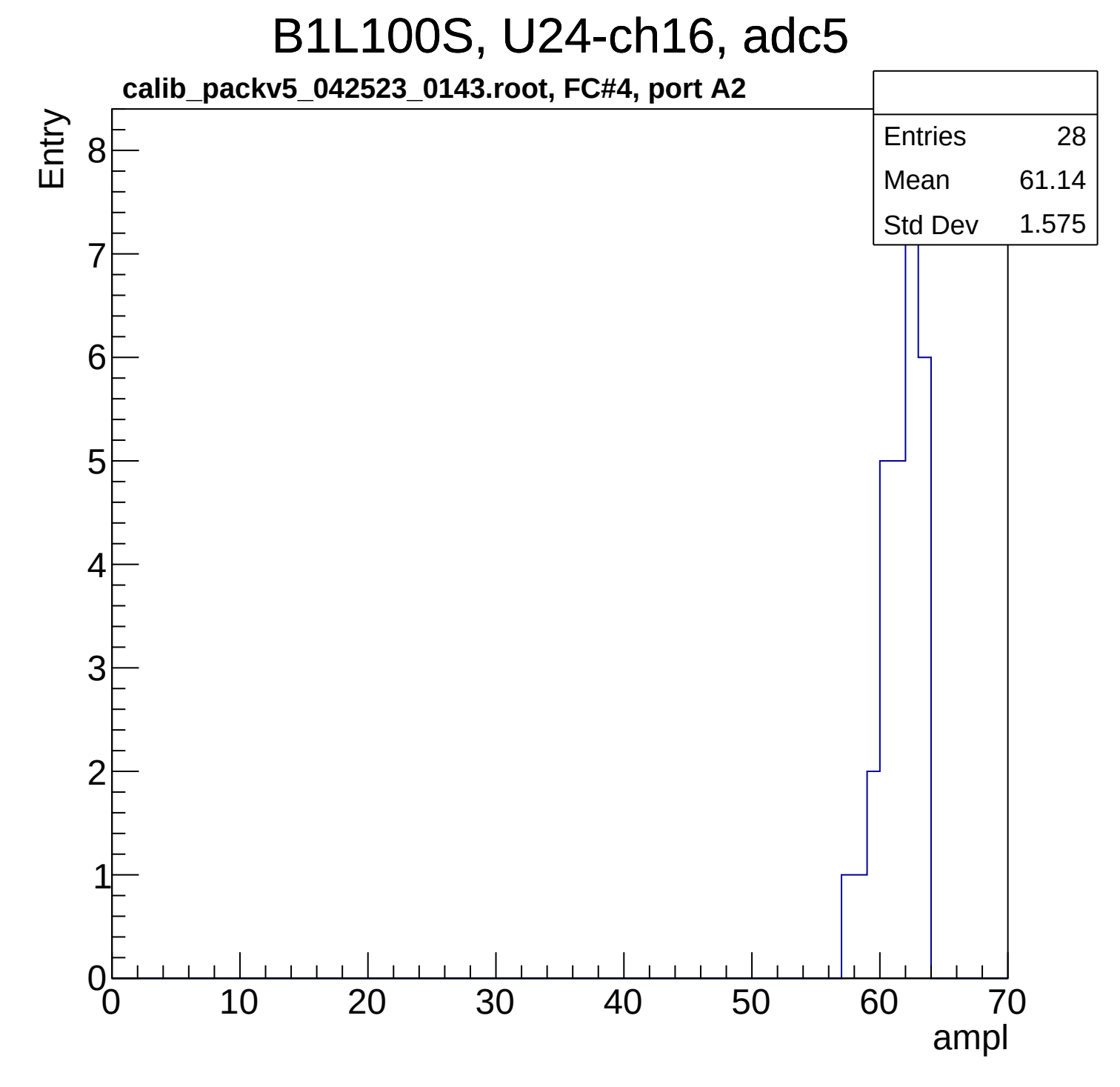
B1L100S, U24-ch16, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 61.14
Std Dev 1.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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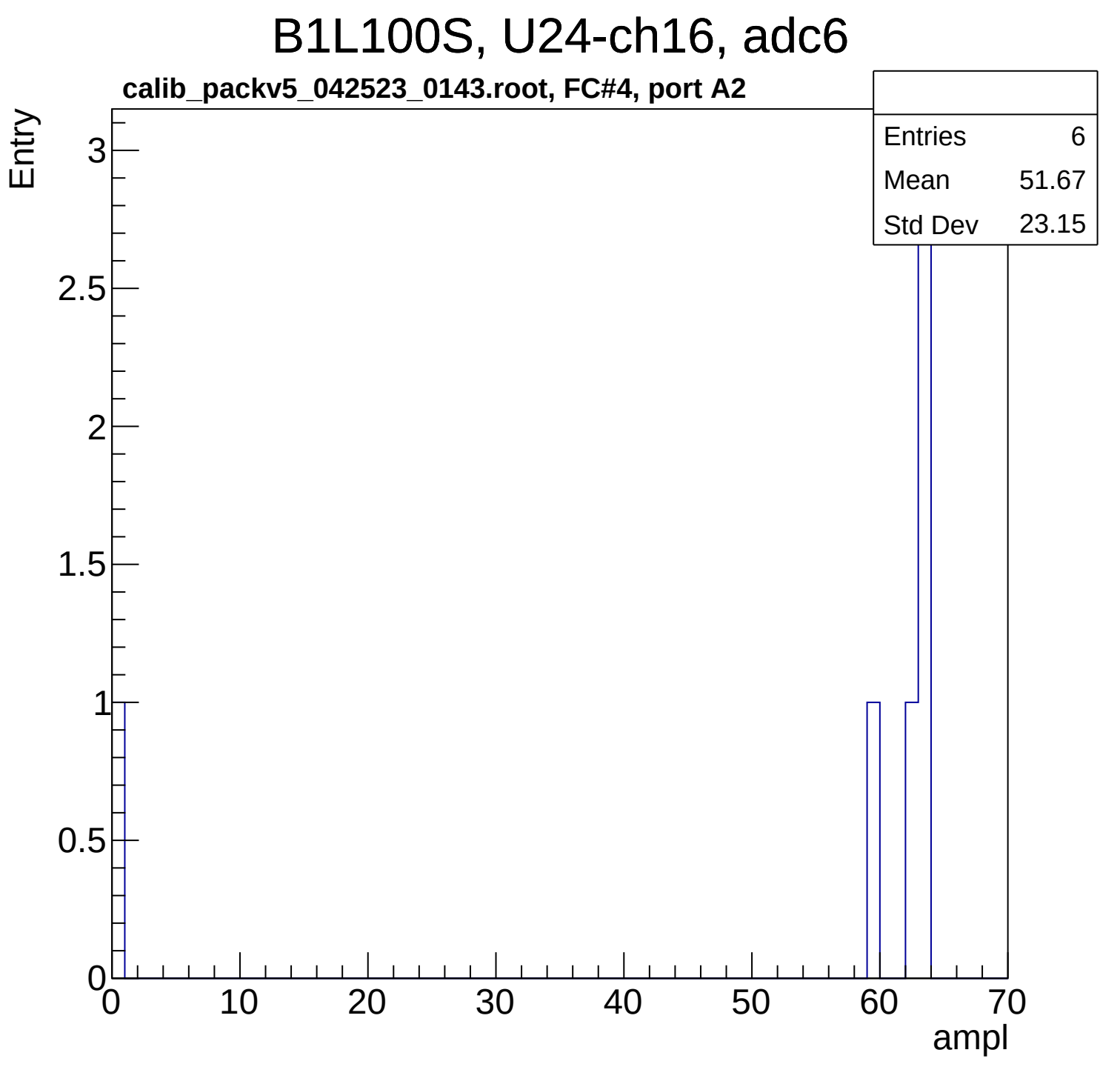
B1L100S, U24-ch16, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.67
Std Dev 23.15
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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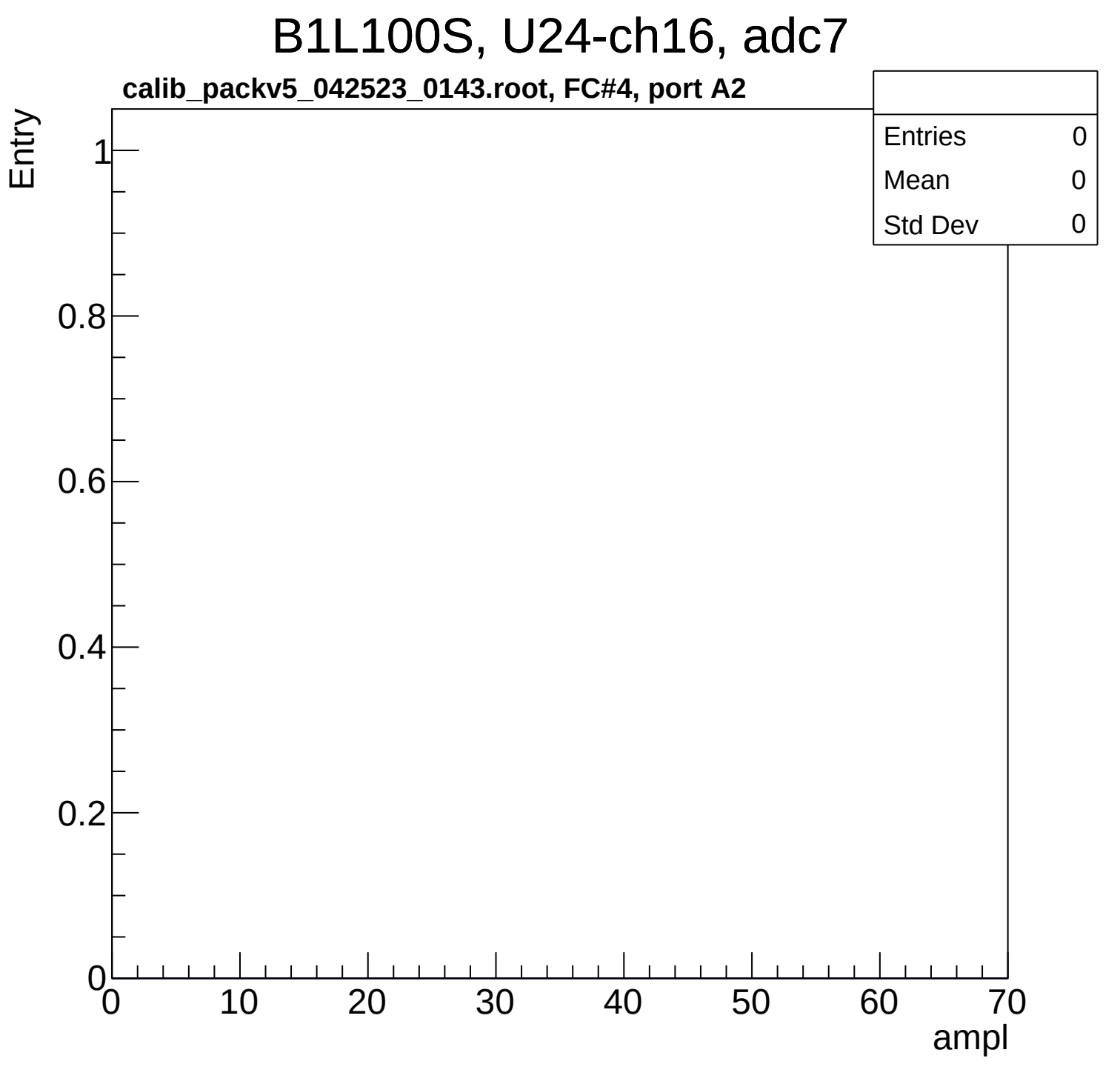
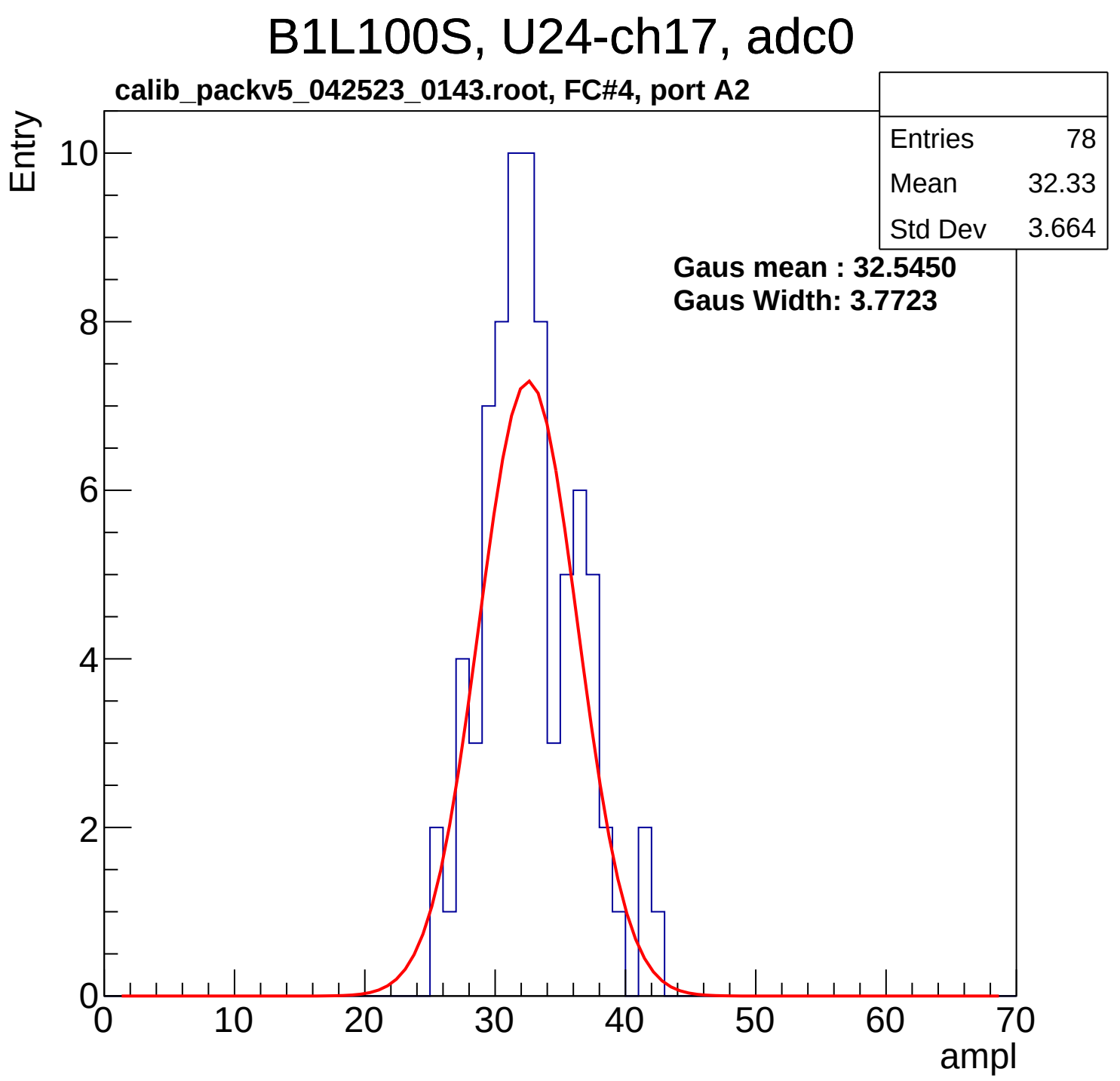
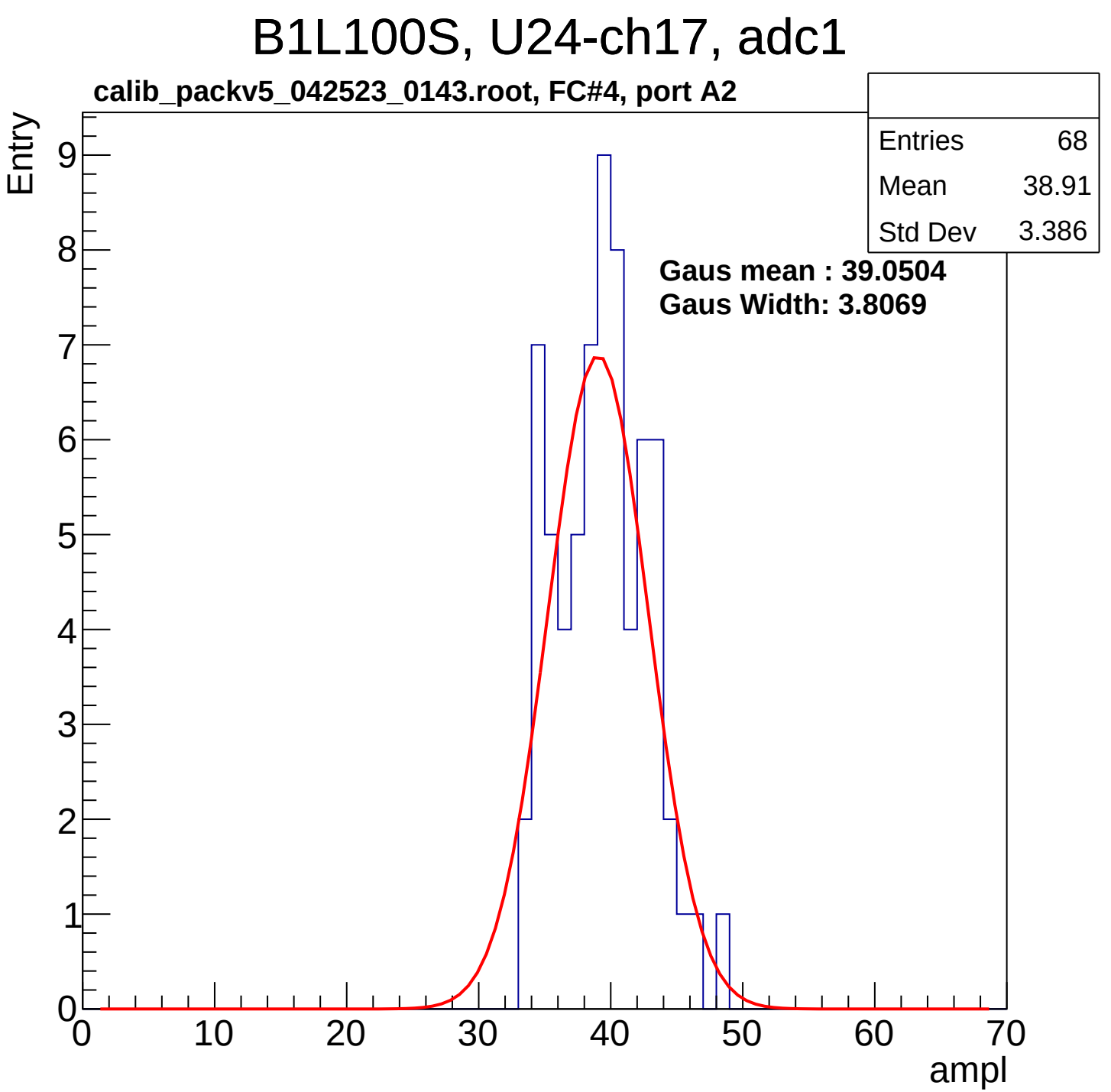
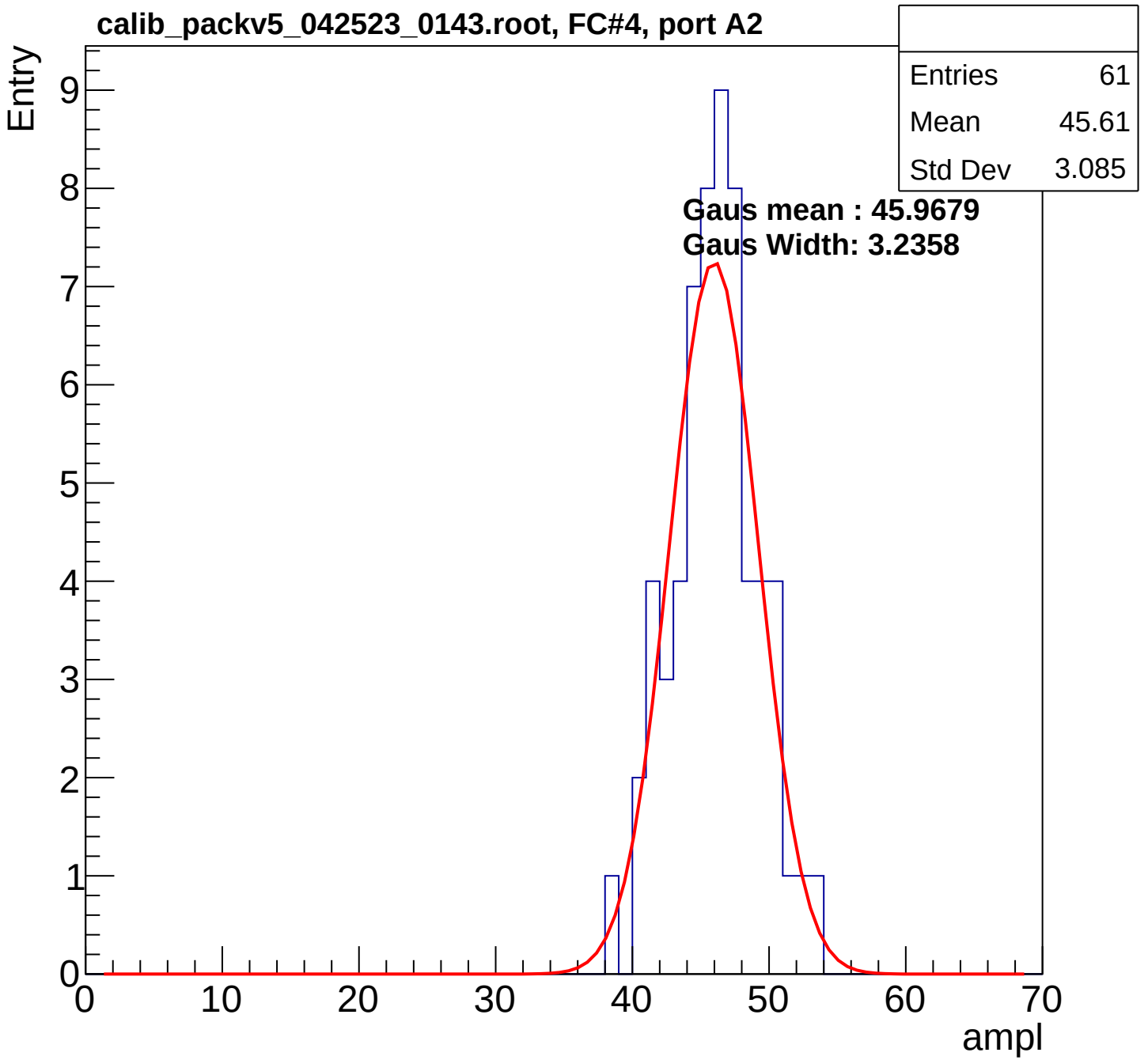

B1L100S, U24-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 32.33
Std Dev 3.664
Gaus mean : 32.5450
Gaus Width: 3.7723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 38.91
Std Dev 3.386
Gaus mean : 39.0504
Gaus Width: 3.8069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 45.61
Std Dev 3.085
Gaus mean : 45.9679
Gaus Width: 3.2358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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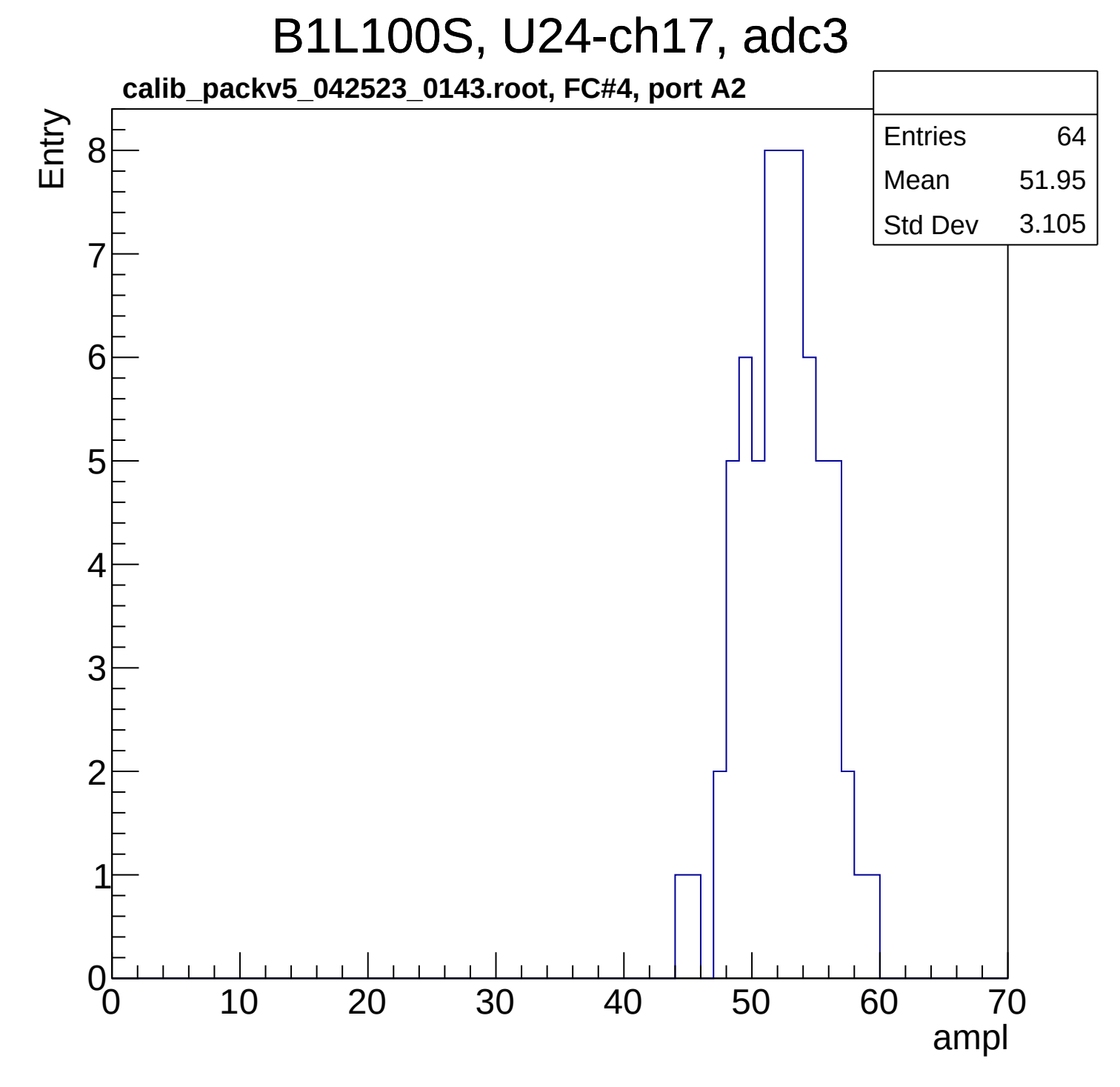
B1L100S, U24-ch17, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 51.95
Std Dev 3.105
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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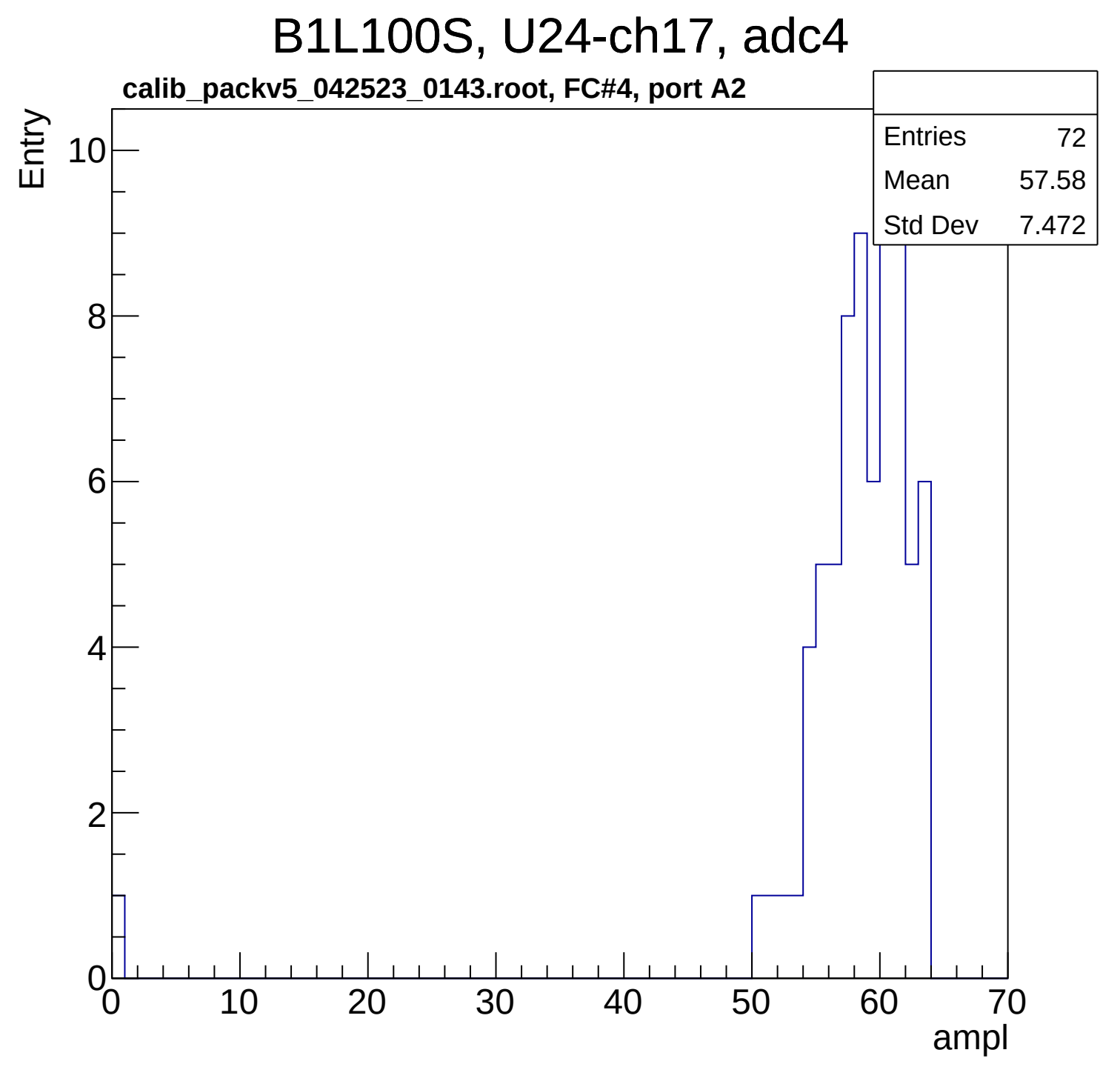
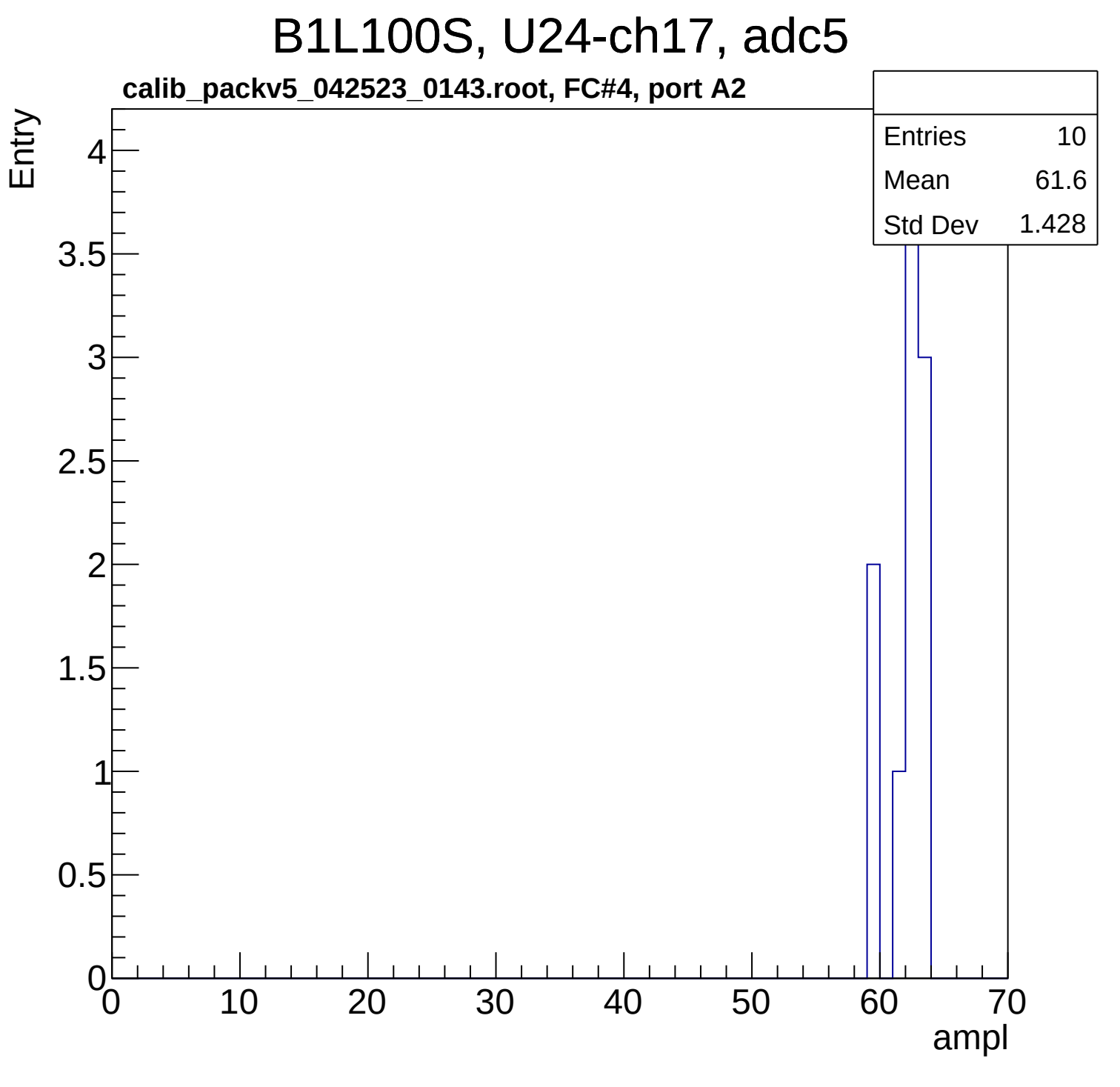

B1L100S, U24-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 57.58
Std Dev 7.472
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch17, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.6
Std Dev 1.428
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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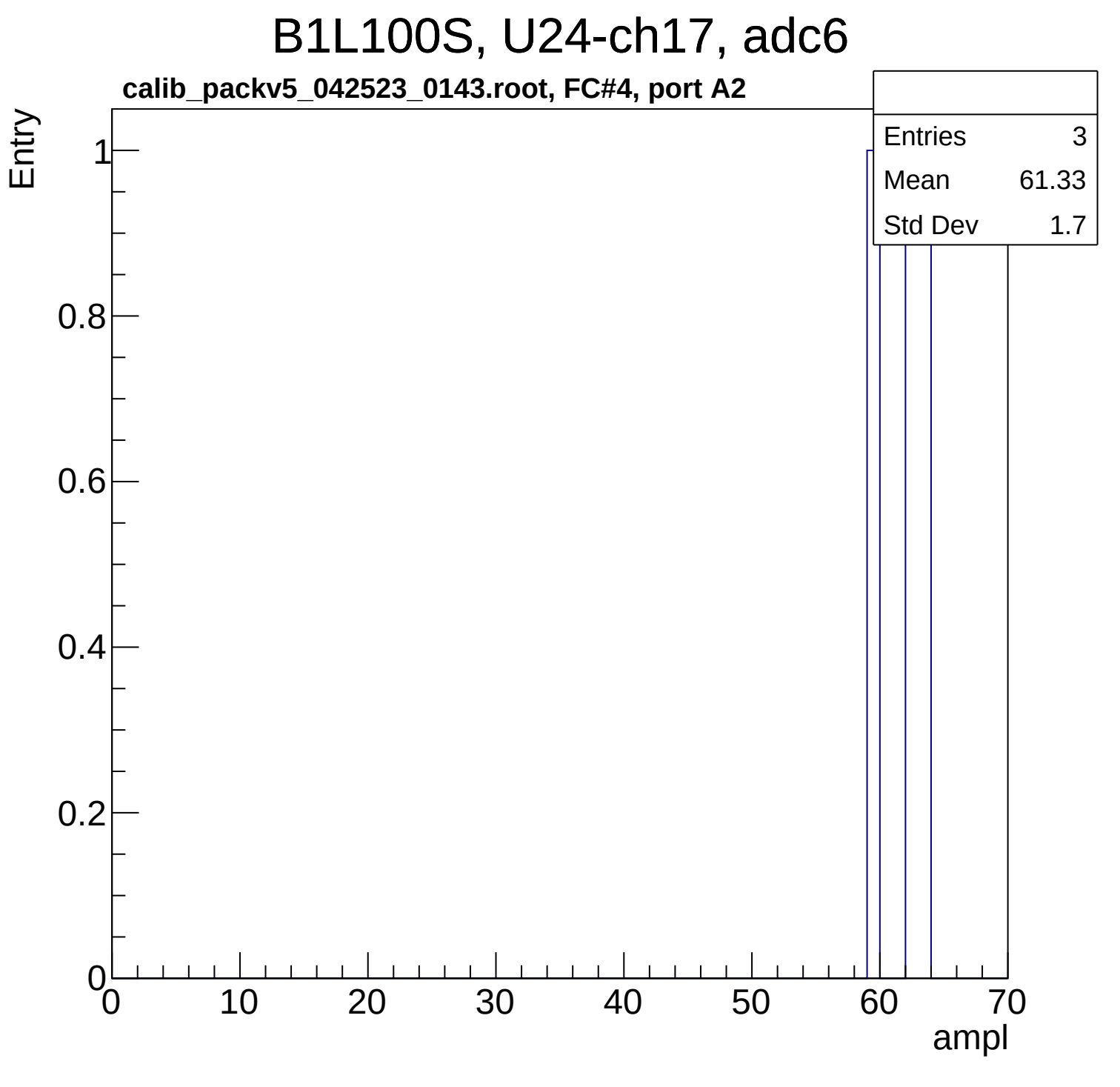
B1L100S, U24-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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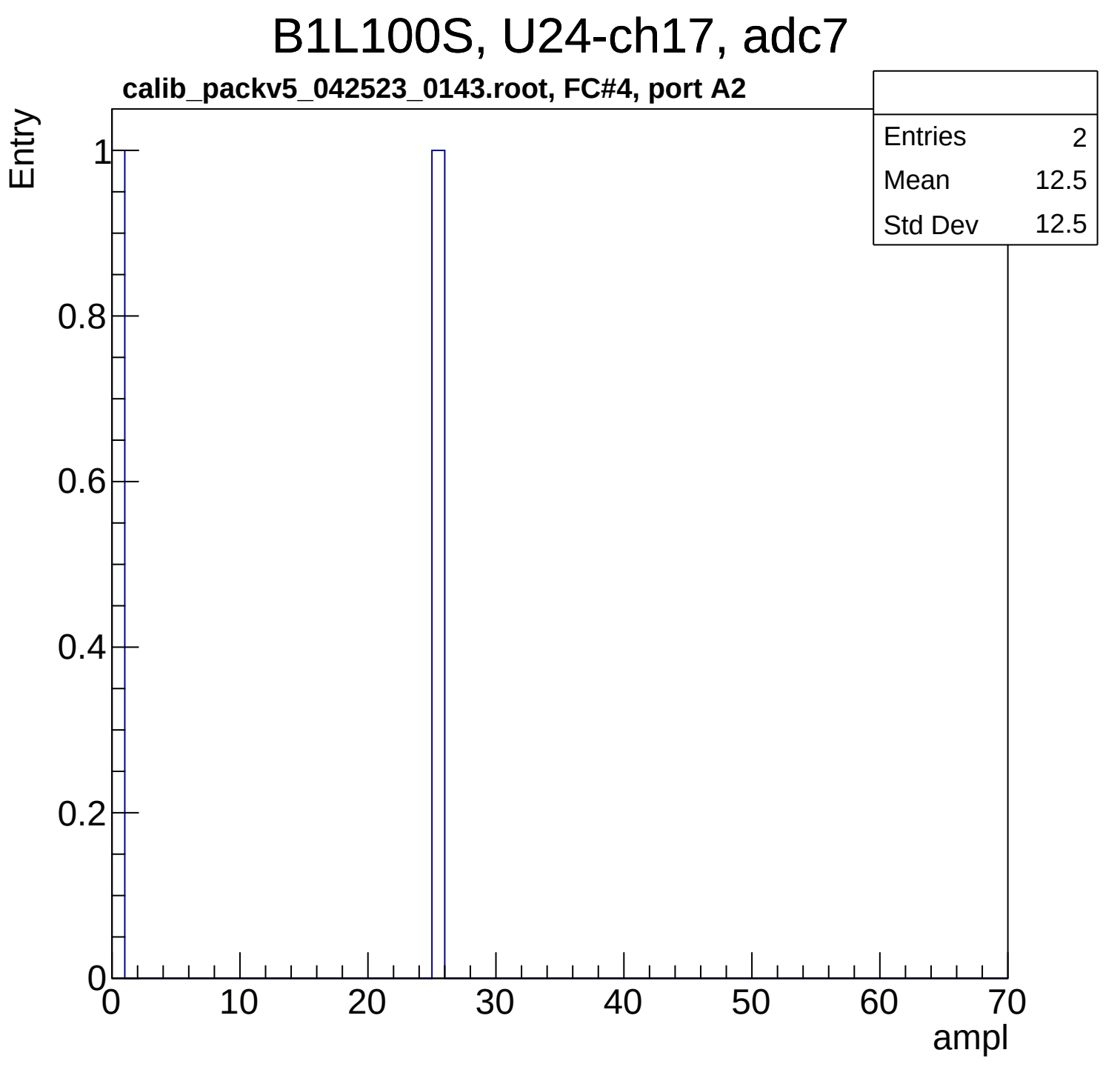
B1L100S, U24-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.6
0.4
0.2
0
0
10
20
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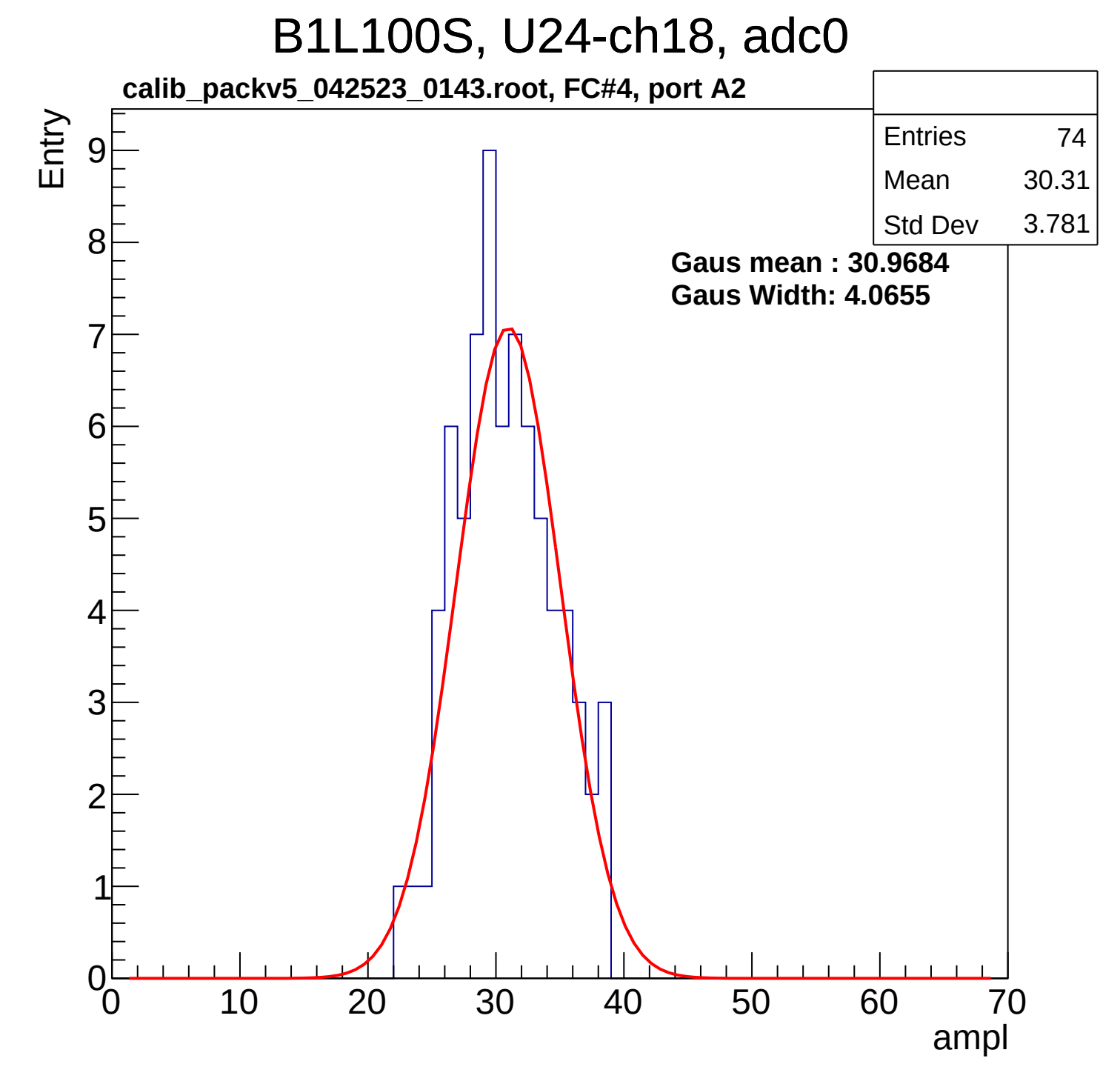
50
60
70
ampl


B1L100S, U24-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 30.31
Std Dev 3.781
Gaus mean : 30.9684
Gaus Width: 4.0655
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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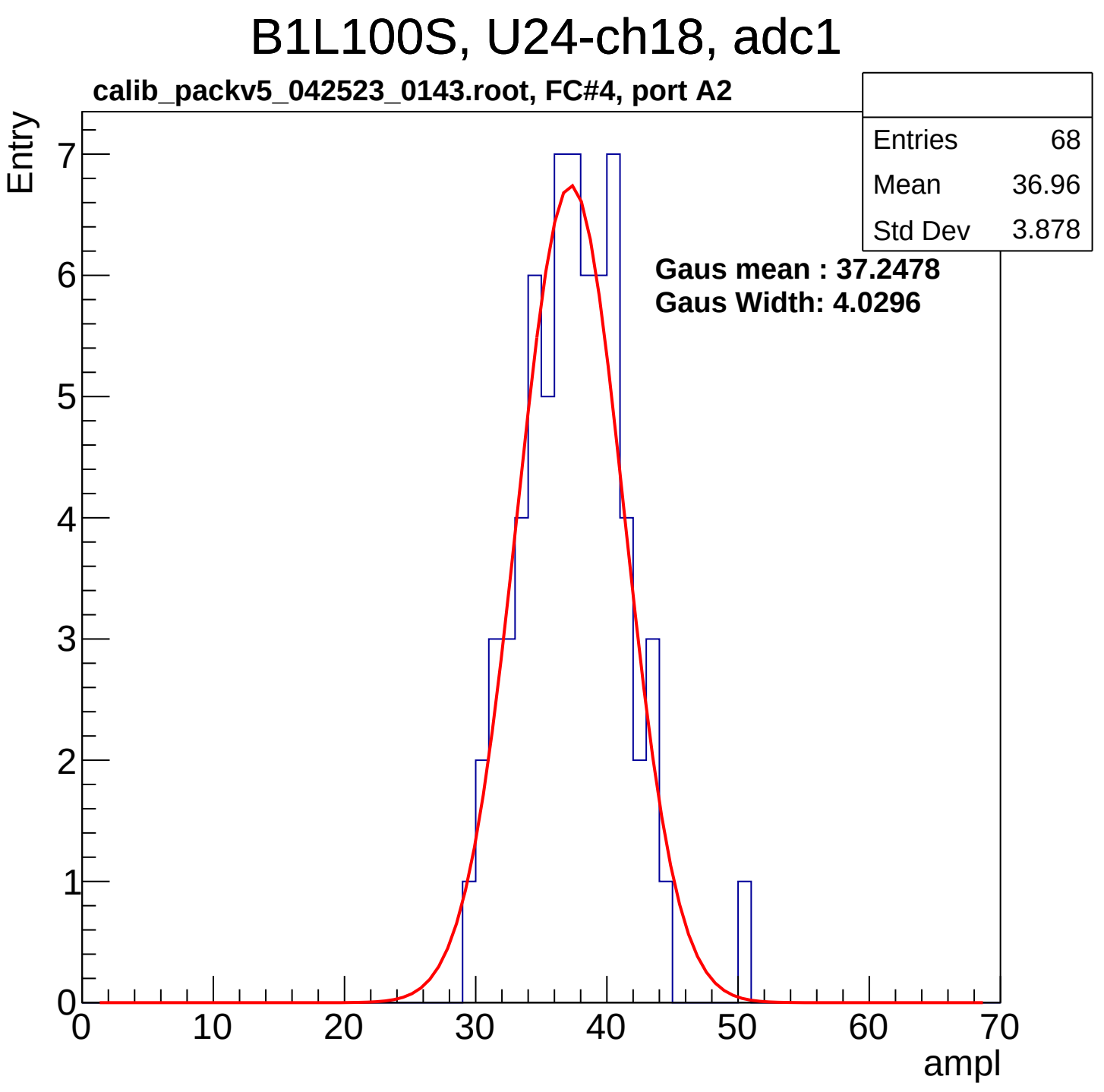
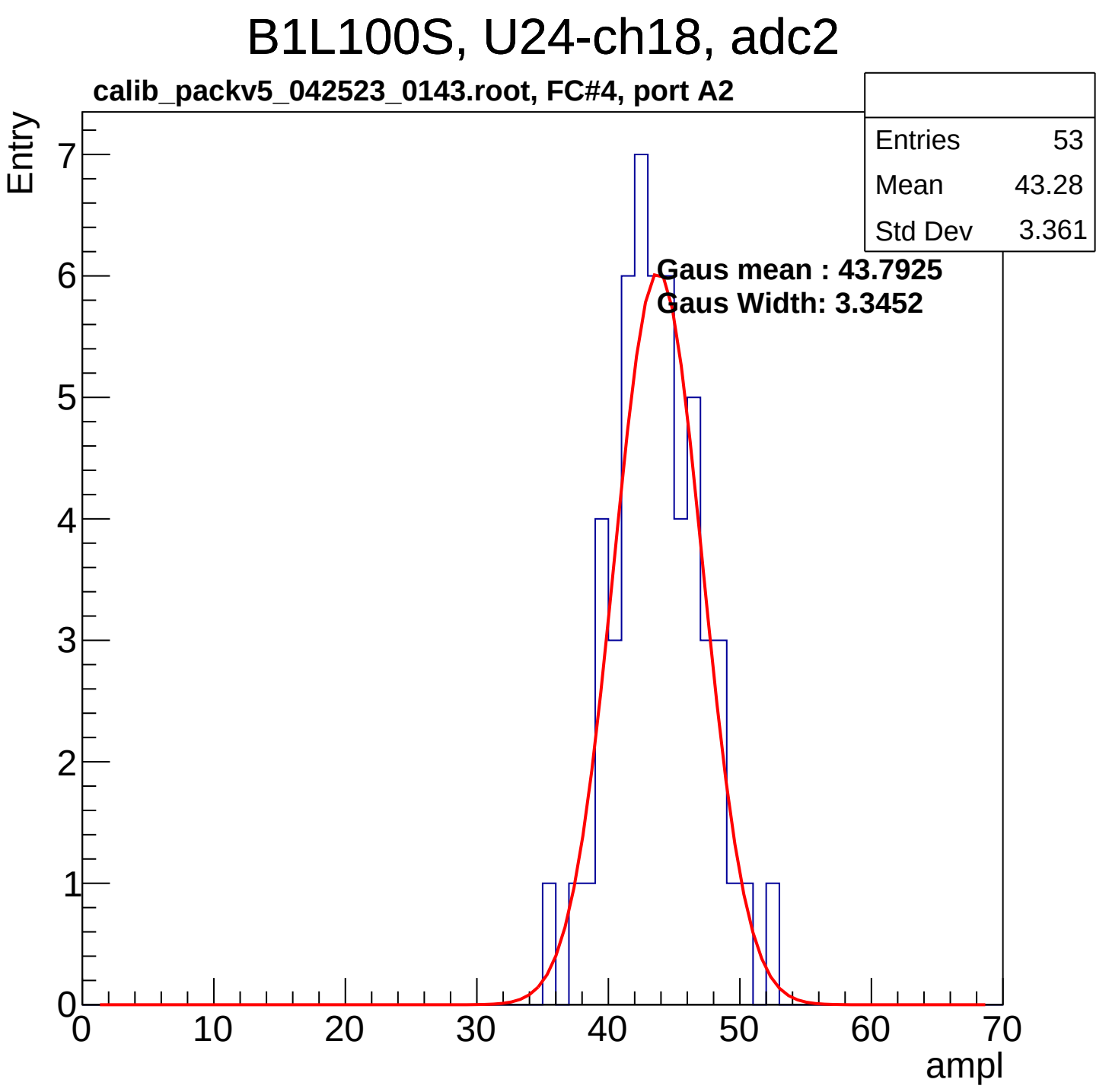

B1L100S, U24-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.96
Std Dev 3.878
Gaus mean : 37.2478
Gaus Width: 4.0296
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 43.28
Std Dev 3.361
Gaus mean : 43.7925
Gaus Width: 3.3452
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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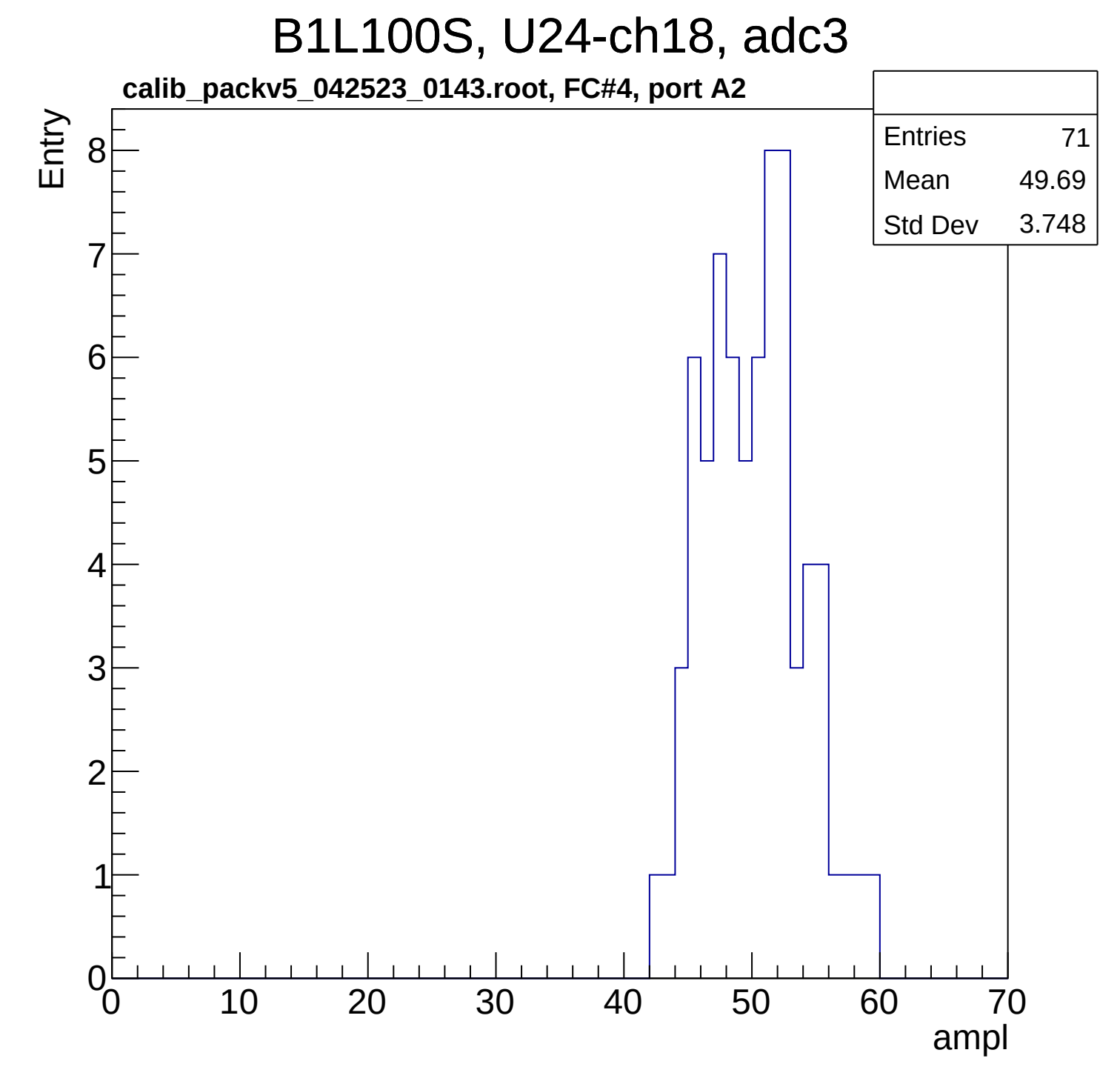
B1L100S, U24-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.69
Std Dev 3.748
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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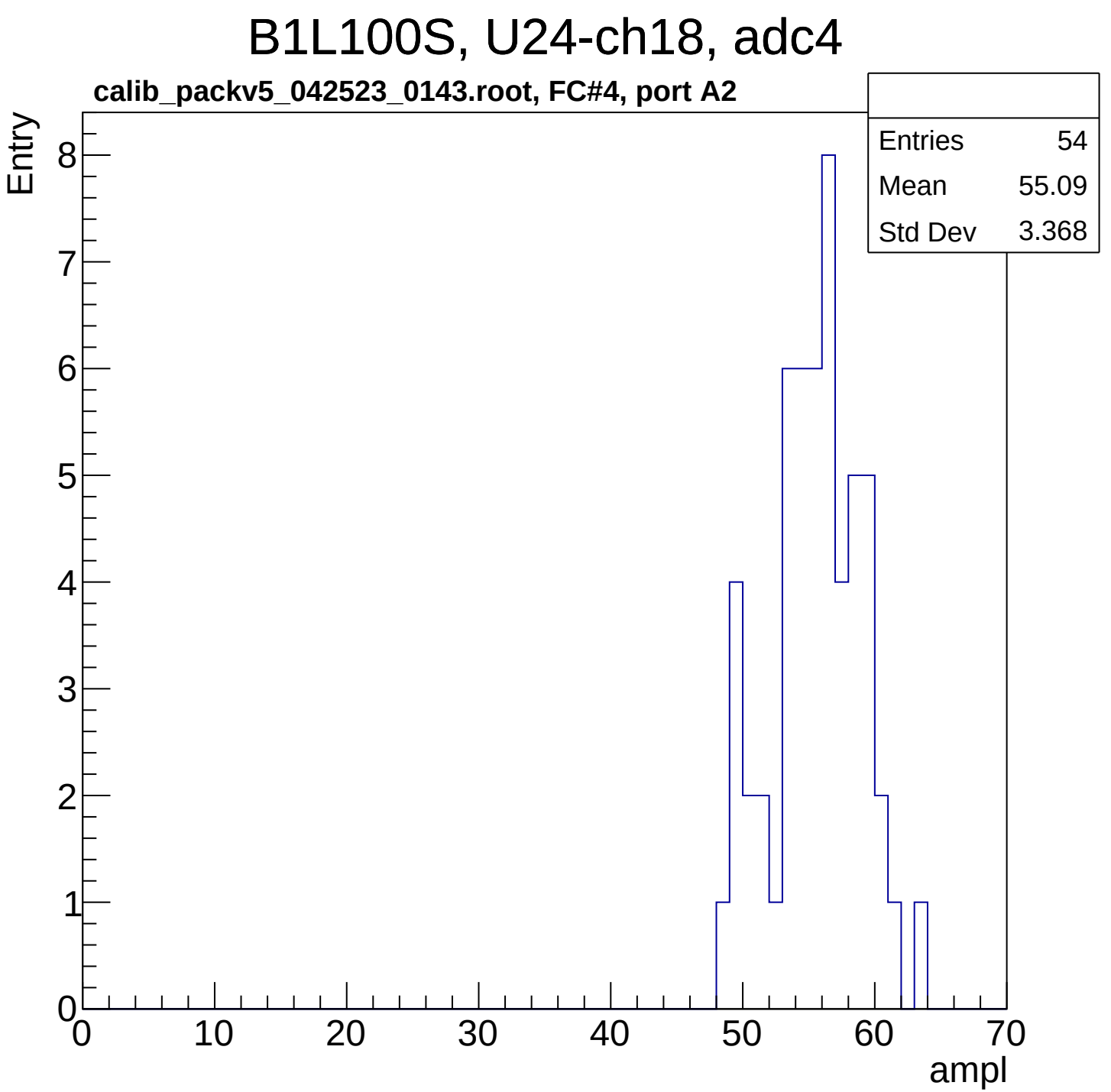
B1L100S, U24-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.09
Std Dev 3.368
Entry
ampl
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3
4
5
6
7
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0
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60
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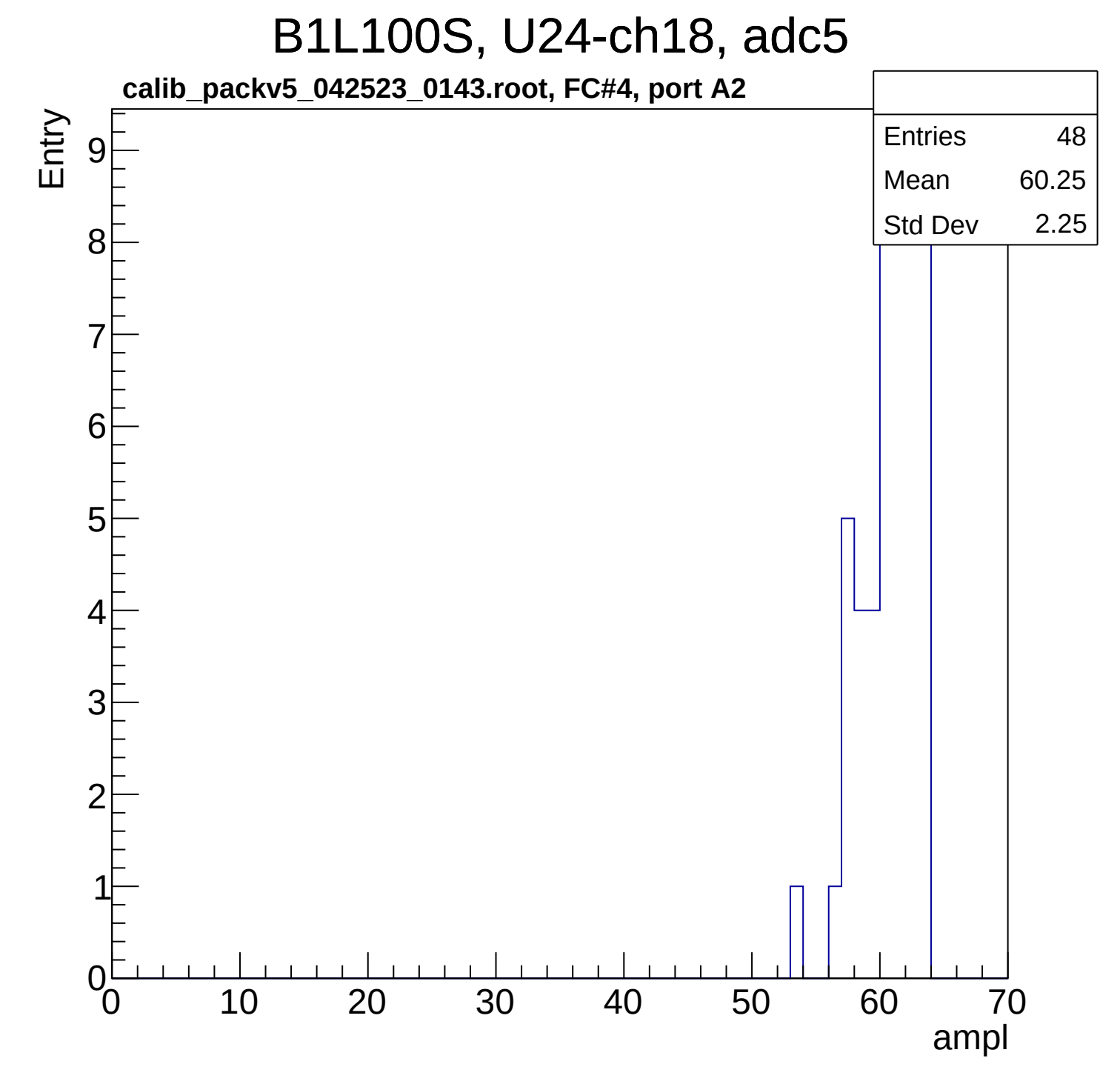
B1L100S, U24-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 60.25
Std Dev 2.25
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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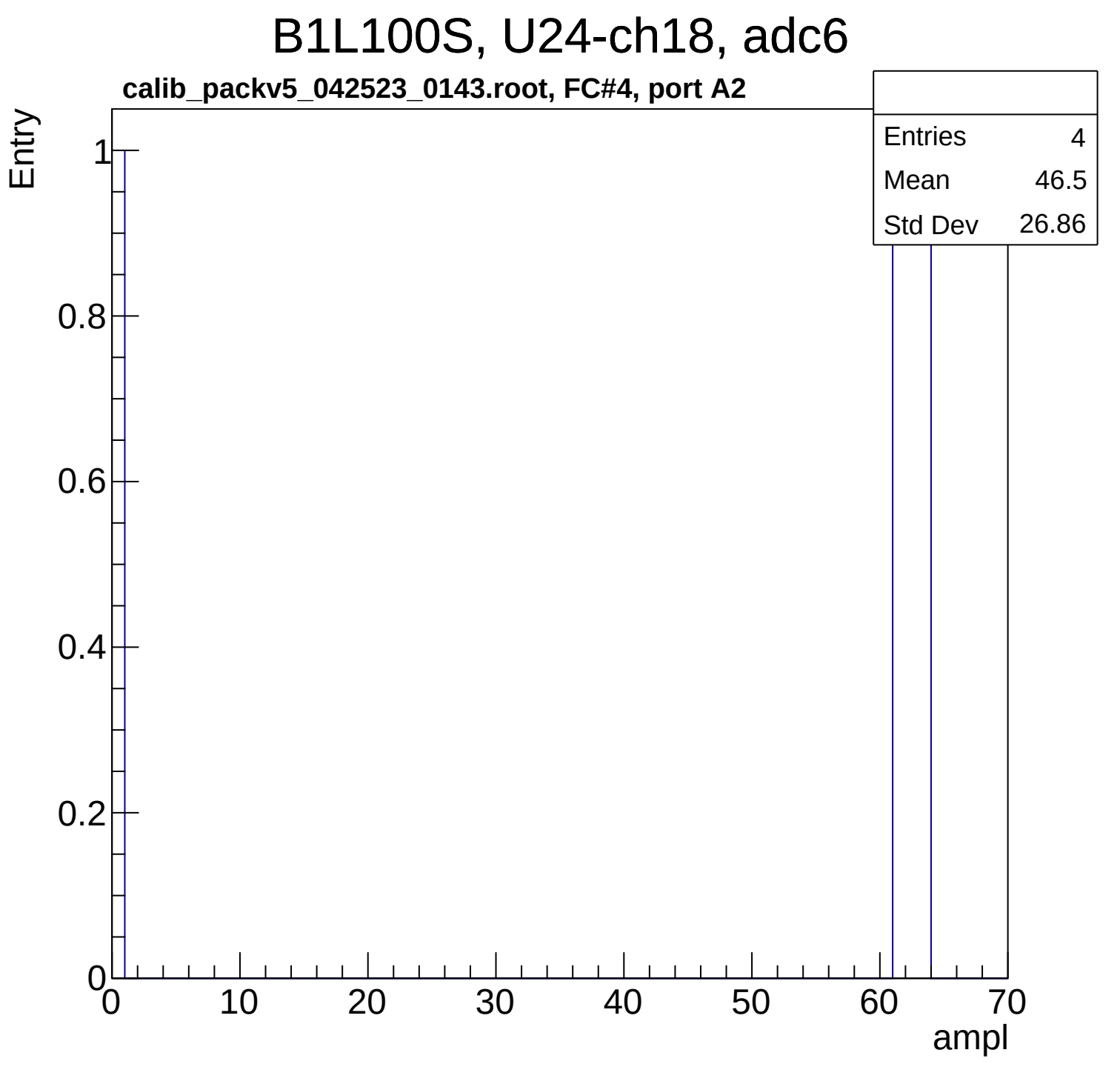
B1L100S, U24-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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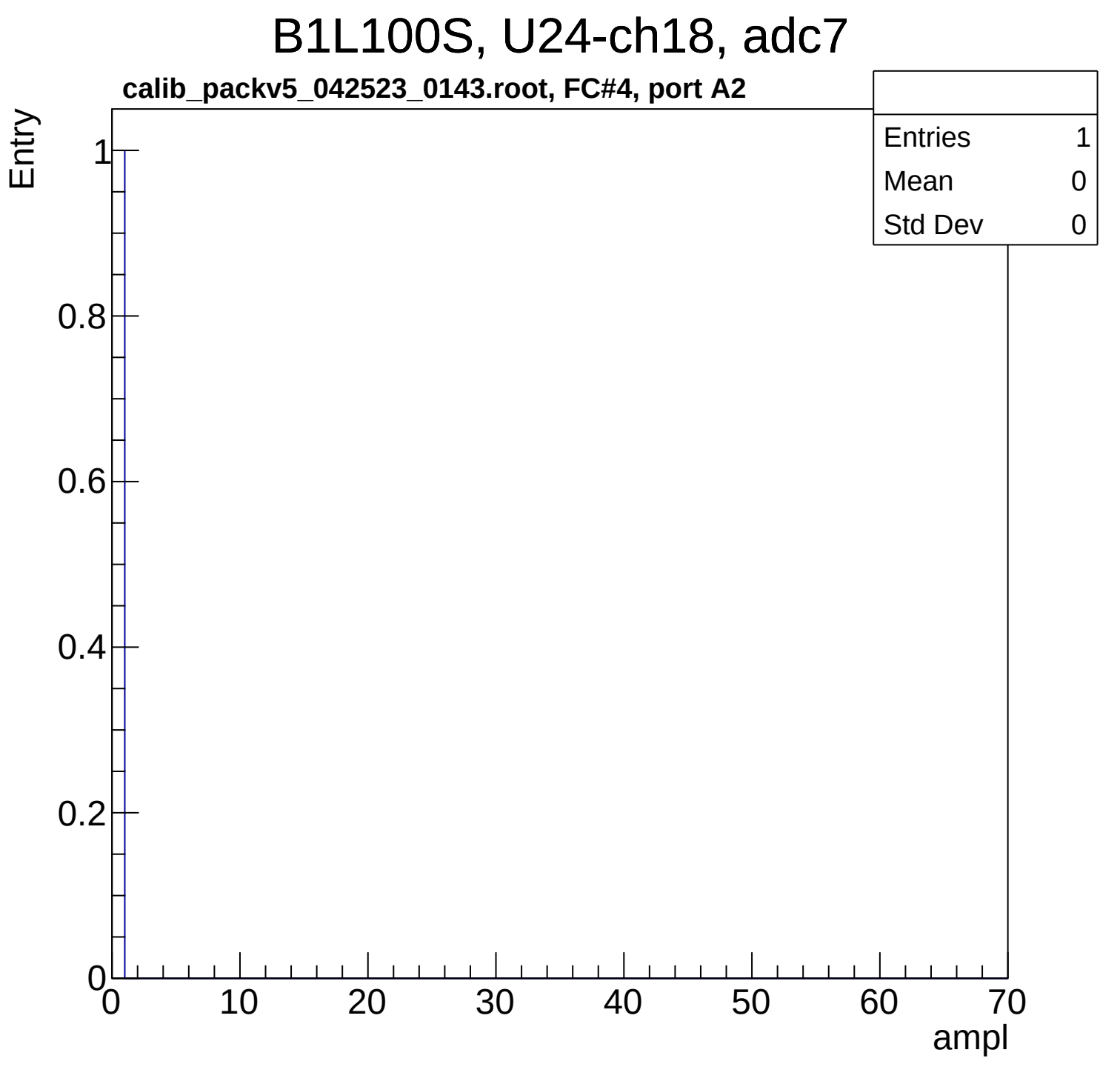
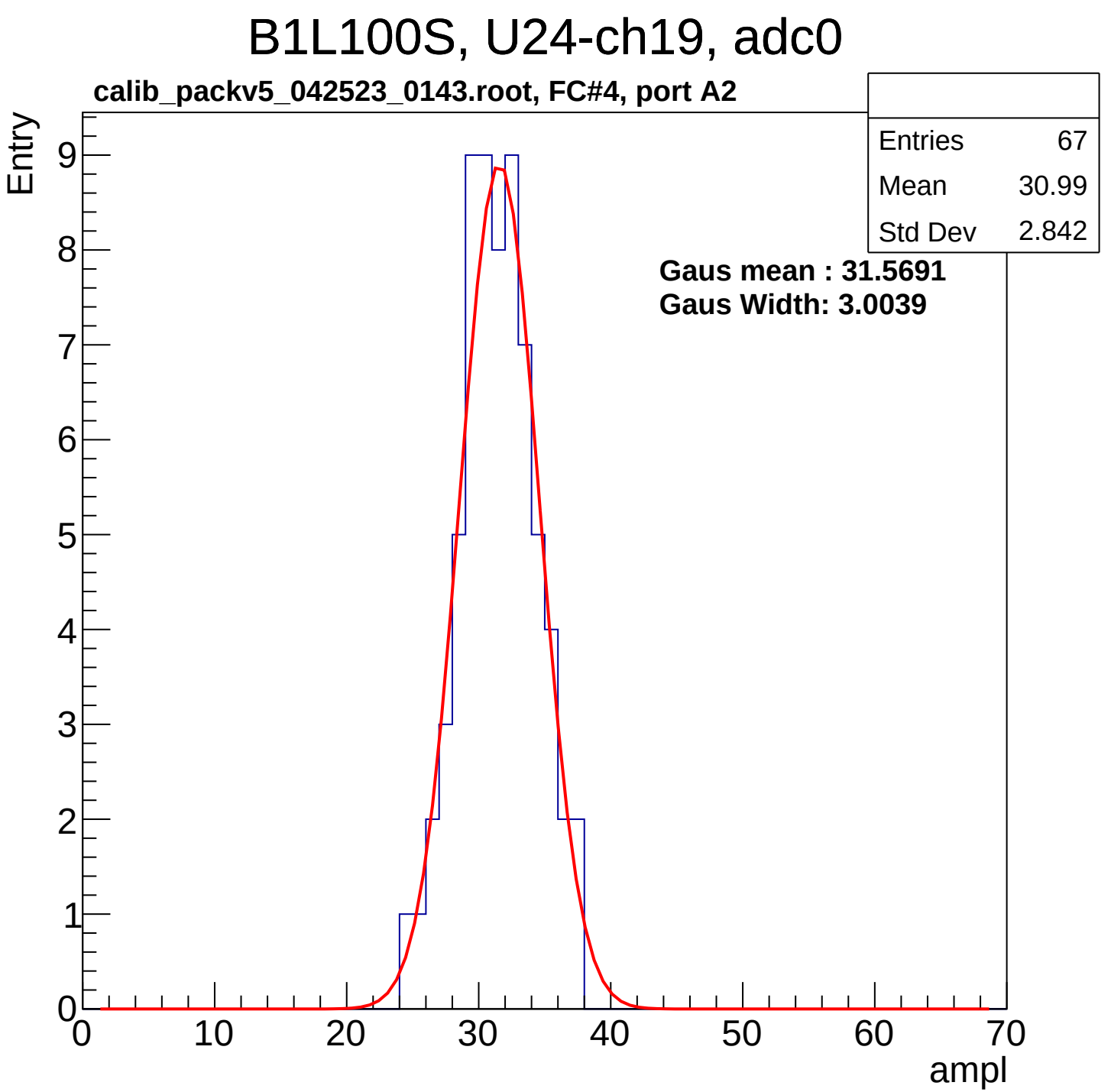
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.99
Std Dev 2.842
Gaus mean : 31.5691
Gaus Width: 3.0039
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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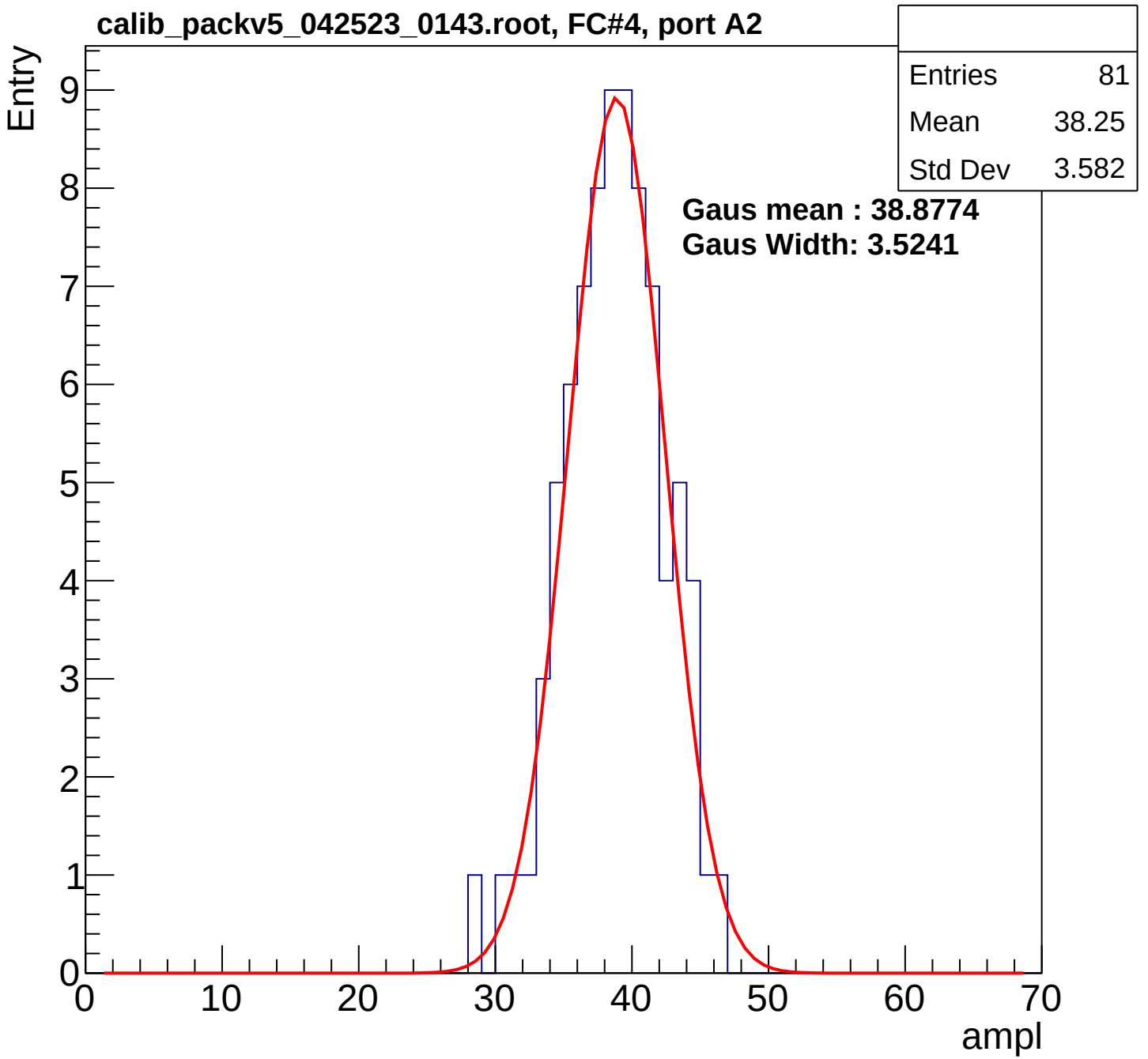
B1L100S, U24-ch19, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 38.25
Std Dev 3.582
Gaus mean : 38.8774
Gaus Width: 3.5241
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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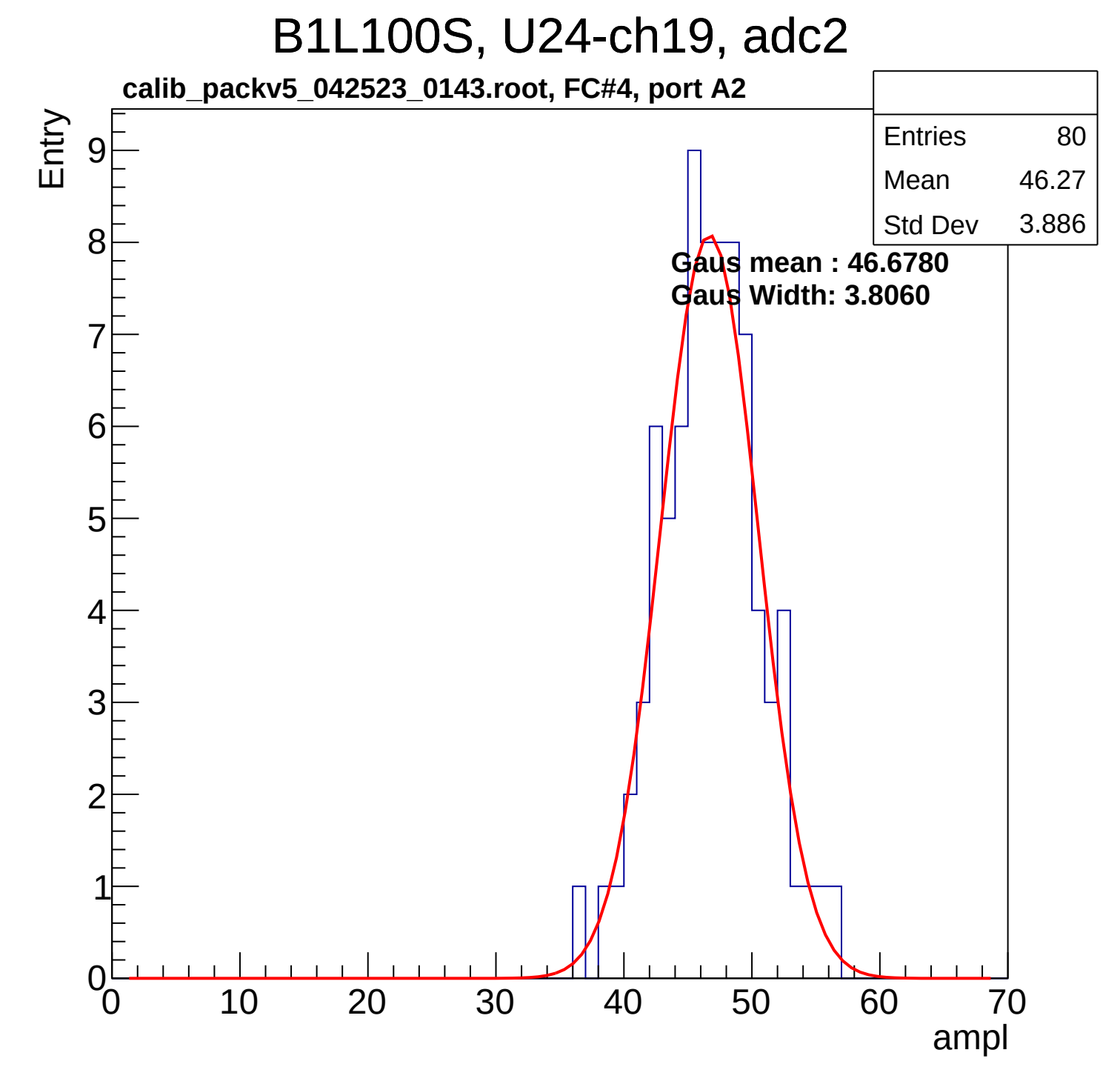
B1L100S, U24-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 46.27
Std Dev 3.886
Gaus mean : 46.6780
Gaus Width: 3.8060
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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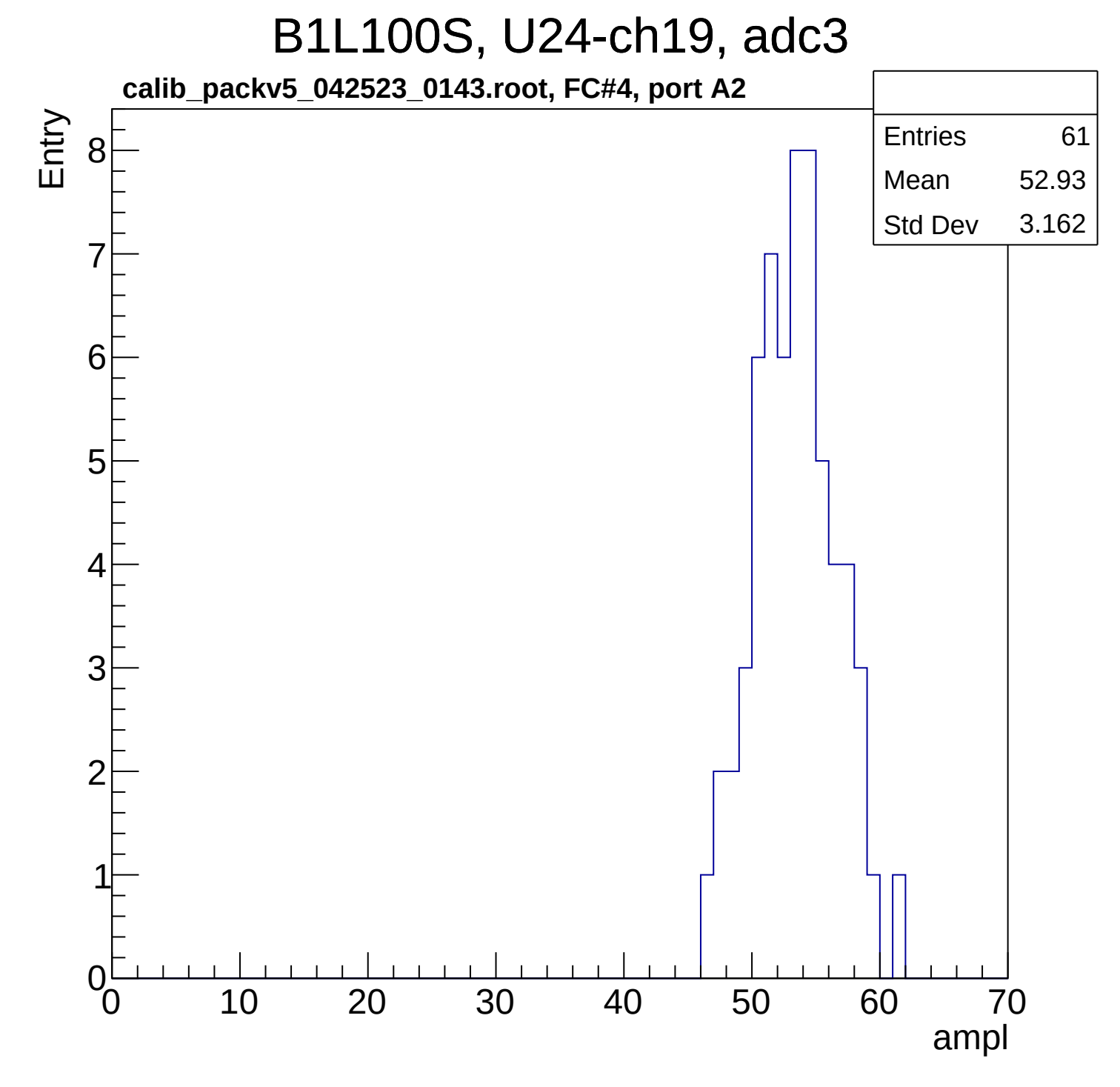
B1L100S, U24-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 52.93
Std Dev 3.162
Entry
ampl
0
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2
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4
5
6
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0
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40
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60
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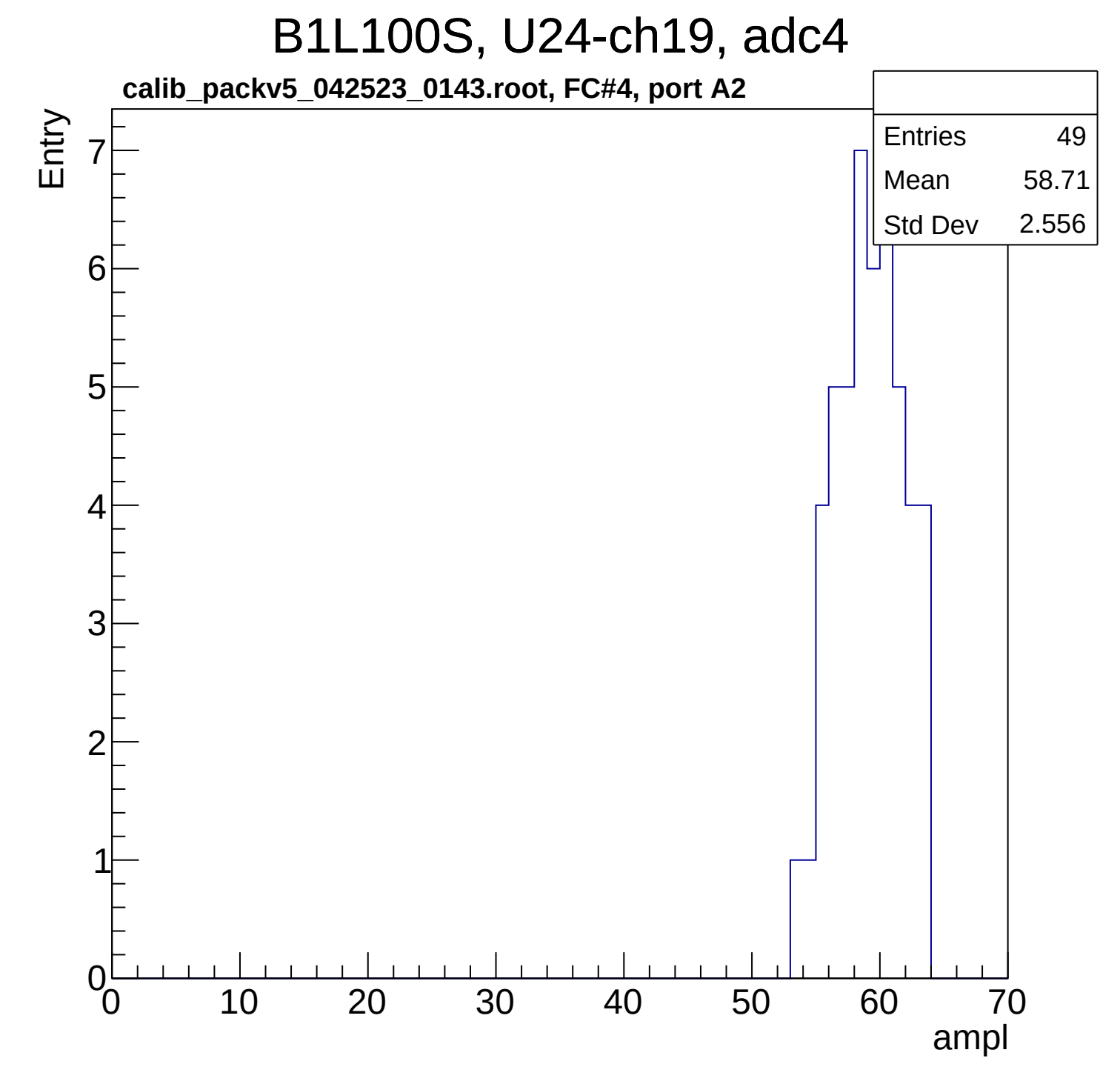
B1L100S, U24-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.71
Std Dev 2.556
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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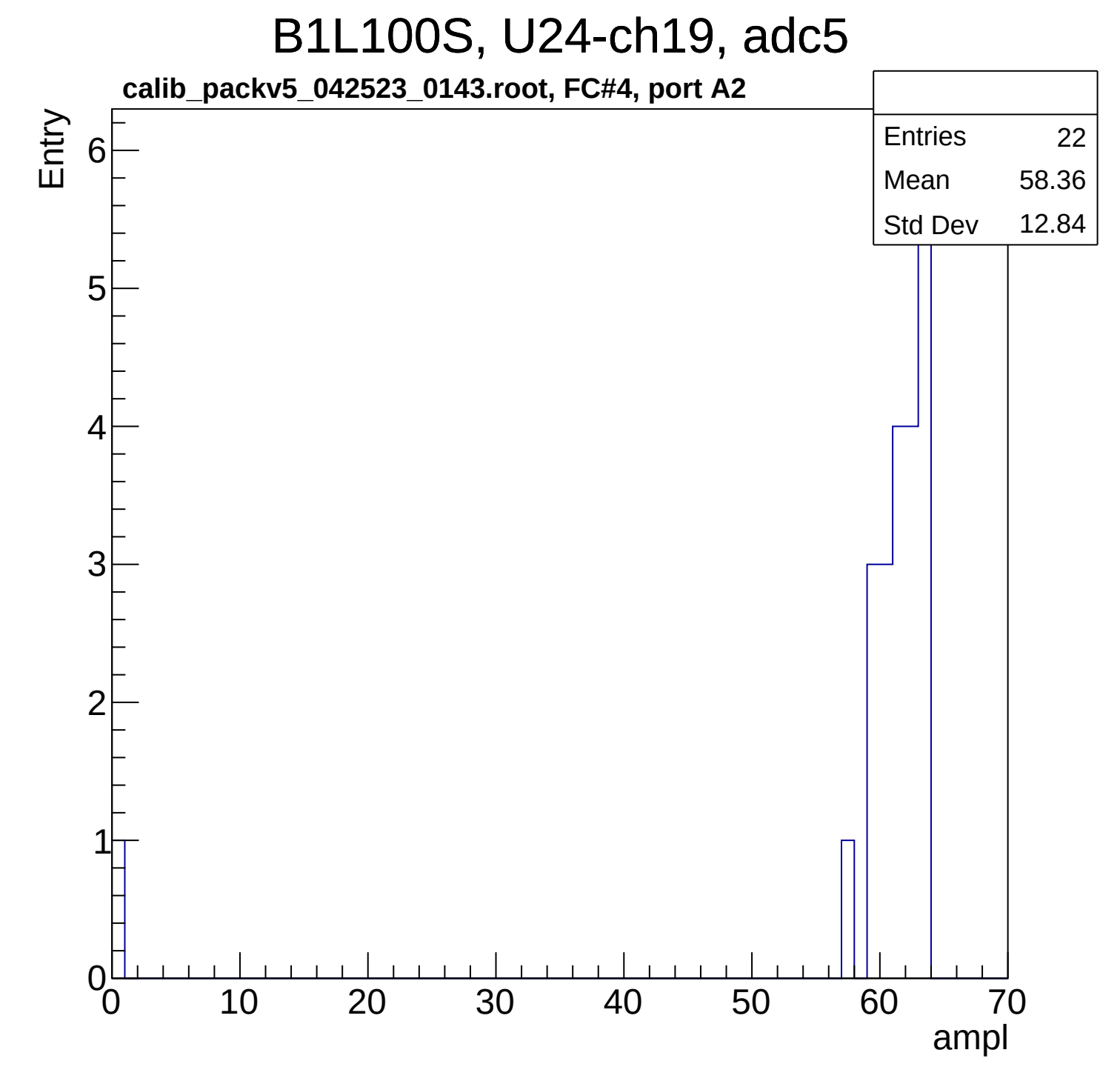
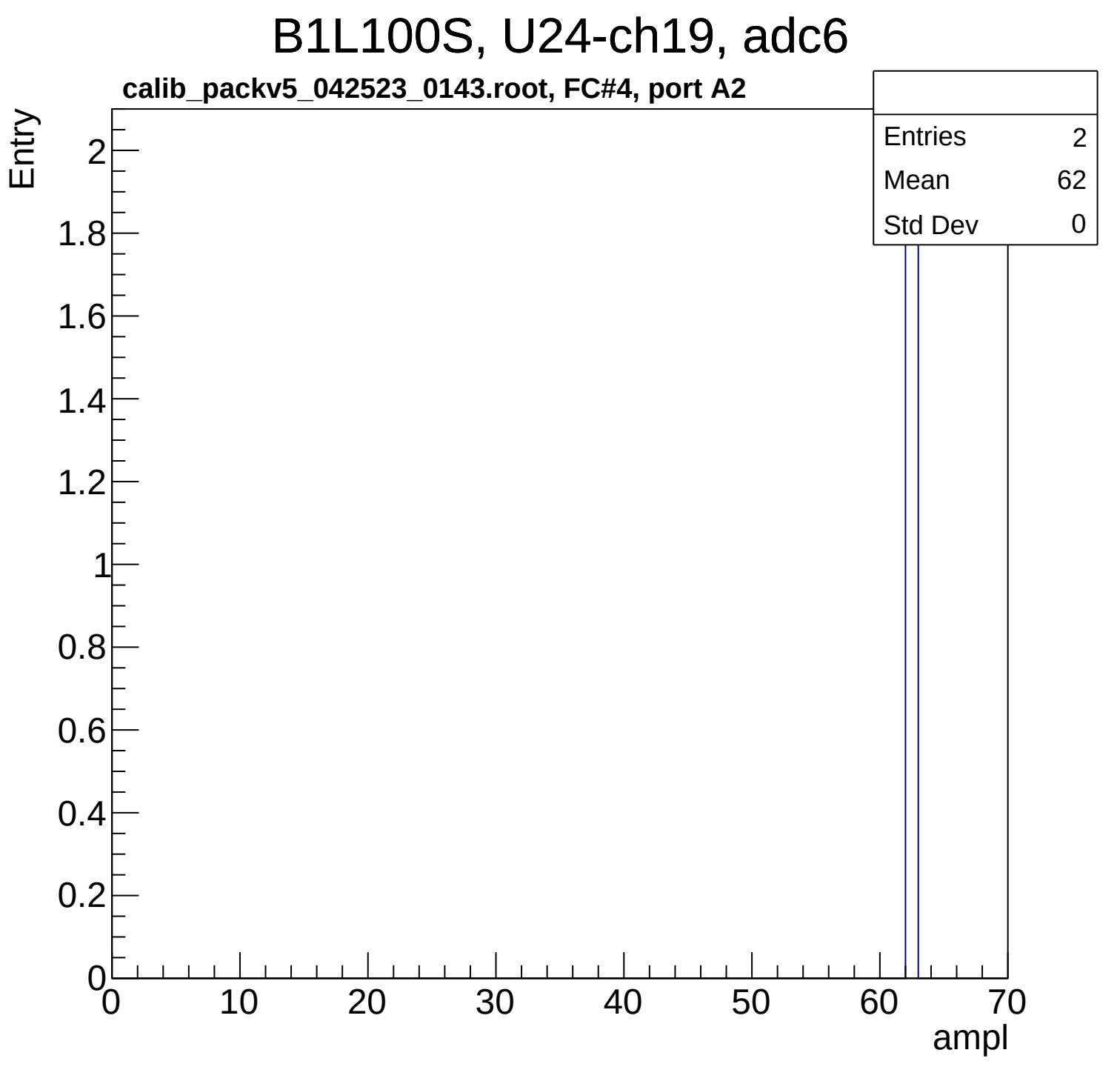
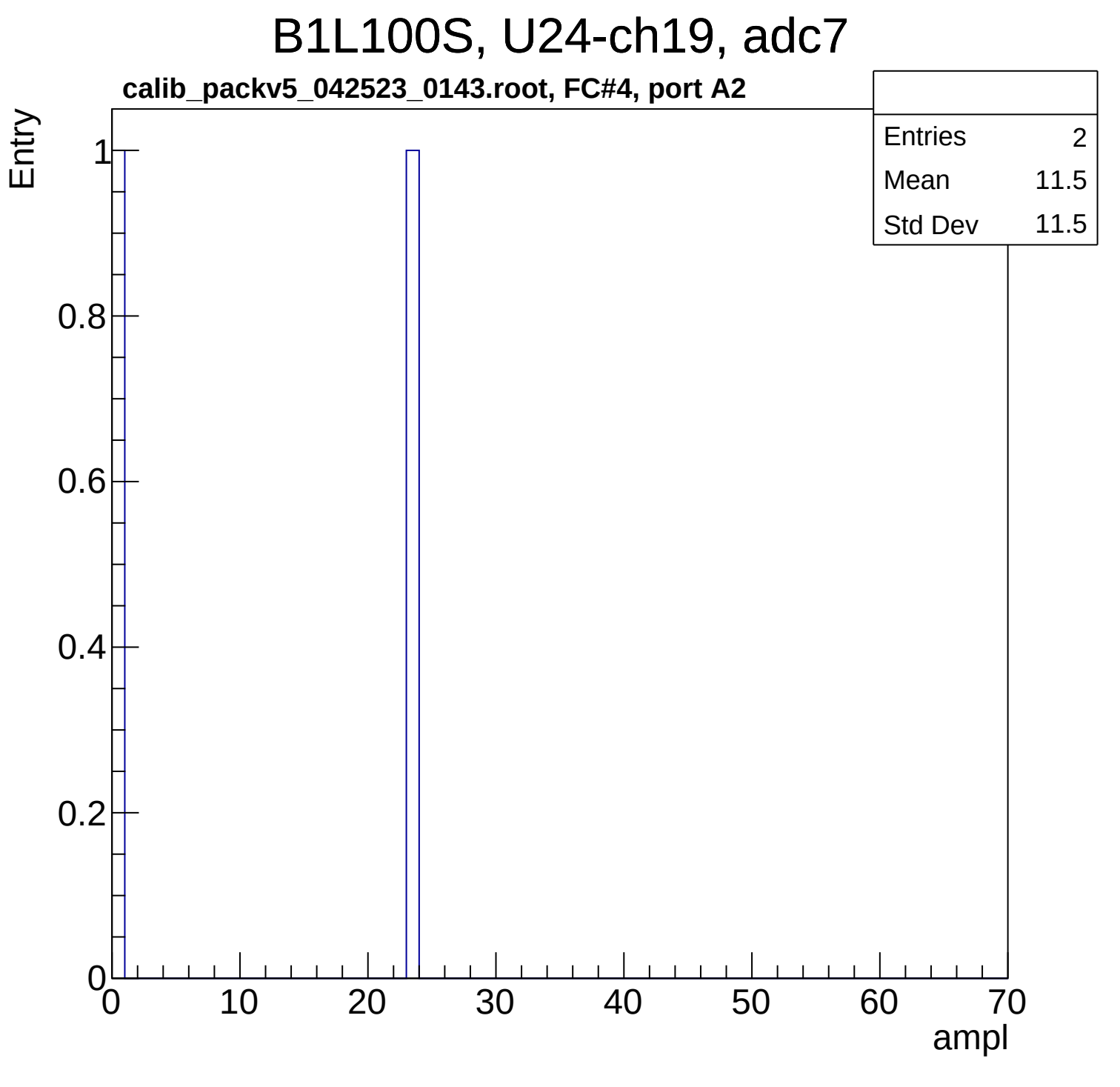
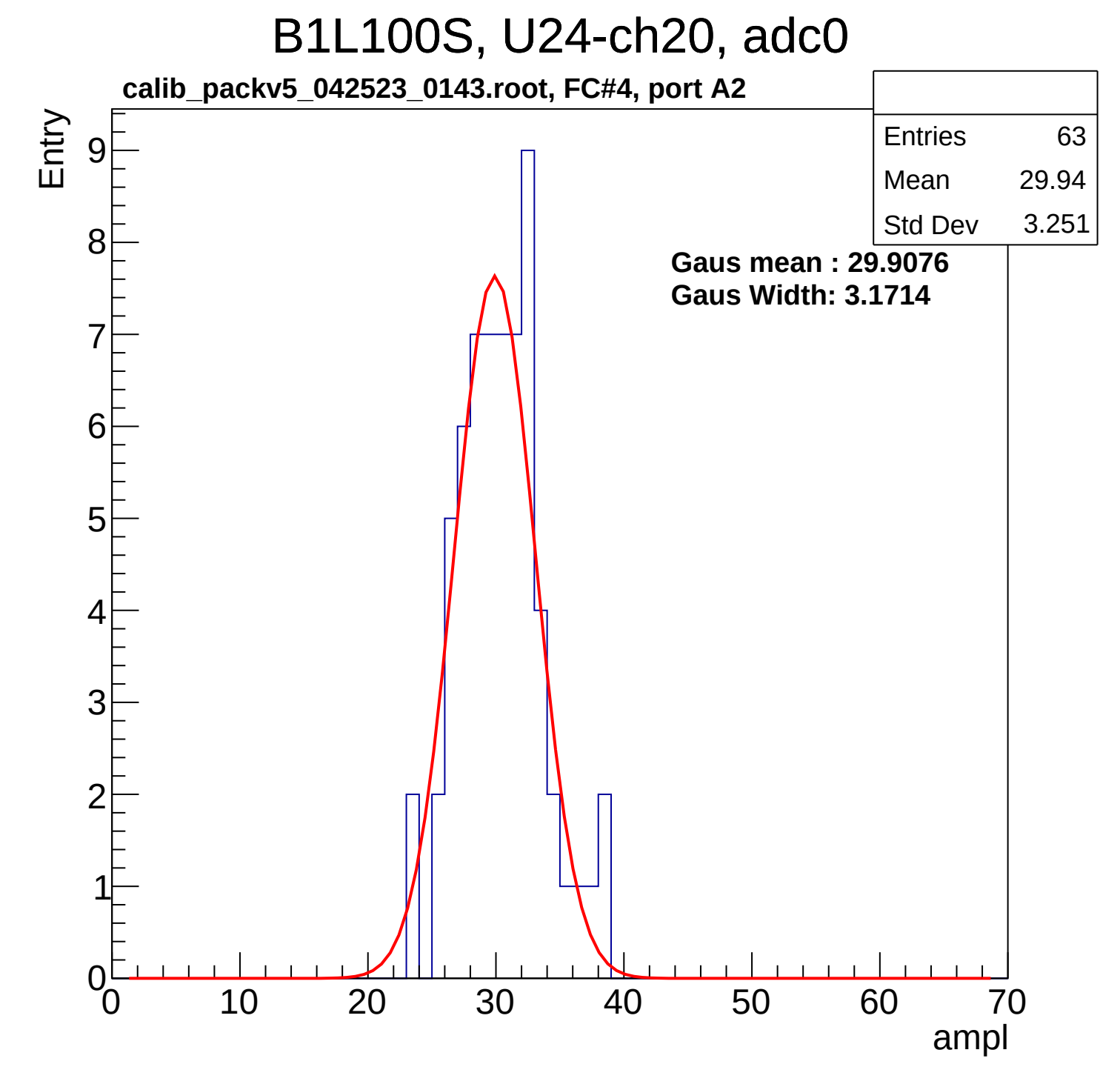

B1L100S, U24-ch19, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 58.36
Std Dev 12.84
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.94
Std Dev 3.251
Gaus mean : 29.9076
Gaus Width: 3.1714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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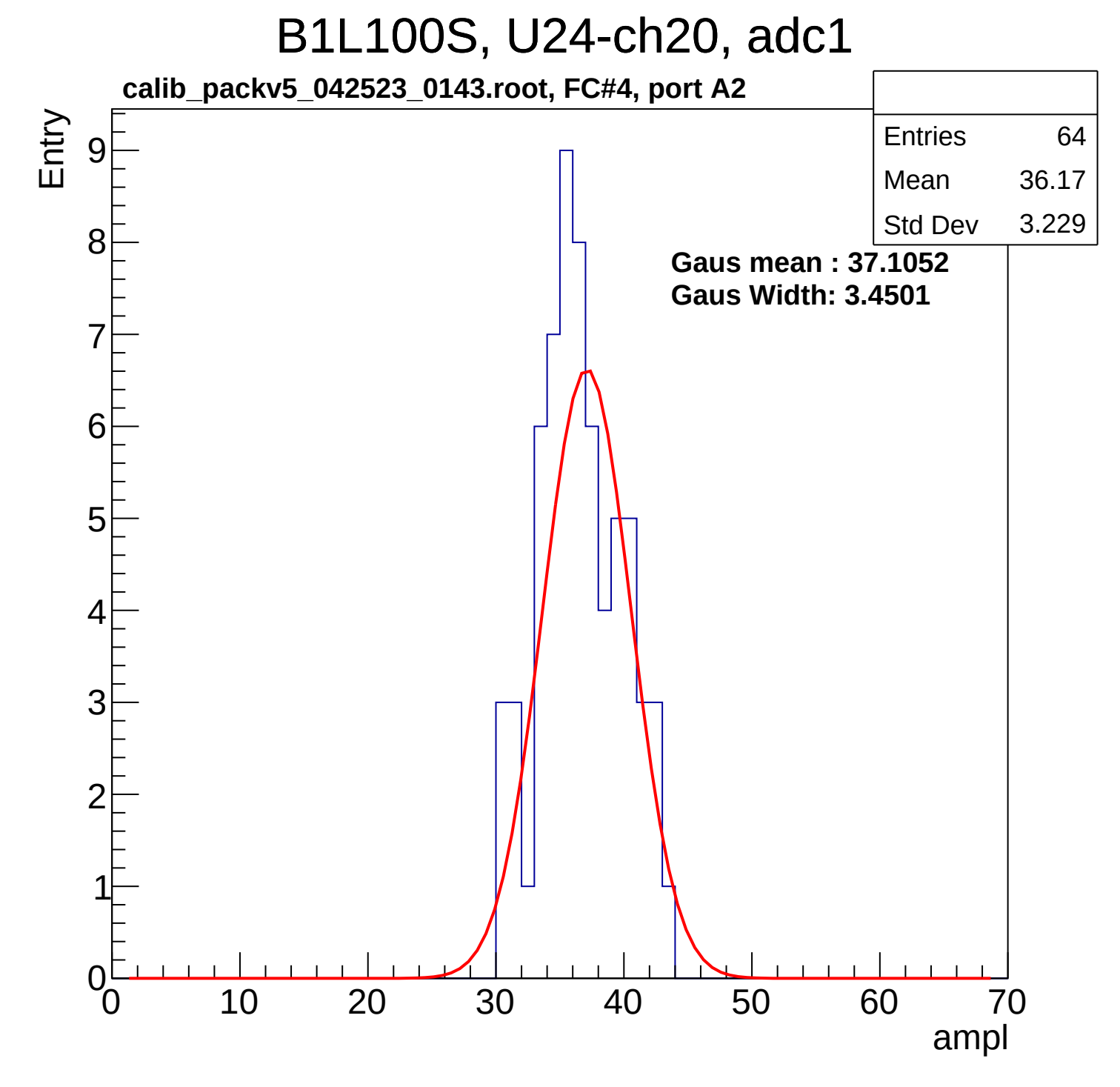
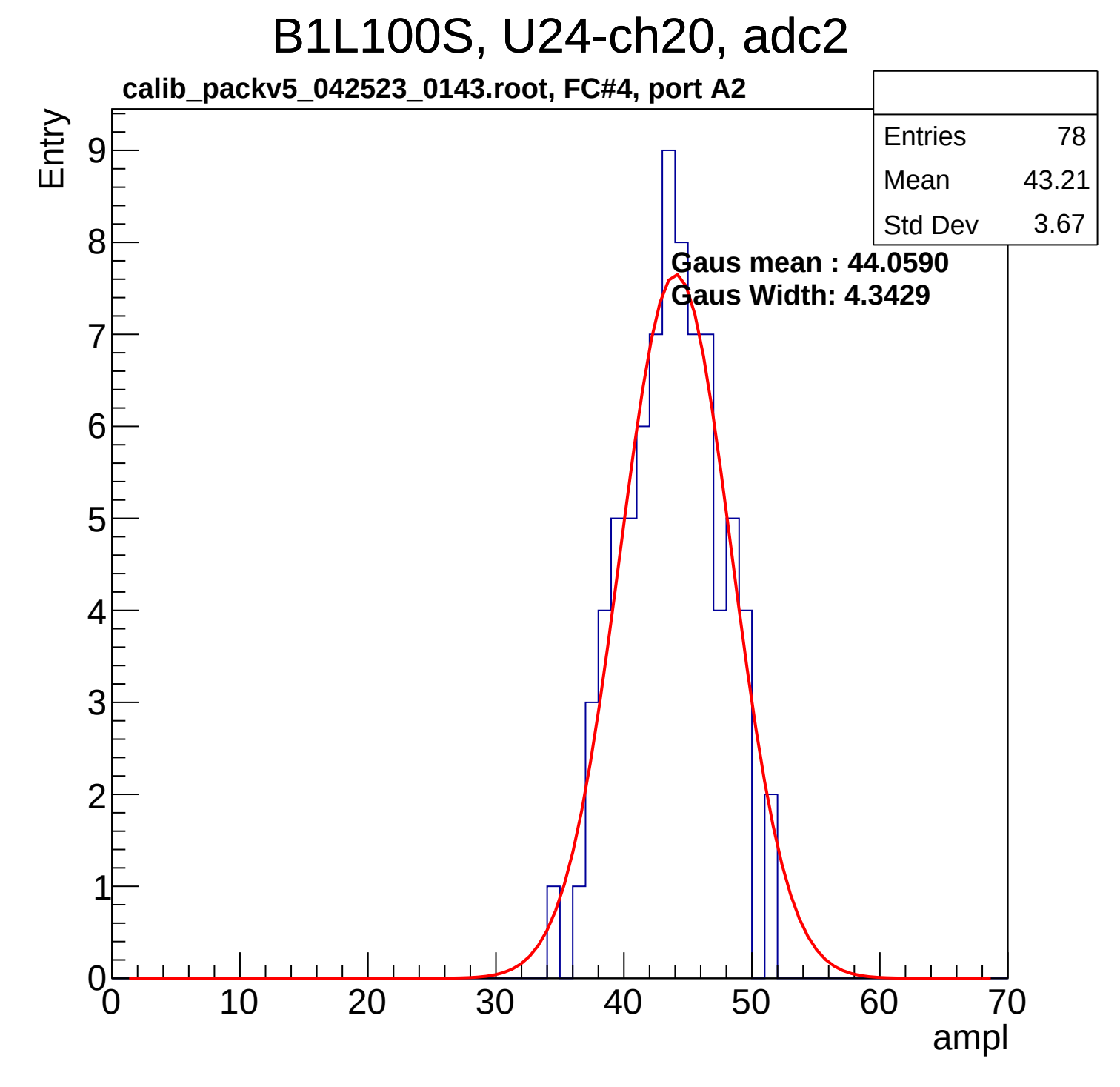

B1L100S, U24-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.17
Std Dev 3.229
Gaus mean : 37.1052
Gaus Width: 3.4501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.21
Std Dev 3.67
Gaus mean : 44.0590
Gaus Width: 4.3429
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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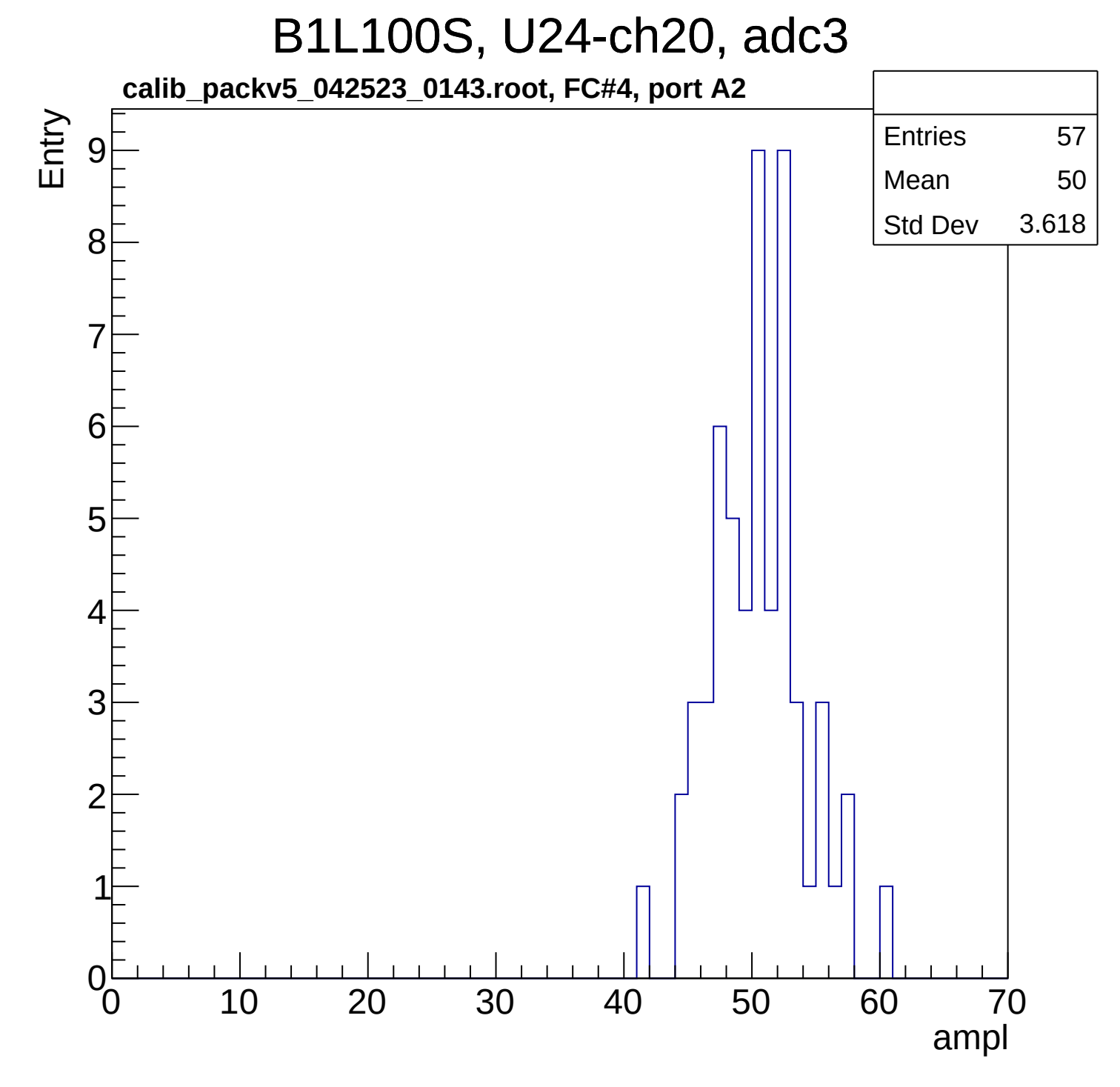
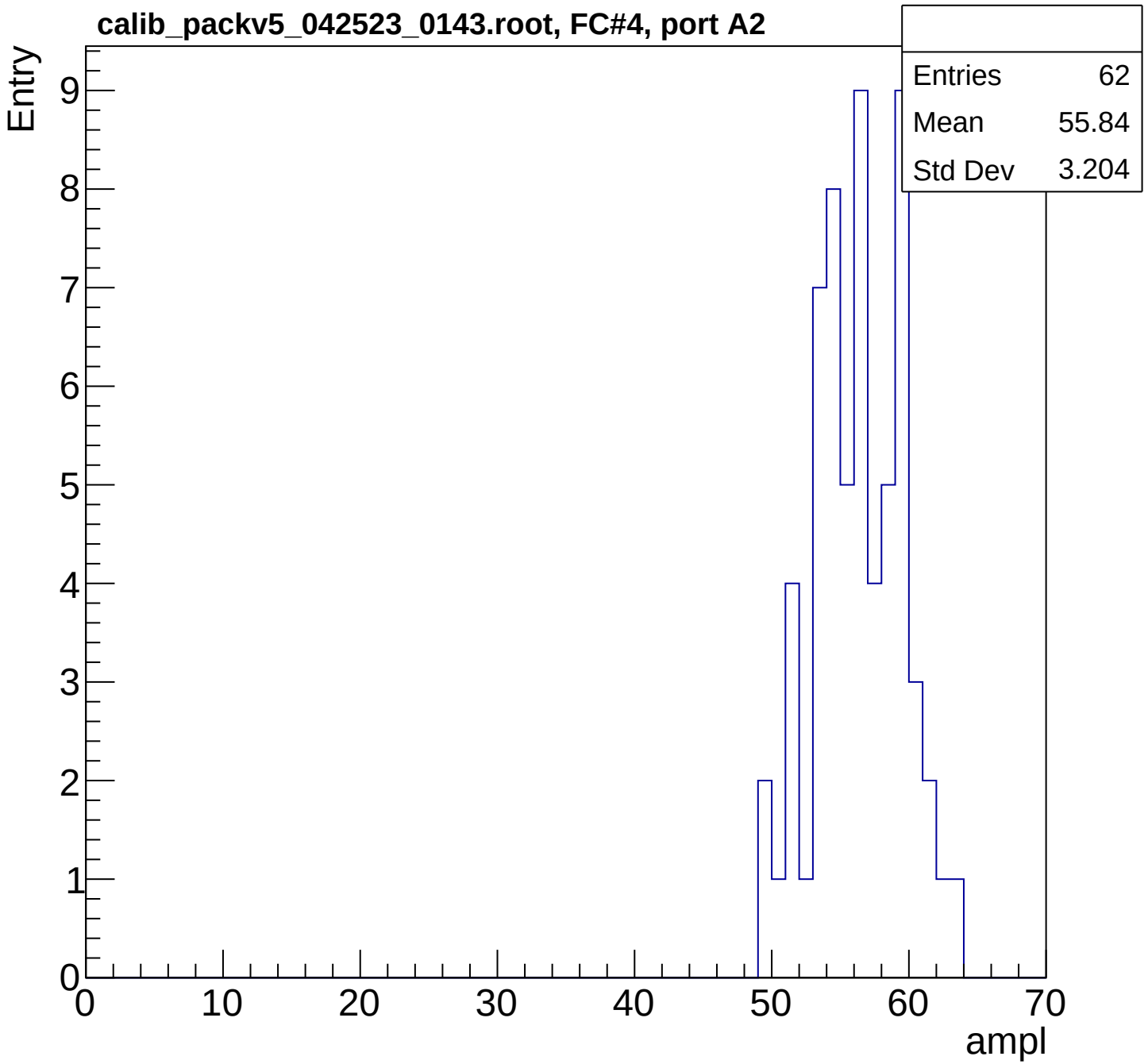

B1L100S, U24-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50
Std Dev 3.618
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.84
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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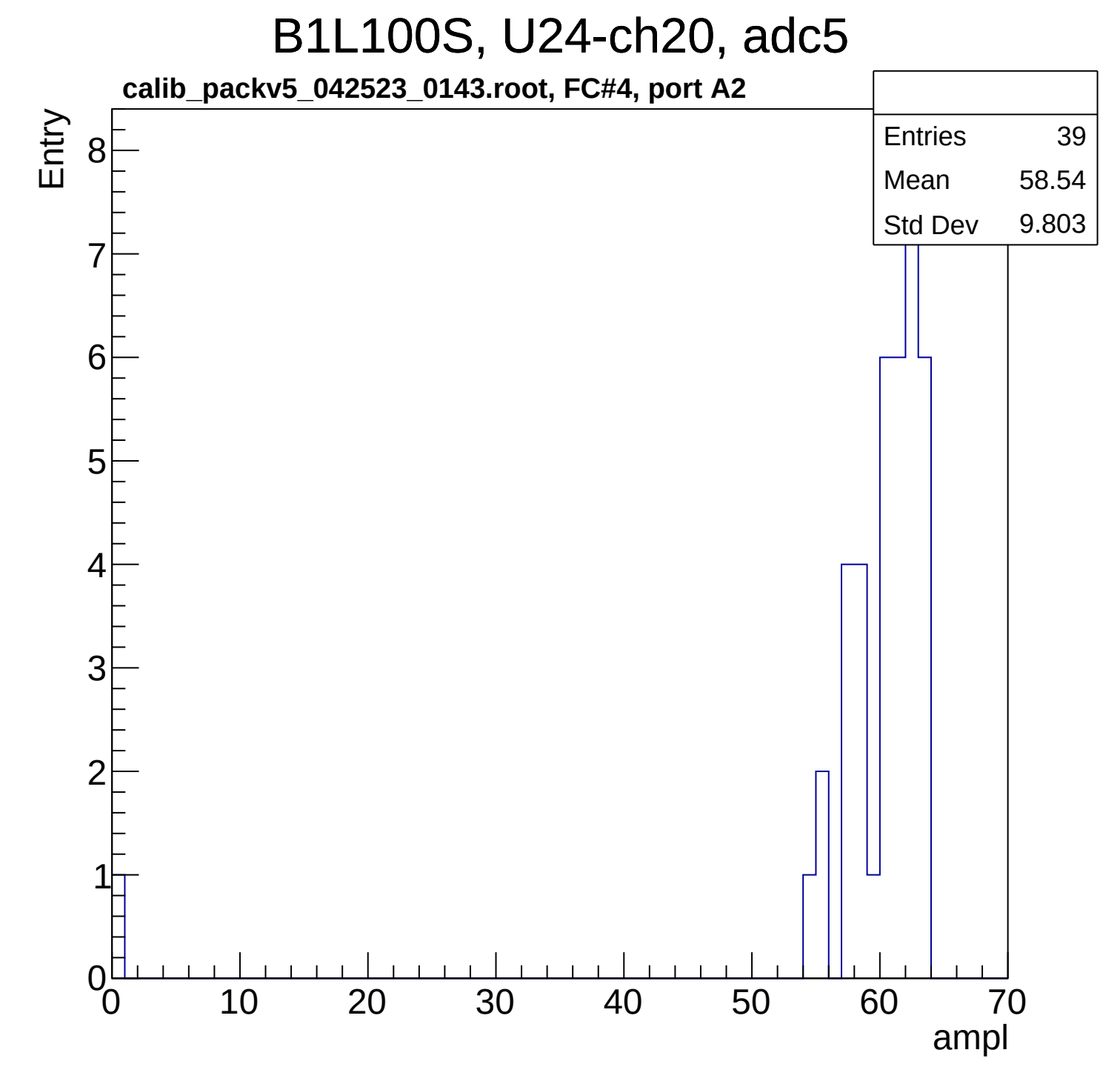
B1L100S, U24-ch20, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.54
Std Dev 9.803
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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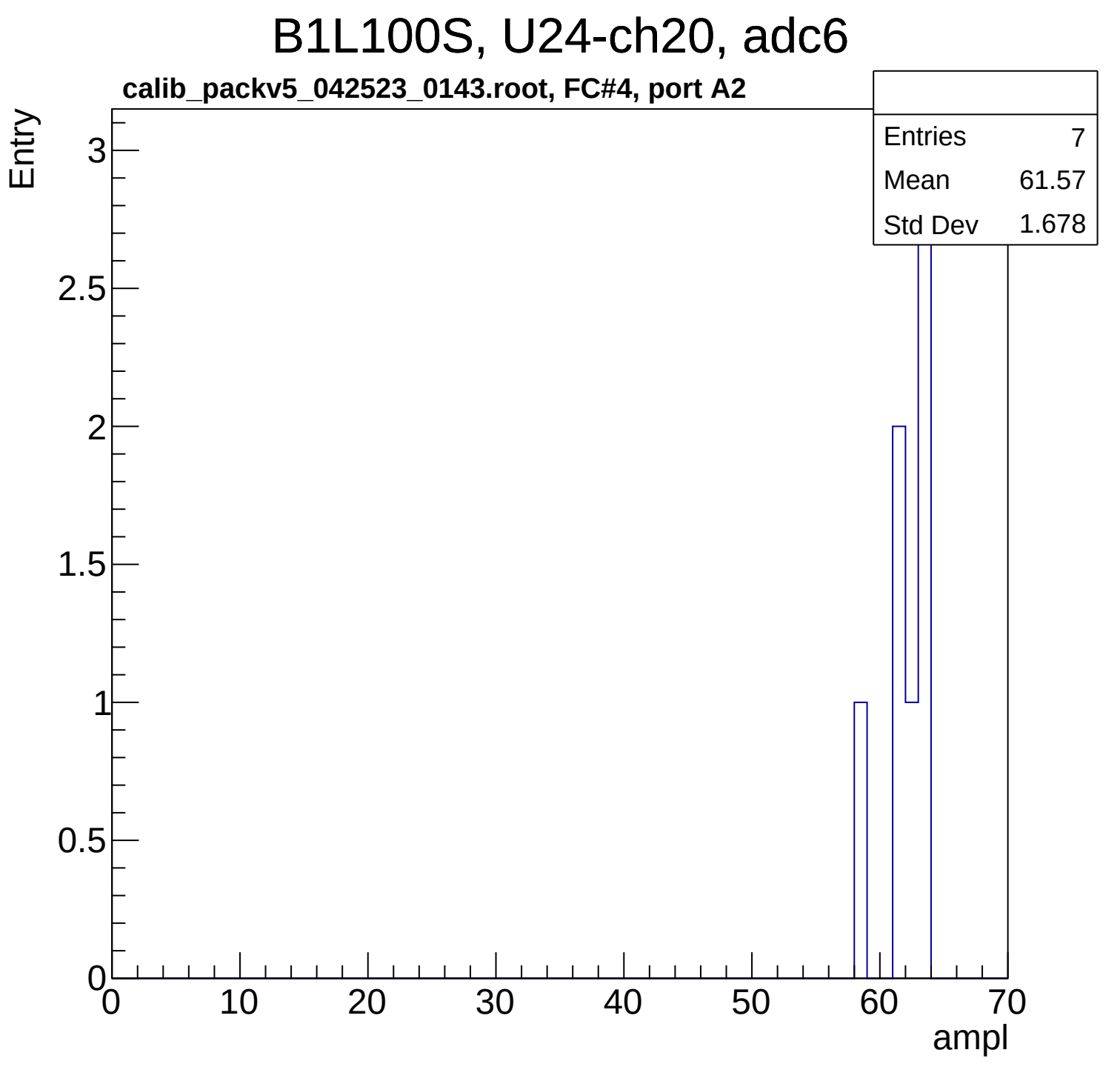
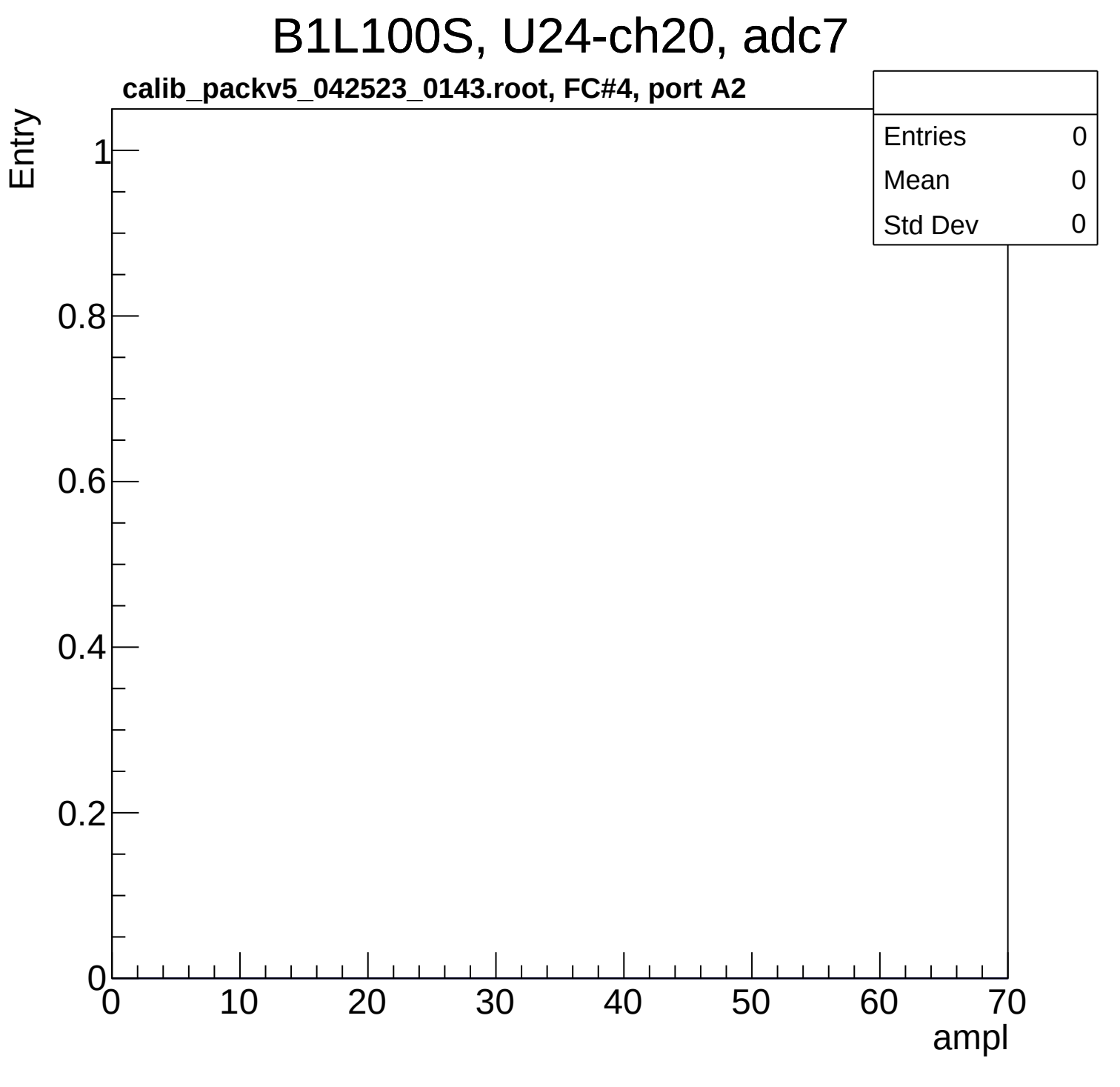
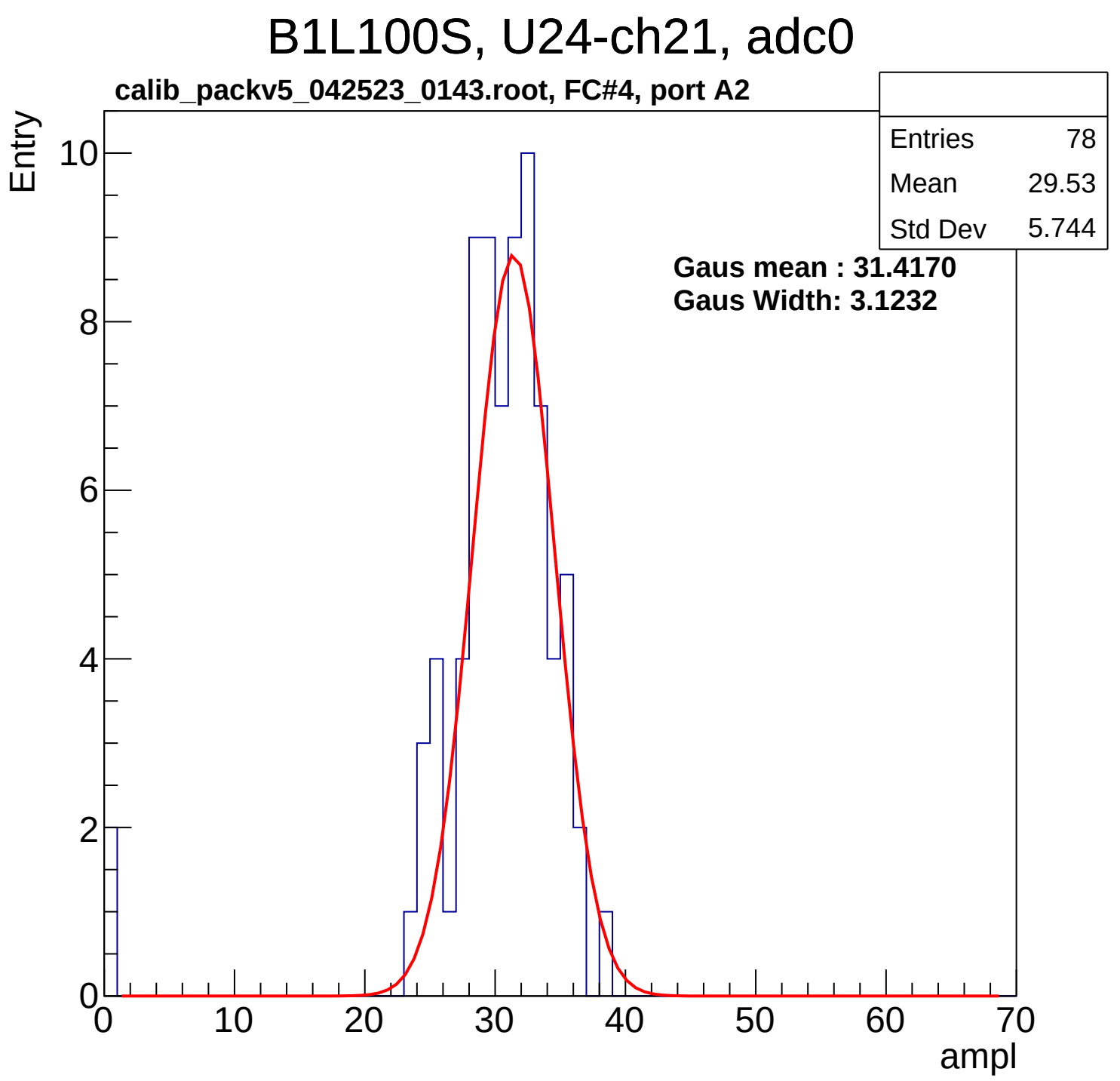
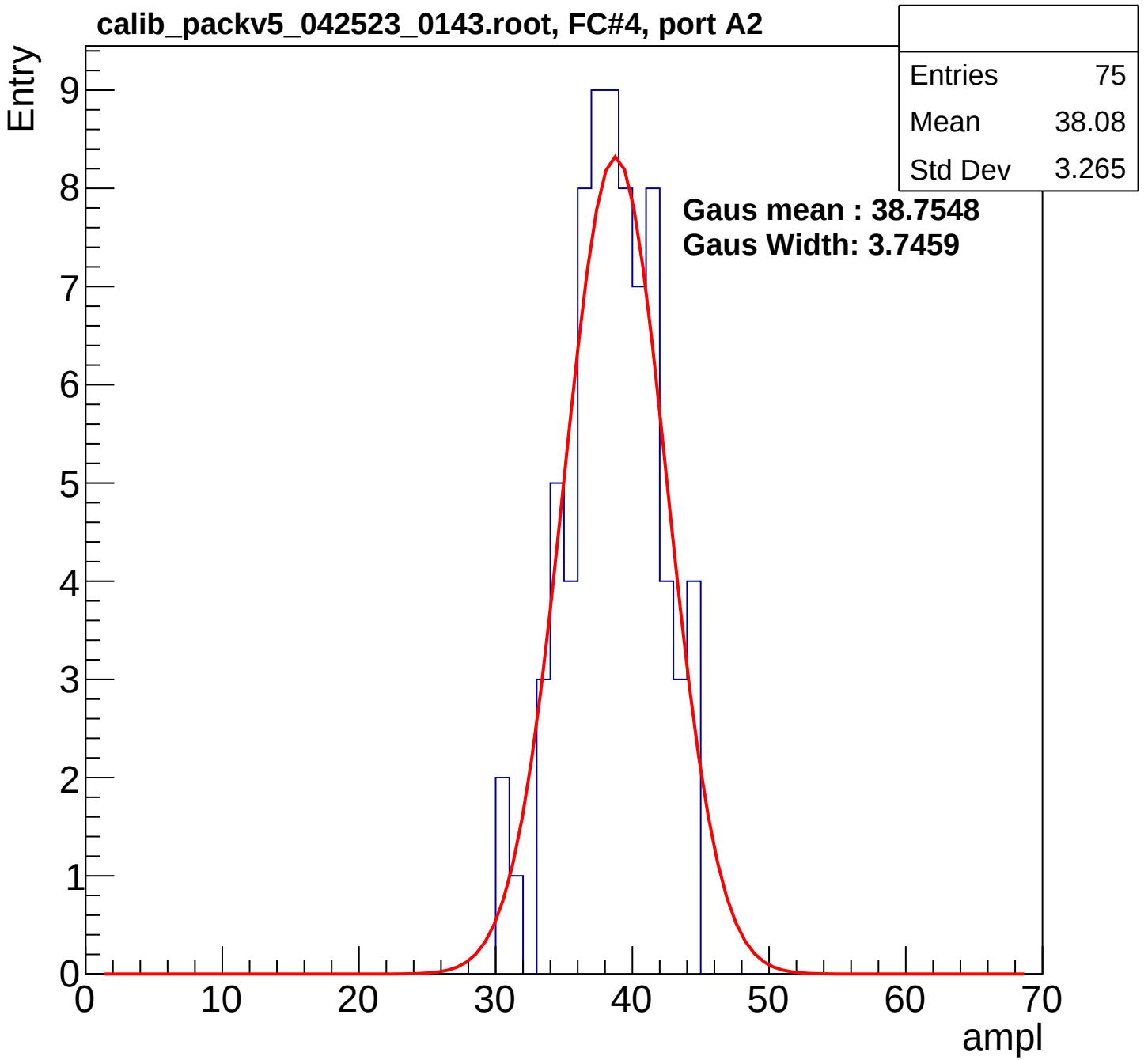
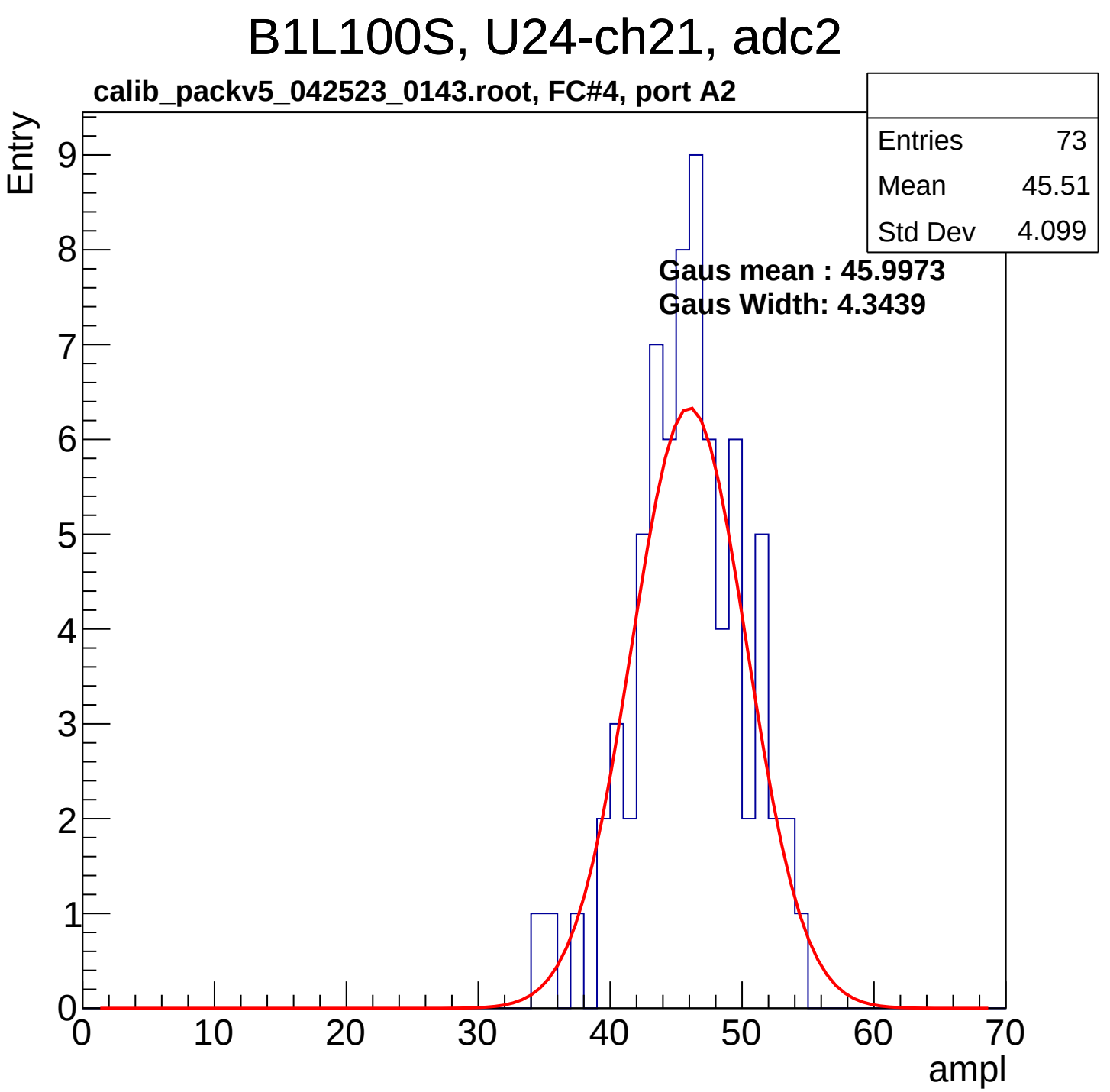

B1L100S, U24-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.57
Std Dev 1.678
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch20, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.53
Std Dev 5.744
Gaus mean : 31.4170
Gaus Width: 3.1232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 38.08
Std Dev 3.265
Gaus mean : 38.7548
Gaus Width: 3.7459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 45.51
Std Dev 4.099
Gaus mean : 45.9973
Gaus Width: 4.3439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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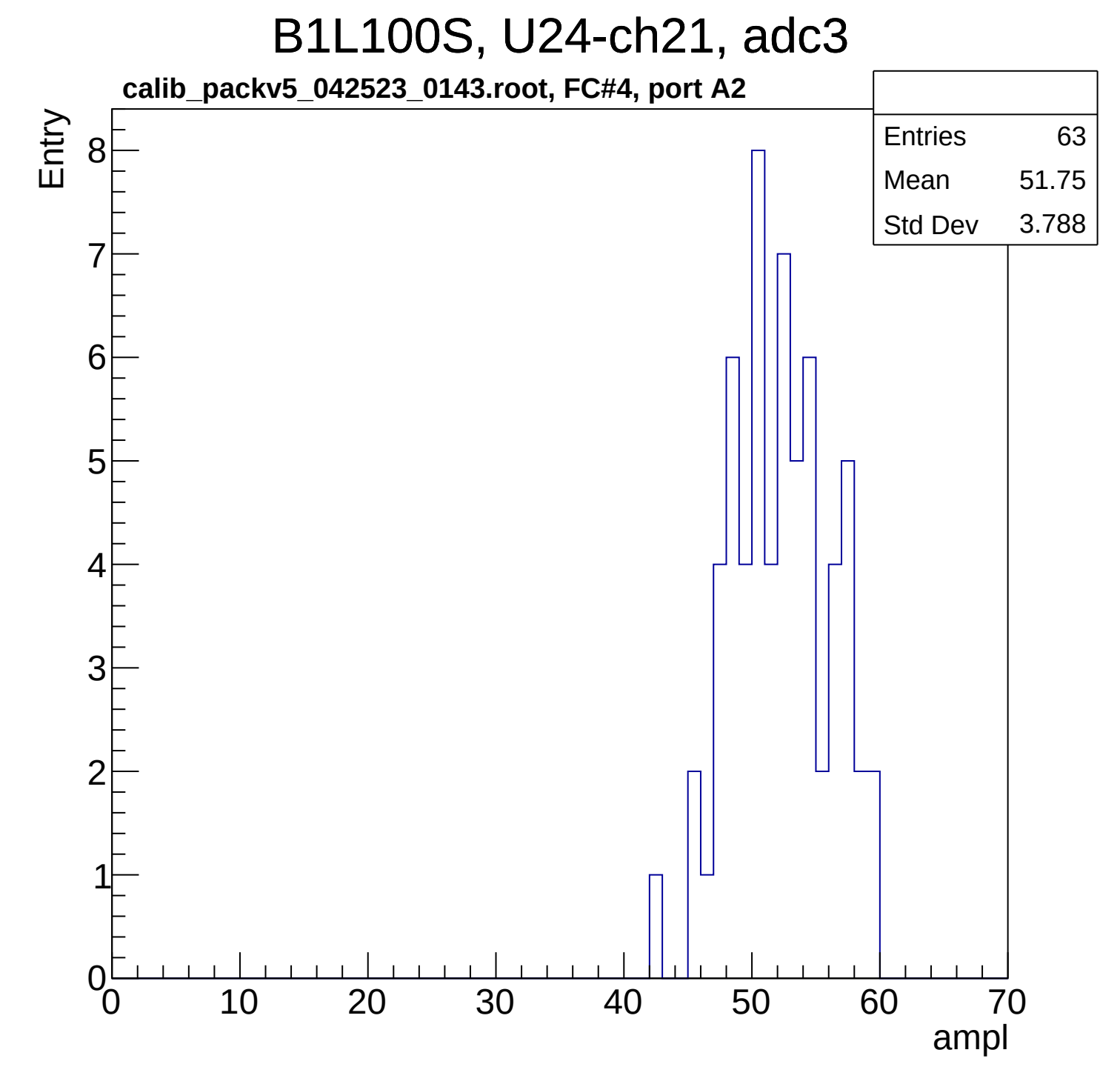
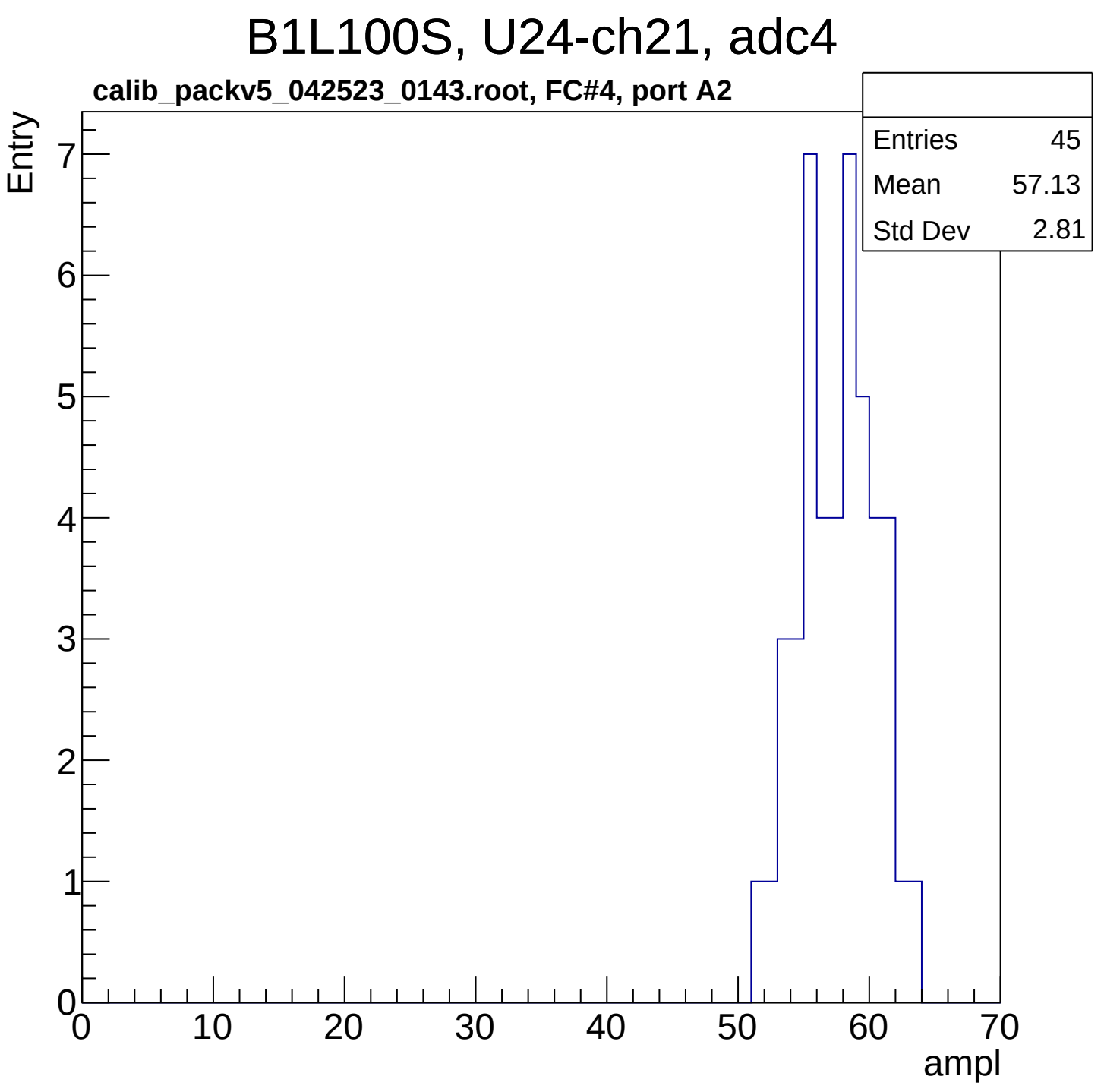

B1L100S, U24-ch21, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.75
Std Dev 3.788
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch21, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 57.13
Std Dev 2.81
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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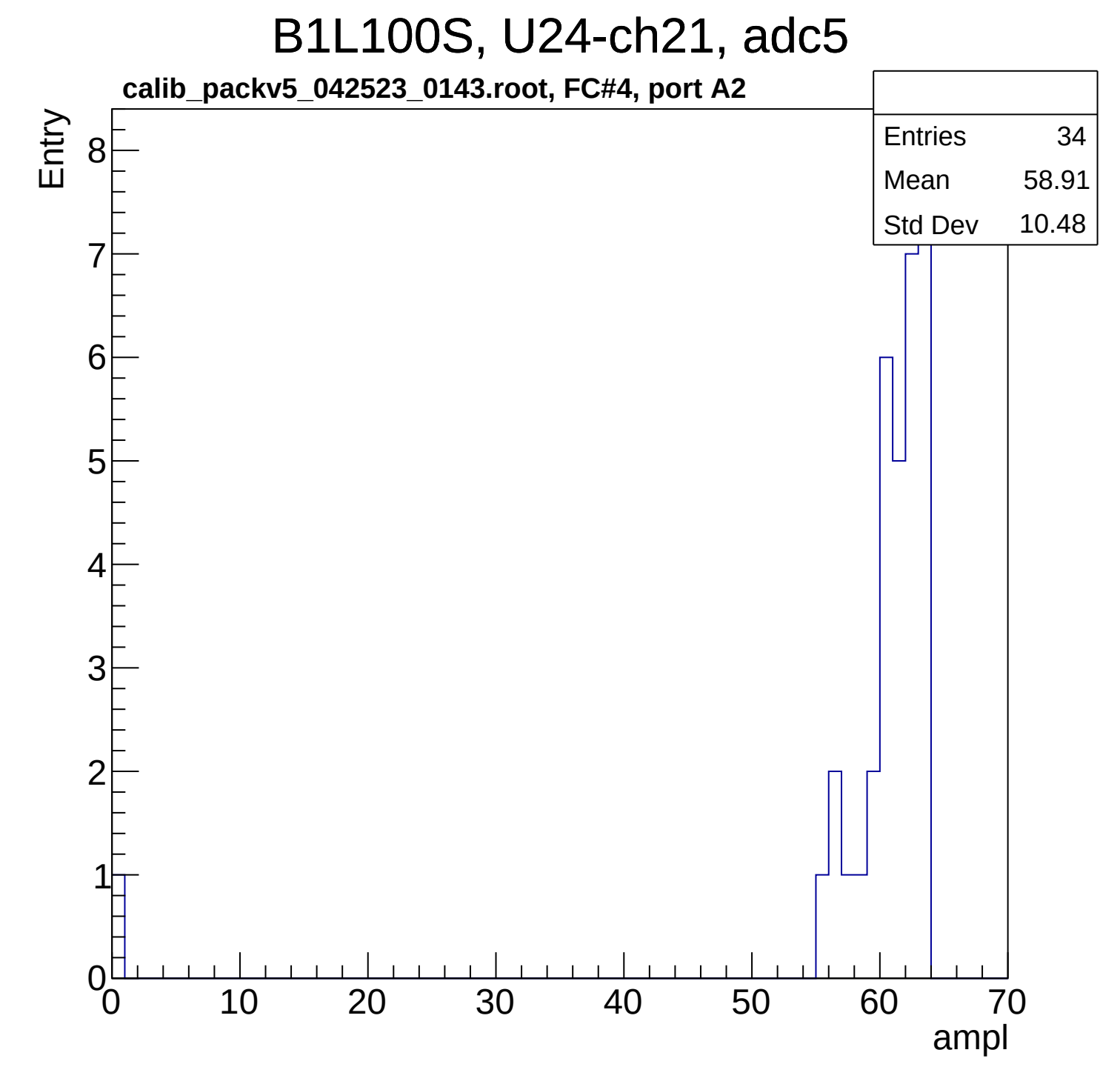
40
50
60
70


B1L100S, U24-ch21, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.91
Std Dev 10.48
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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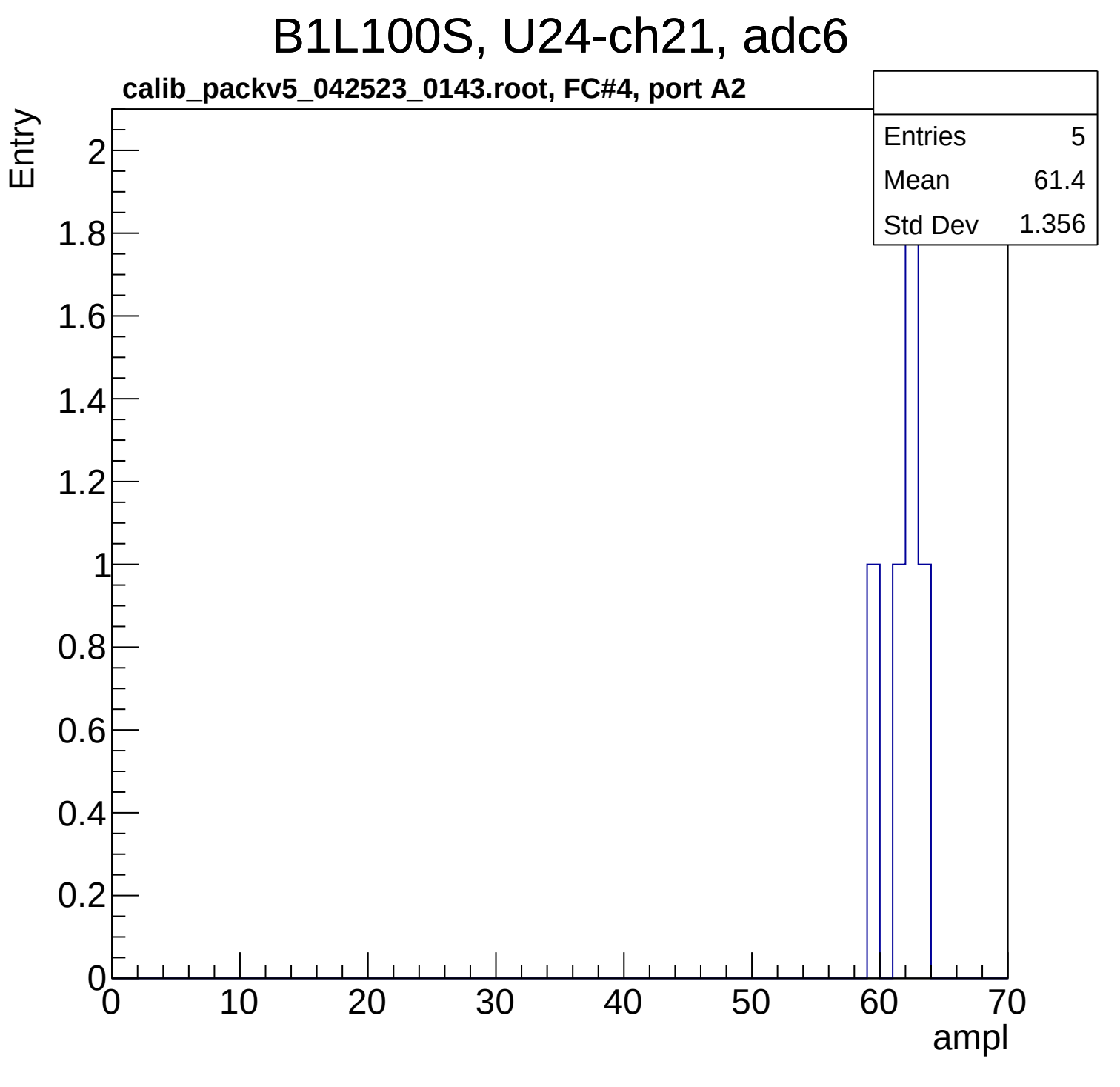
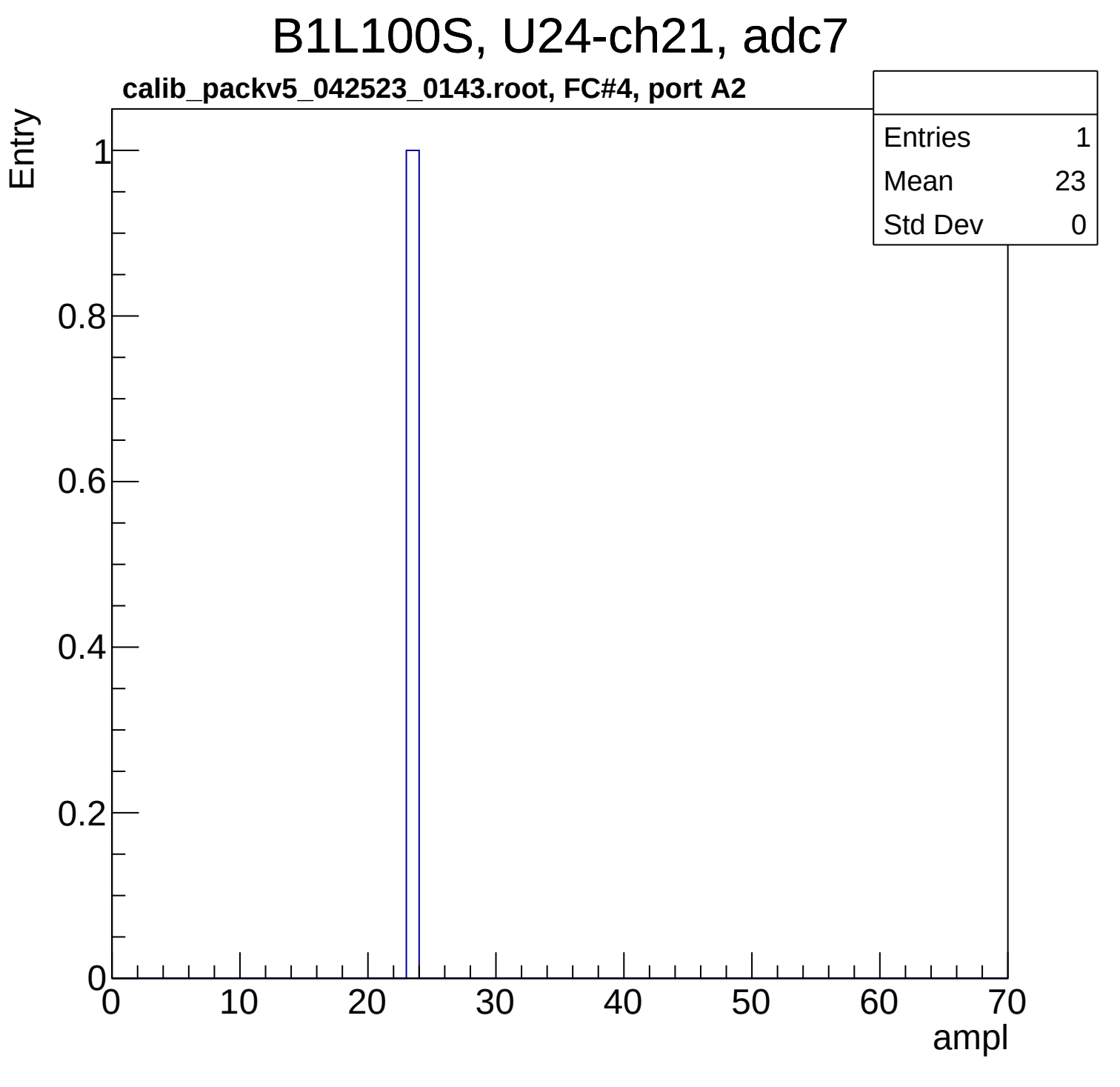
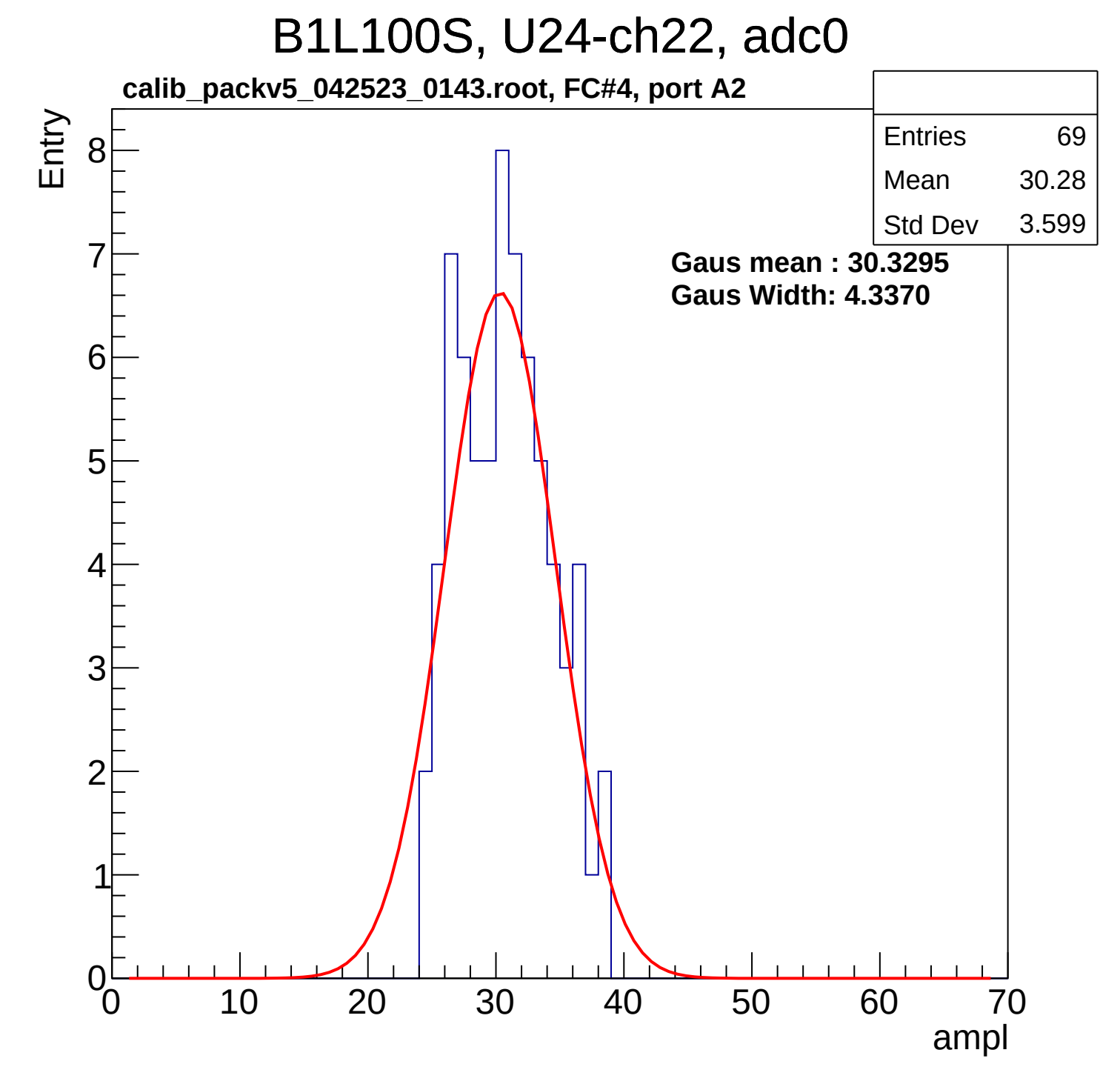
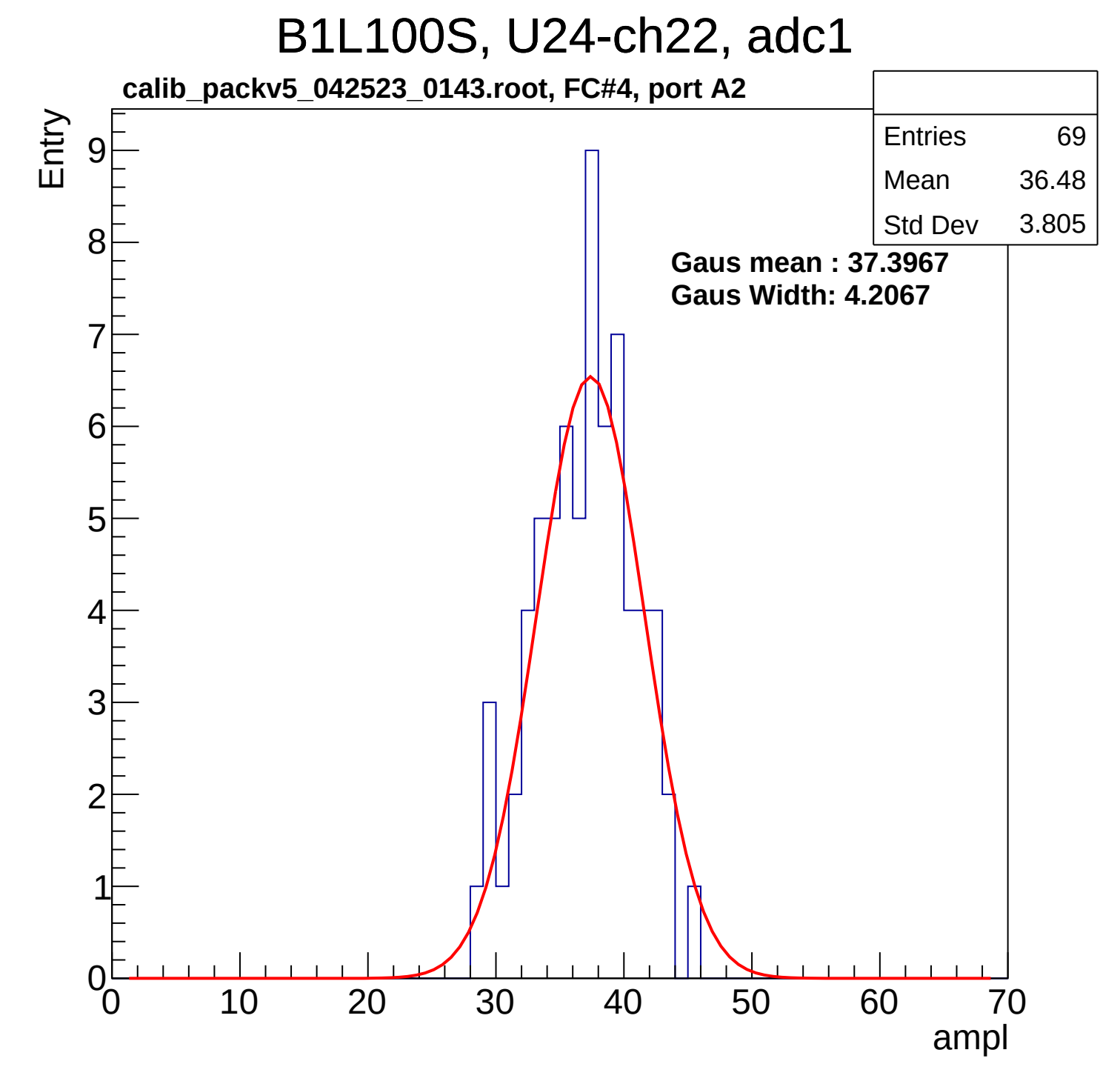
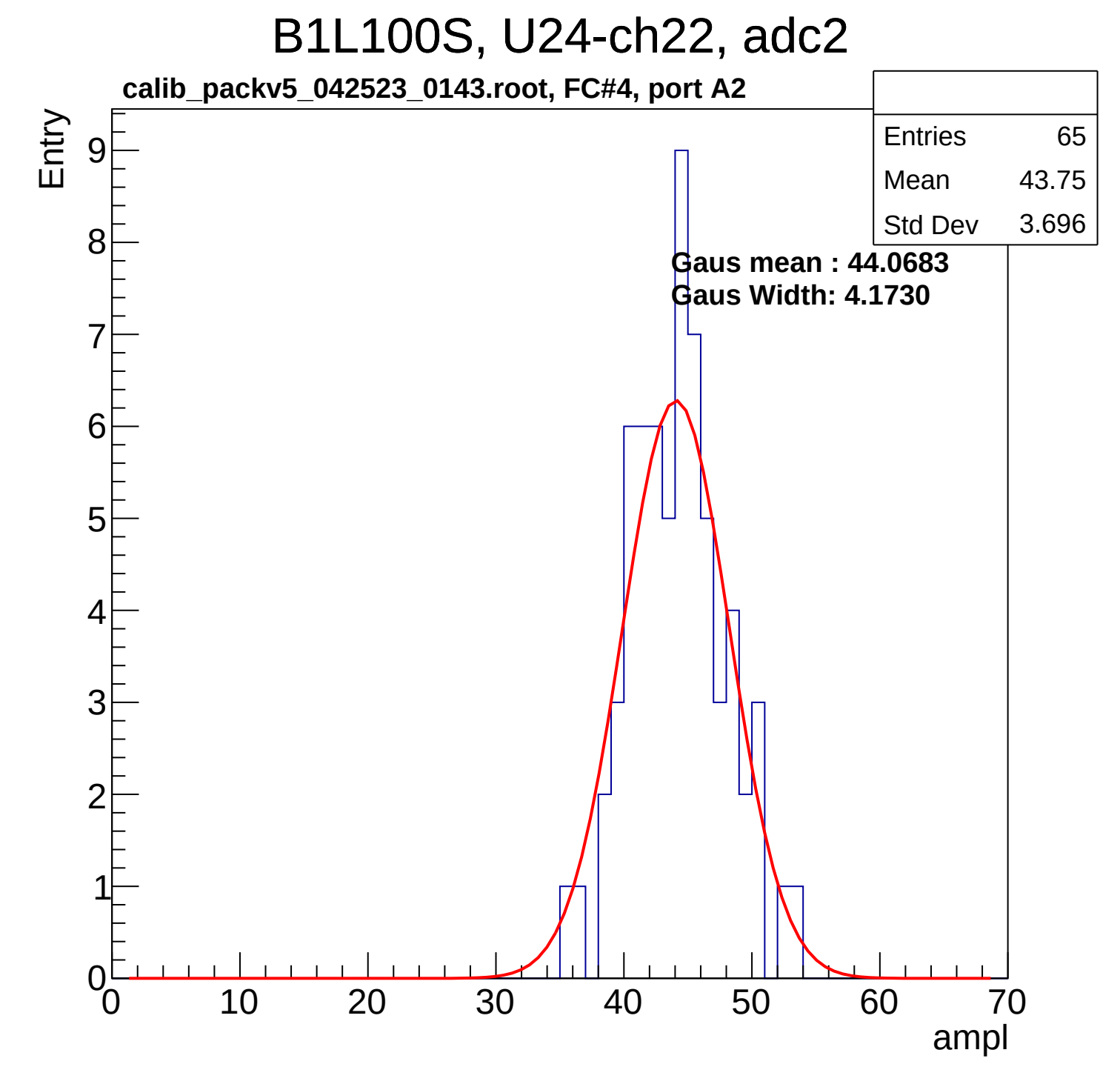

B1L100S, U24-ch21, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch22, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 30.28
Std Dev 3.599
Gaus mean : 30.3295
Gaus Width: 4.3370
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.48
Std Dev 3.805
Gaus mean : 37.3967
Gaus Width: 4.2067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.75
Std Dev 3.696
Gaus mean : 44.0683
Gaus Width: 4.1730
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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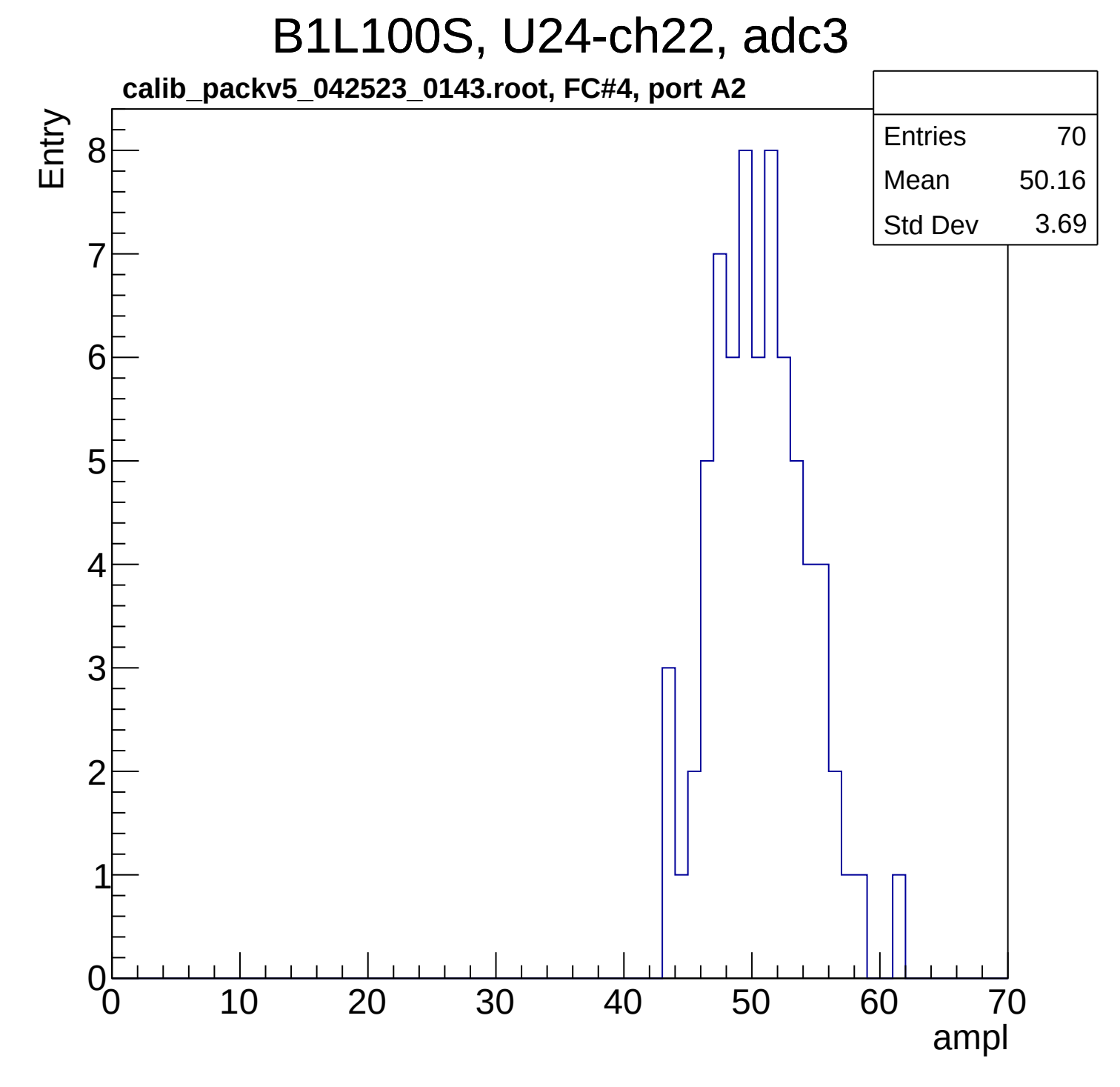
B1L100S, U24-ch22, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.16
Std Dev 3.69
Entry
ampl
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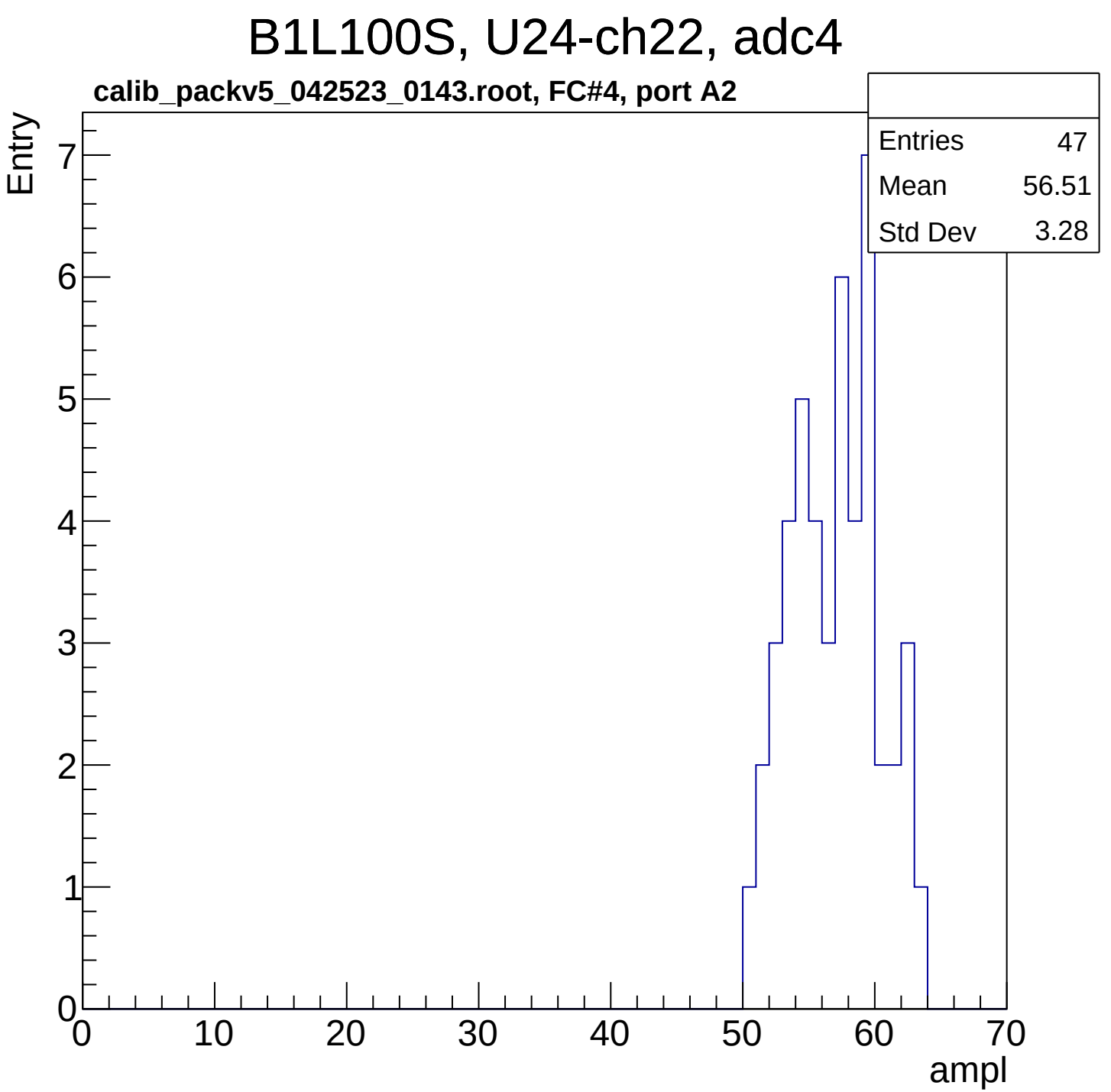
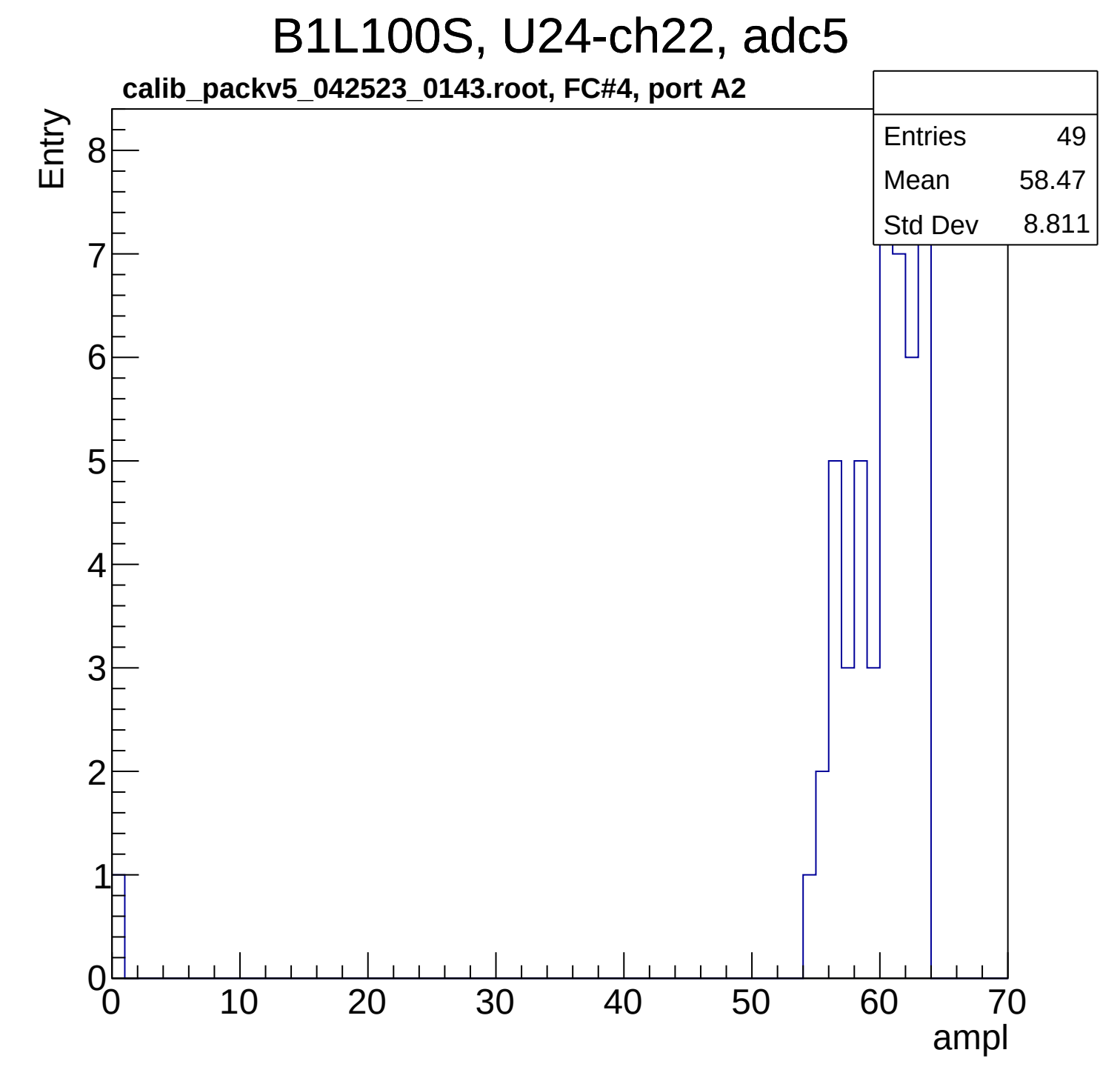
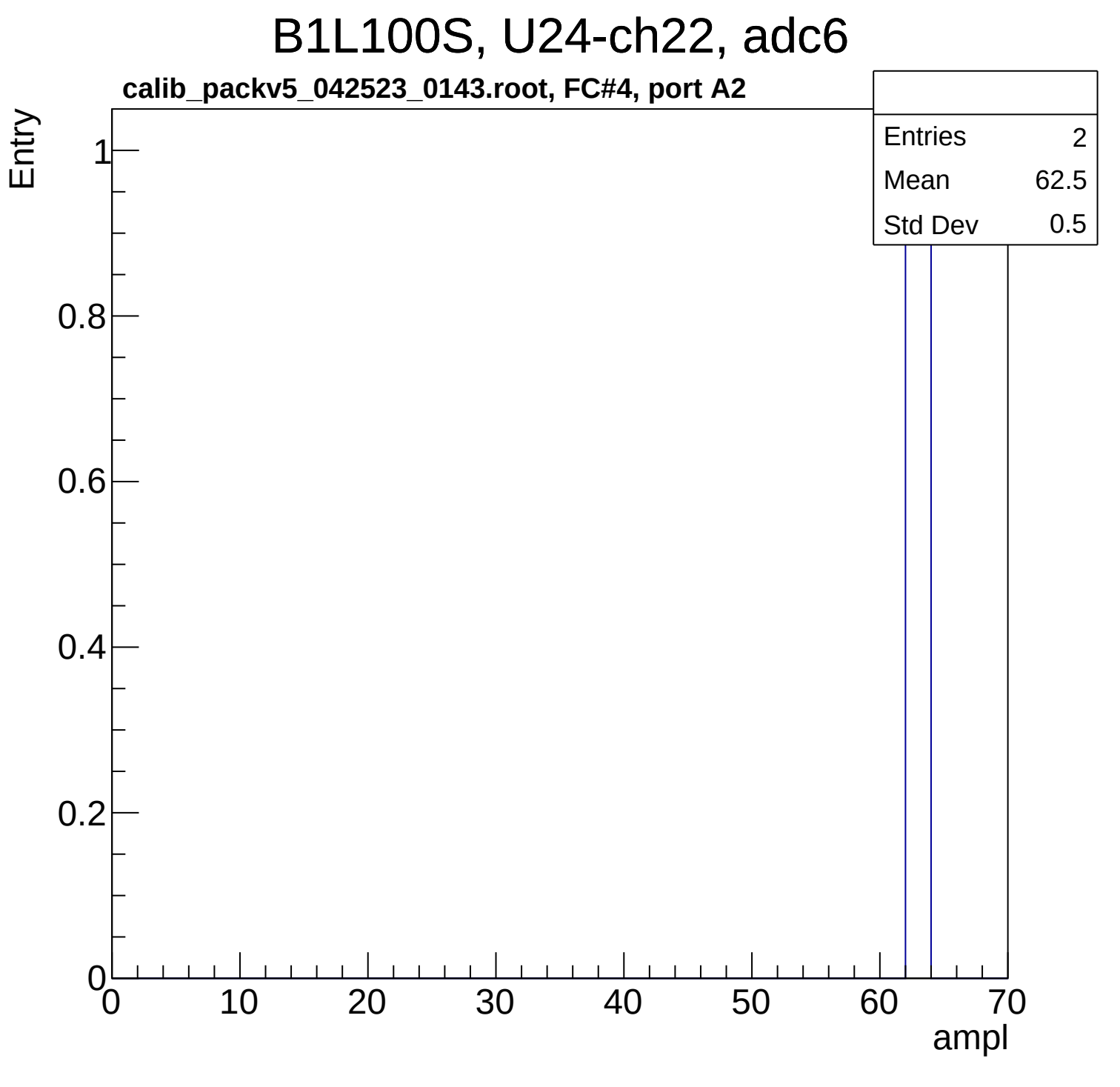
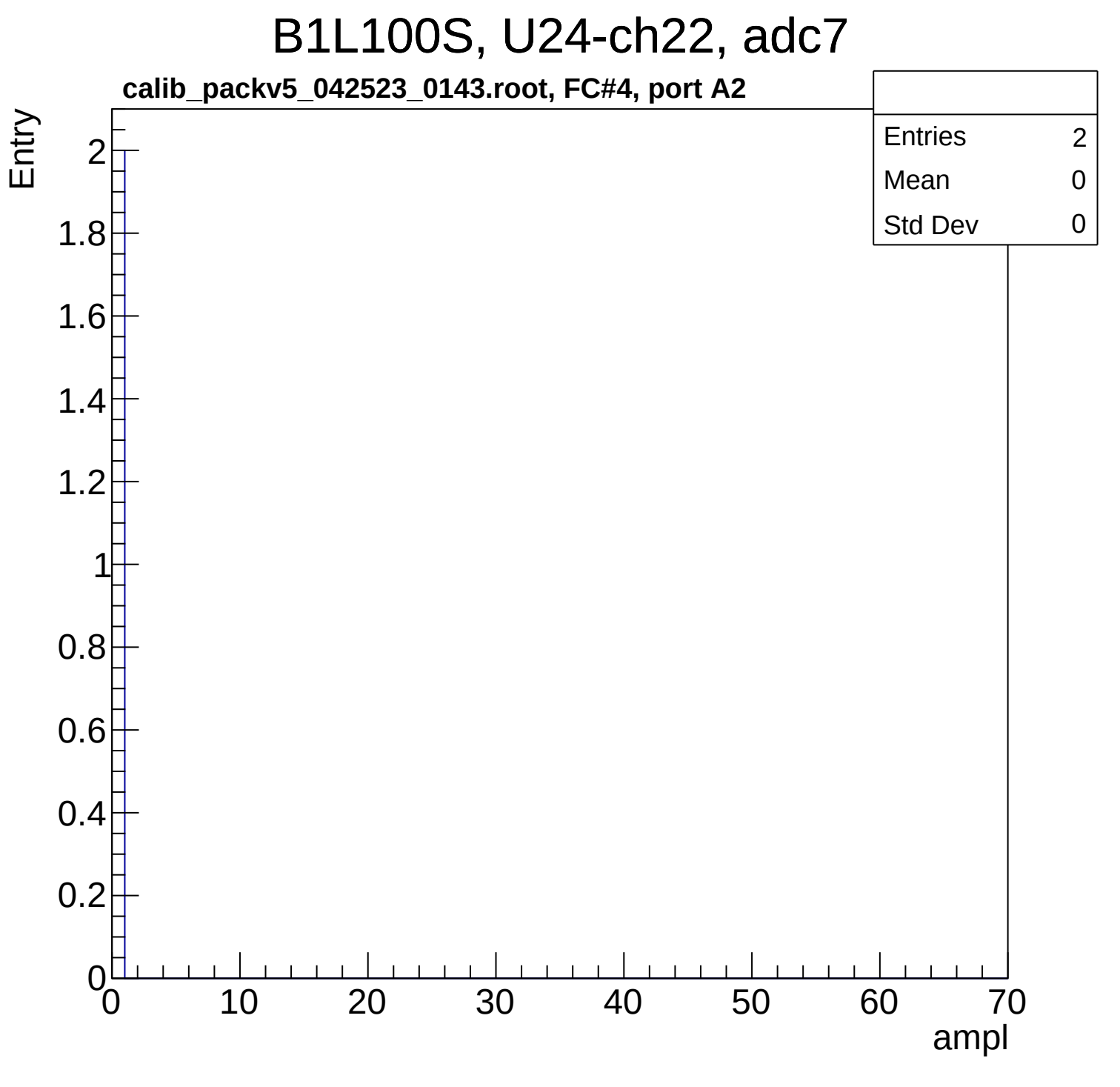
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch22, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 56.51
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch22, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.47
Std Dev 8.811
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch22, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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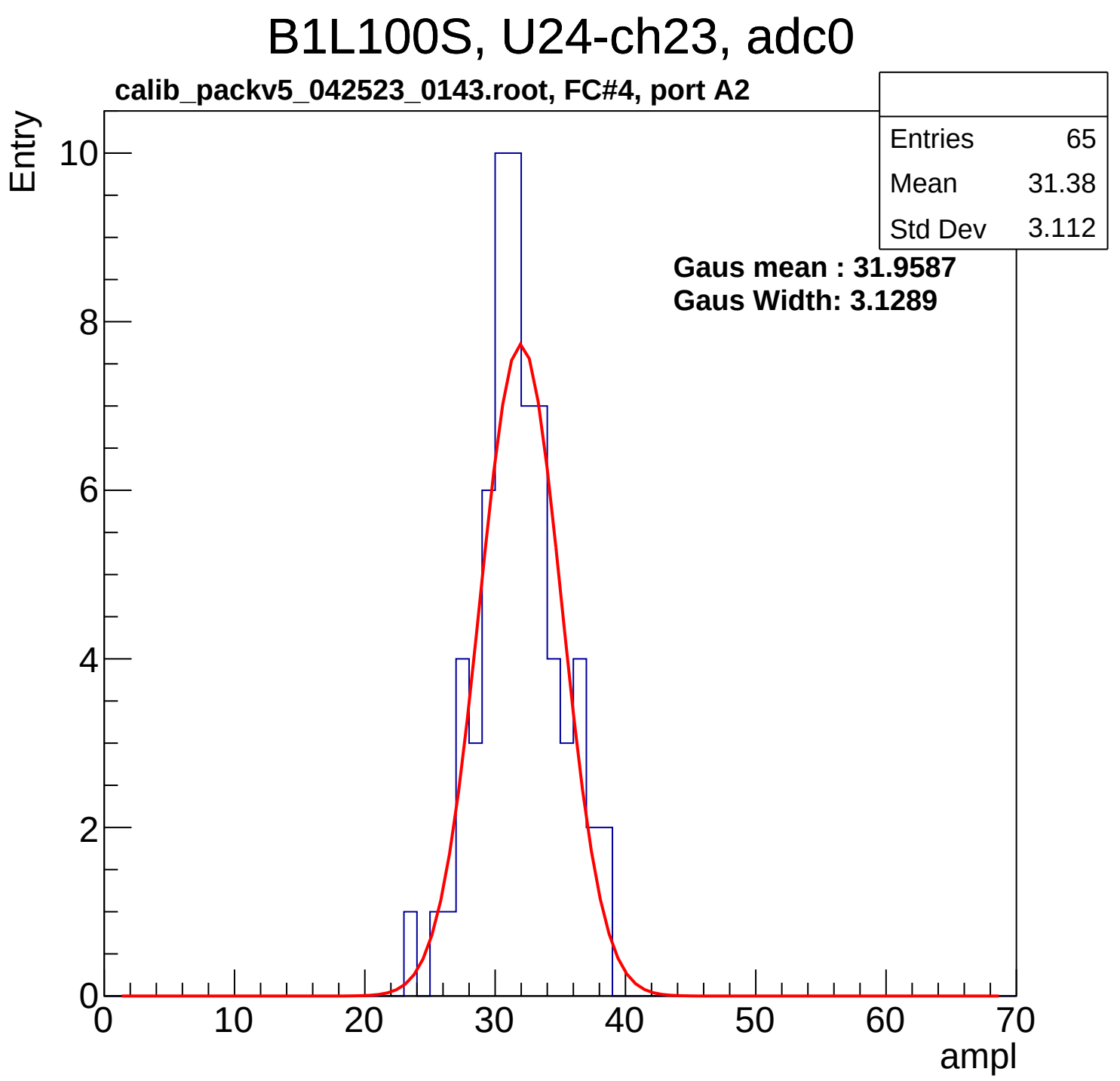
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 31.38
Std Dev 3.112
Gaus mean : 31.9587
Gaus Width: 3.1289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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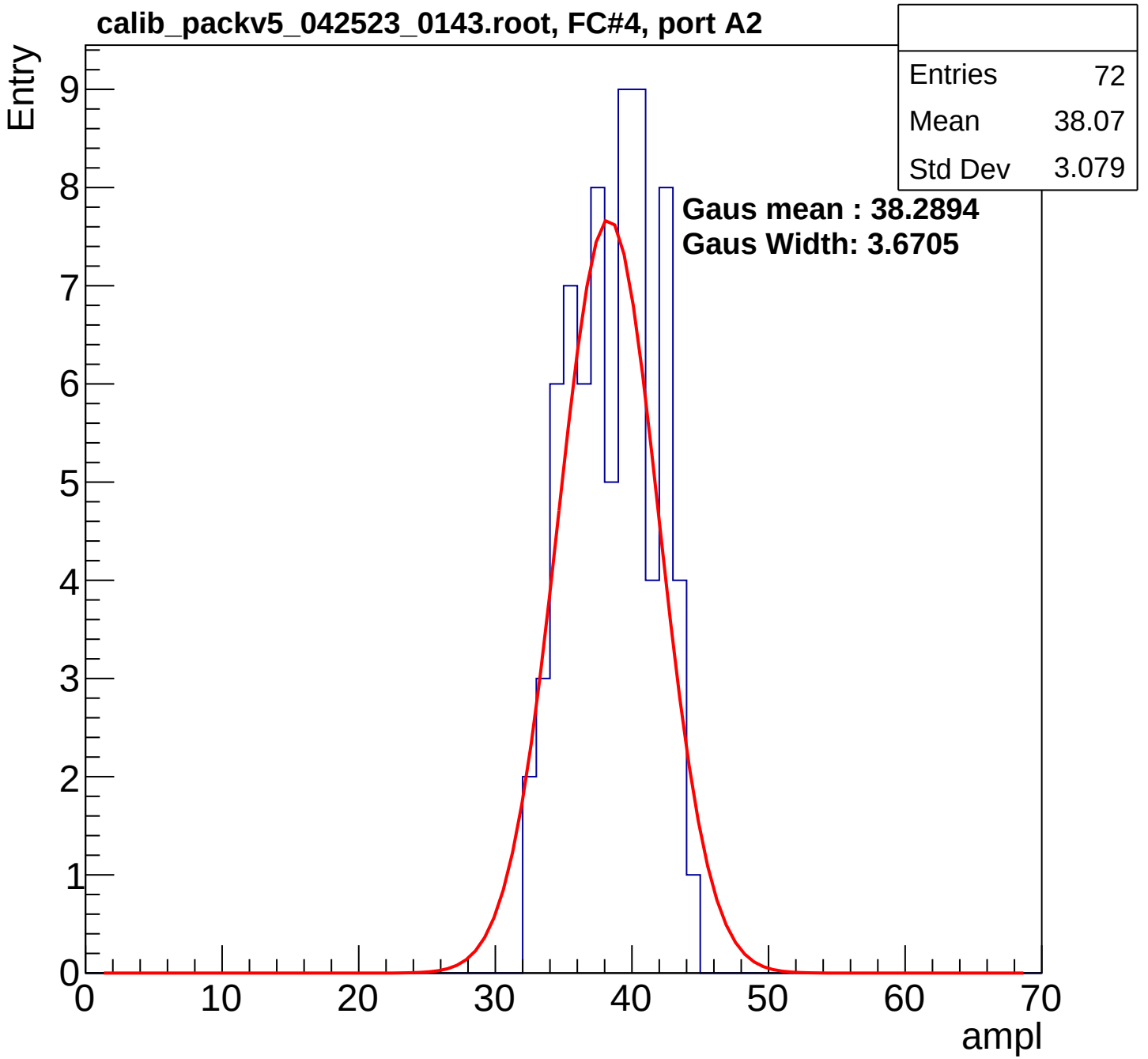
B1L100S, U24-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 38.07
Std Dev 3.079
Gaus mean : 38.2894
Gaus Width: 3.6705
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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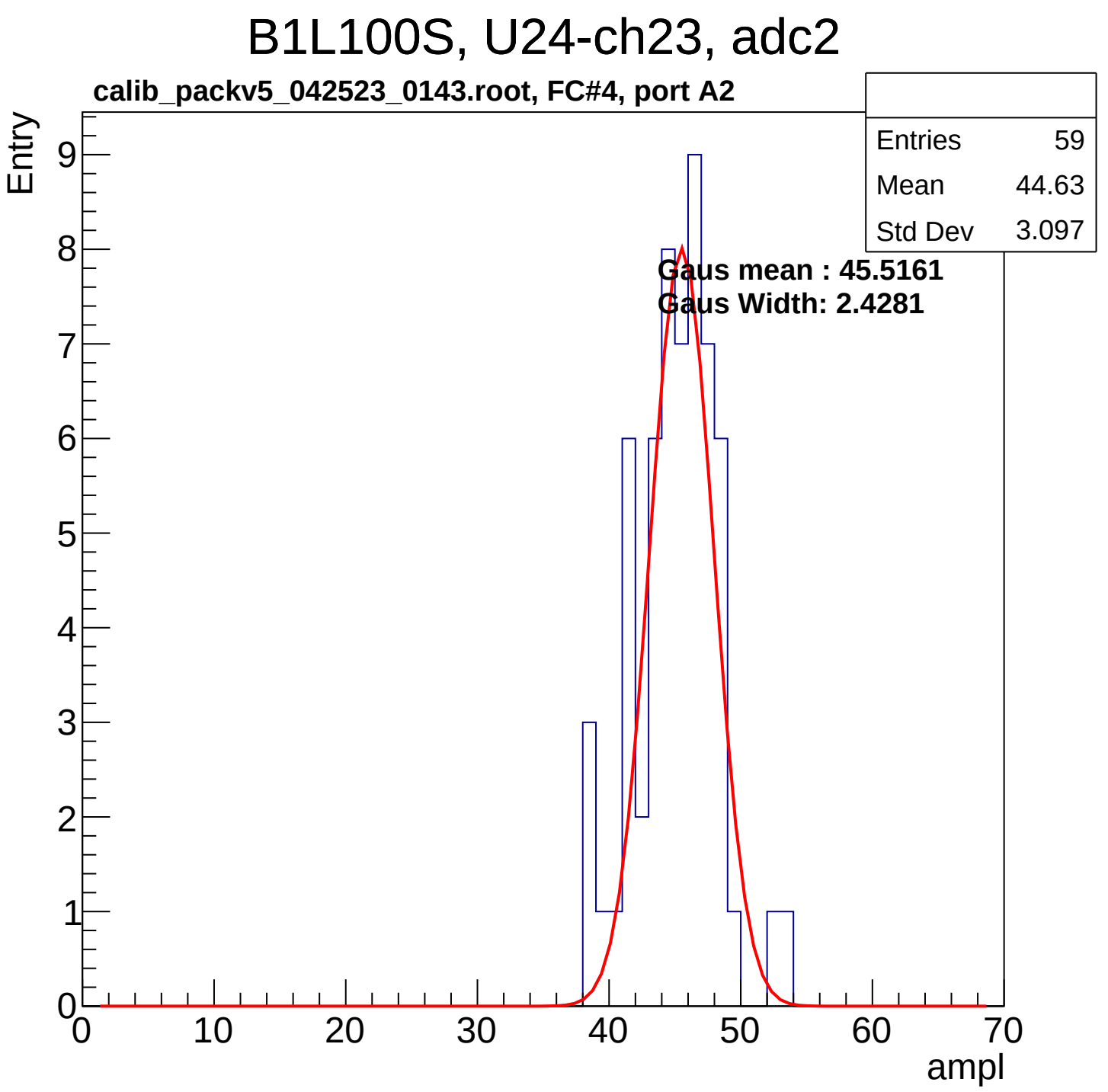
40
50
60
70


B1L100S, U24-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44.63
Std Dev 3.097
Gaus mean : 45.5161
Gaus Width: 2.4281
Entry
ampl
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2
3
4
5
6
7
8
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0
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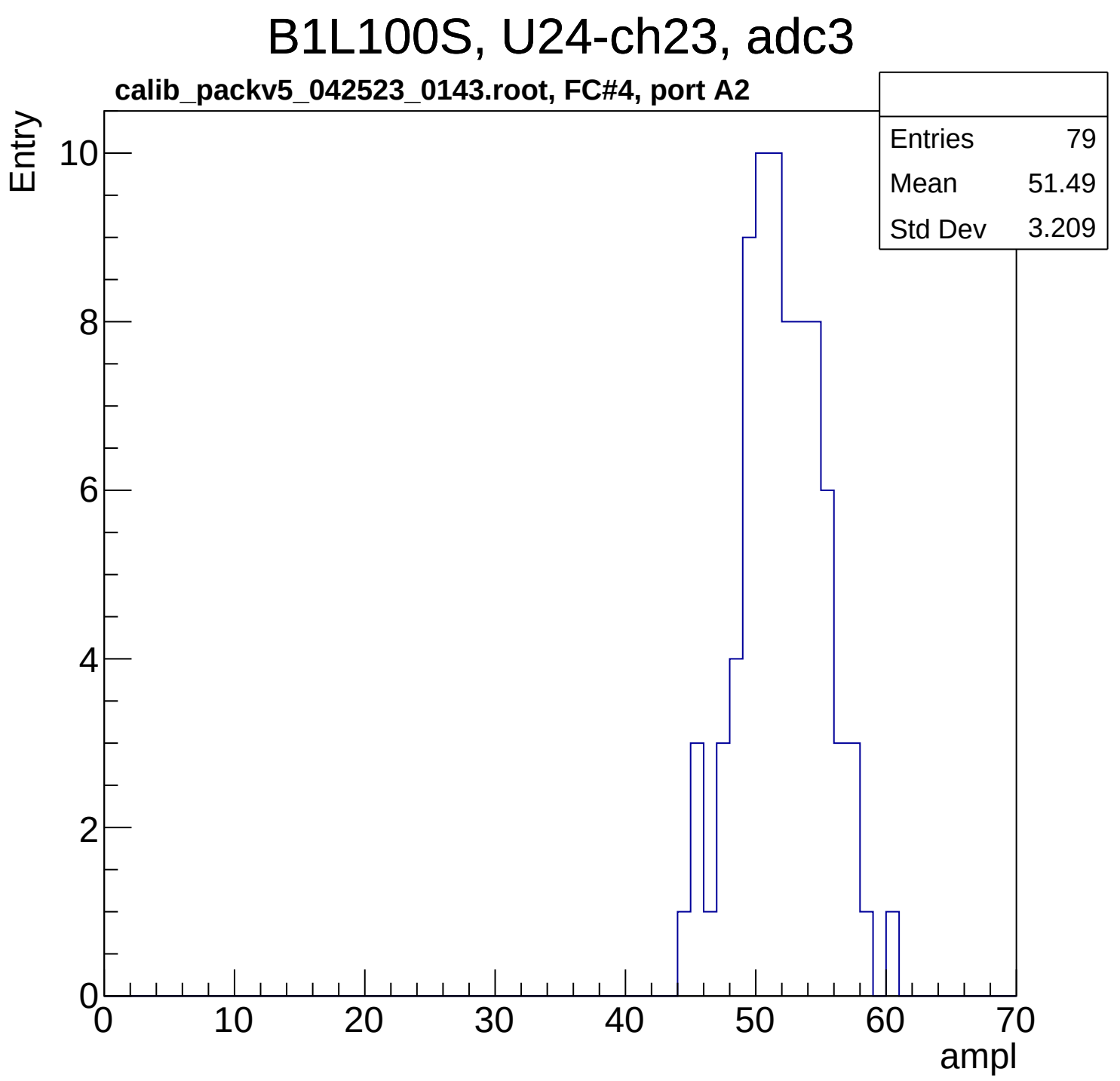
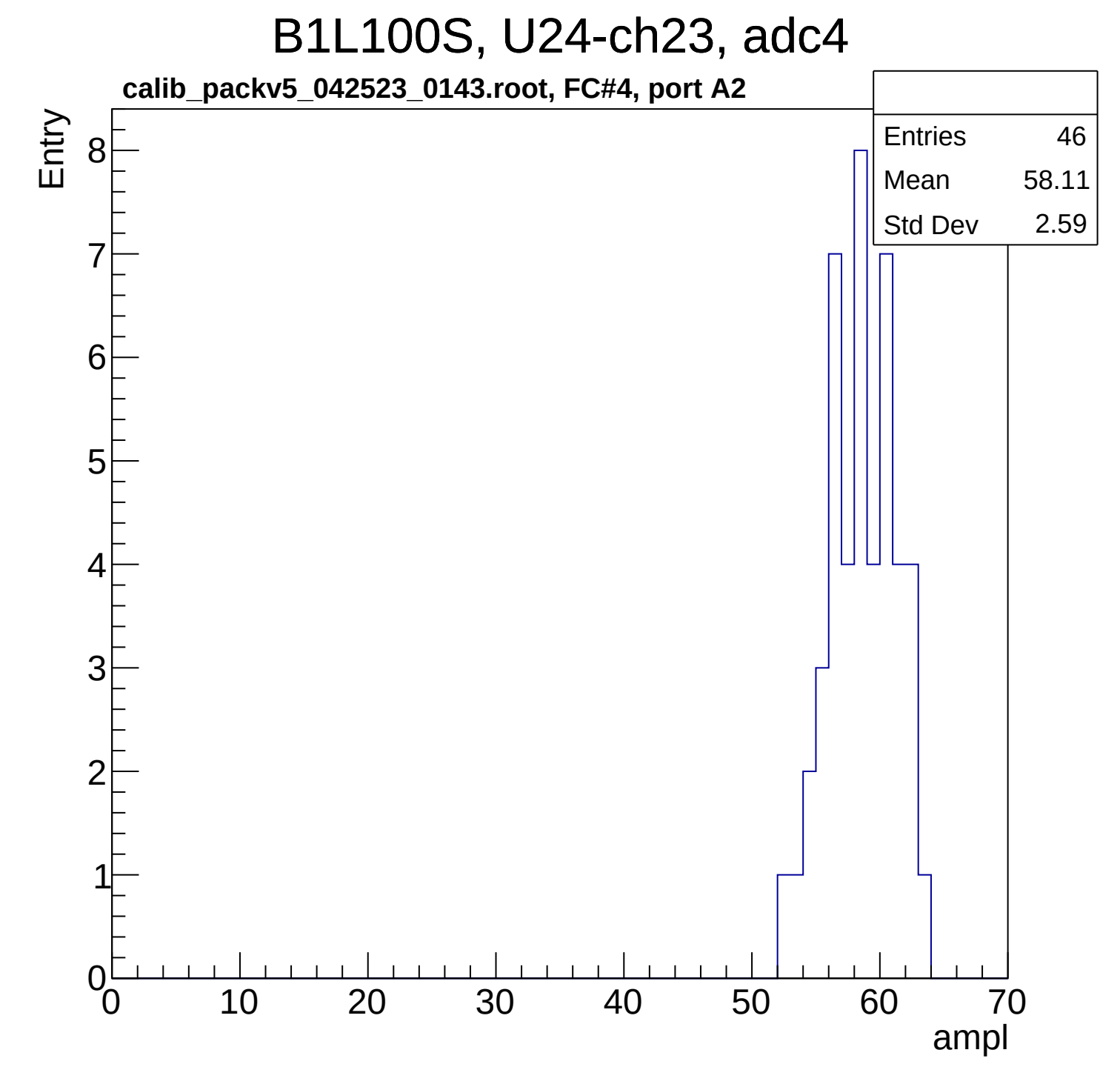

B1L100S, U24-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 51.49
Std Dev 3.209
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch23, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.11
Std Dev 2.59
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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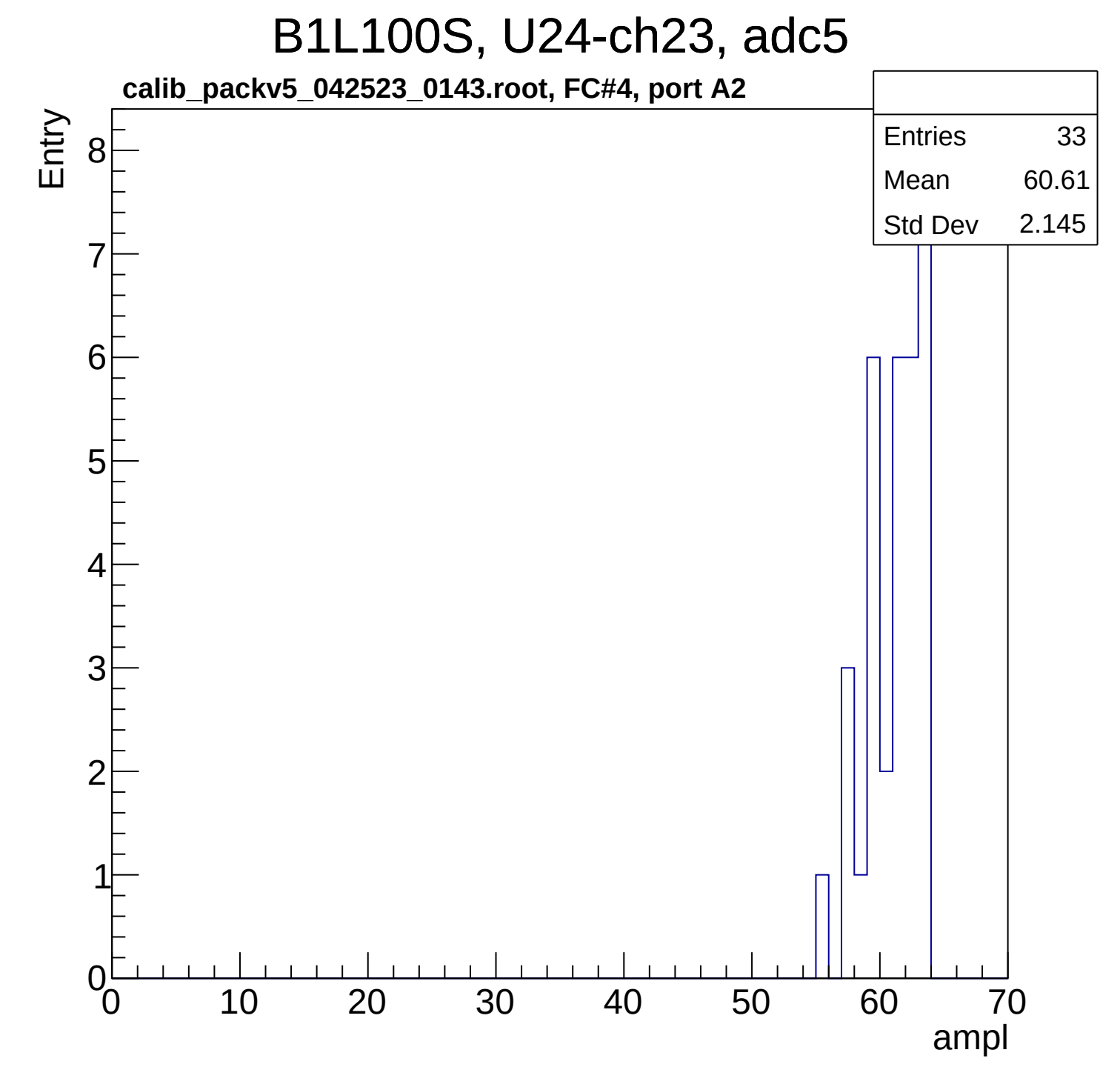
B1L100S, U24-ch23, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 60.61
Std Dev 2.145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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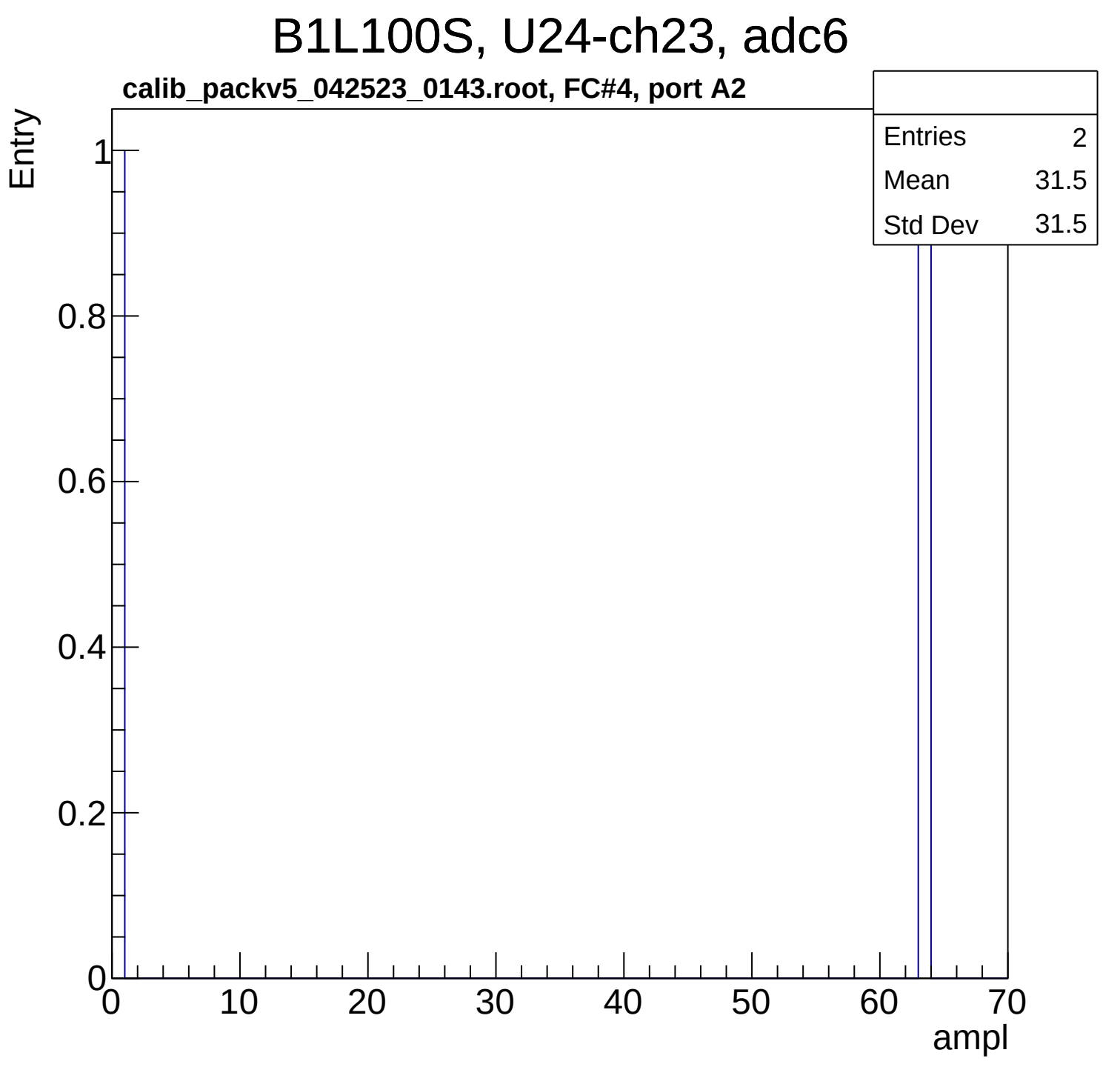
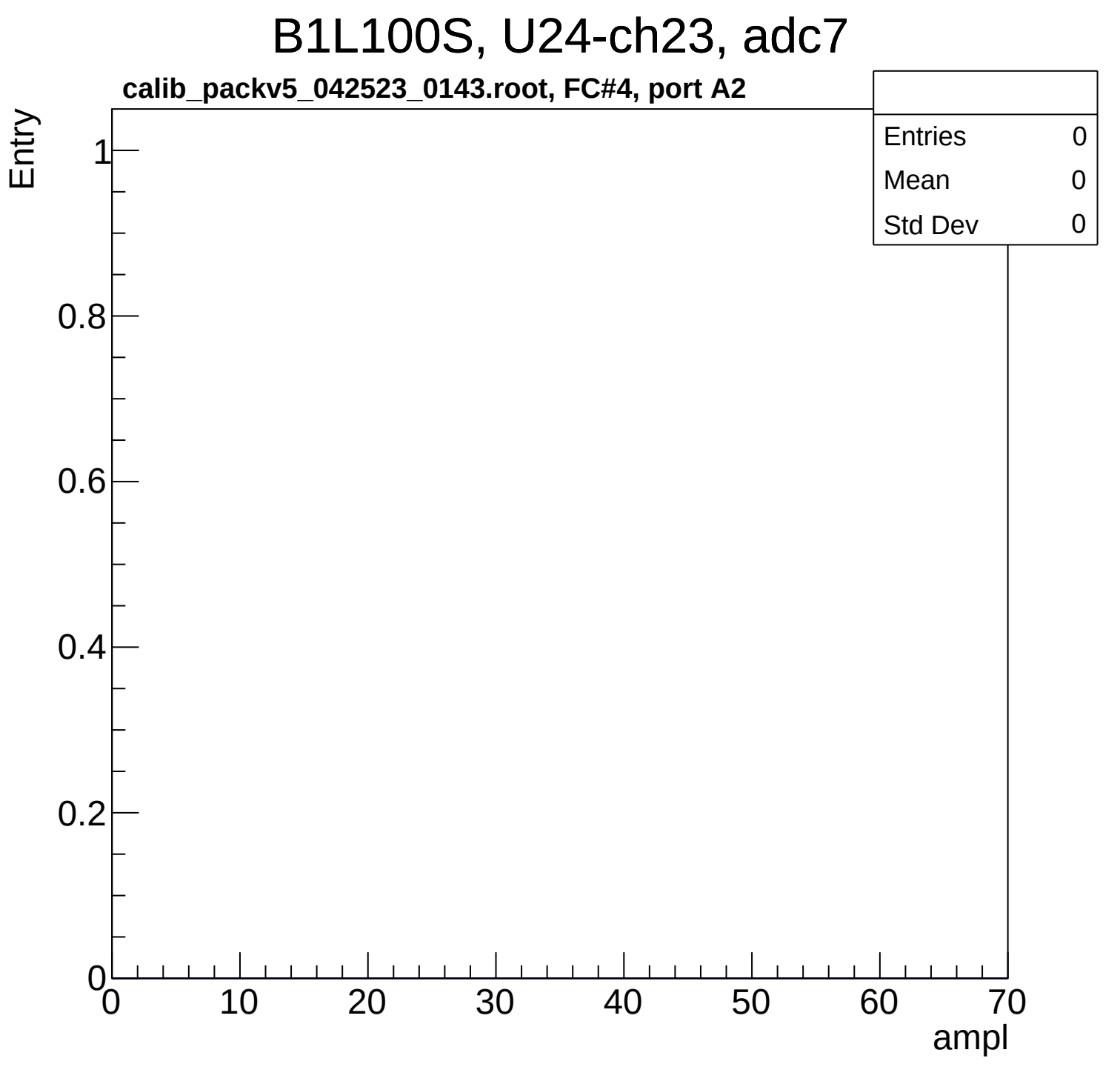
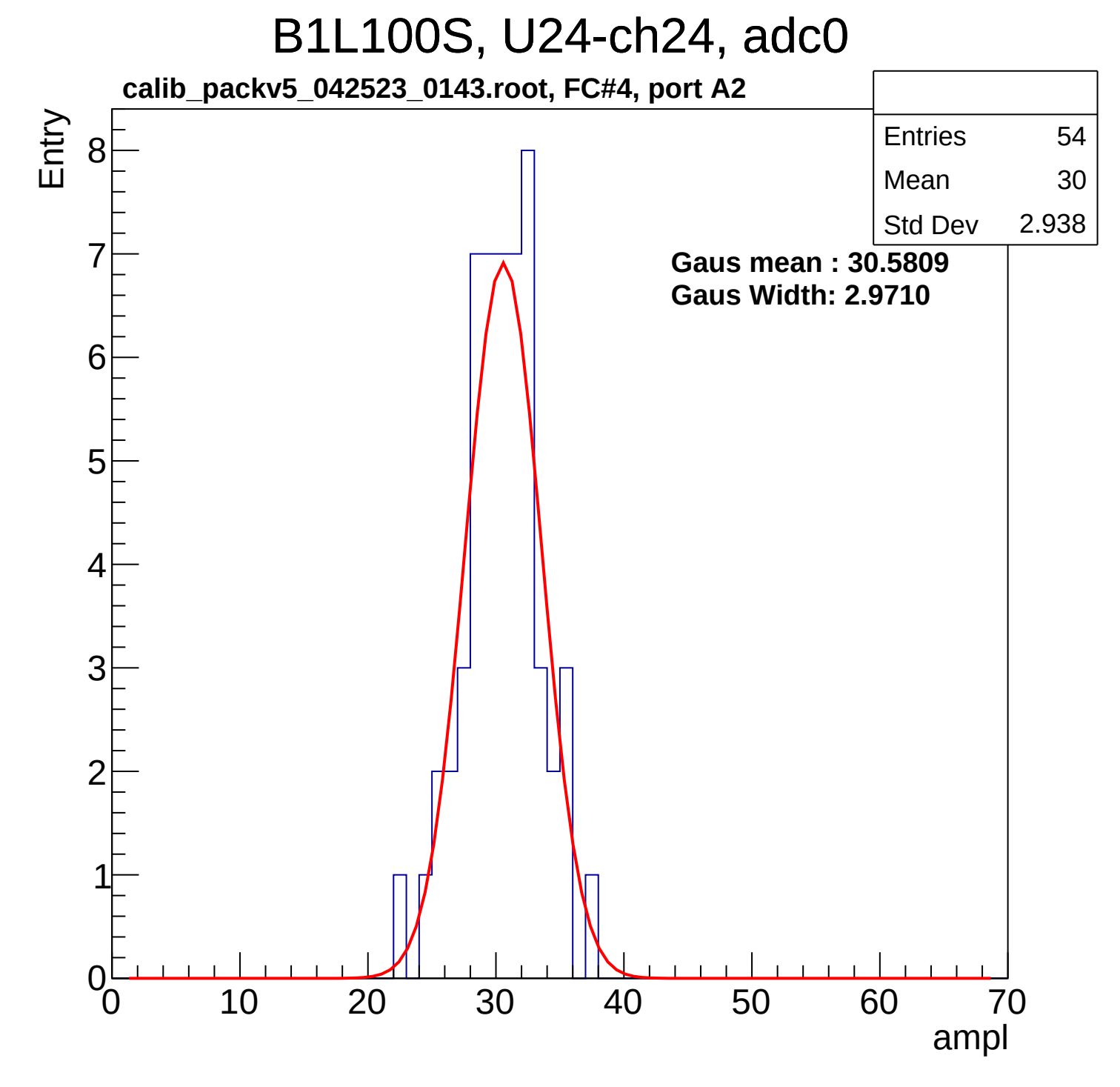
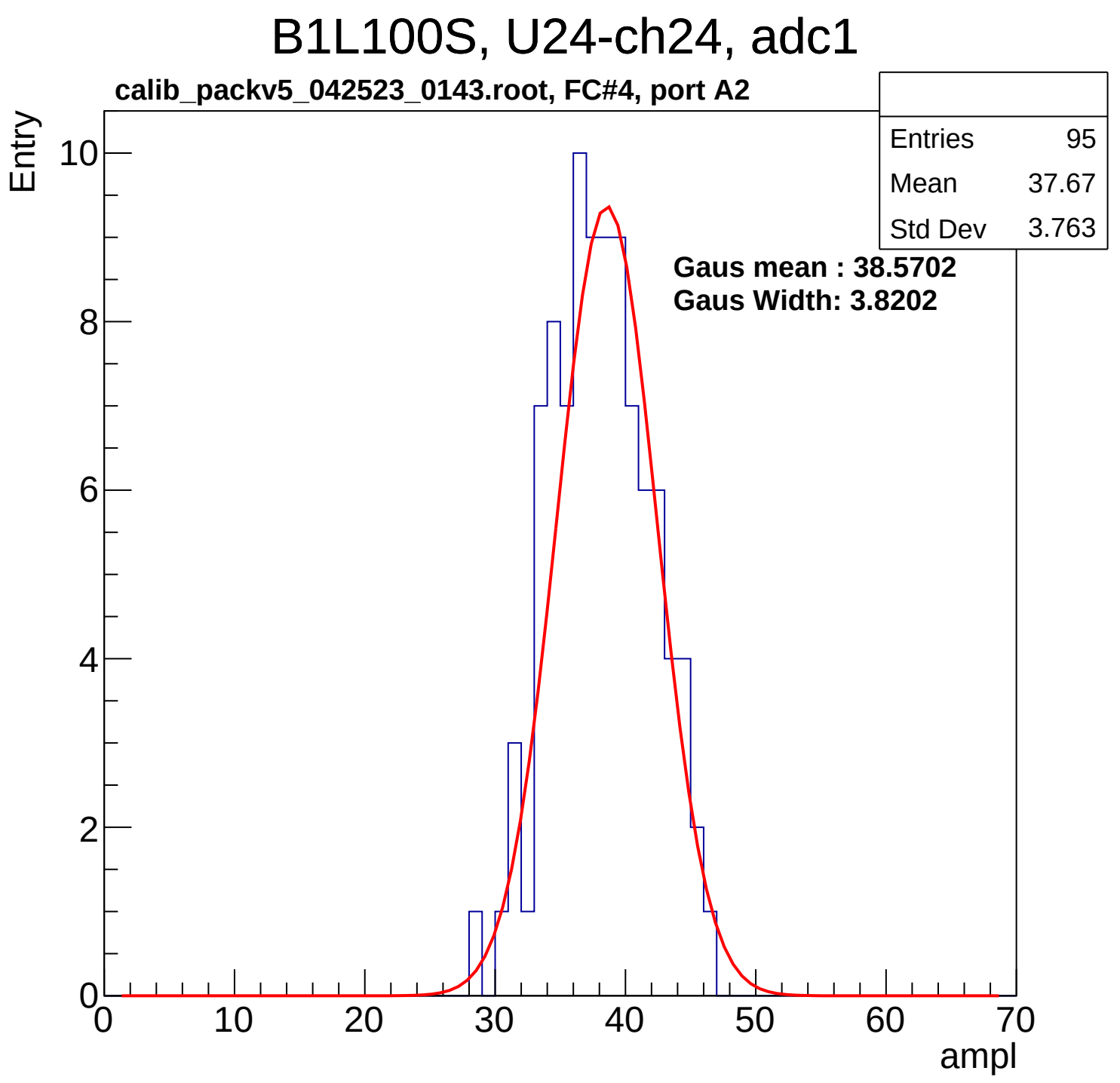

B1L100S, U24-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 30
Std Dev 2.938
Gaus mean : 30.5809
Gaus Width: 2.9710
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 37.67
Std Dev 3.763
Gaus mean : 38.5702
Gaus Width: 3.8202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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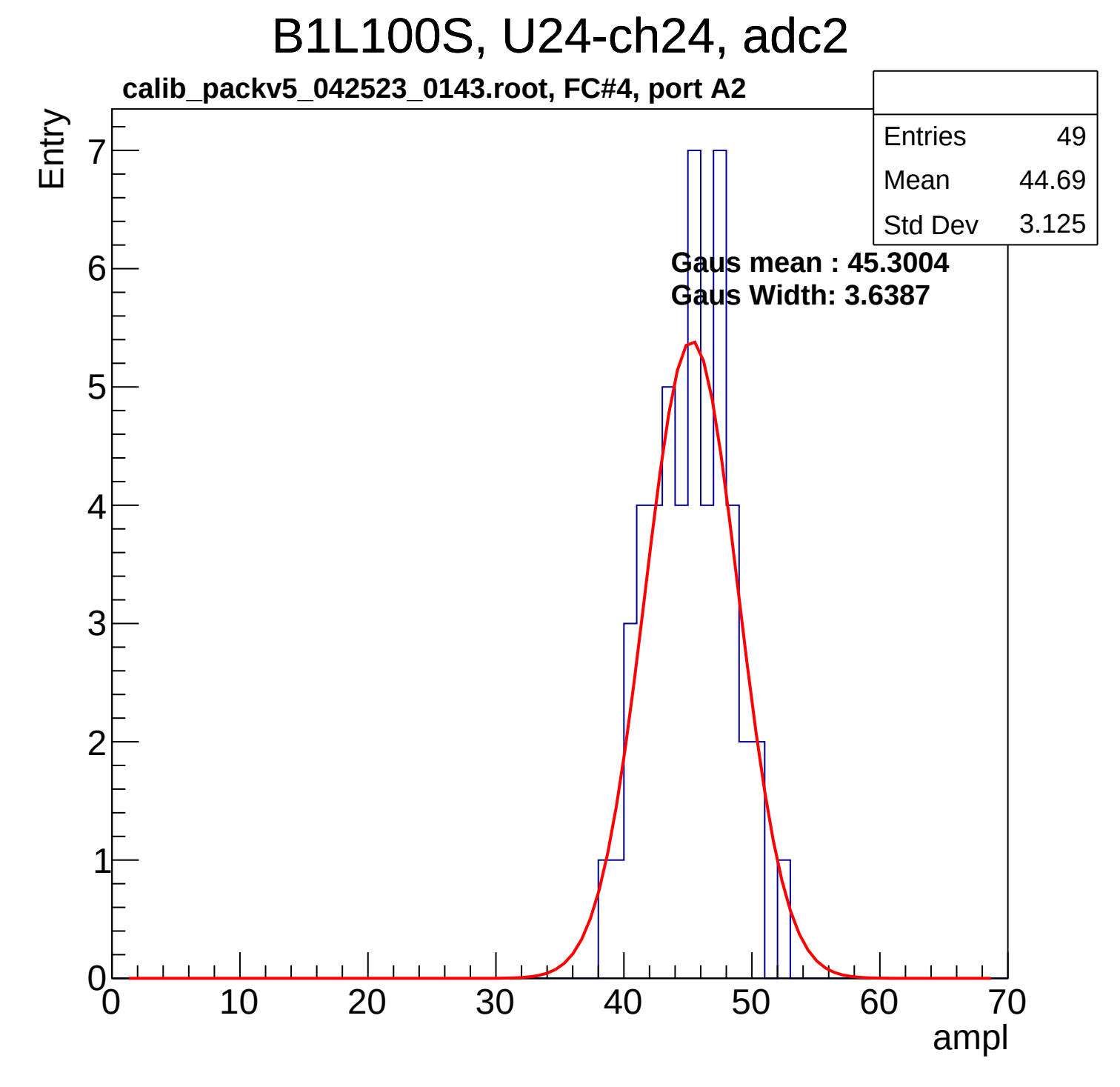
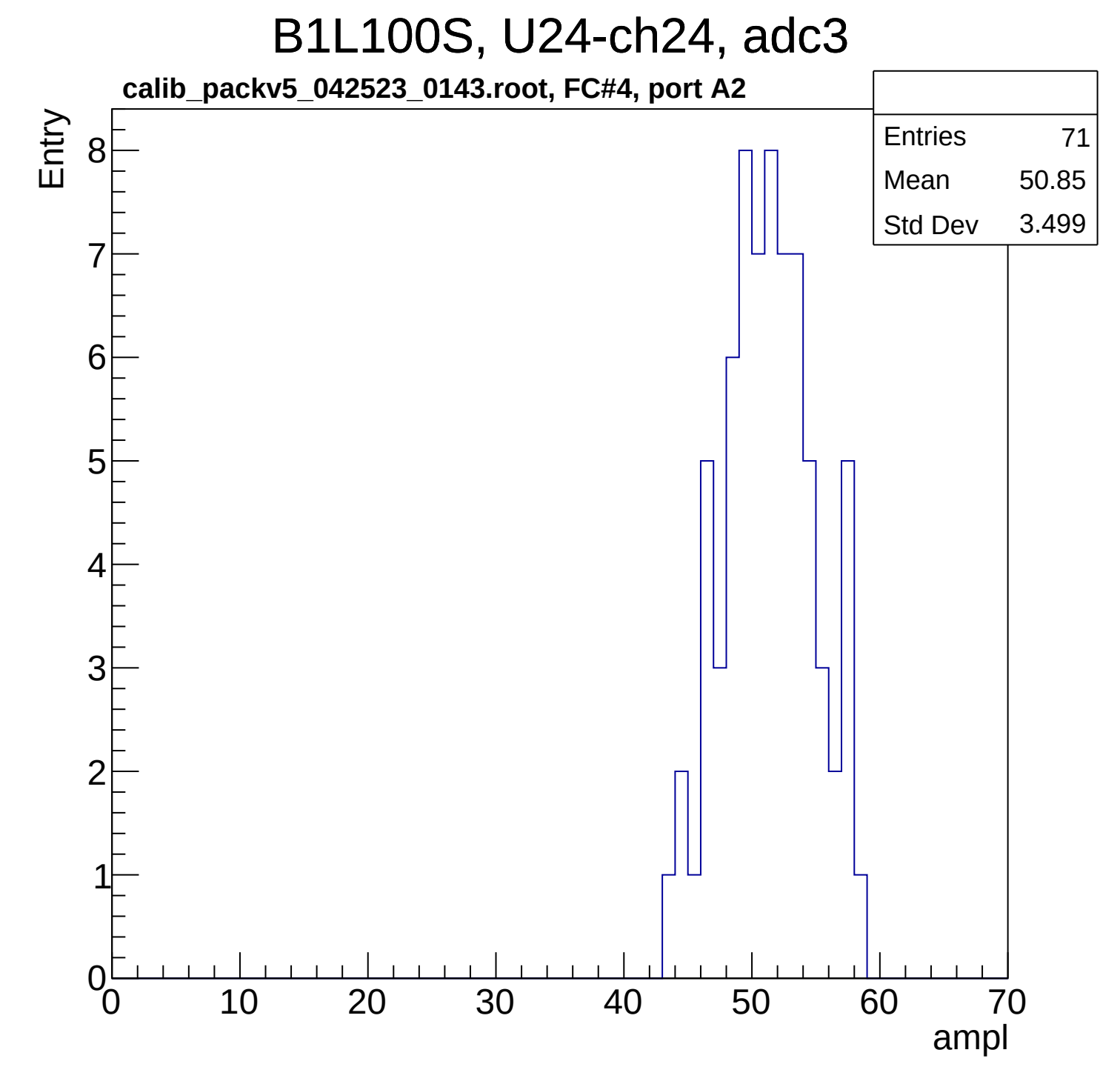

B1L100S, U24-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 44.69
Std Dev 3.125
Gaus mean : 45.3004
Gaus Width: 3.6387
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch24, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.85
Std Dev 3.499
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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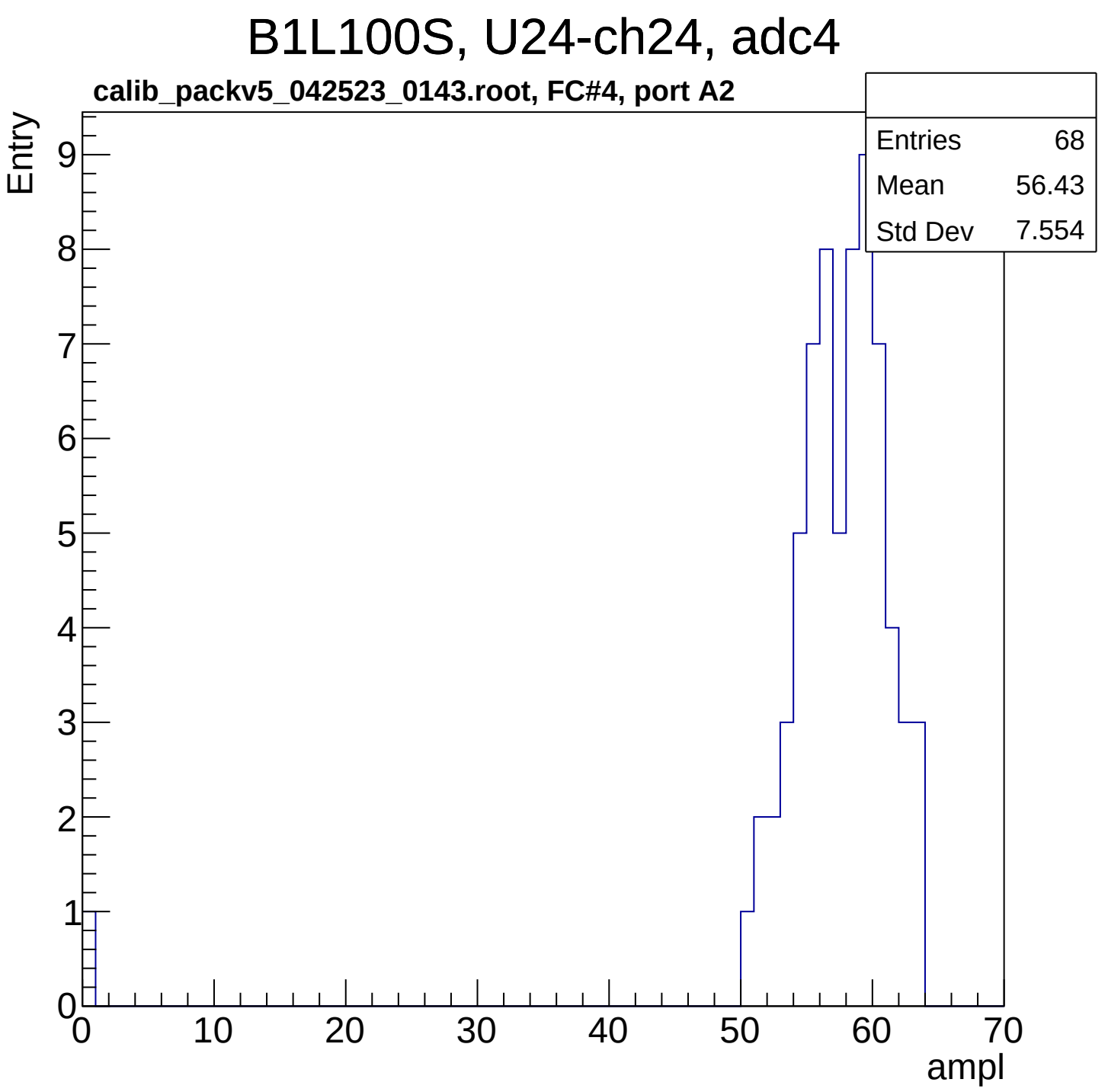
B1L100S, U24-ch24, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.43
Std Dev 7.554
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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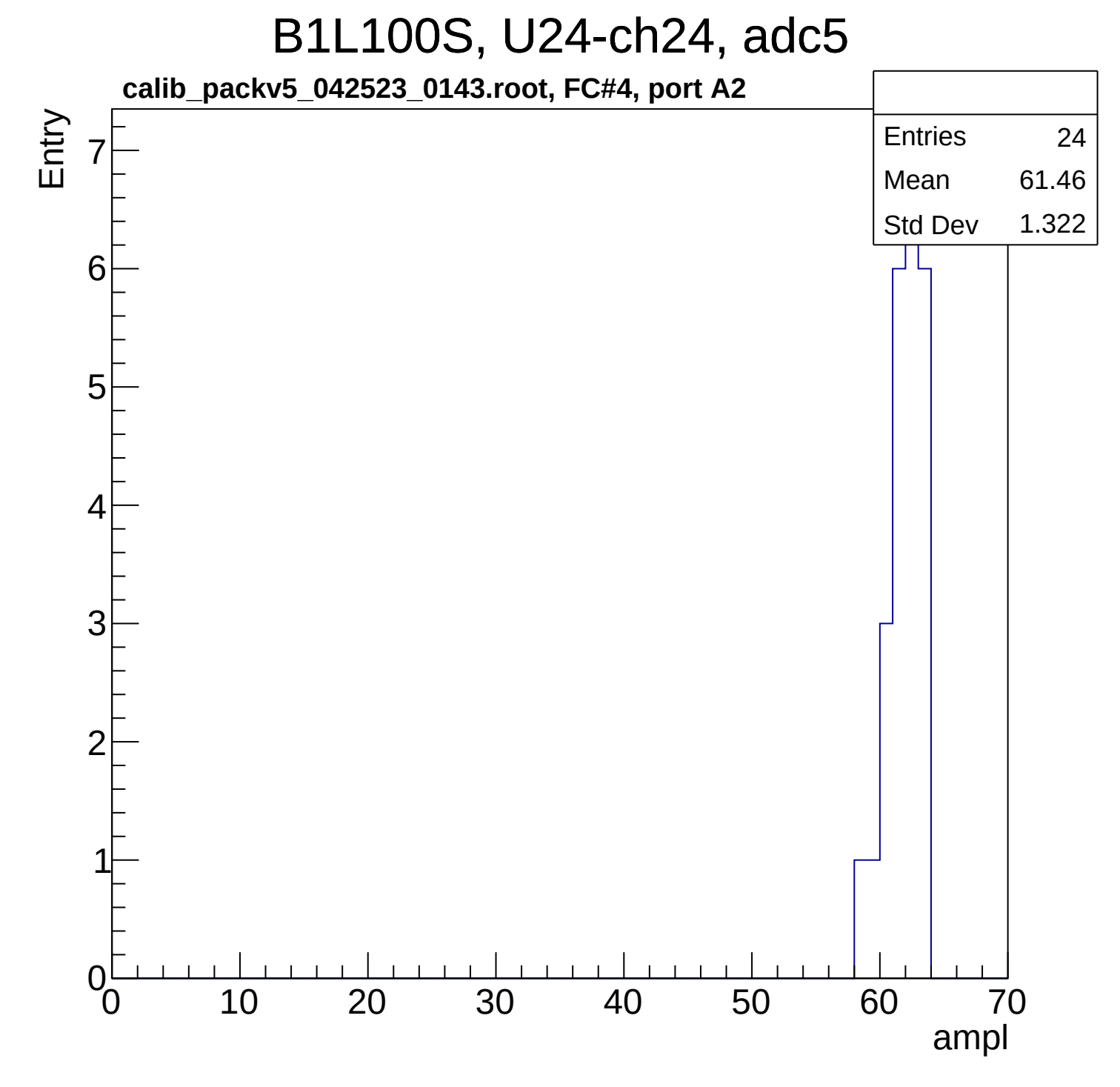
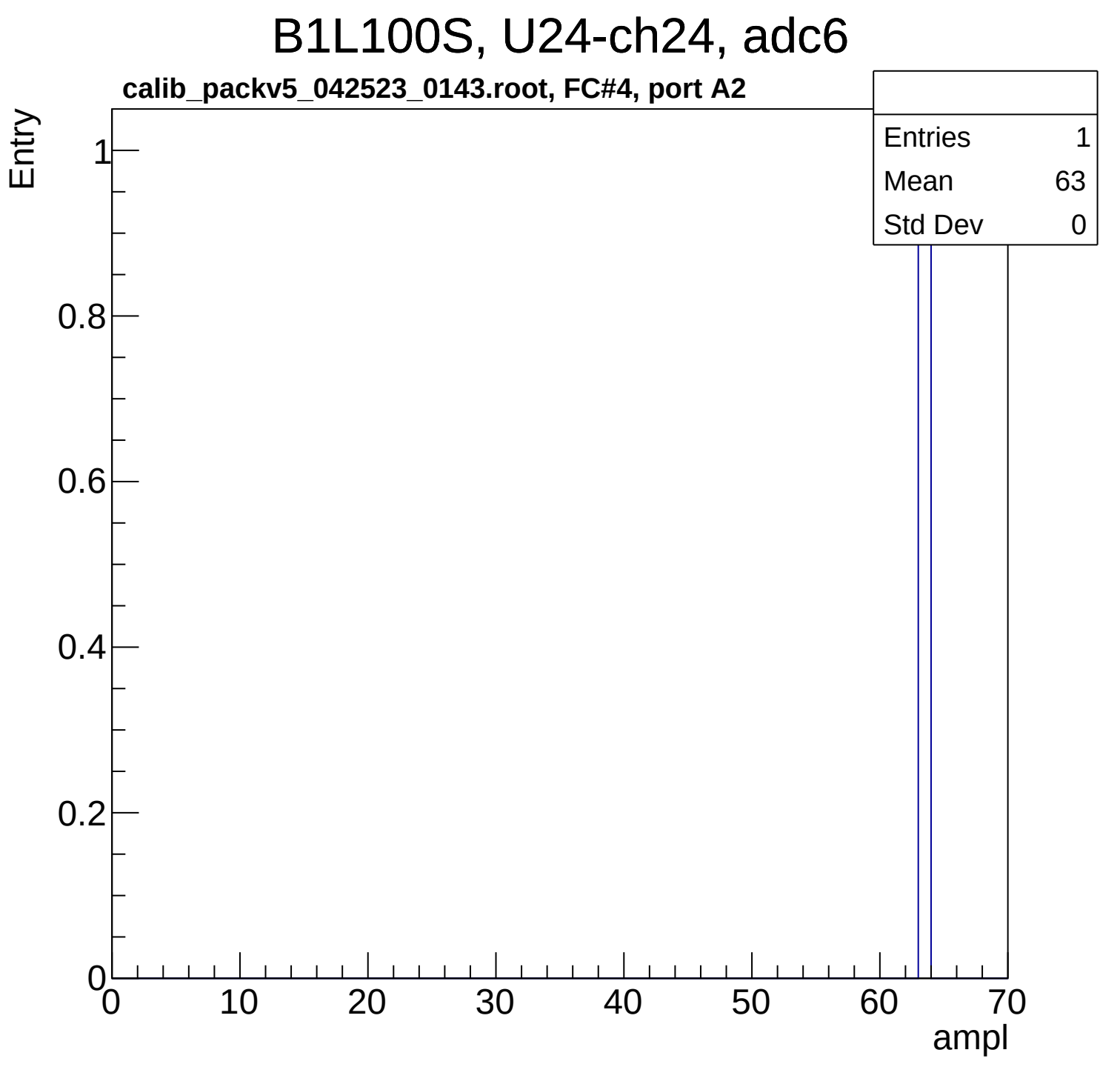
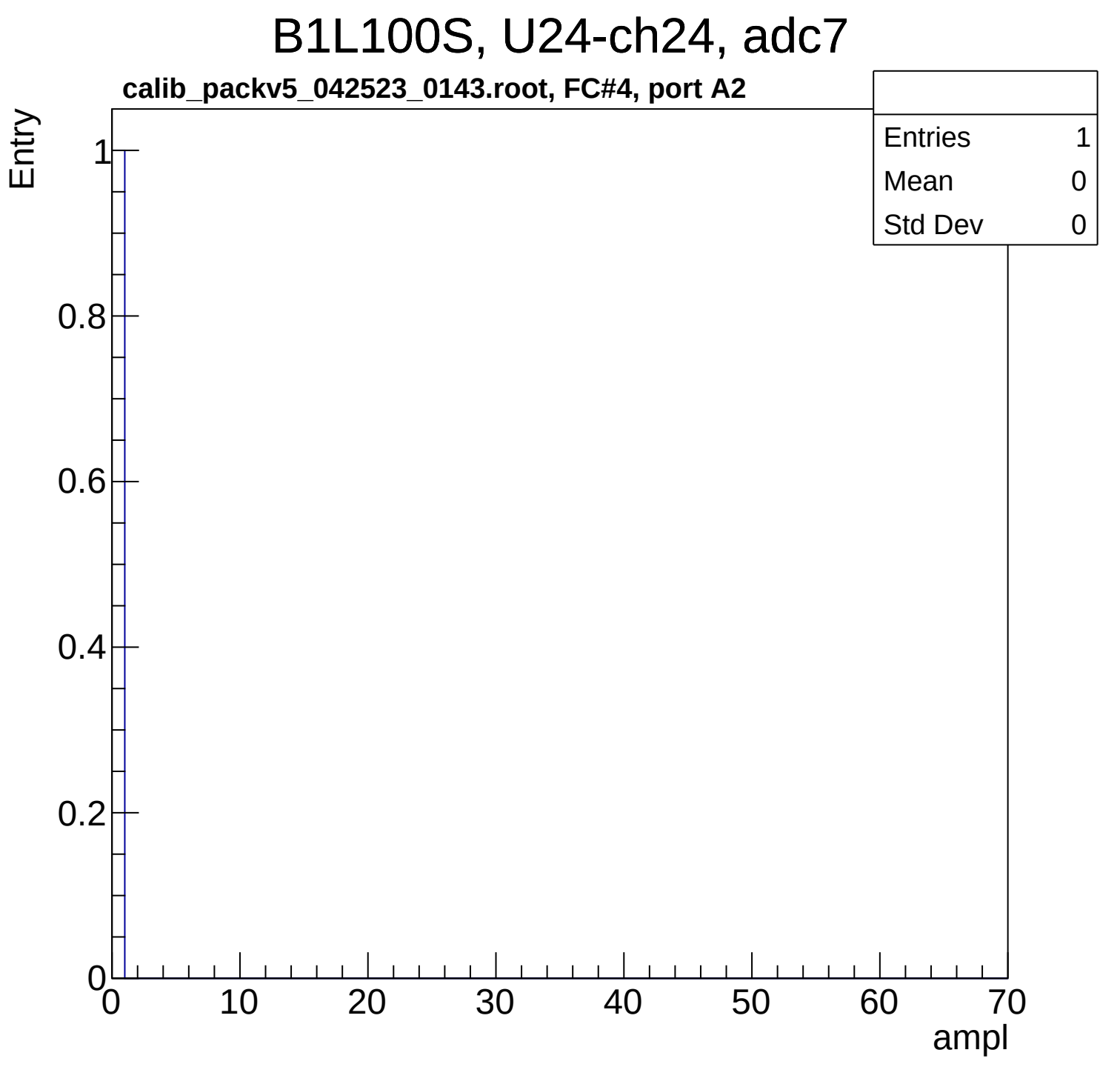
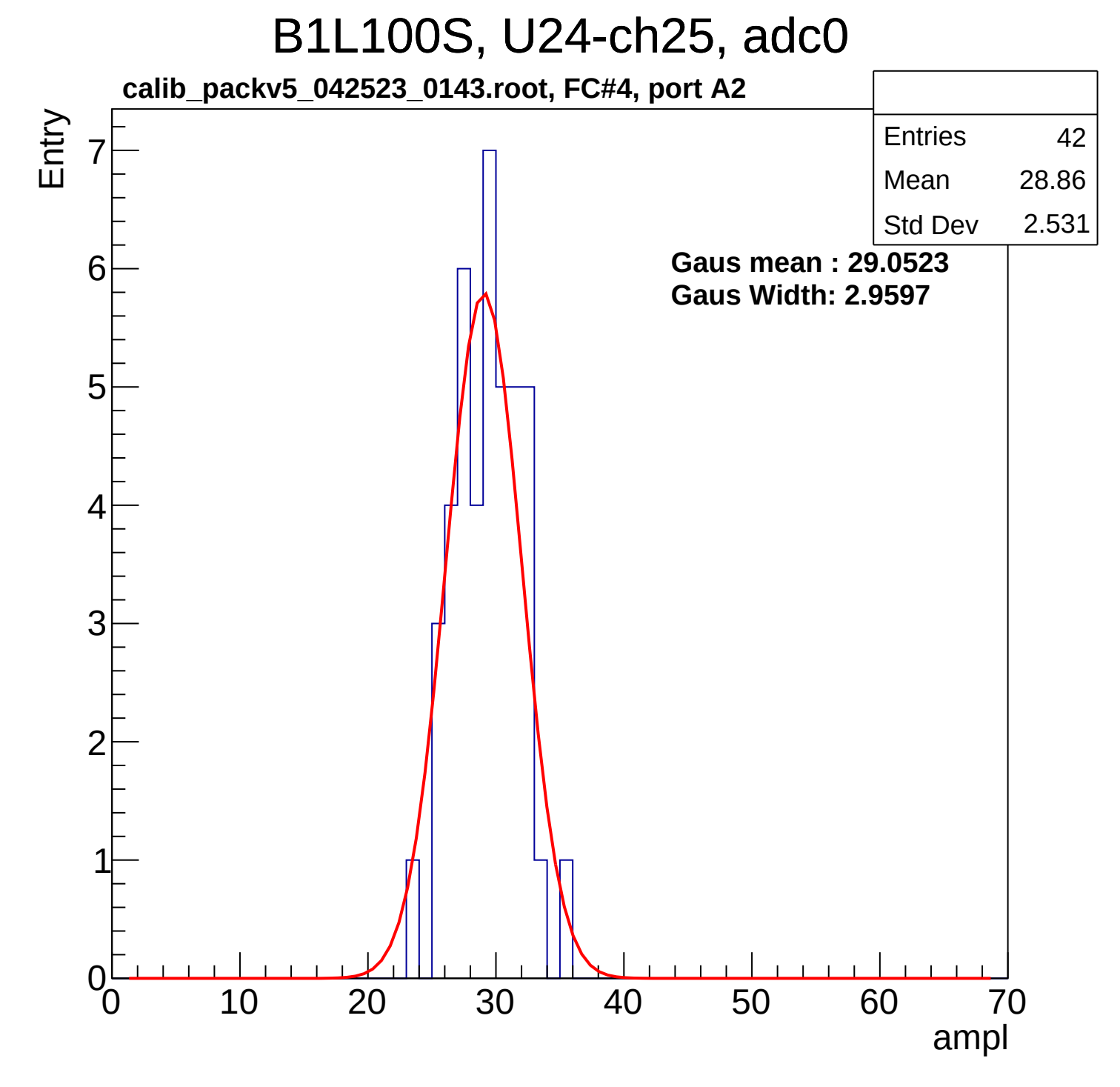
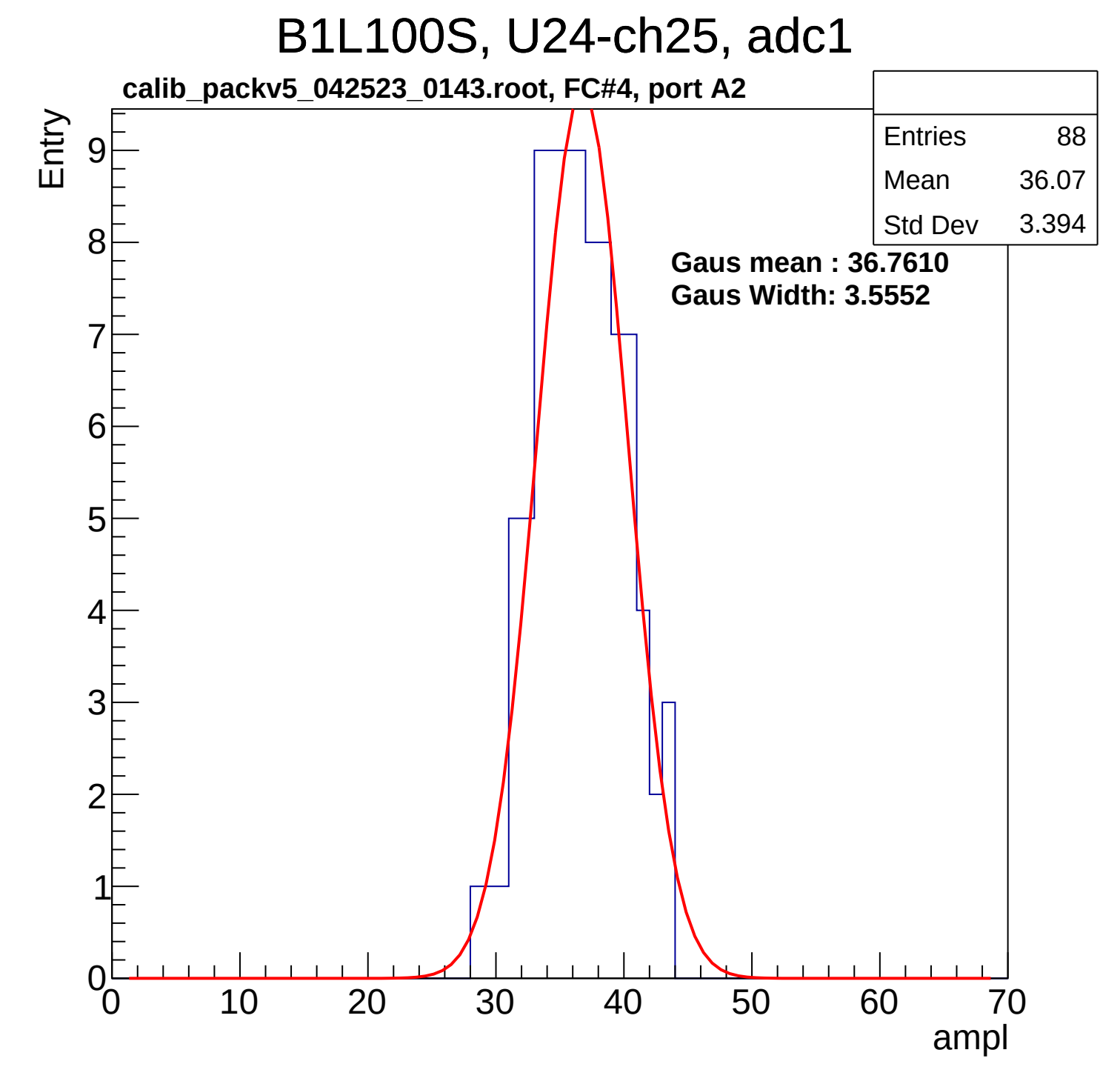
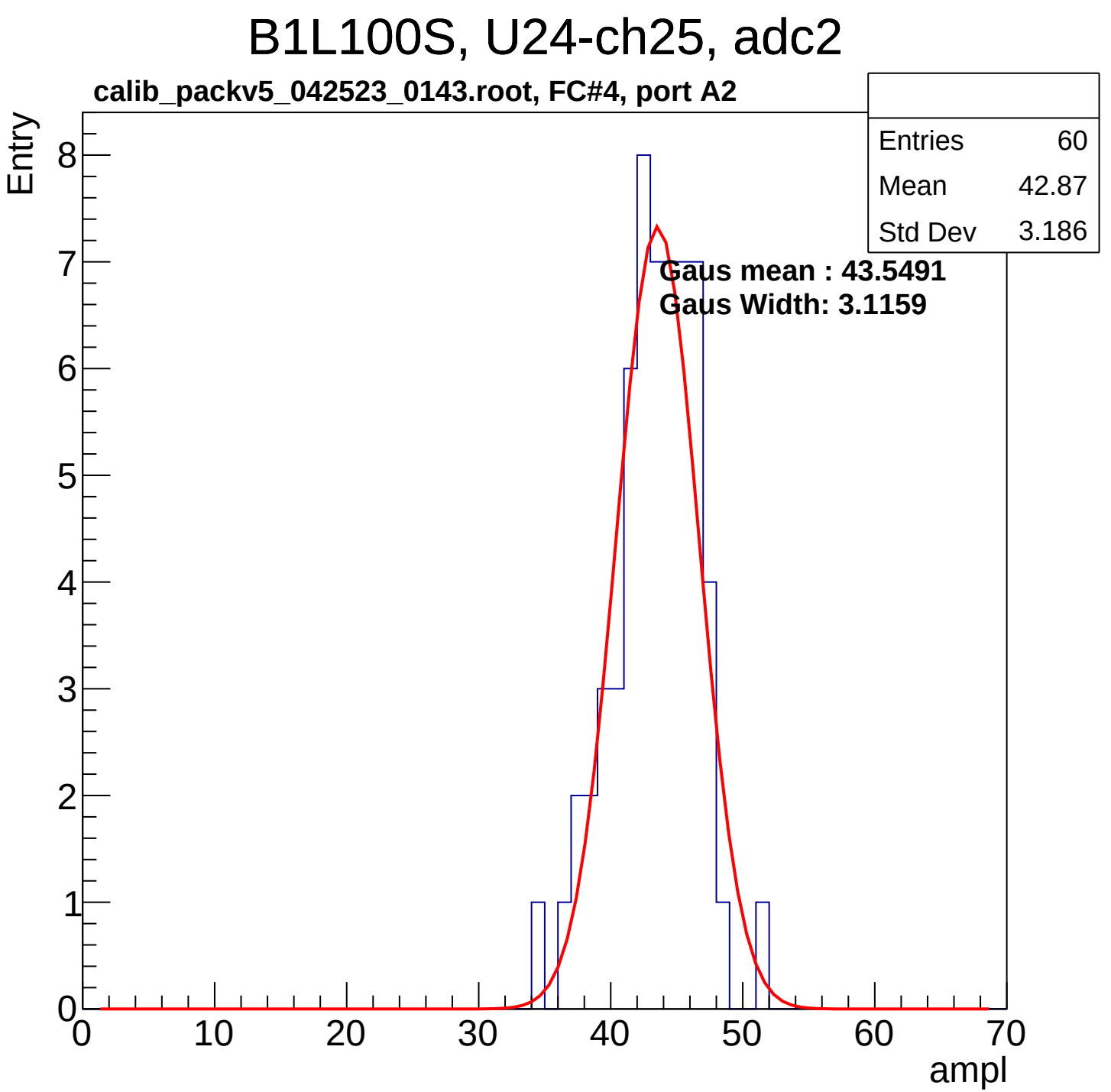

B1L100S, U24-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 61.46
Std Dev 1.322
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

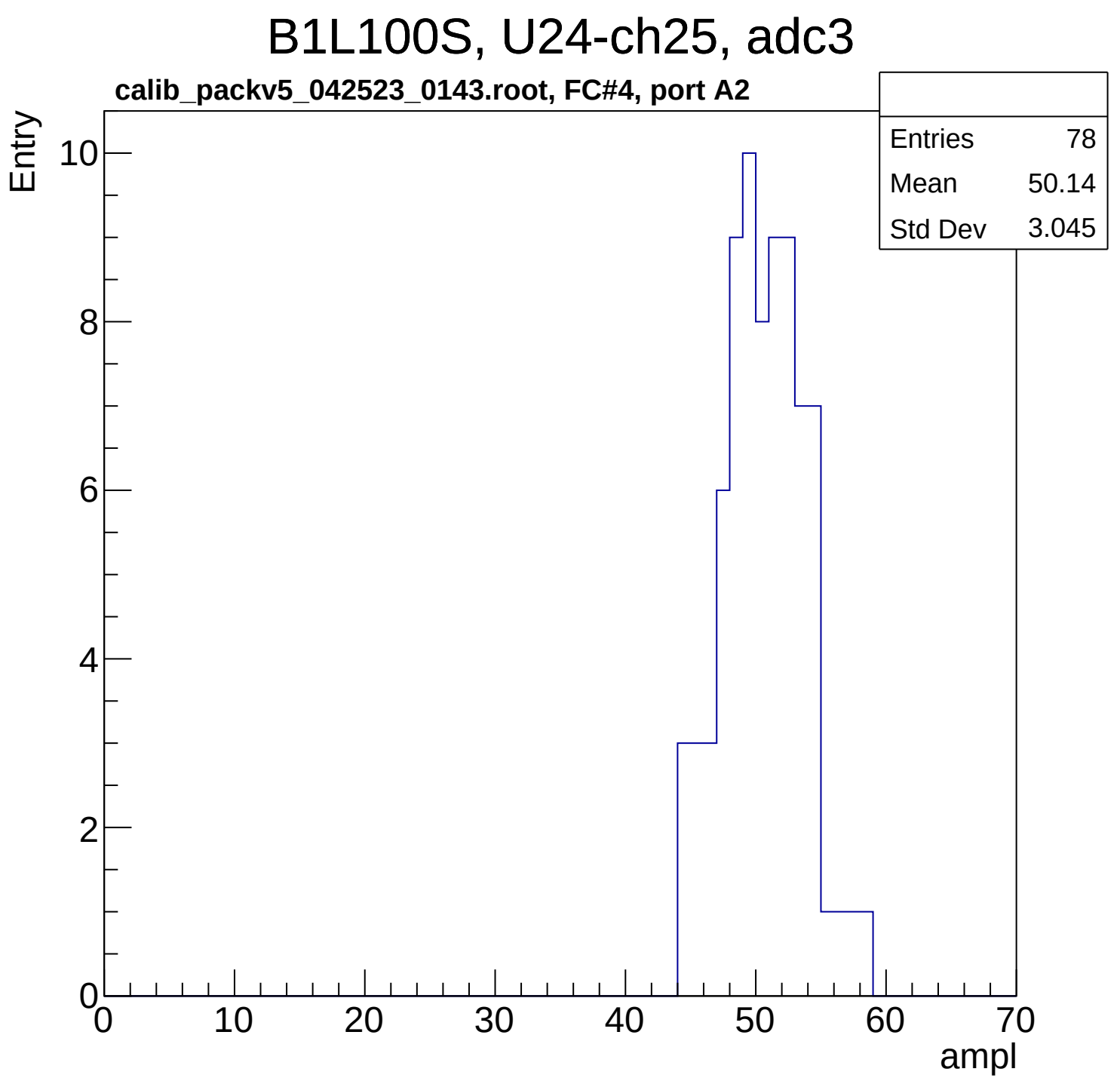

B1L100S, U24-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

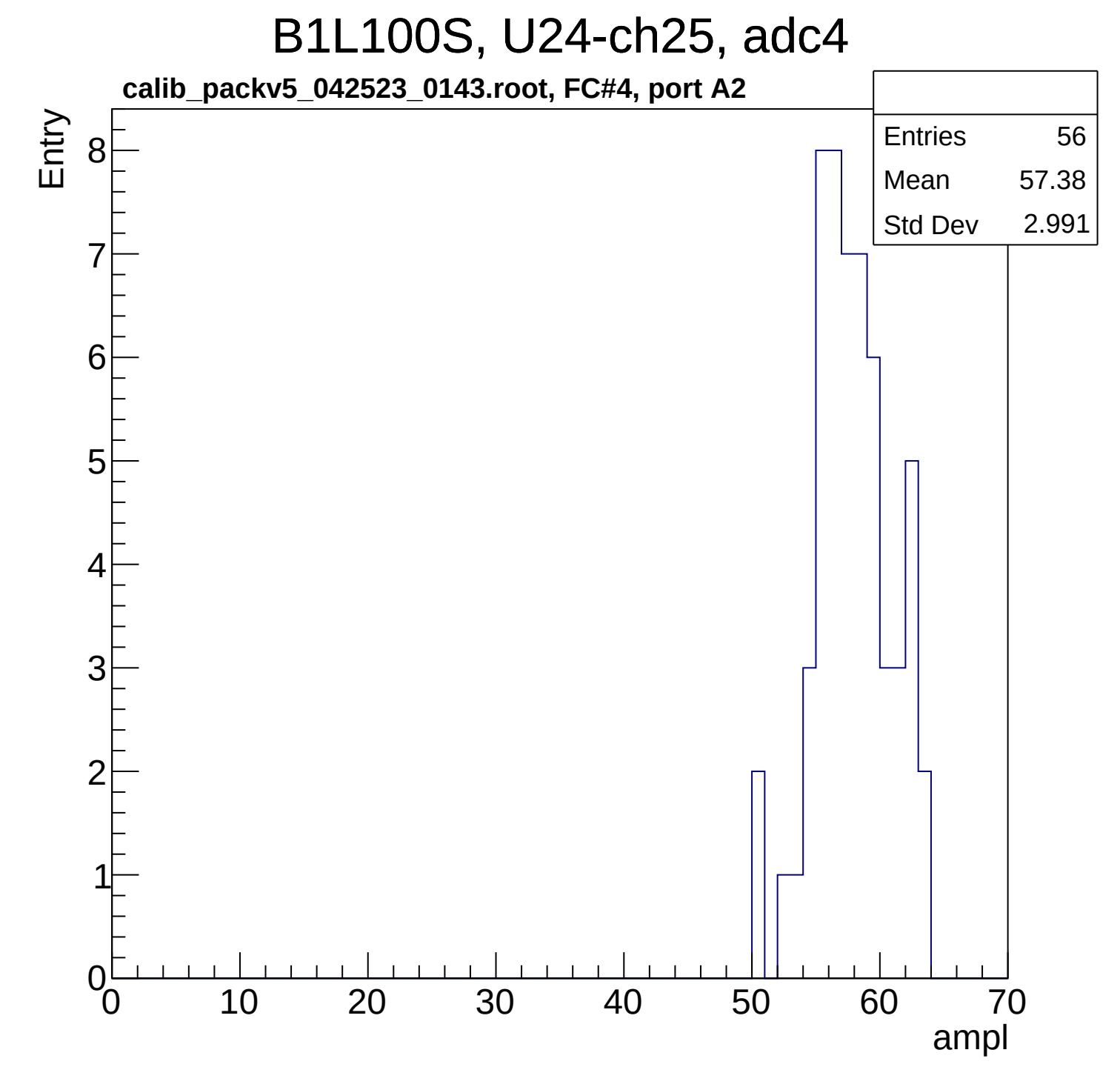

B1L100S, U24-ch24, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 28.86
Std Dev 2.531
Gaus mean : 29.0523
Gaus Width: 2.9597
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 36.07
Std Dev 3.394
Gaus mean : 36.7610
Gaus Width: 3.5552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.87
Std Dev 3.186
Gaus mean : 43.5491
Gaus Width: 3.1159
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch25, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 50.14
Std Dev 3.045
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.38
Std Dev 2.991
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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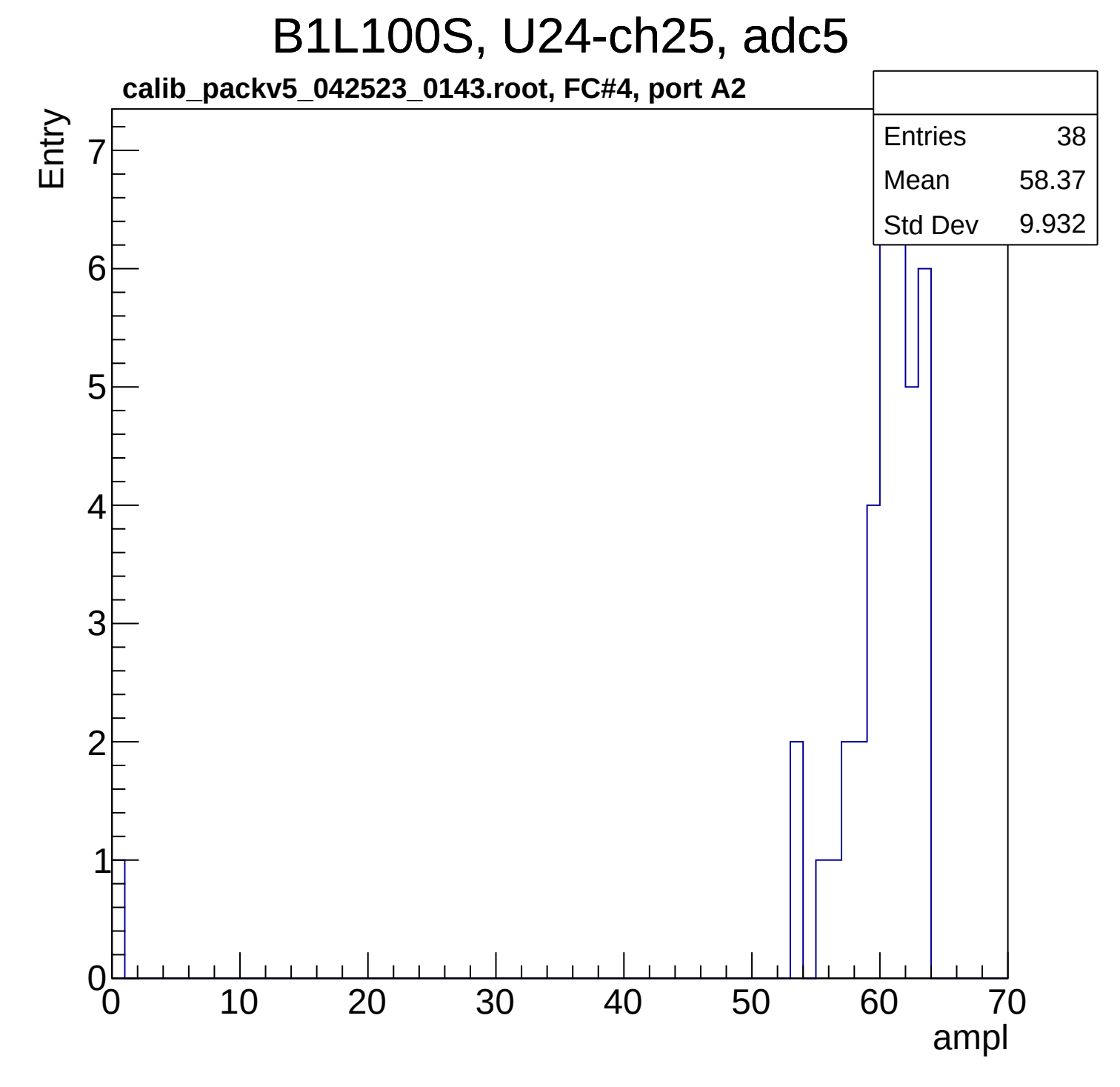
B1L100S, U24-ch25, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.37
Std Dev 9.932
Entry
ampl
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1
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4
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6
7
0
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20
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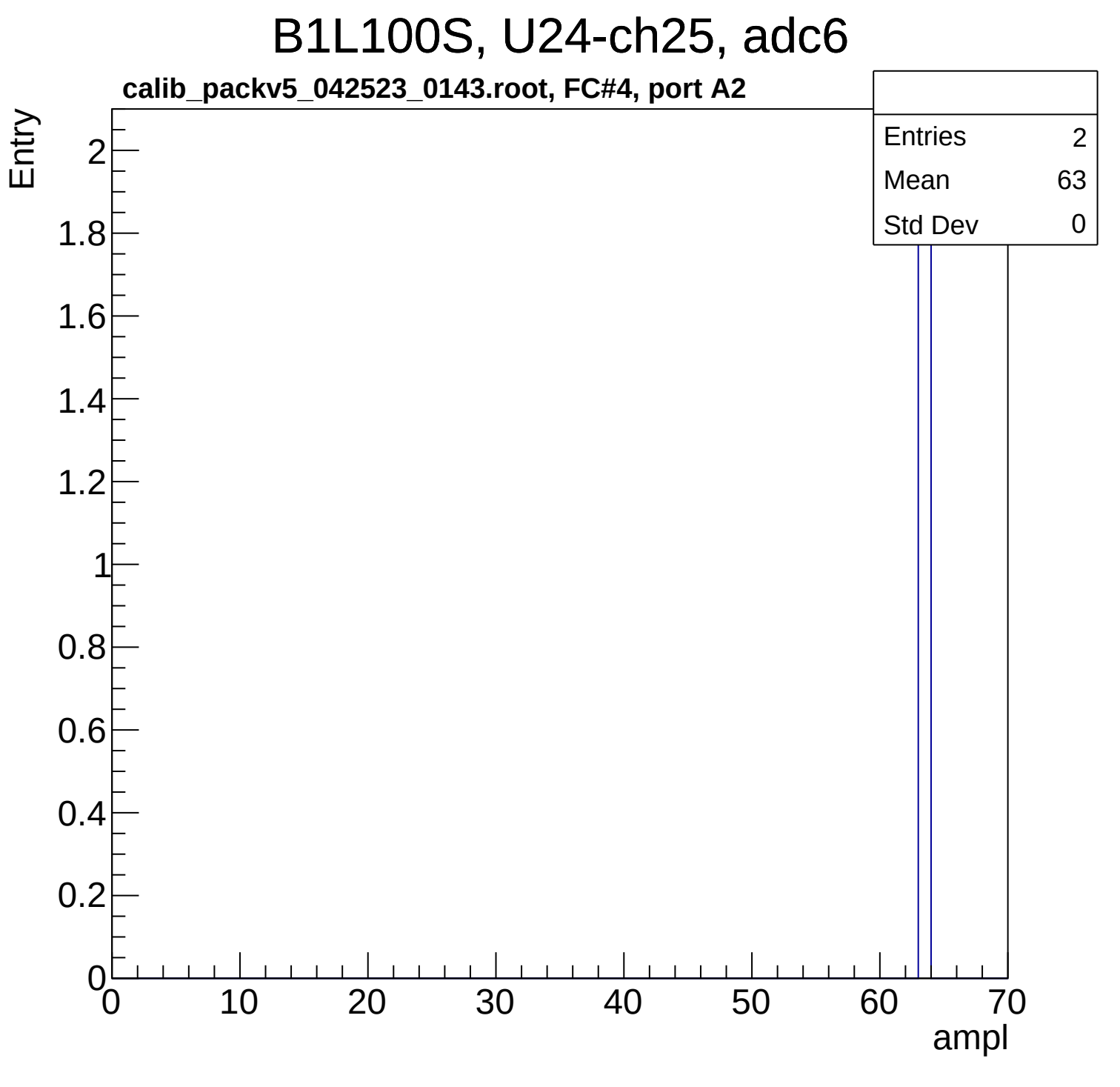
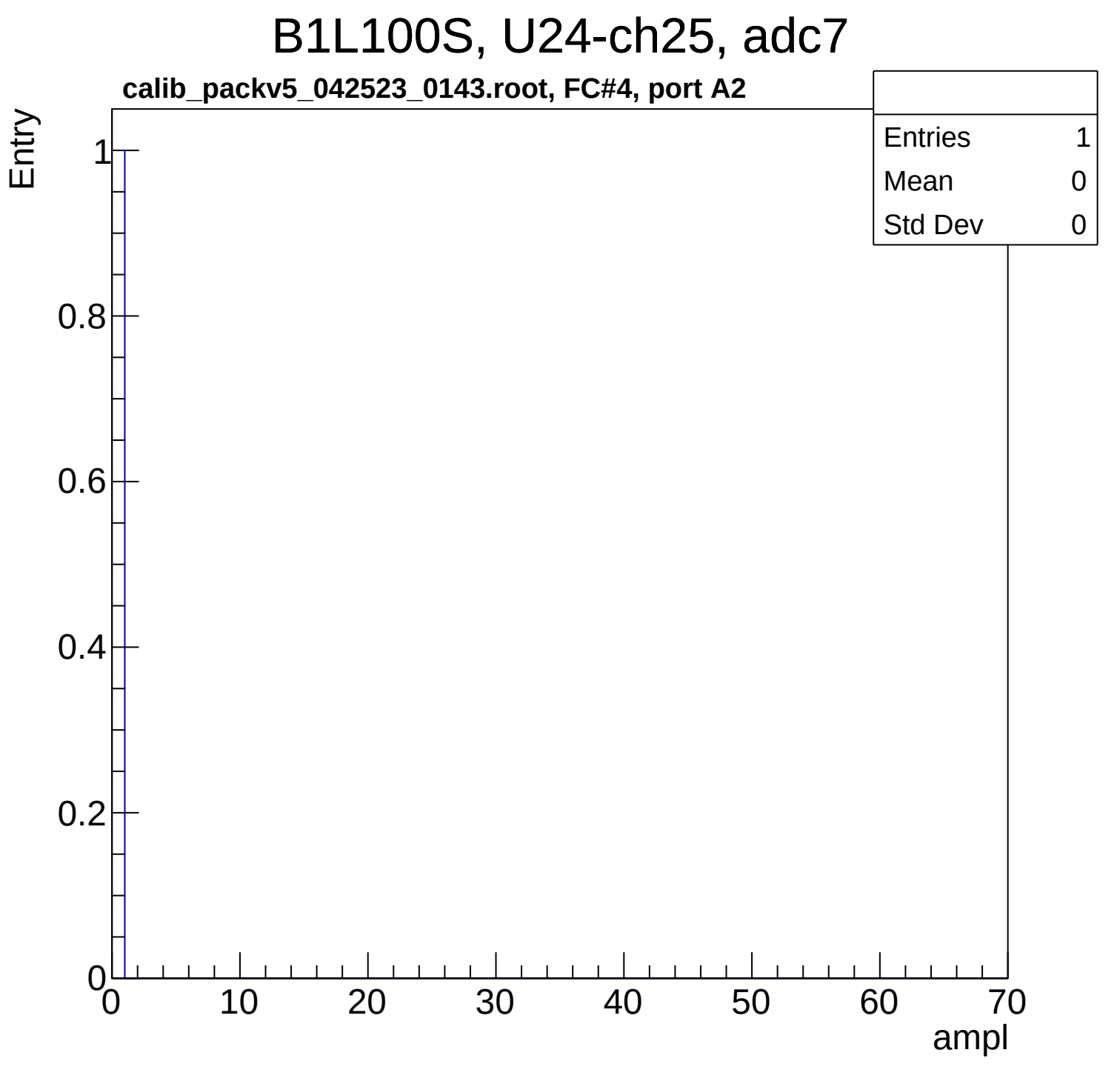
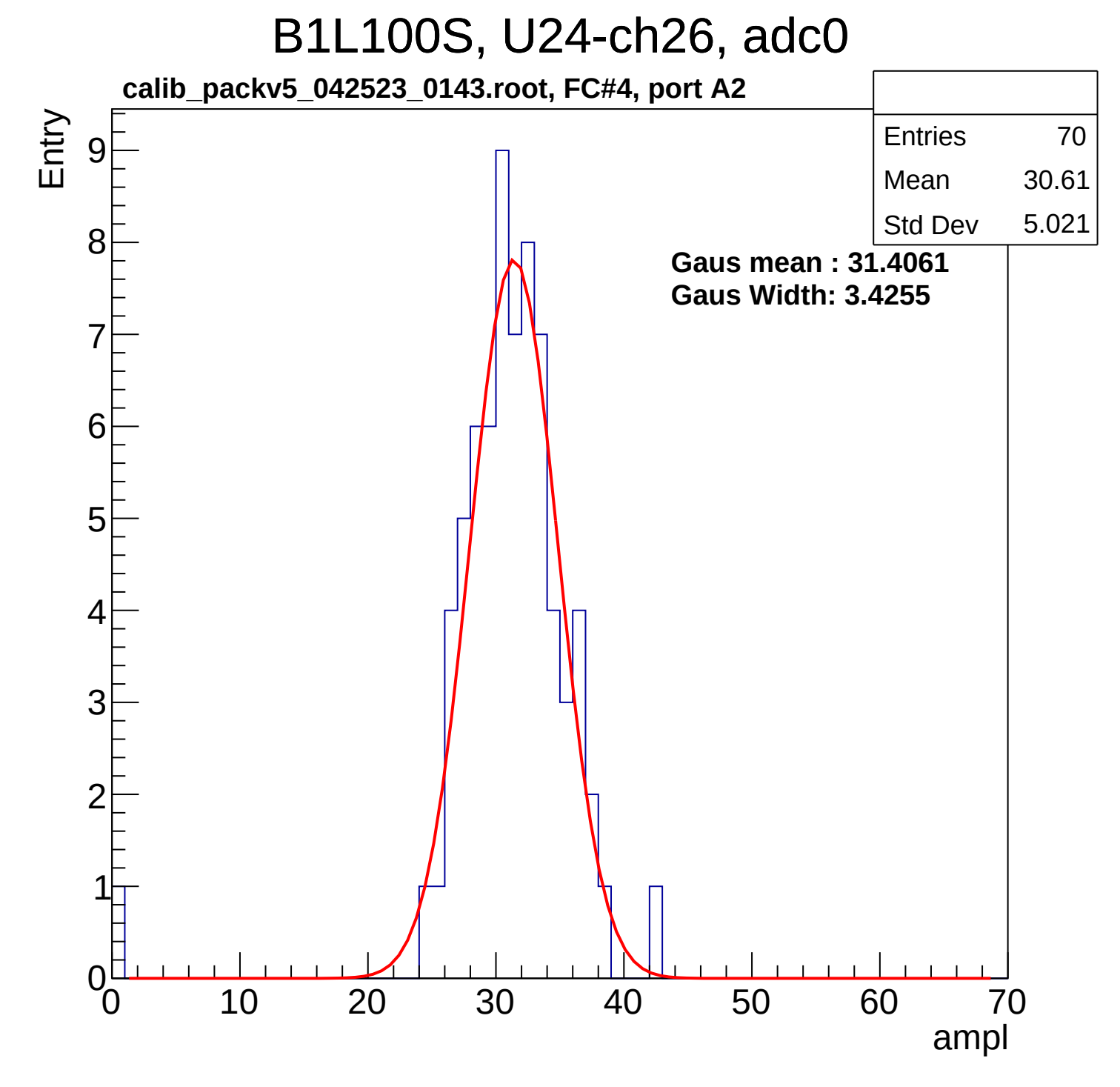
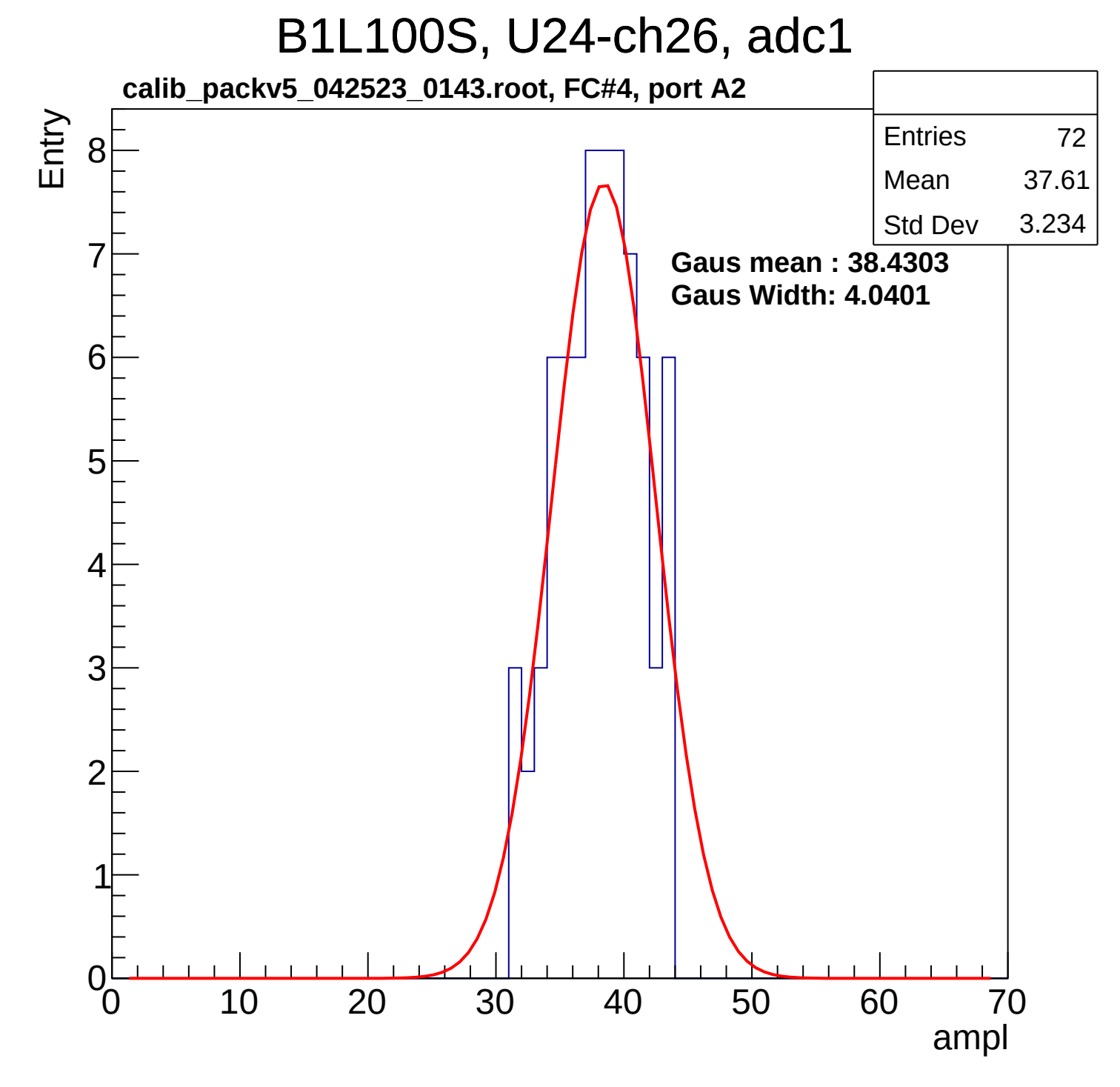
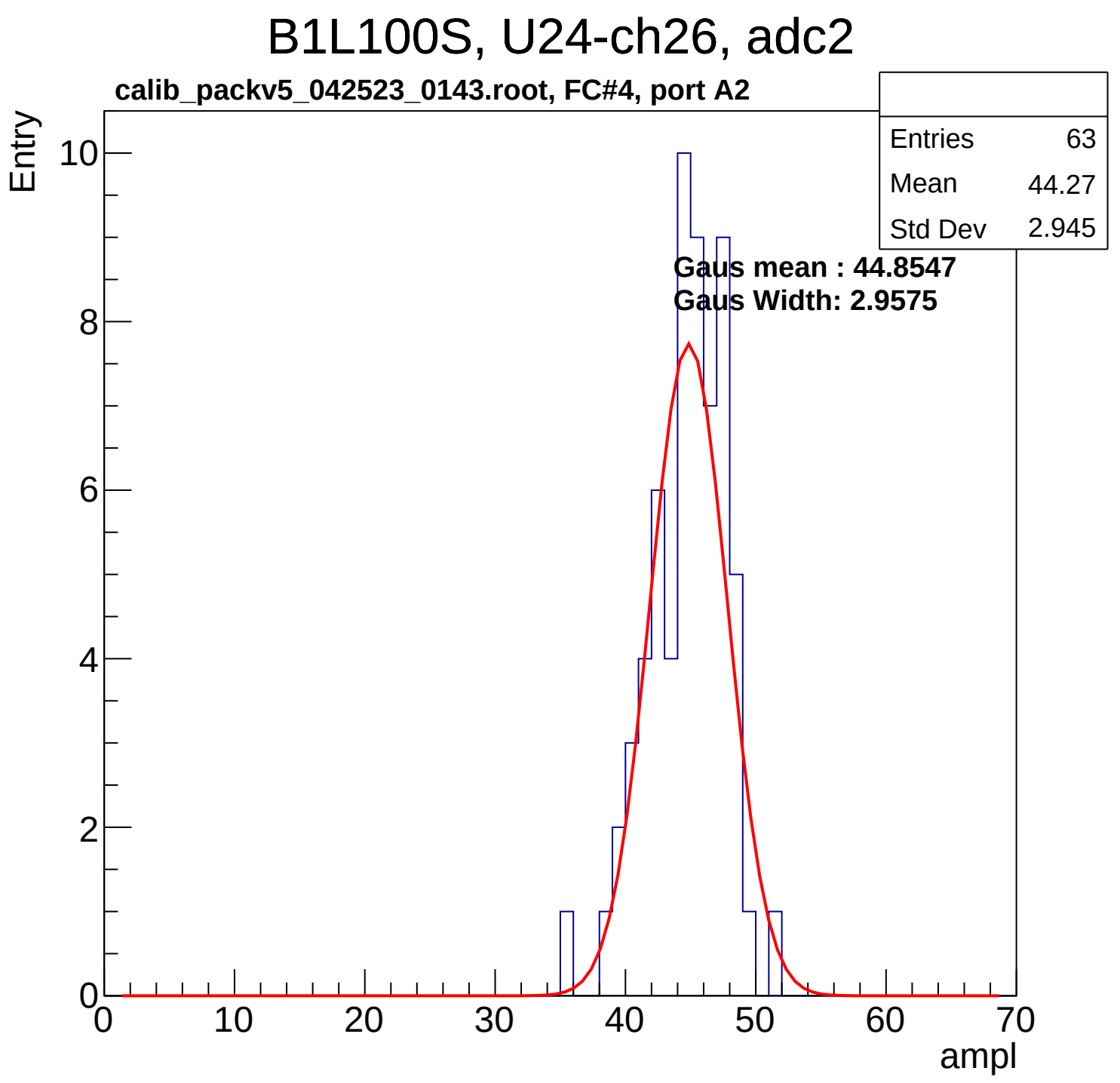
40
50
60
70


B1L100S, U24-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch25, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 30.61
Std Dev 5.021
Gaus mean : 31.4061
Gaus Width: 3.4255
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.61
Std Dev 3.234
Gaus mean : 38.4303
Gaus Width: 4.0401
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.27
Std Dev 2.945
Gaus mean : 44.8547
Gaus Width: 2.9575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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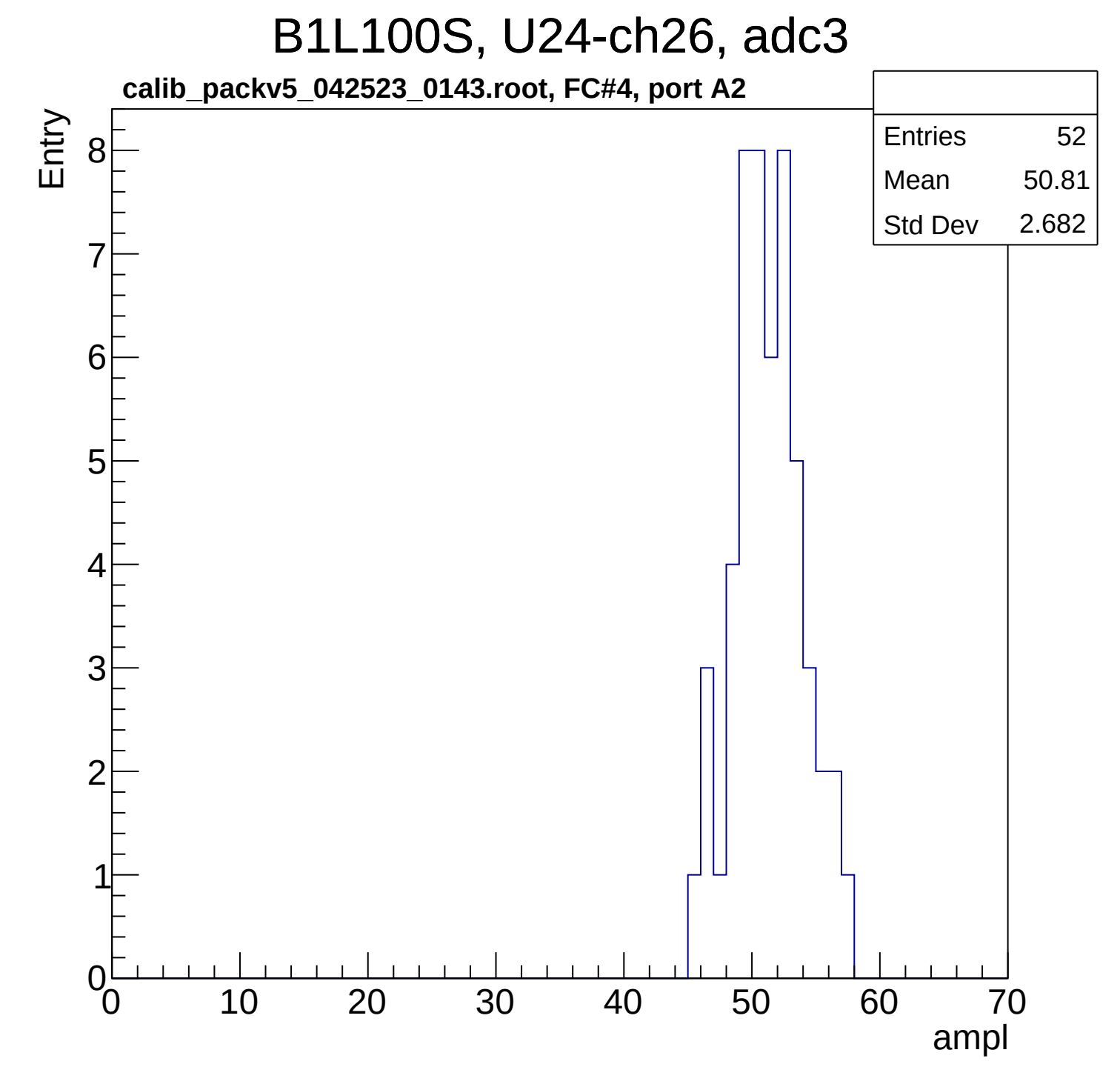
B1L100S, U24-ch26, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 50.81
Std Dev 2.682
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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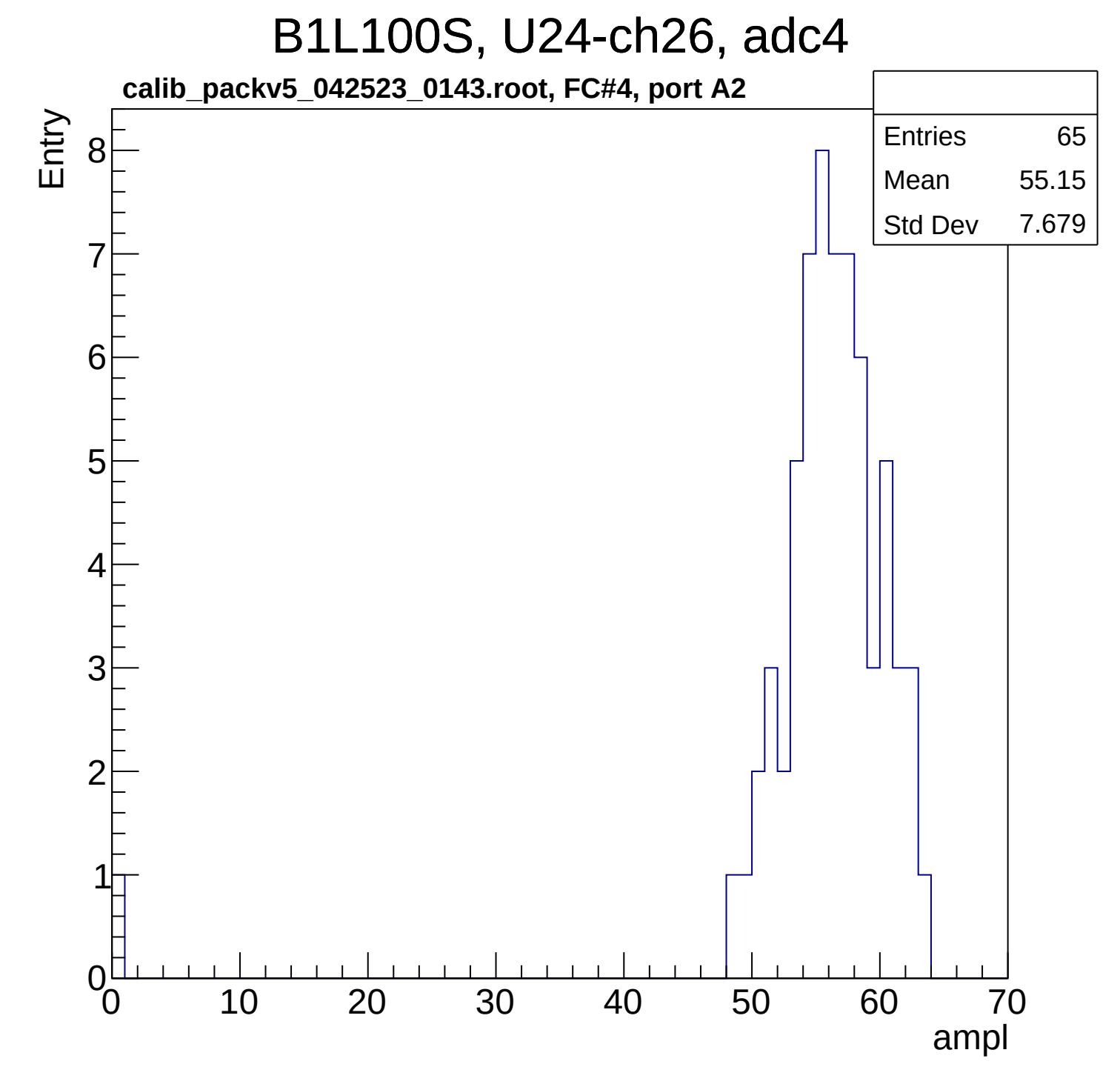
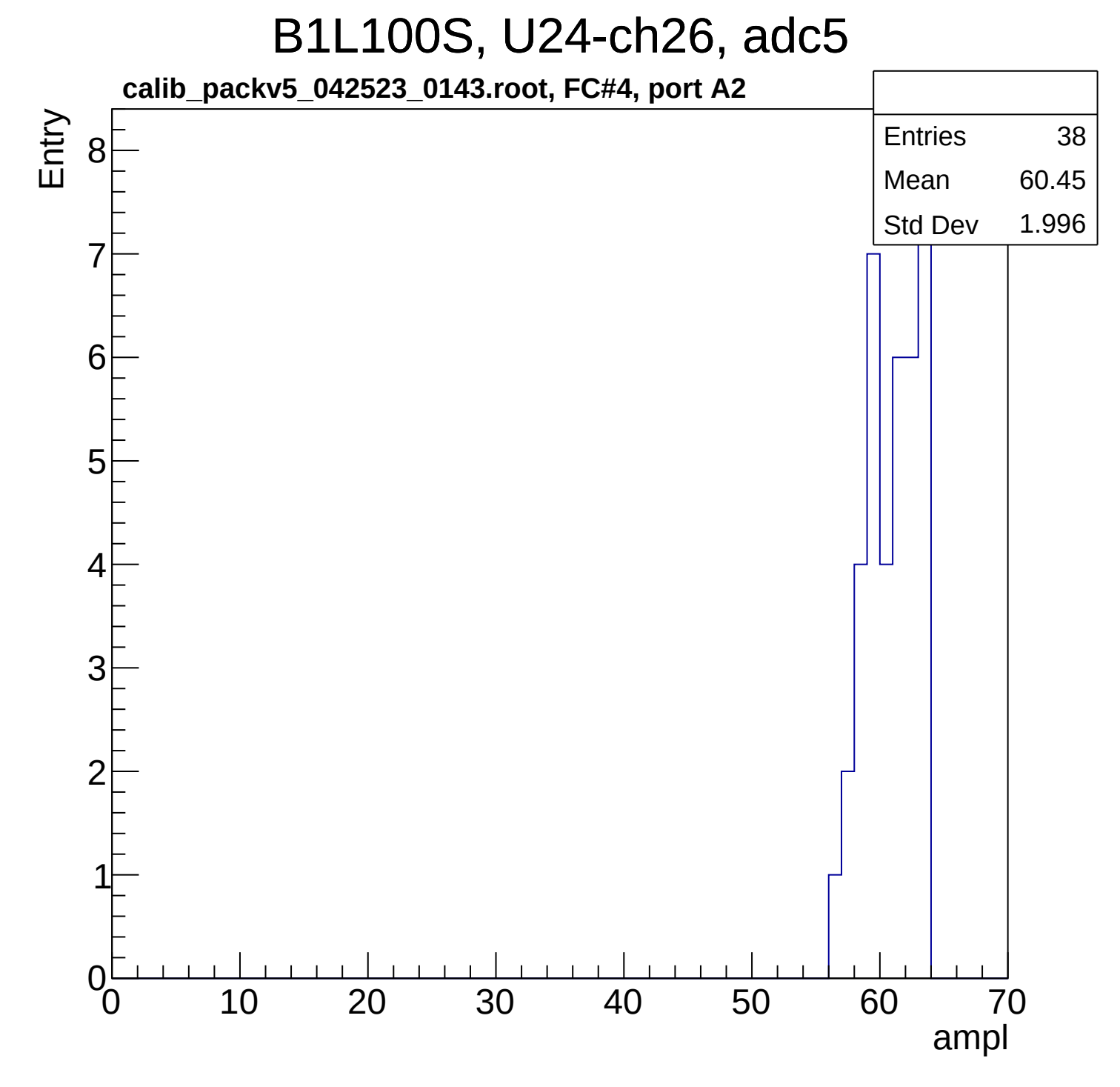

B1L100S, U24-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.15
Std Dev 7.679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch26, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.45
Std Dev 1.996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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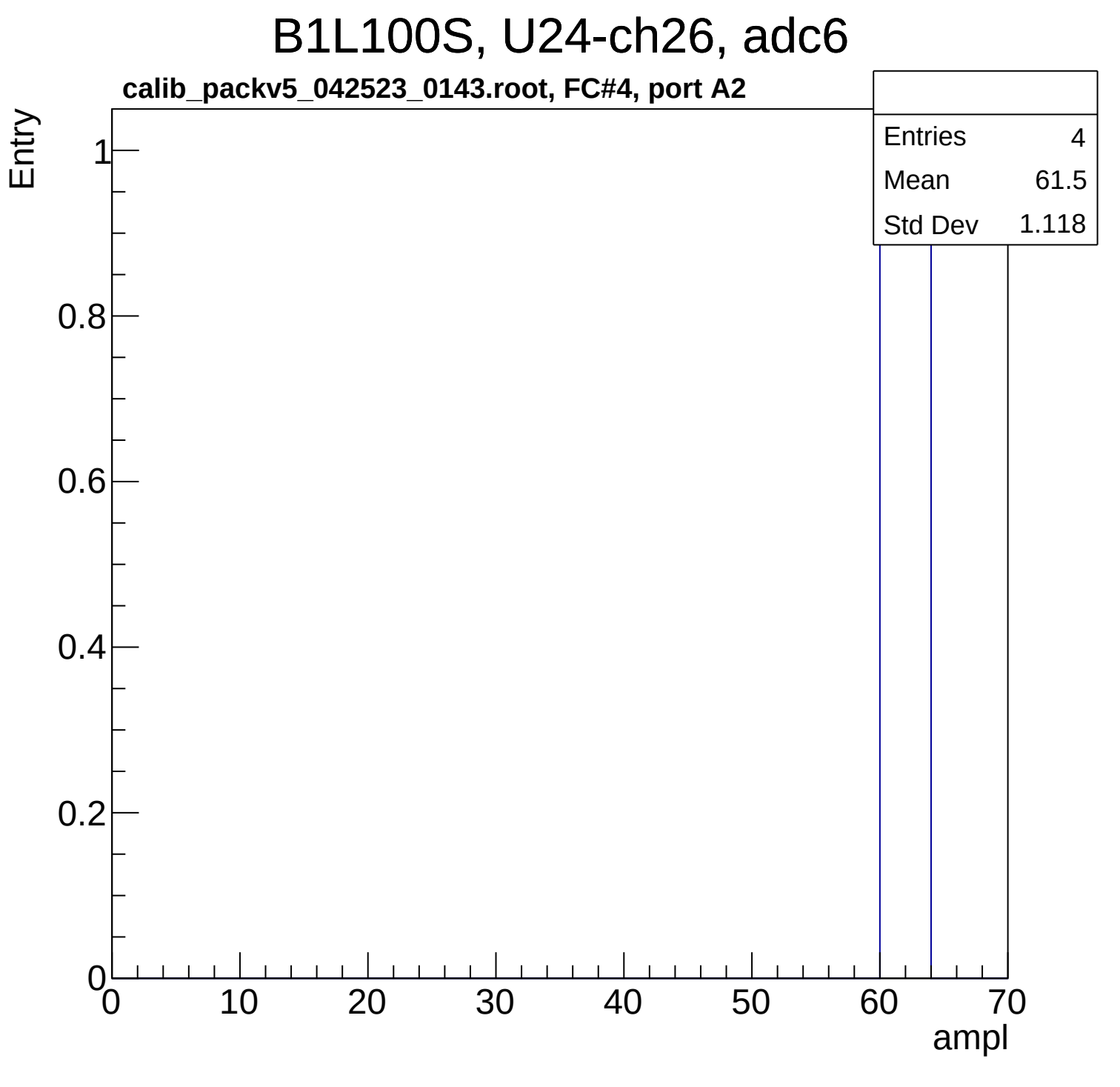
B1L100S, U24-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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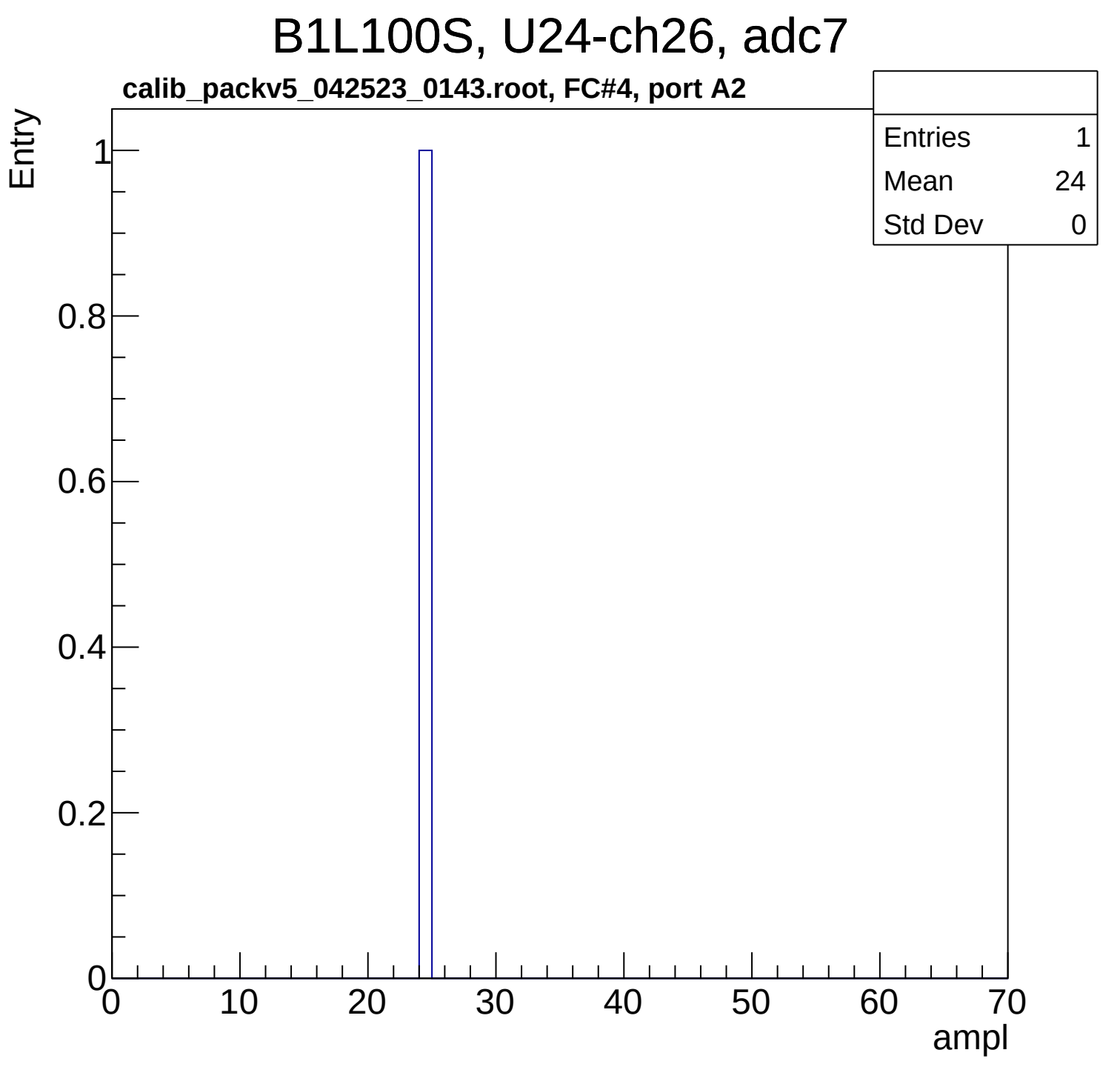
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch26, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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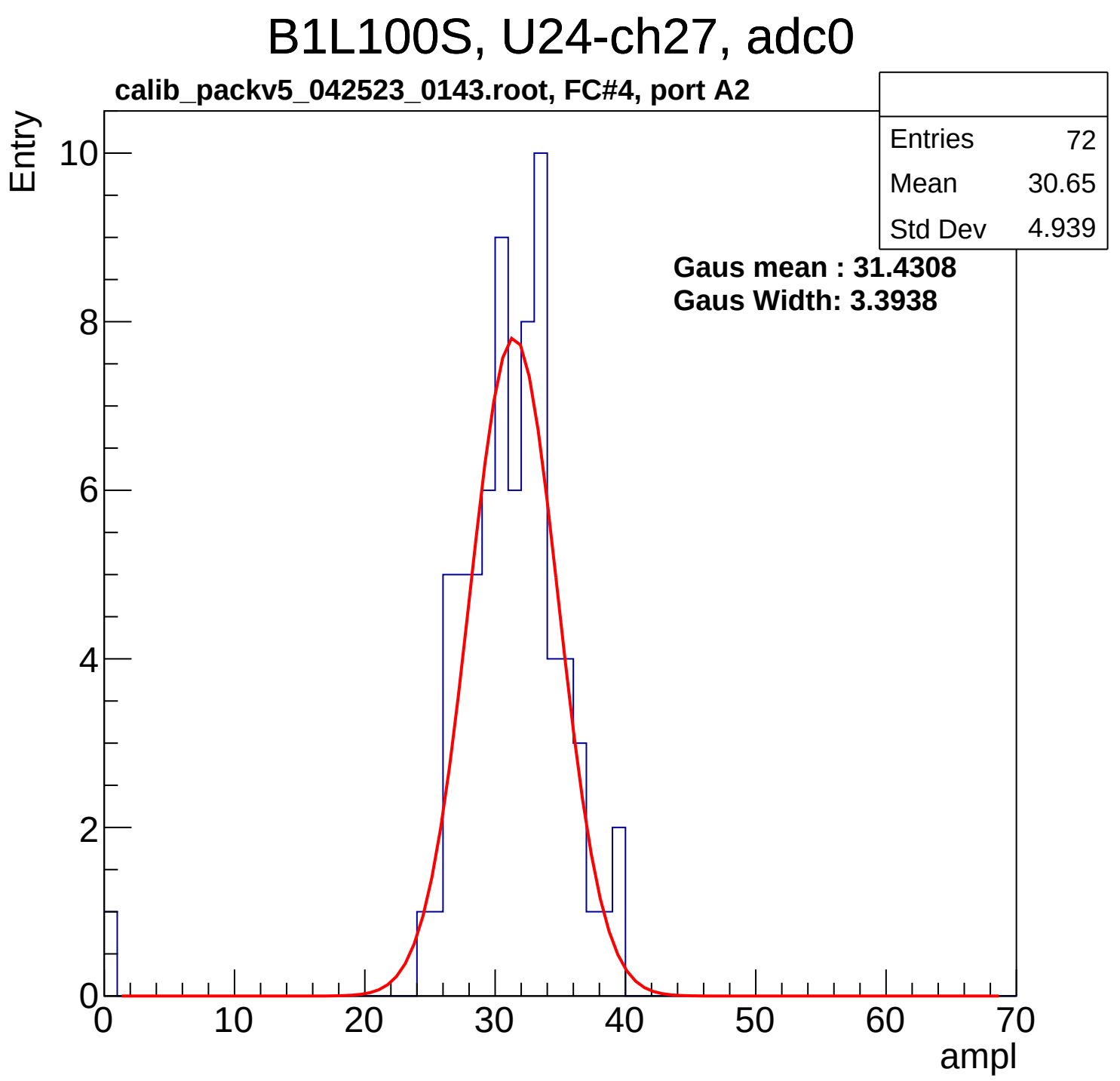
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.65
Std Dev 4.939
Gaus mean : 31.4308
Gaus Width: 3.3938
Entry
ampl
0
2
4
6
8
10
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60
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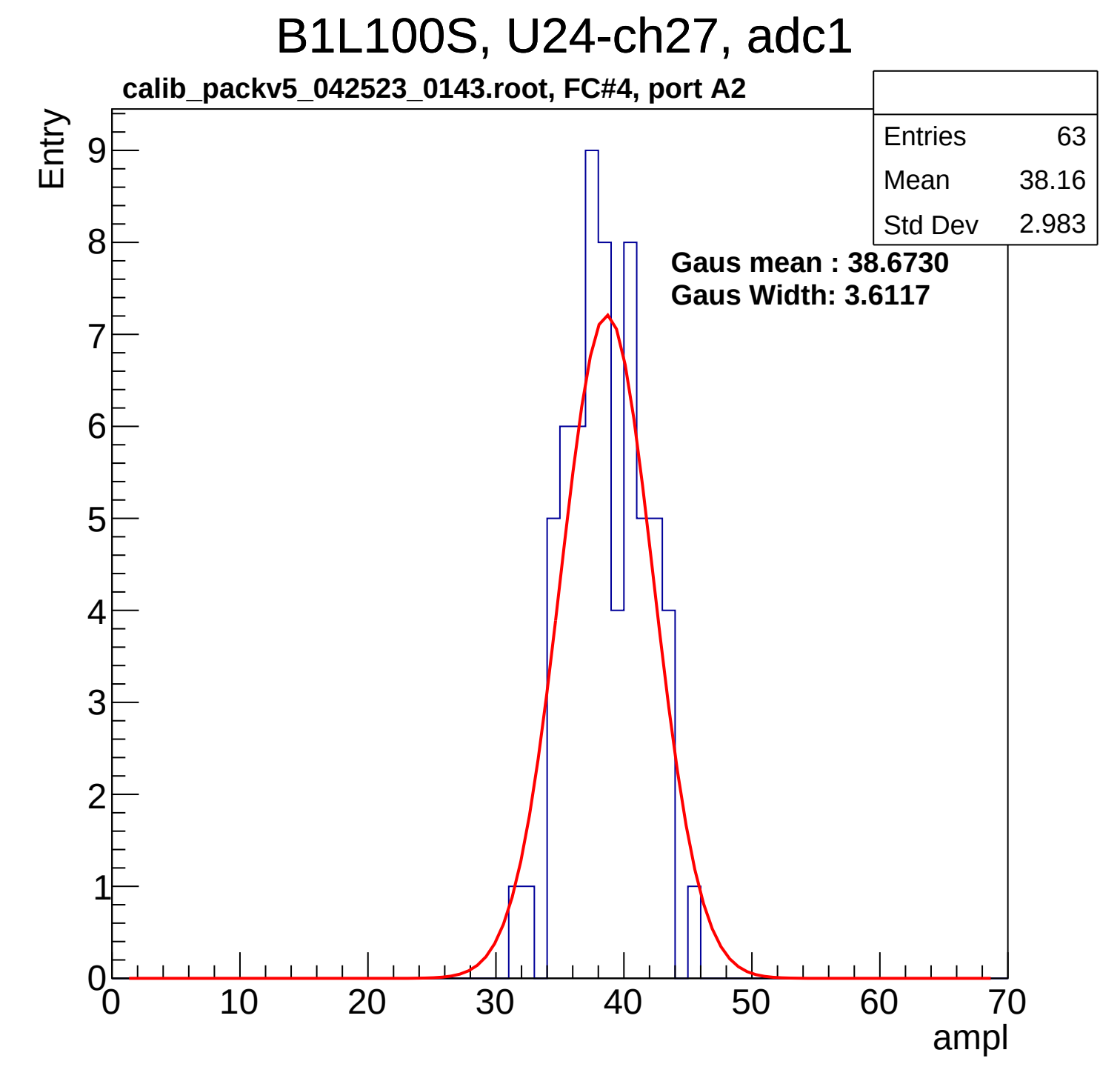
B1L100S, U24-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 38.16
Std Dev 2.983
Gaus mean : 38.6730
Gaus Width: 3.6117
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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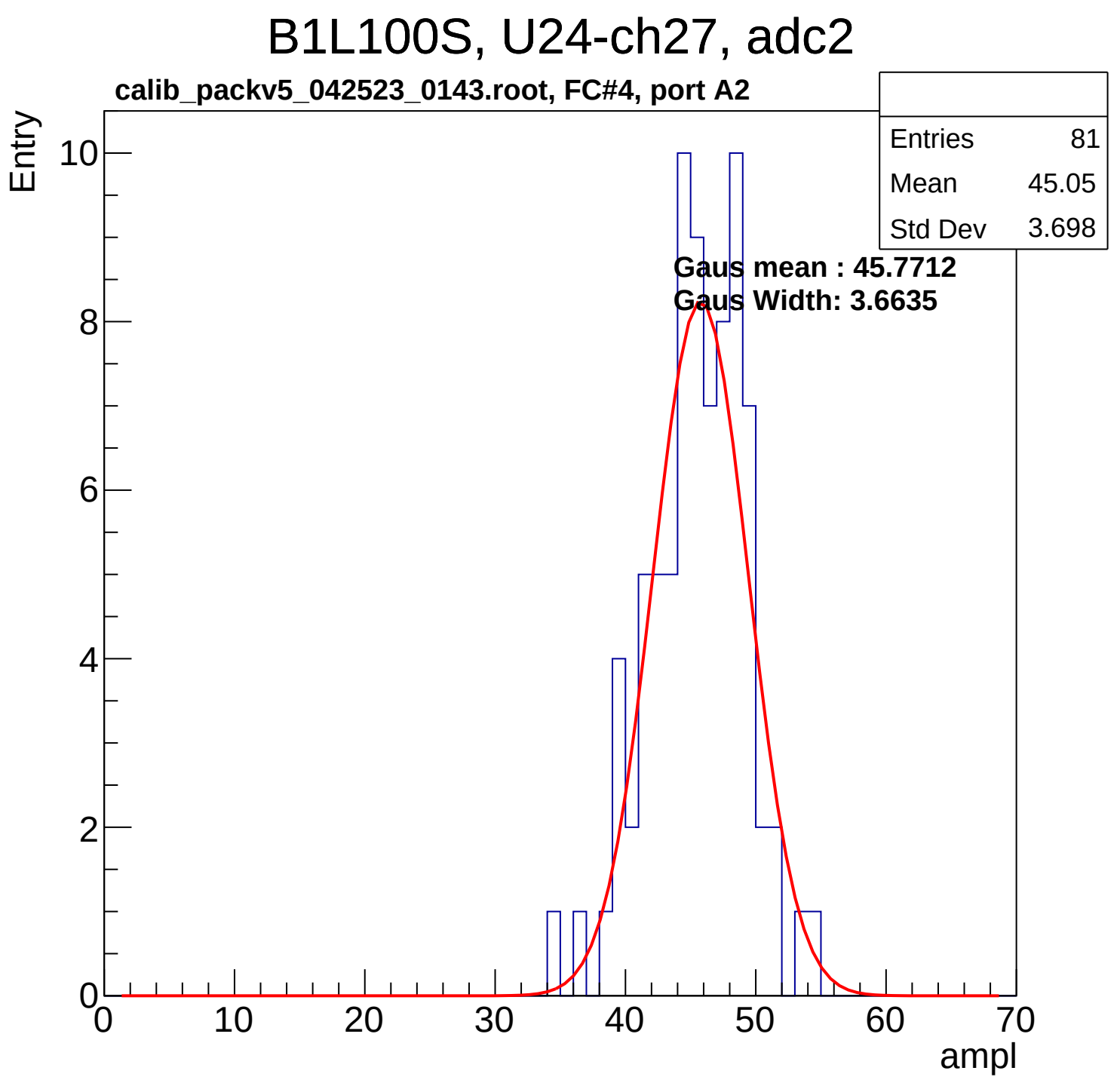
B1L100S, U24-ch27, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 45.05
Std Dev 3.698
Gaus mean : 45.7712
Gaus Width: 3.6635
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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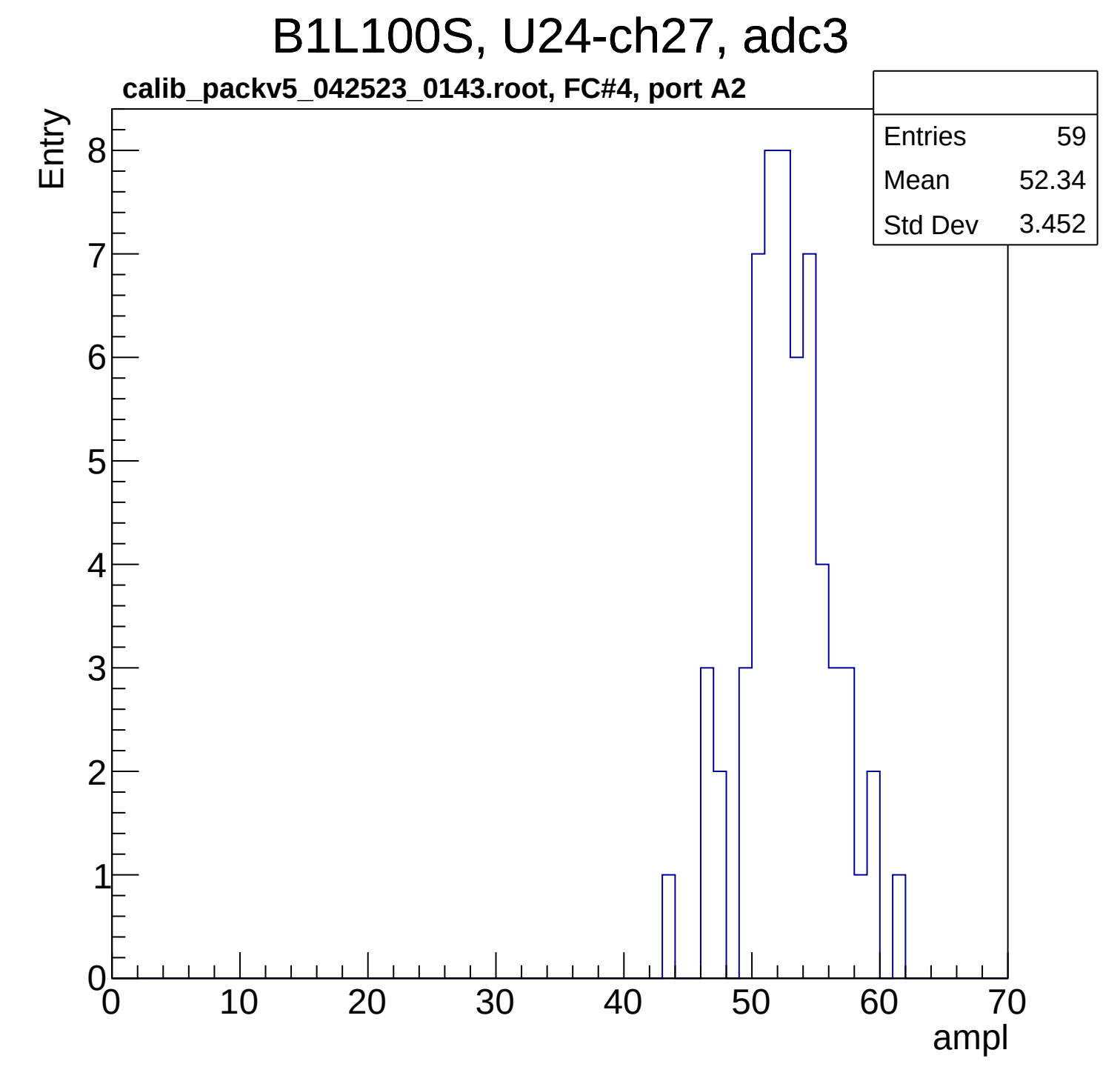
B1L100S, U24-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 52.34
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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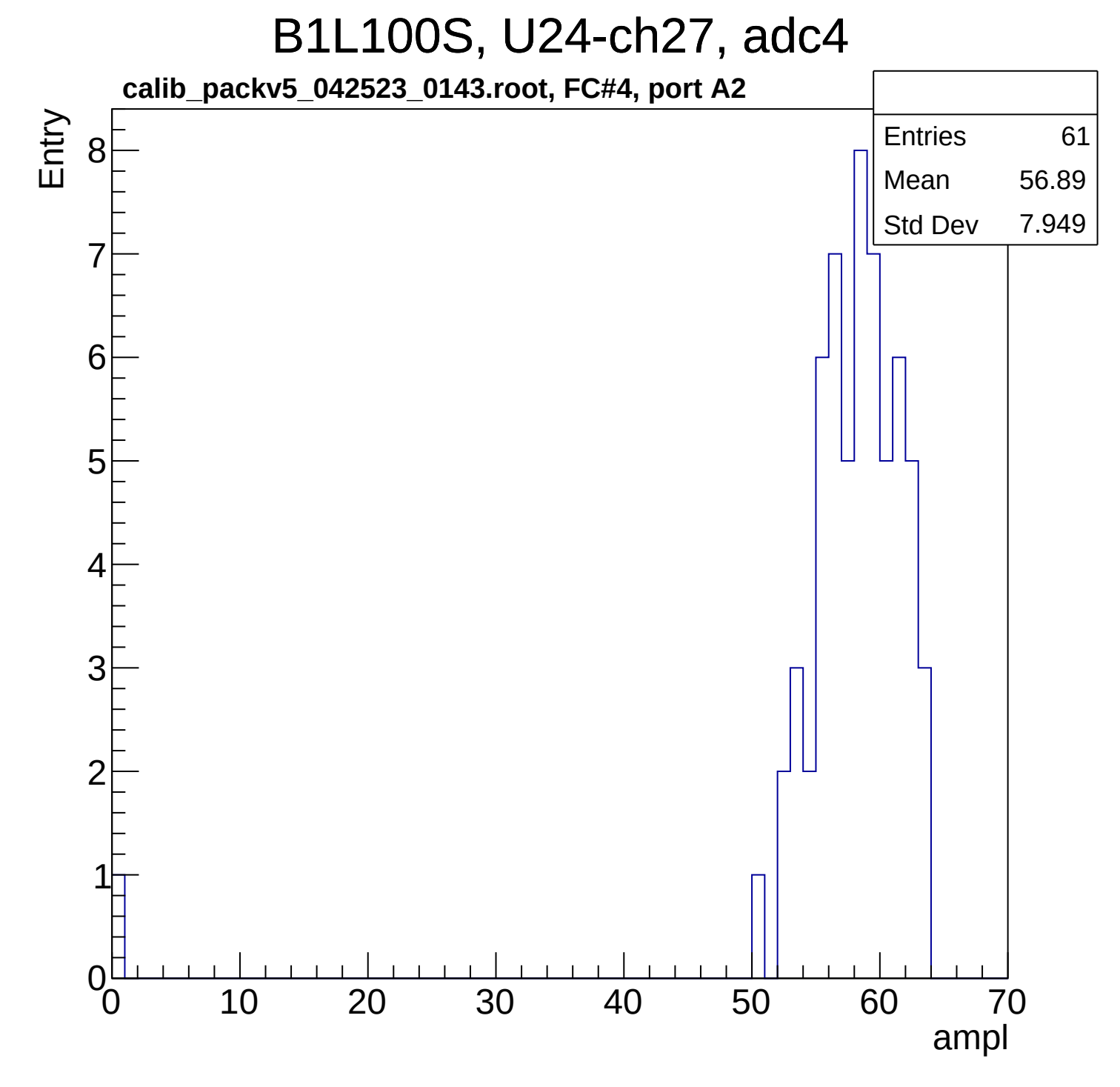
B1L100S, U24-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 56.89
Std Dev 7.949
Entry
ampl
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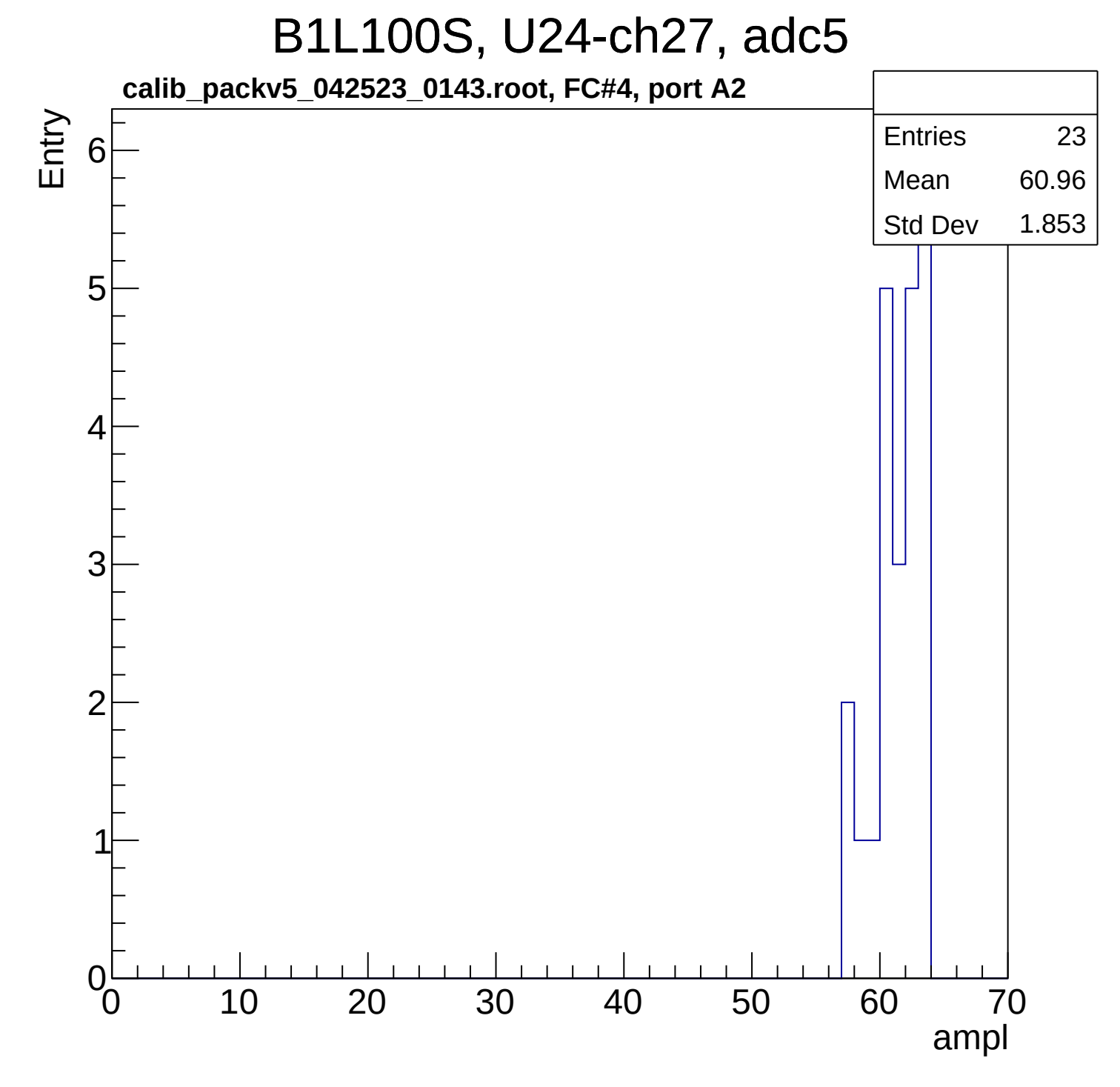
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 60.96
Std Dev 1.853
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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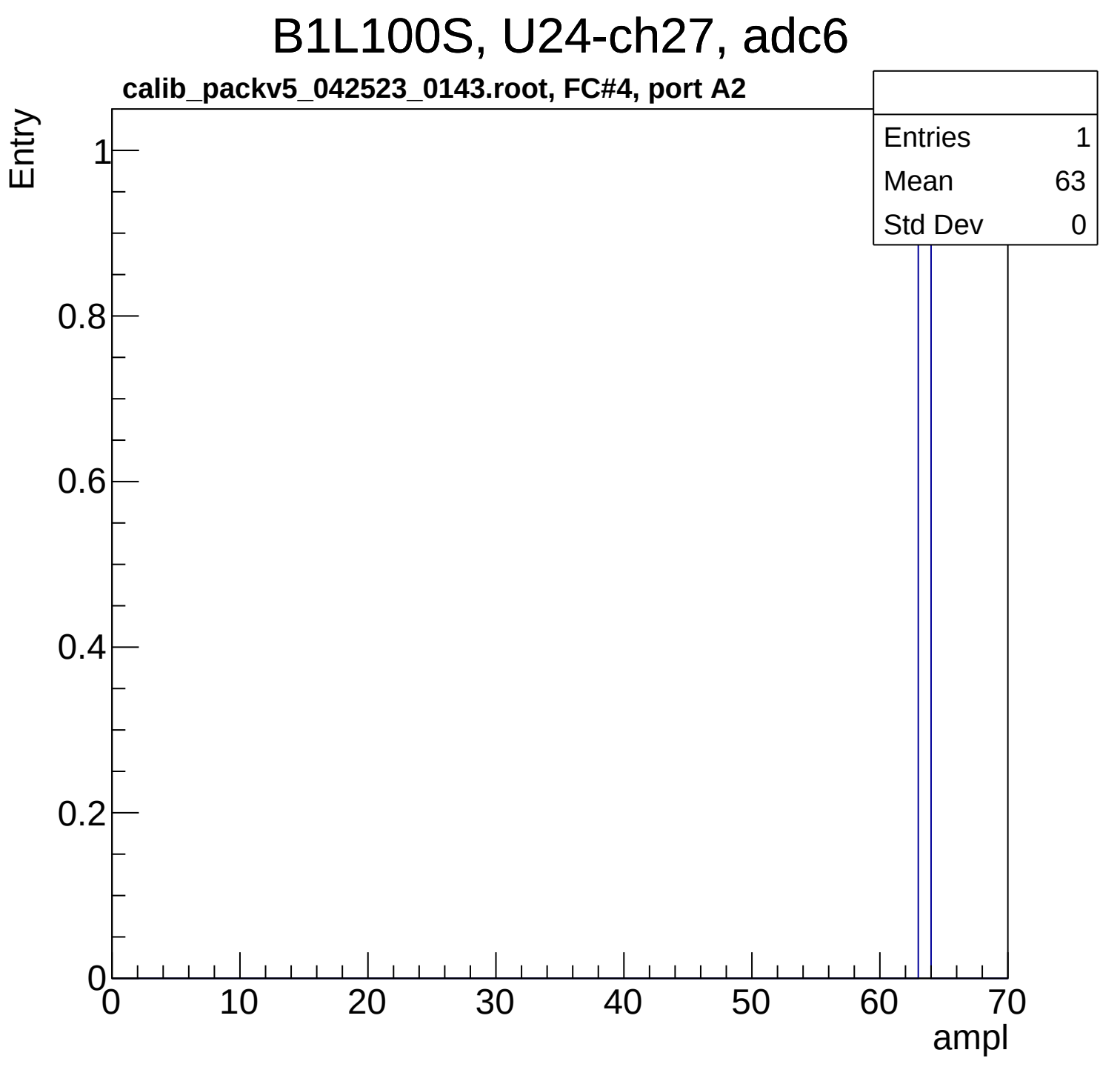
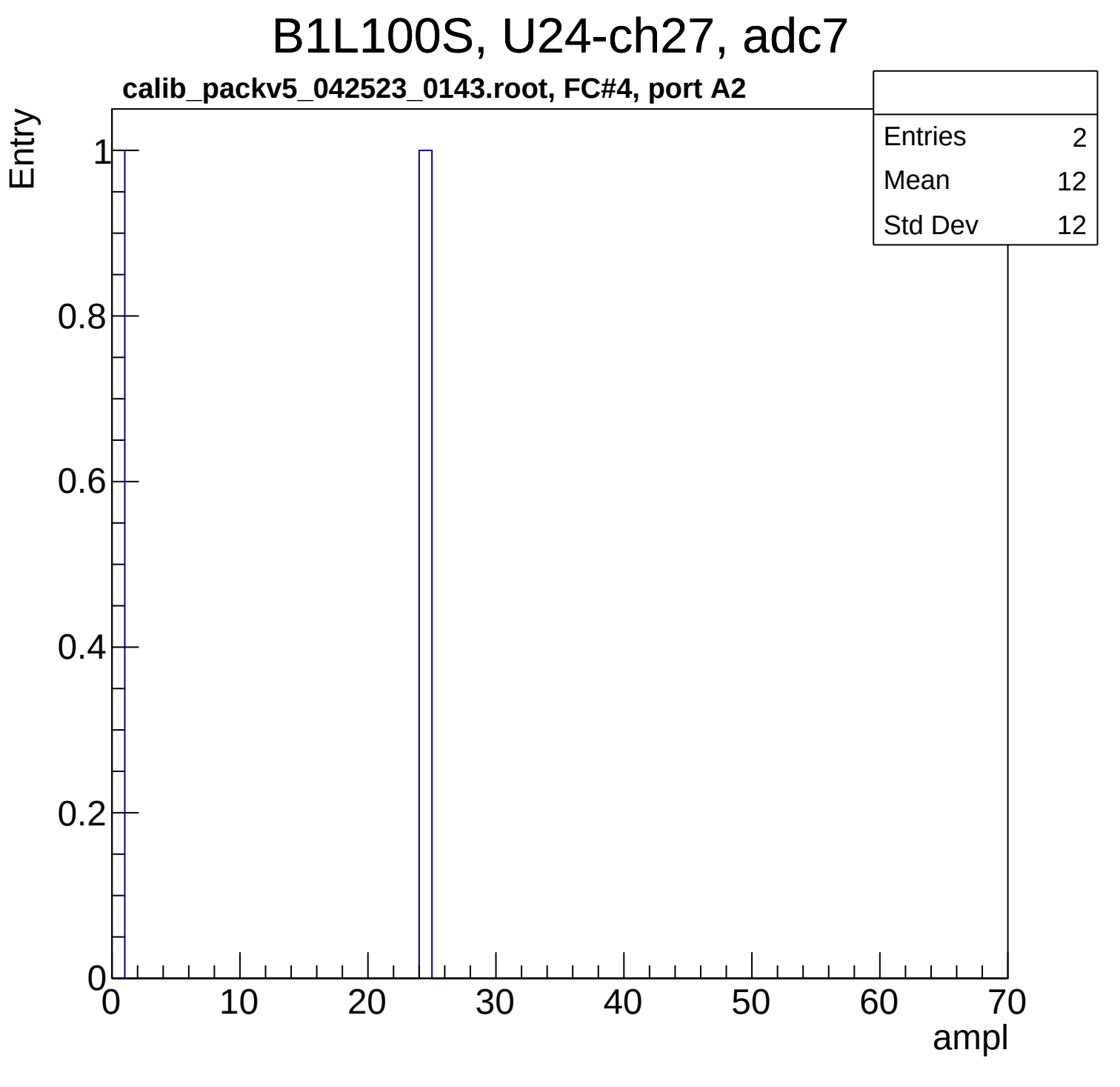

B1L100S, U24-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch27, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12
Std Dev 12
Entry
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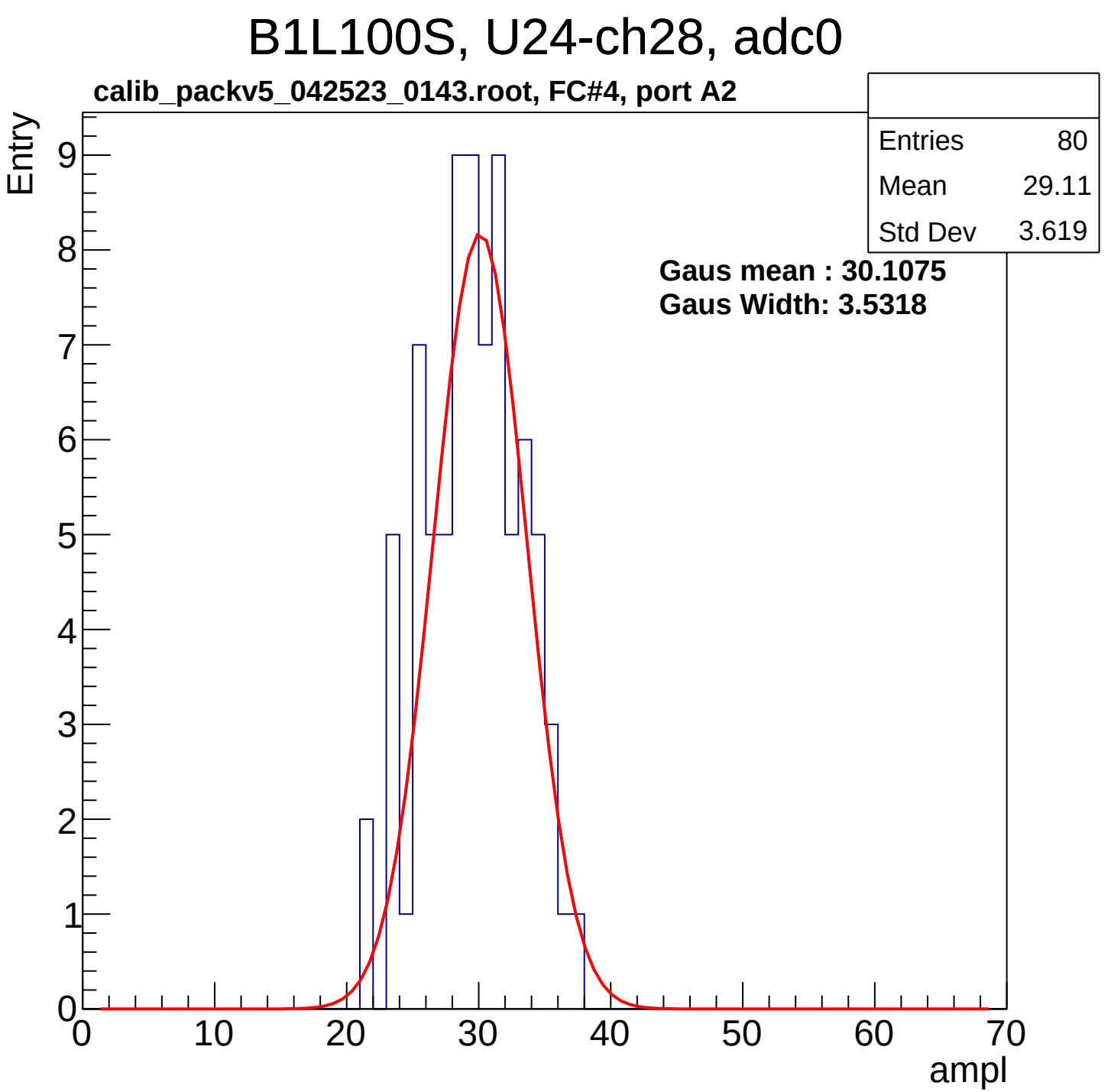
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.11
Std Dev 3.619
Gaus mean : 30.1075
Gaus Width: 3.5318
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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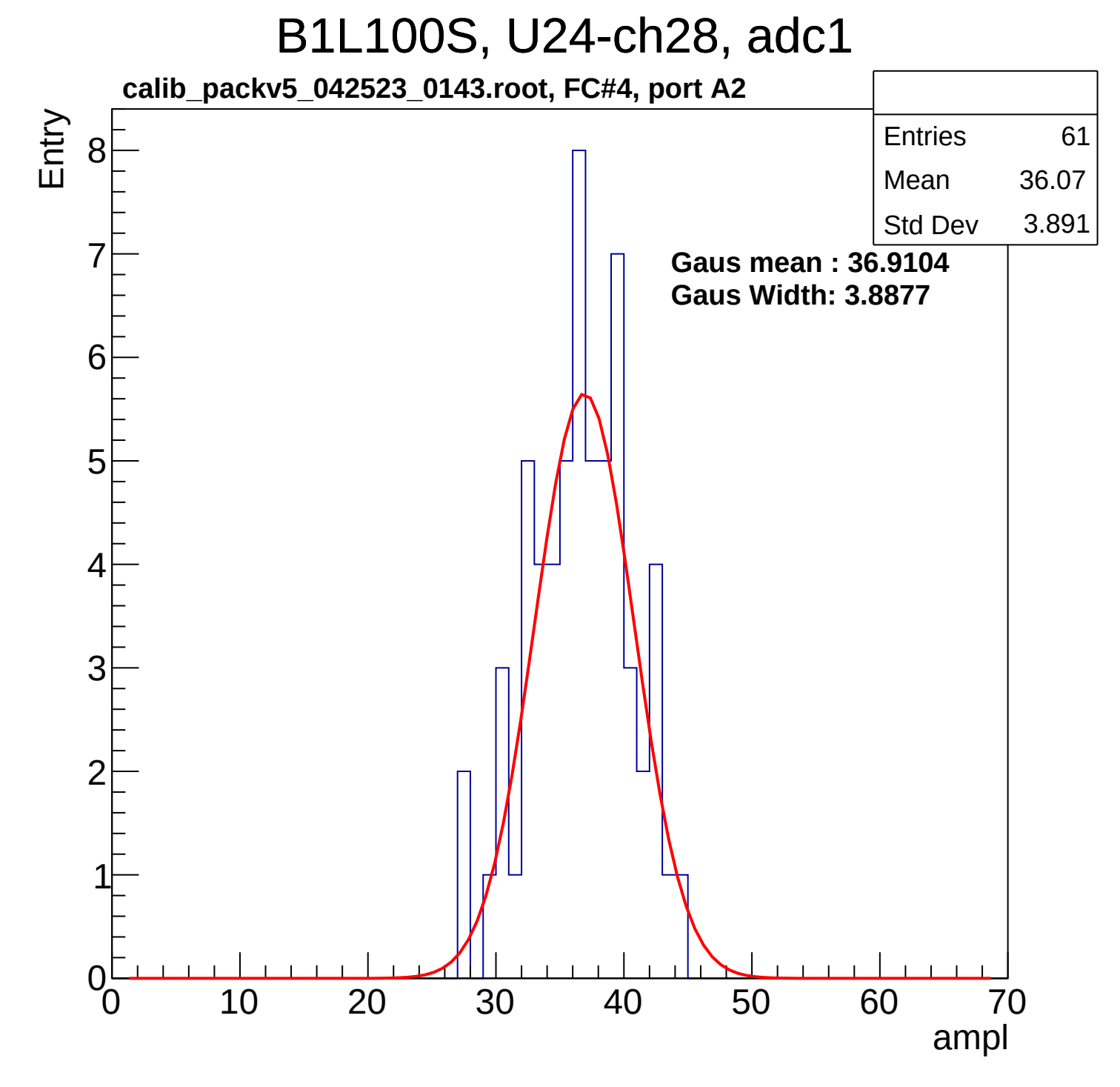
B1L100S, U24-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 36.07
Std Dev 3.891
Gaus mean : 36.9104
Gaus Width: 3.8877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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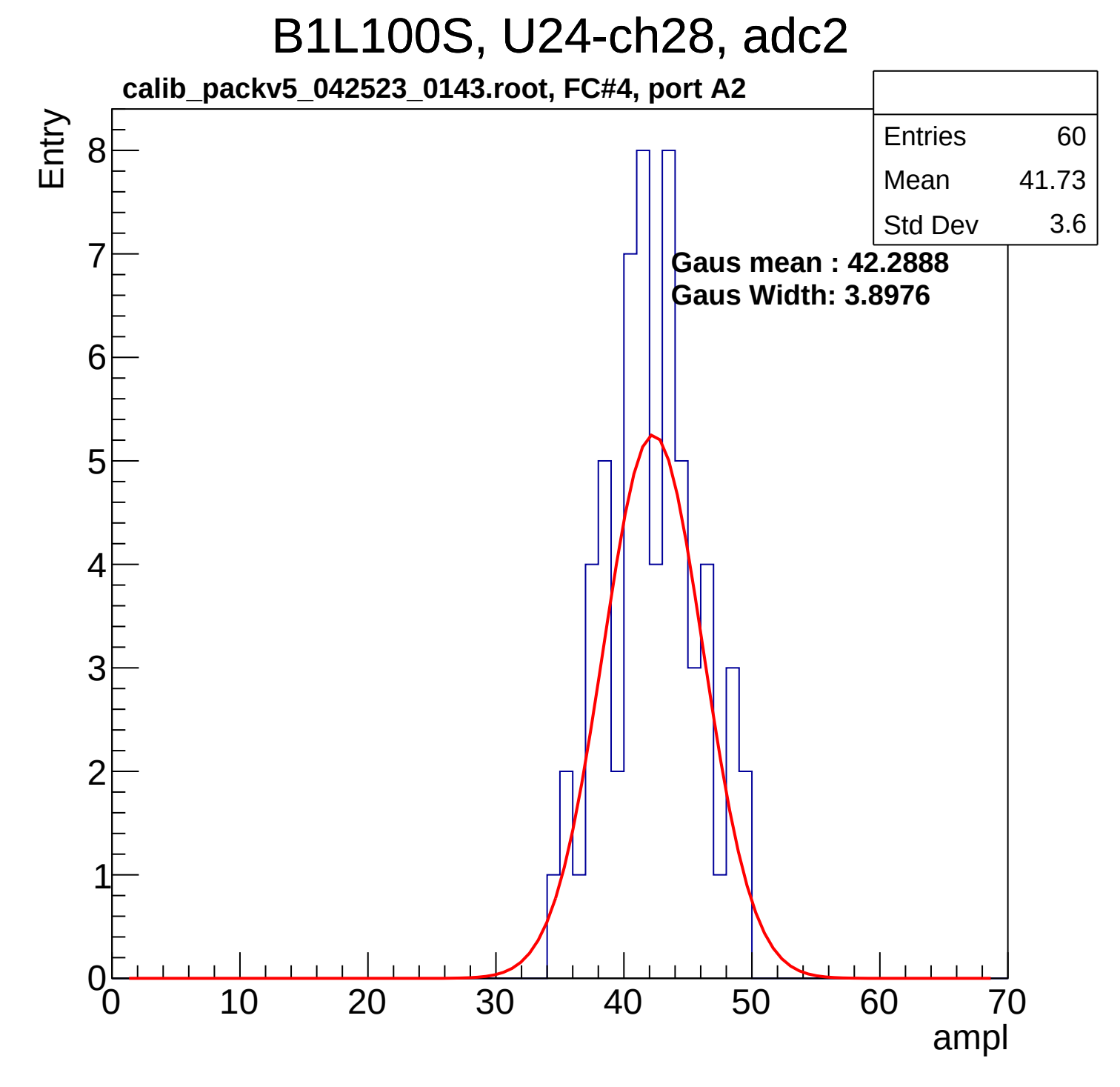
B1L100S, U24-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.73
Std Dev 3.6
Gaus mean : 42.2888
Gaus Width: 3.8976
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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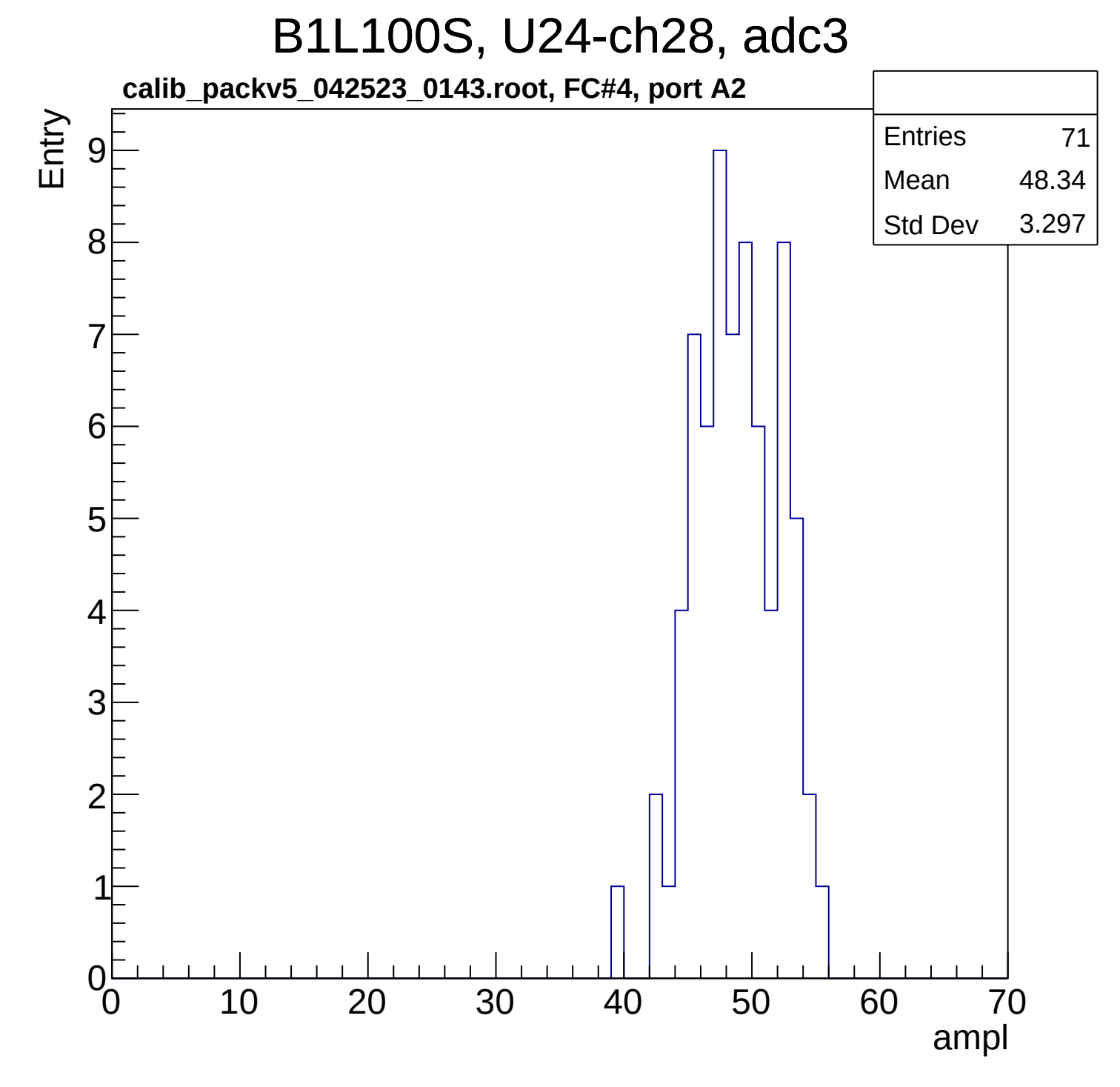
B1L100S, U24-ch28, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.34
Std Dev 3.297
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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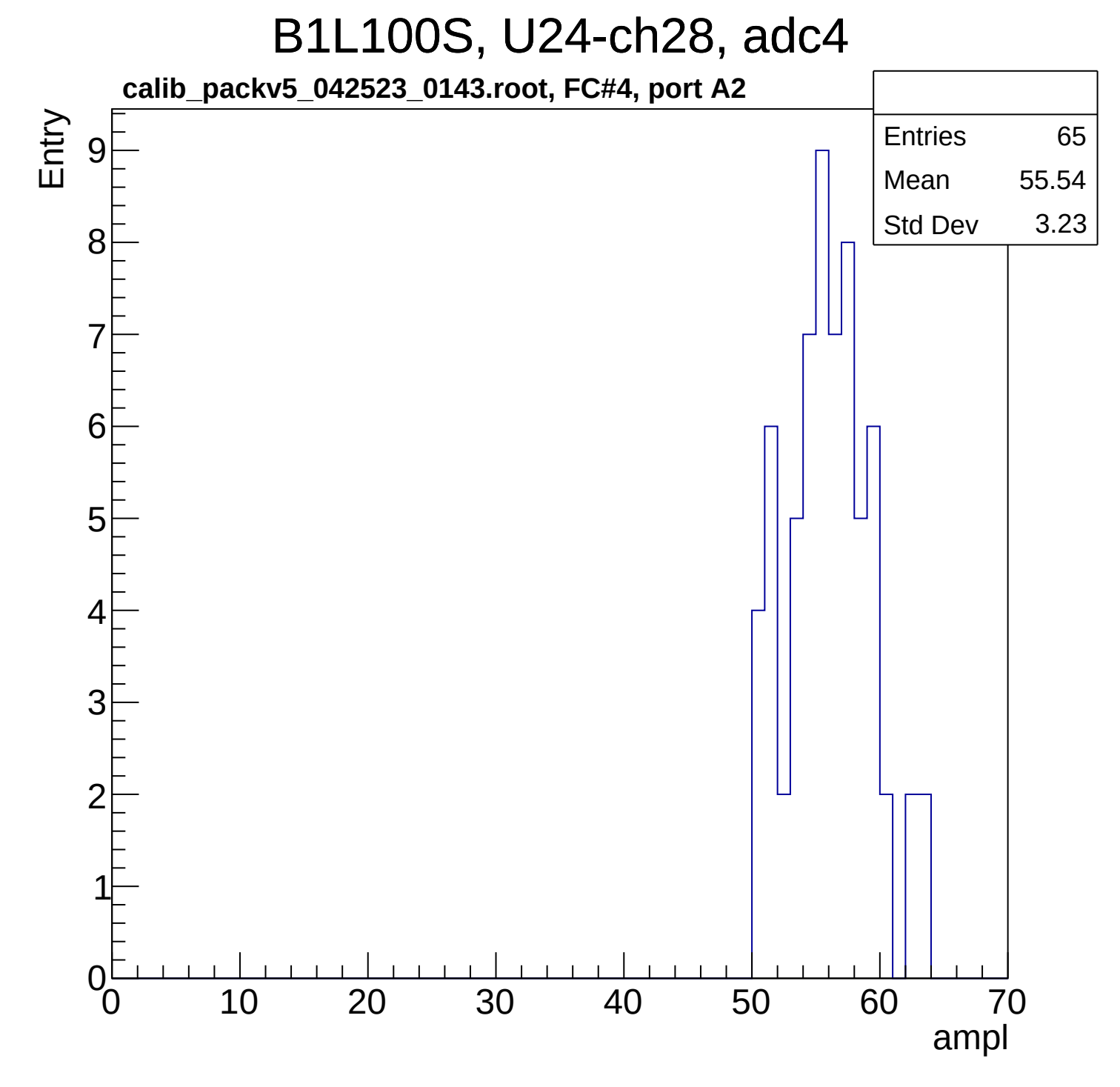
B1L100S, U24-ch28, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.54
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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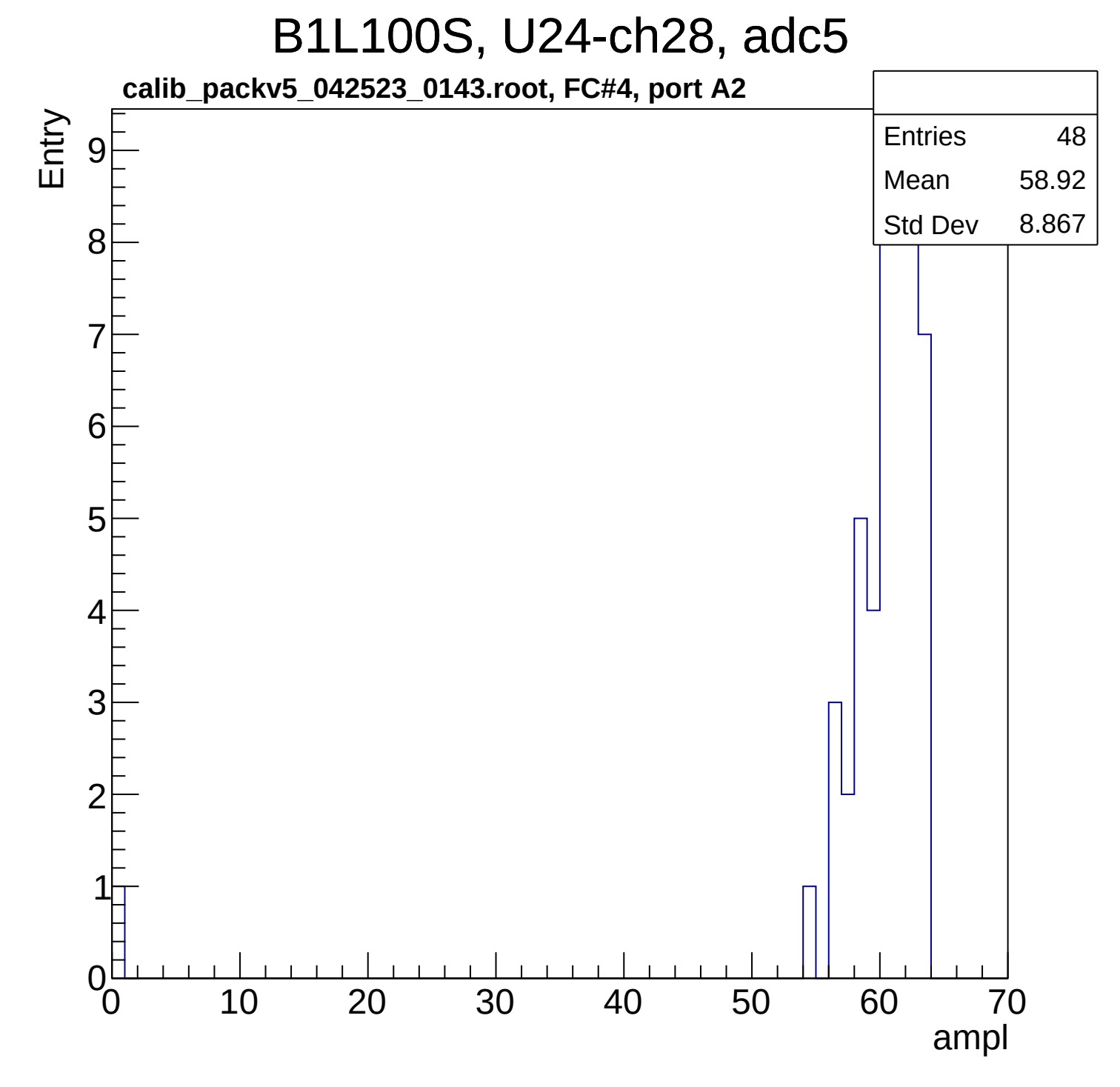
B1L100S, U24-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.92
Std Dev 8.867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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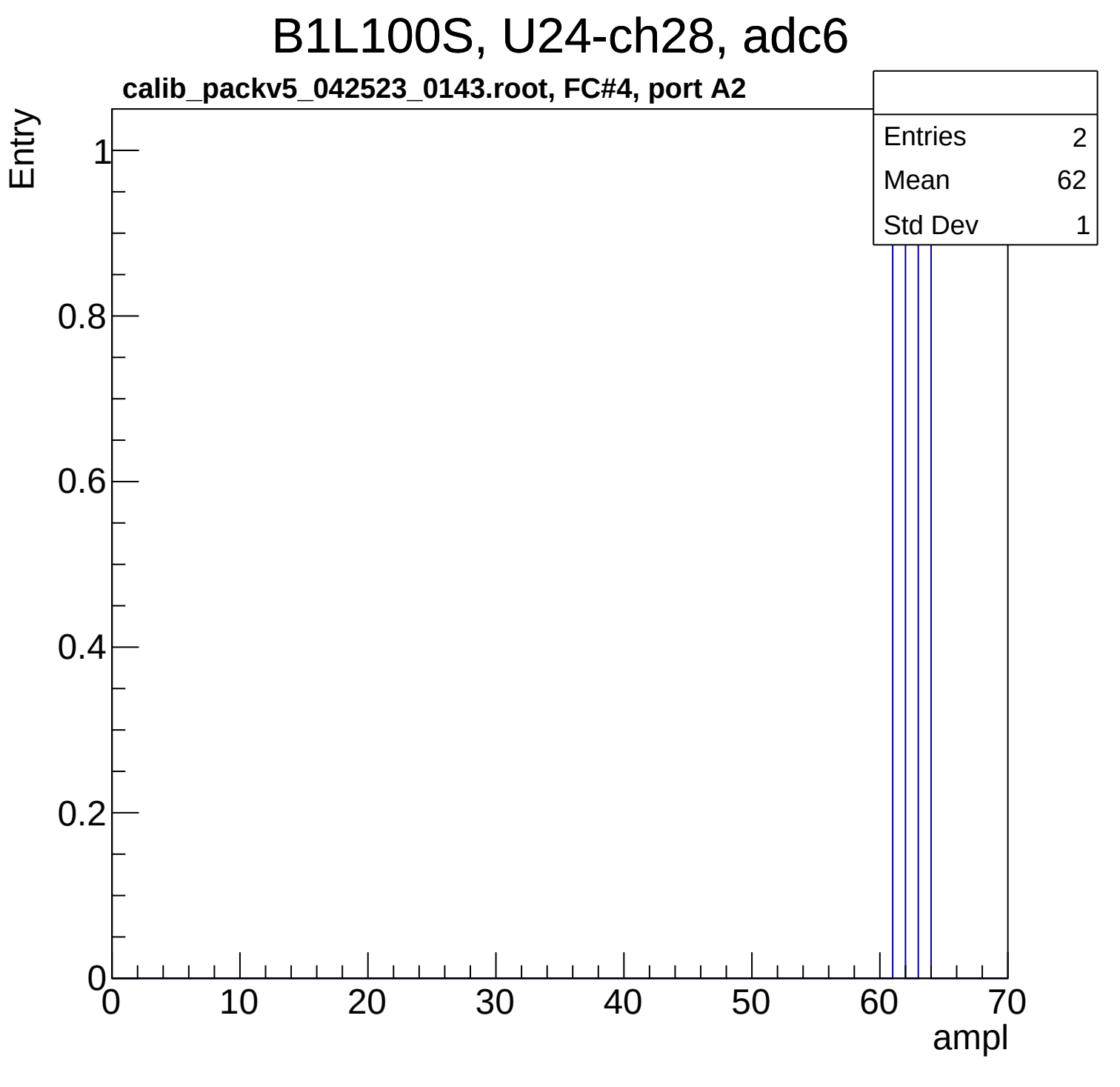
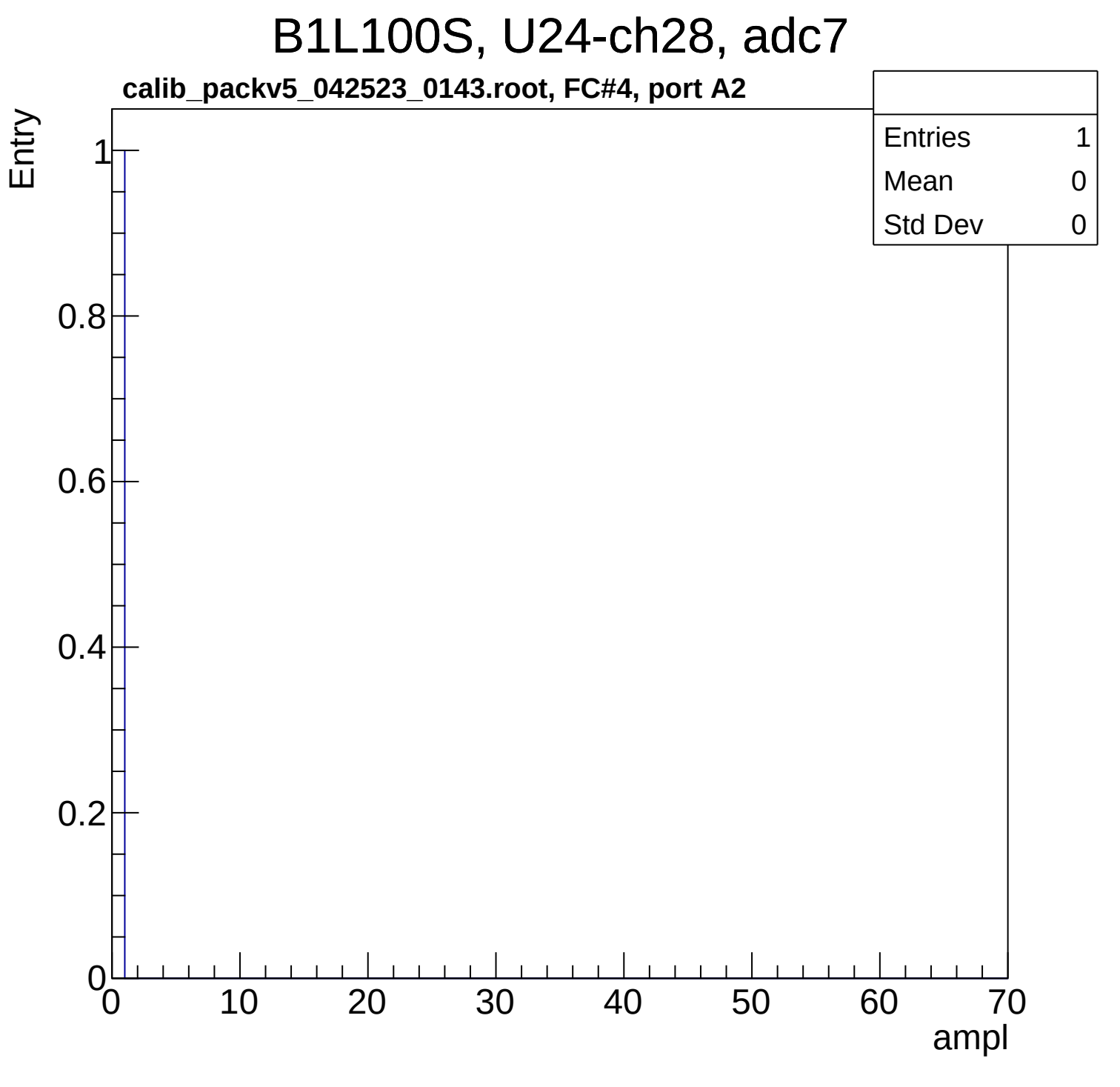
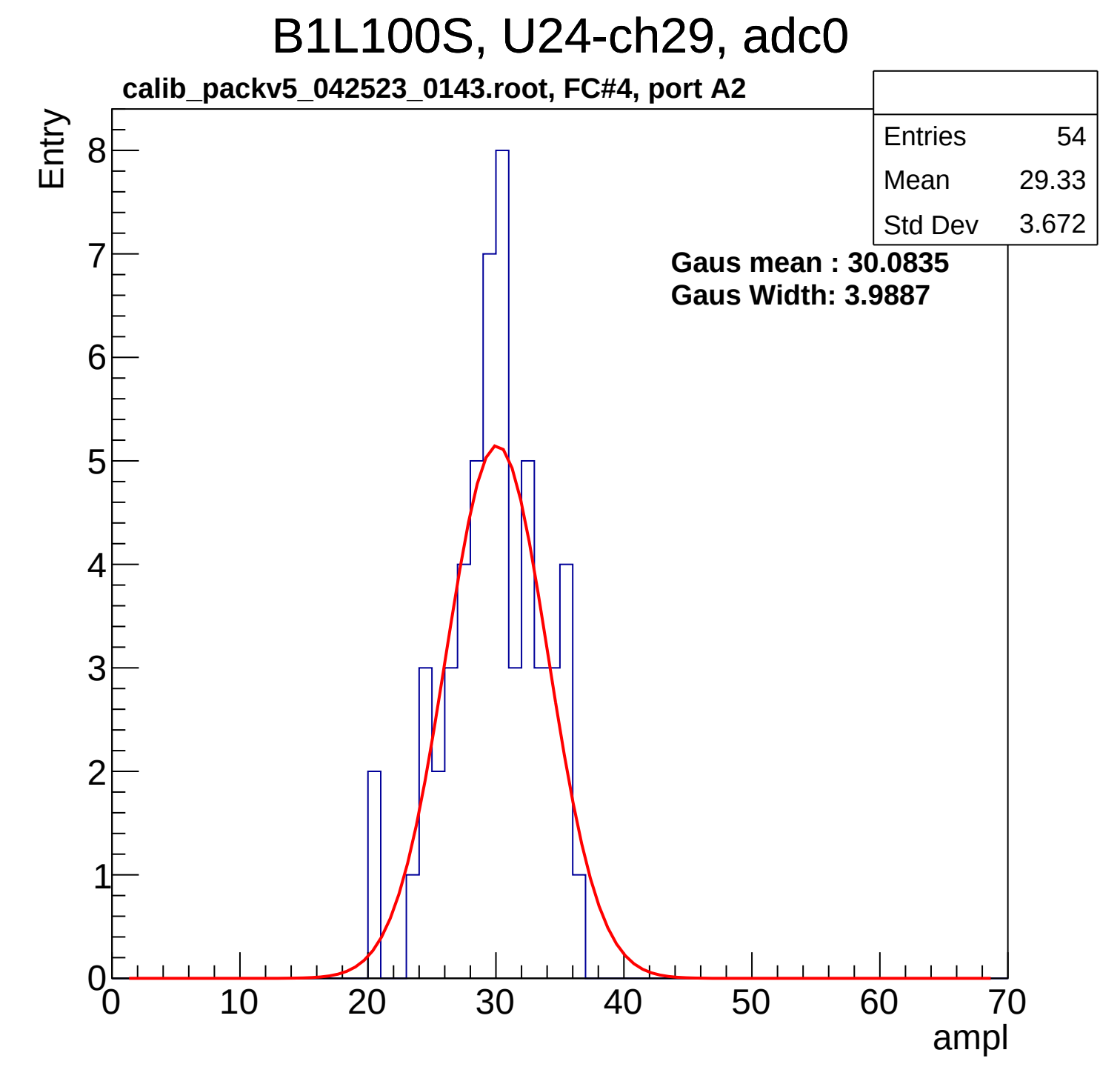

B1L100S, U24-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 29.33
Std Dev 3.672
Gaus mean : 30.0835
Gaus Width: 3.9887
Entry
ampl
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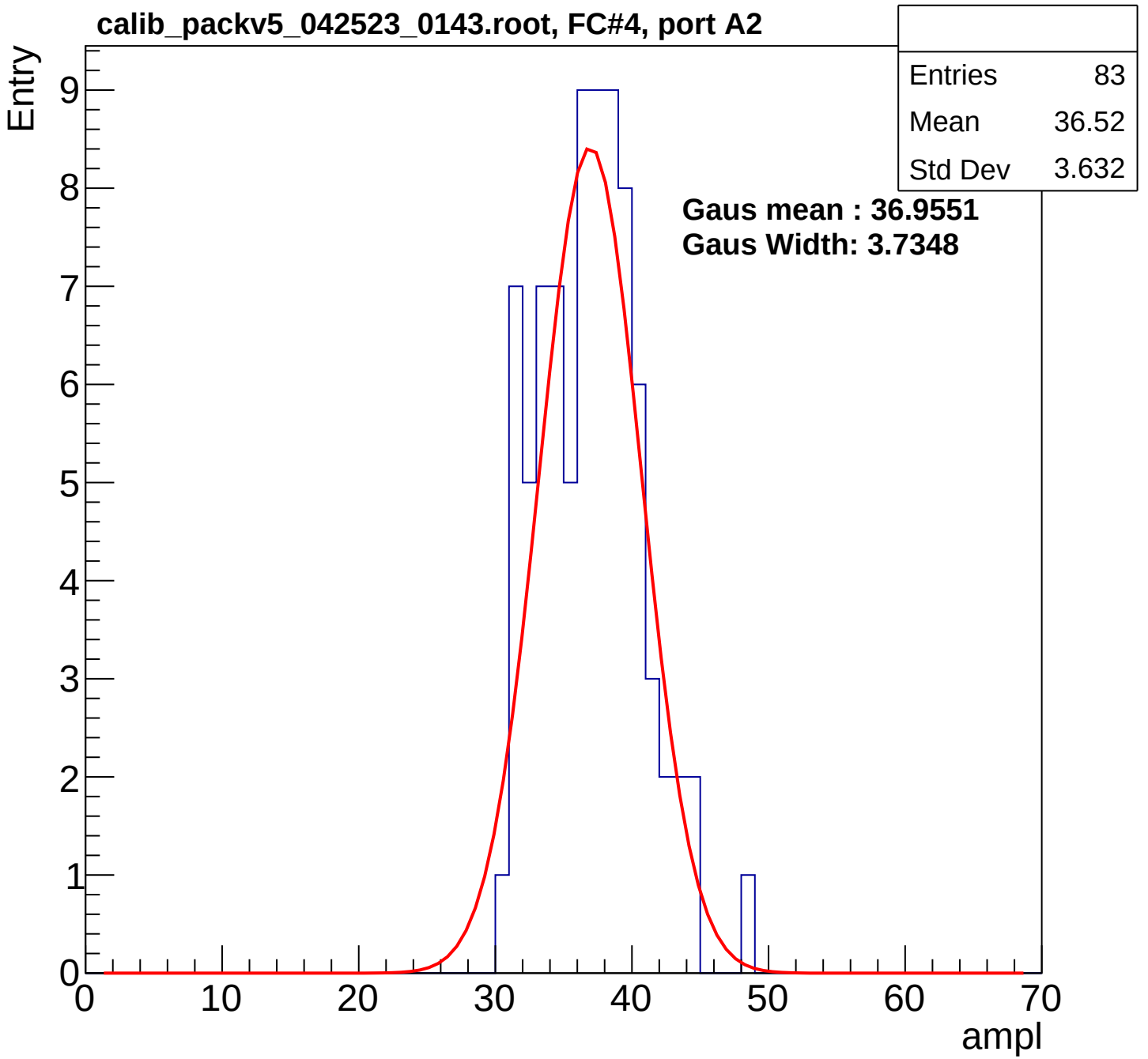
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.52
Std Dev 3.632
Gaus mean : 36.9551
Gaus Width: 3.7348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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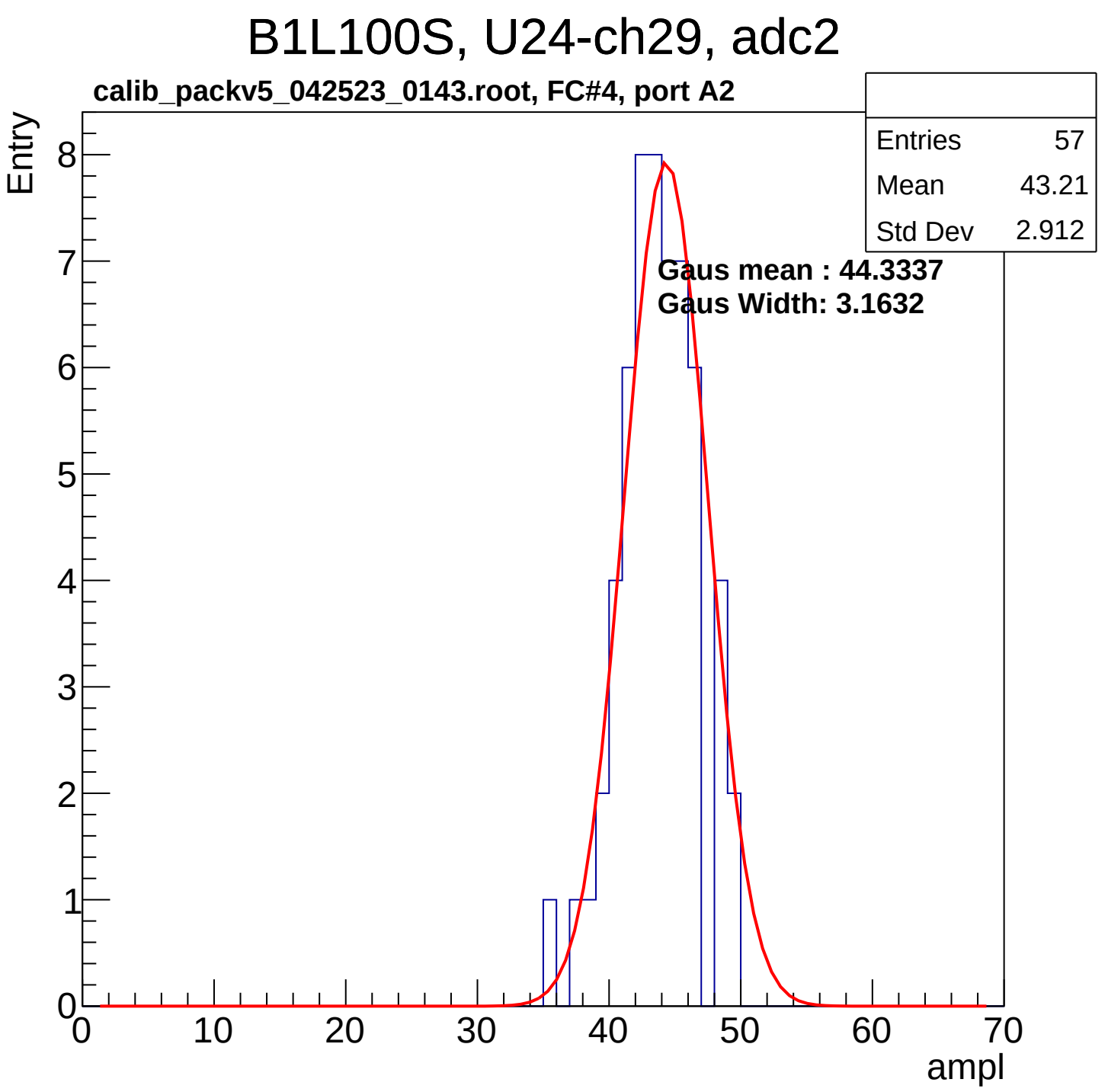
B1L100S, U24-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43.21
Std Dev 2.912
Gaus mean : 44.3337
Gaus Width: 3.1632
Entry
ampl
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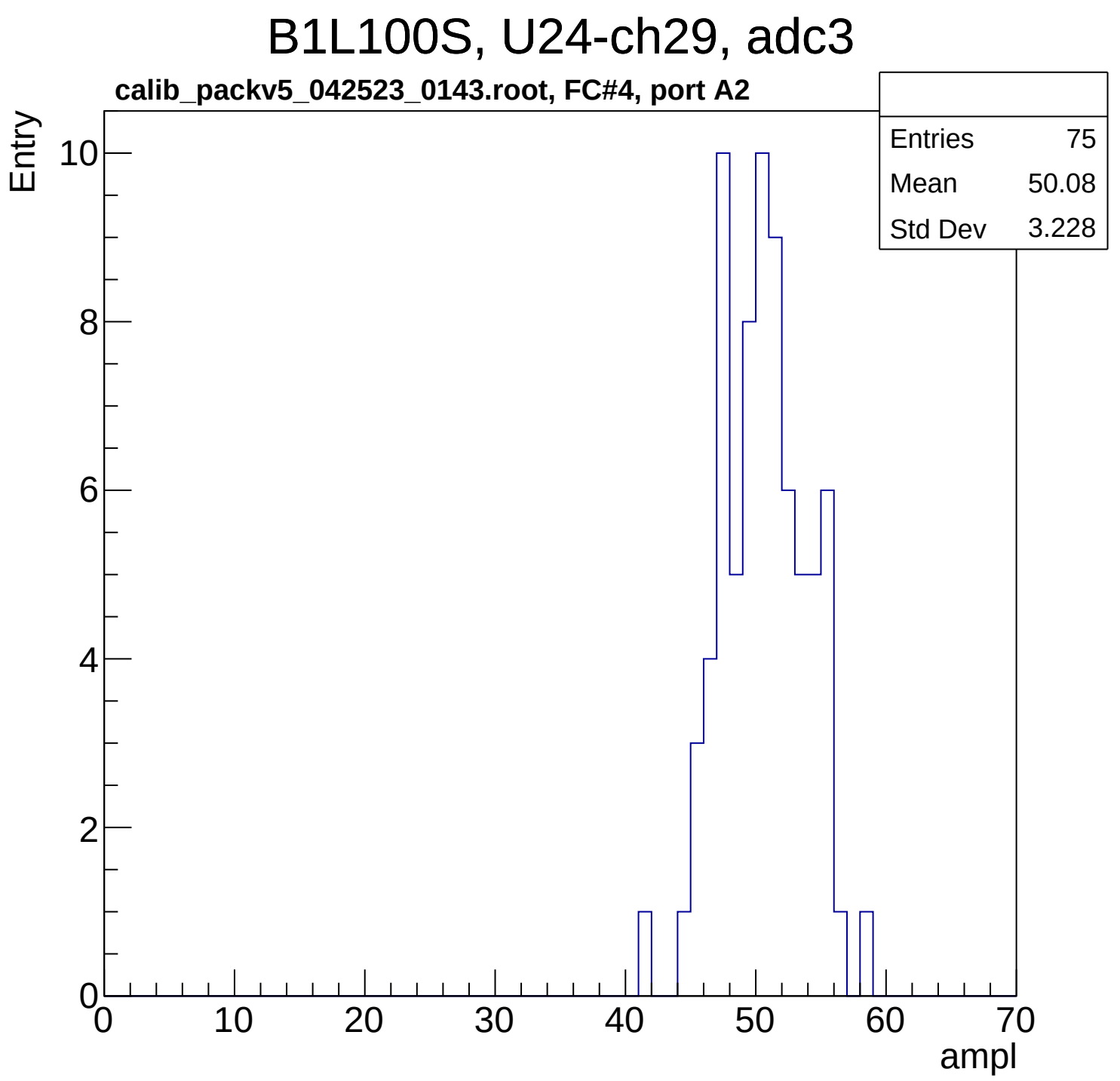
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch29, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.08
Std Dev 3.228
Entry
ampl
0
2
4
6
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0
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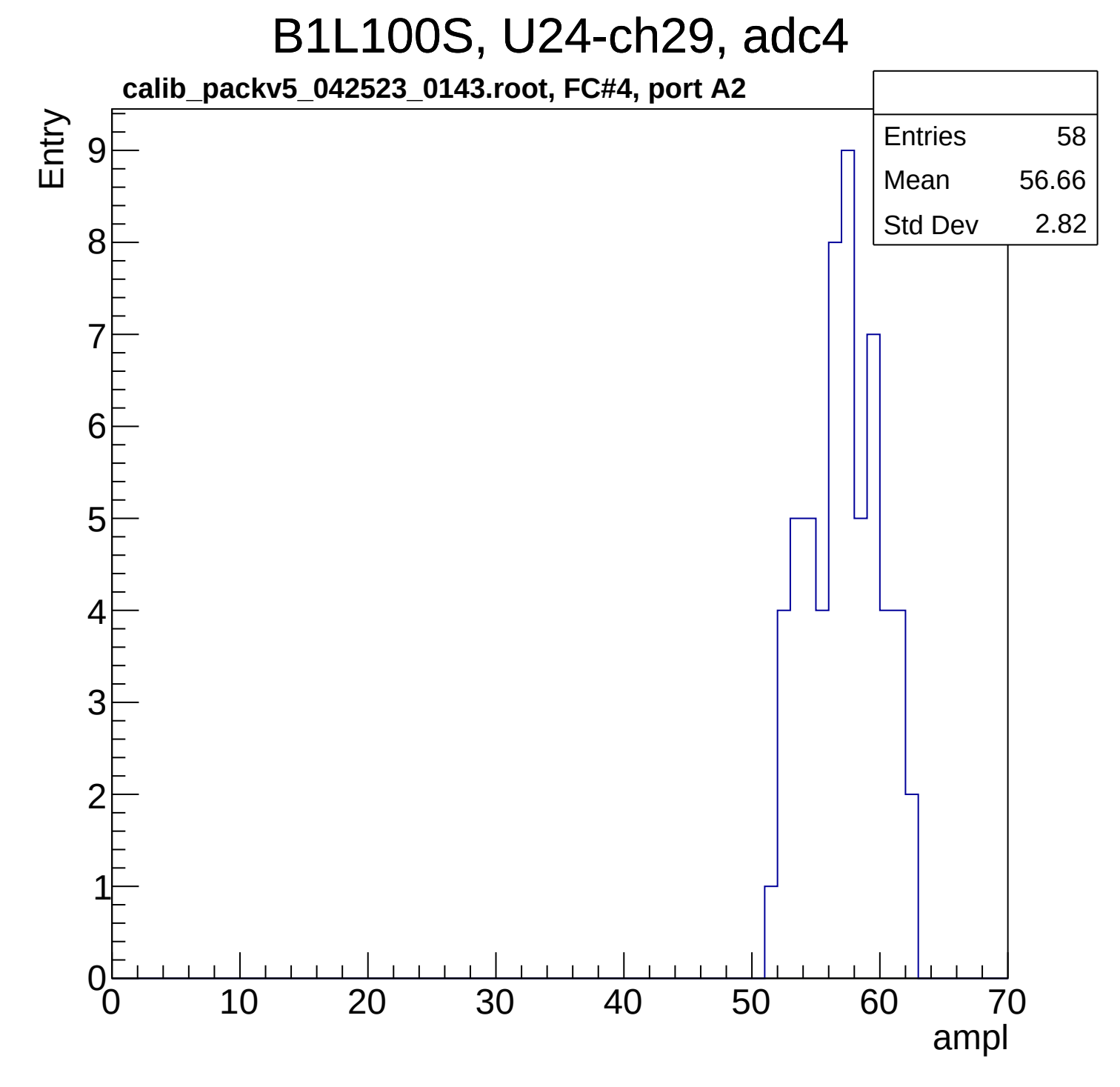
40
50
60
70


B1L100S, U24-ch29, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.66
Std Dev 2.82
Entry
ampl
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7
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0
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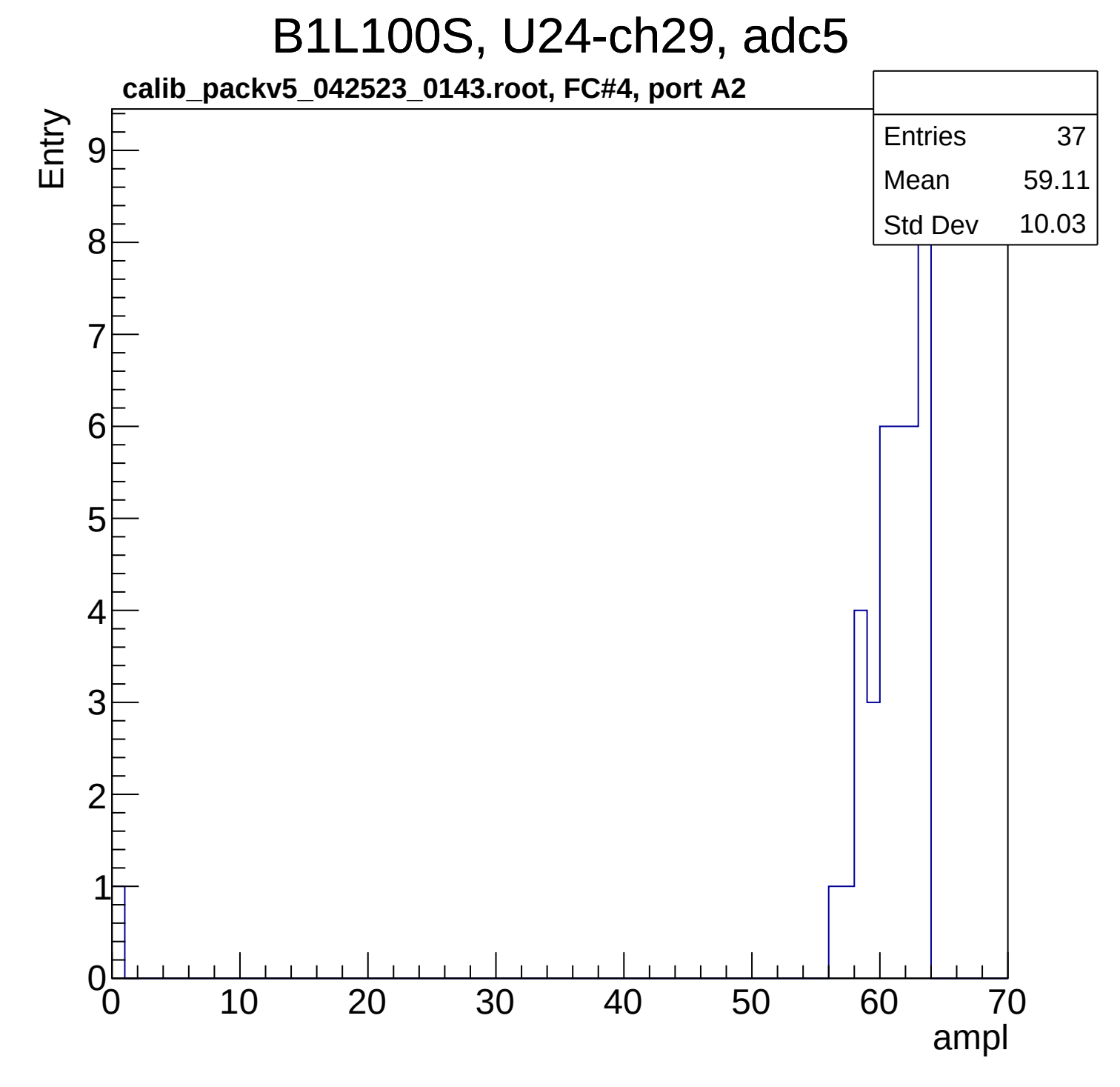
B1L100S, U24-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.11
Std Dev 10.03
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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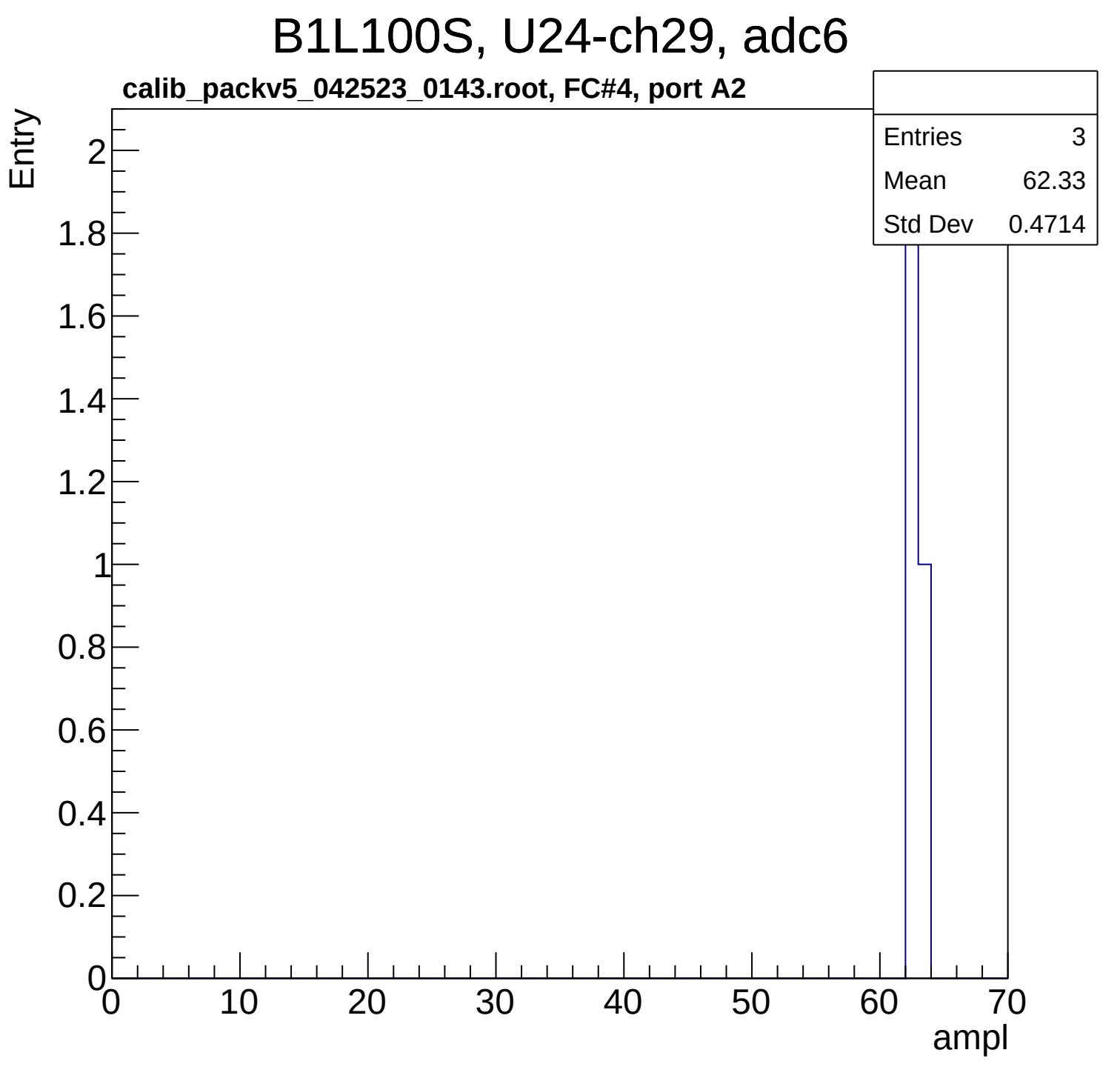
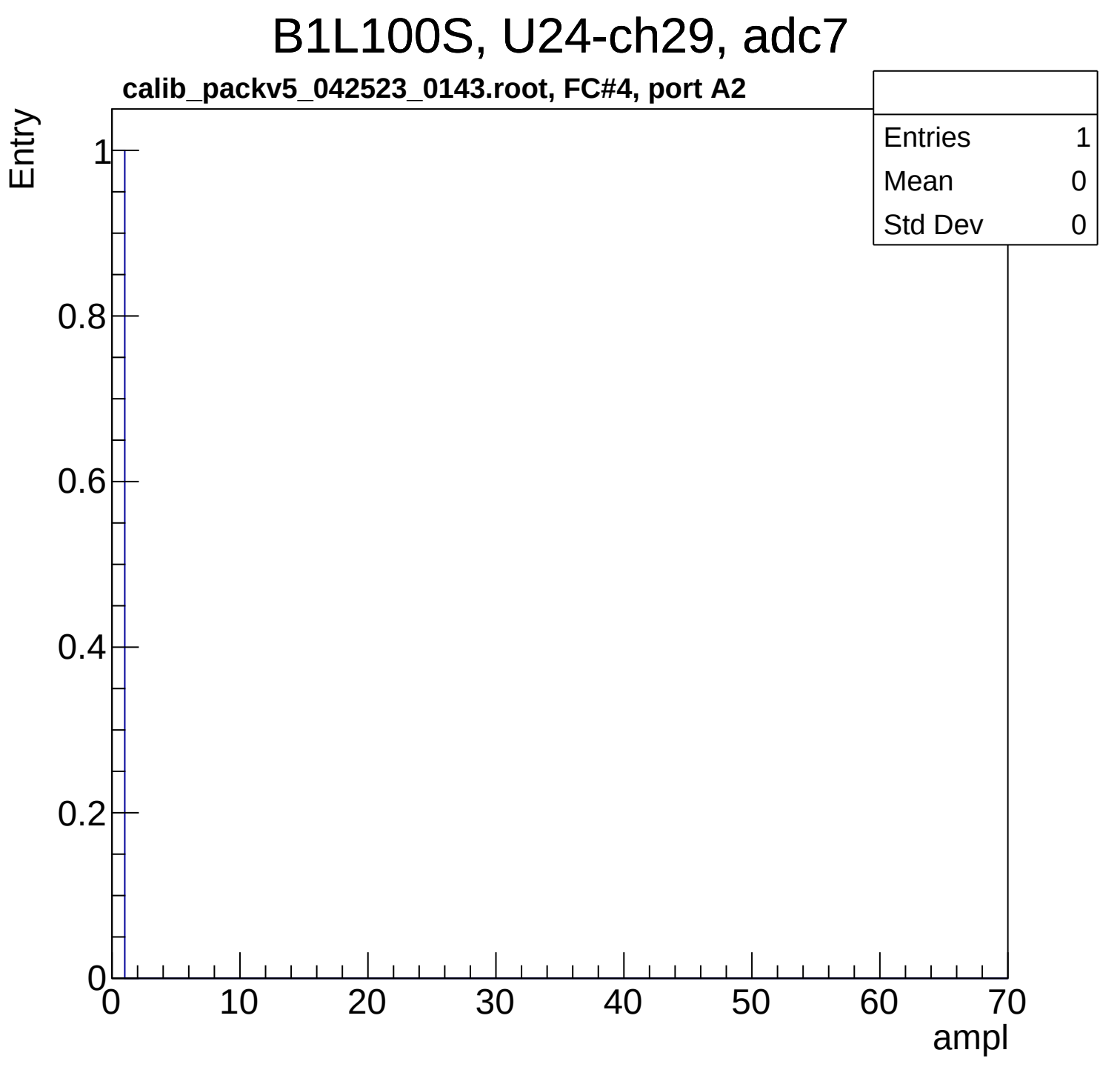

B1L100S, U24-ch29, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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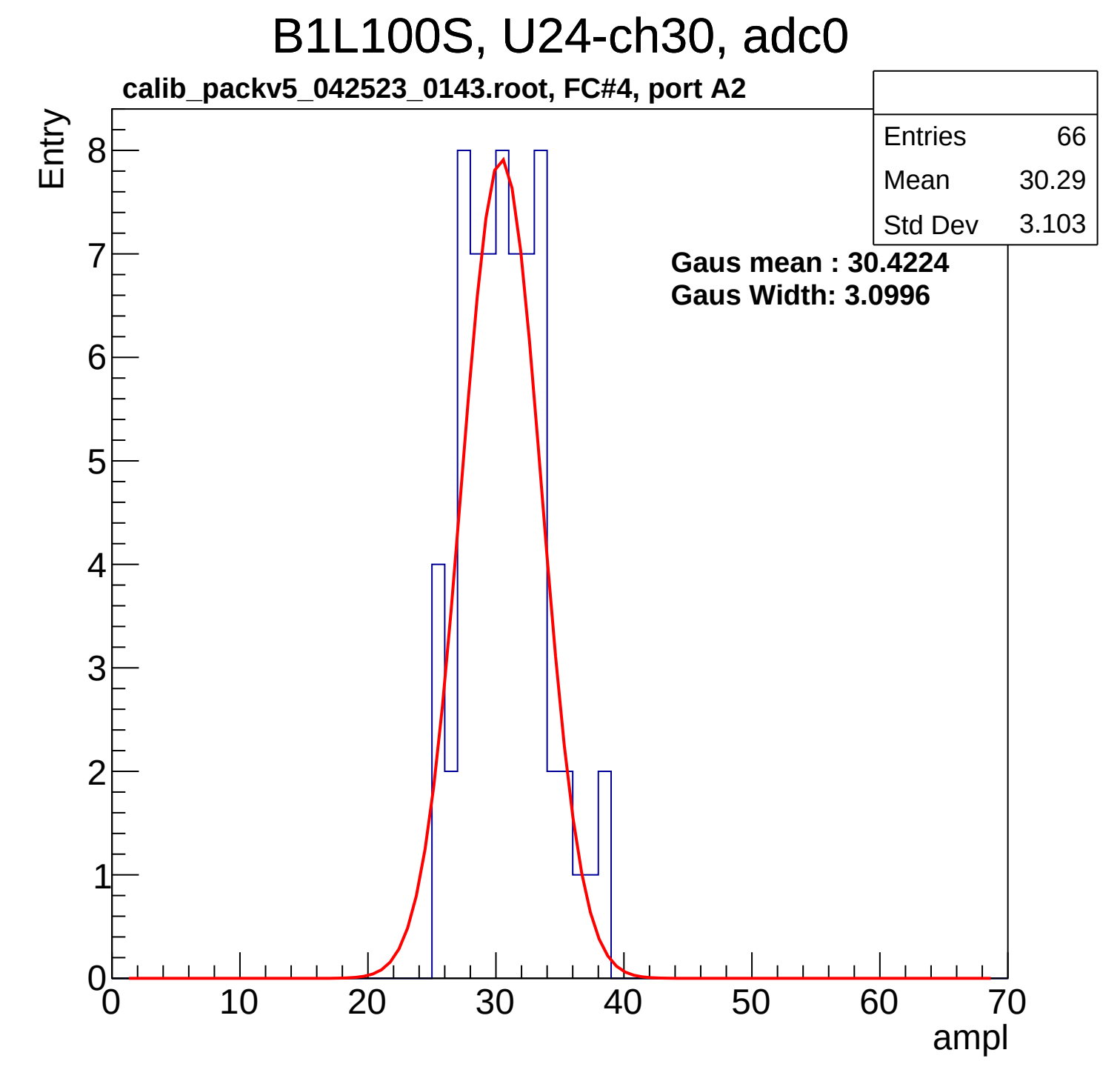
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.29
Std Dev 3.103
Gaus mean : 30.4224
Gaus Width: 3.0996
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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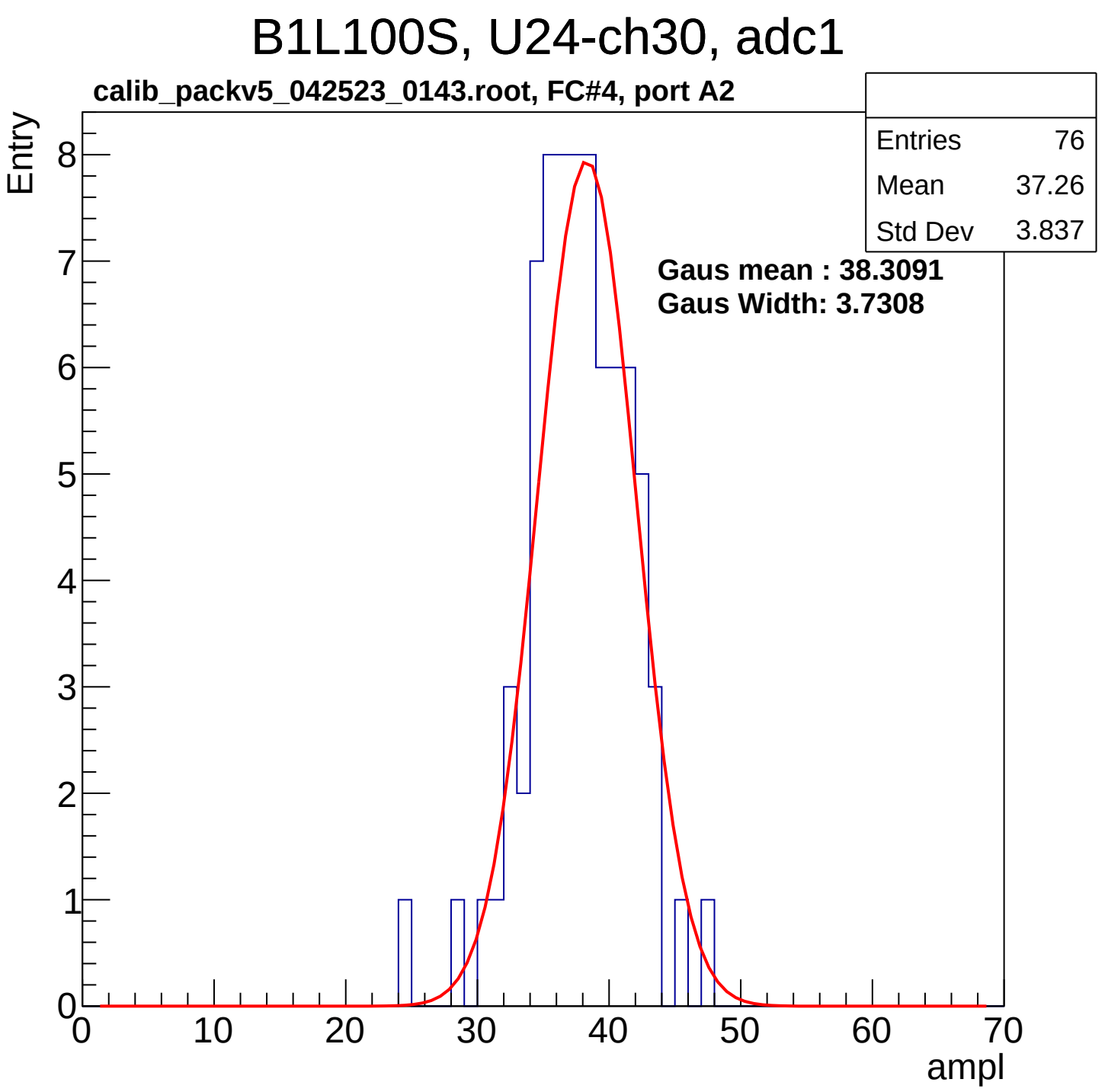
B1L100S, U24-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.26
Std Dev 3.837
Gaus mean : 38.3091
Gaus Width: 3.7308
Entry
ampl
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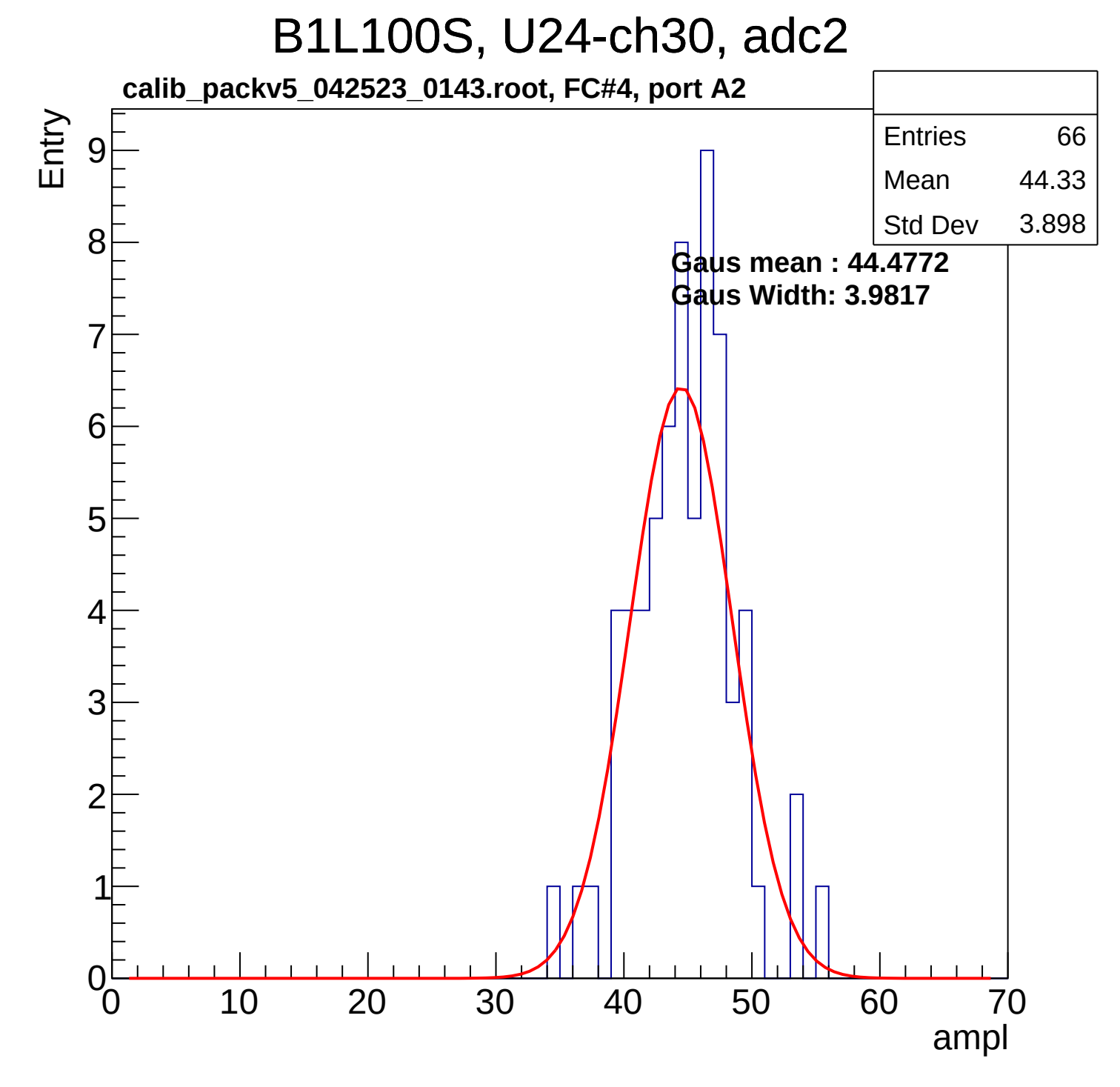
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 44.33
Std Dev 3.898
Gaus mean : 44.4772
Gaus Width: 3.9817
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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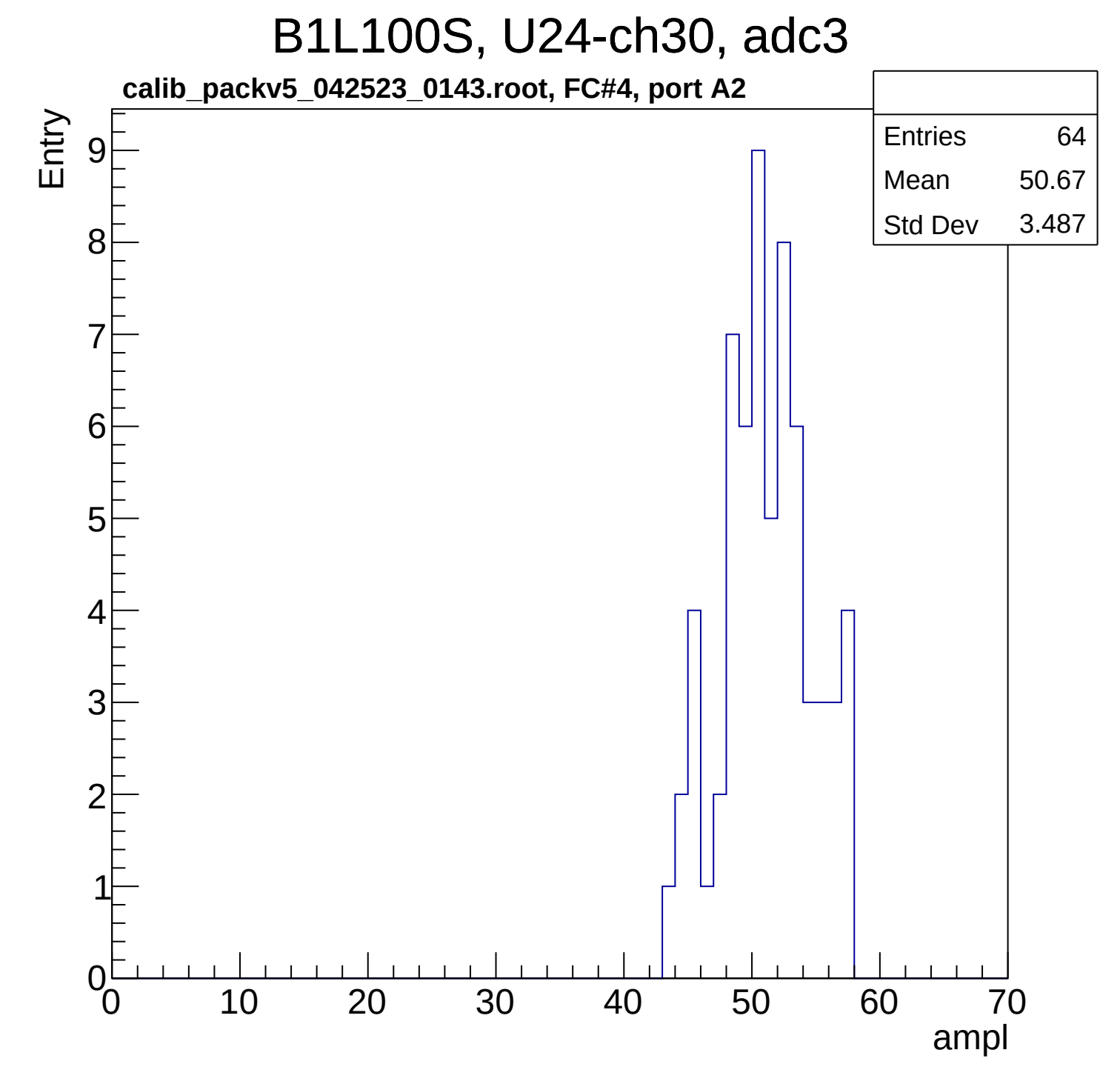
B1L100S, U24-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.67
Std Dev 3.487
Entry
ampl
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6
7
8
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0
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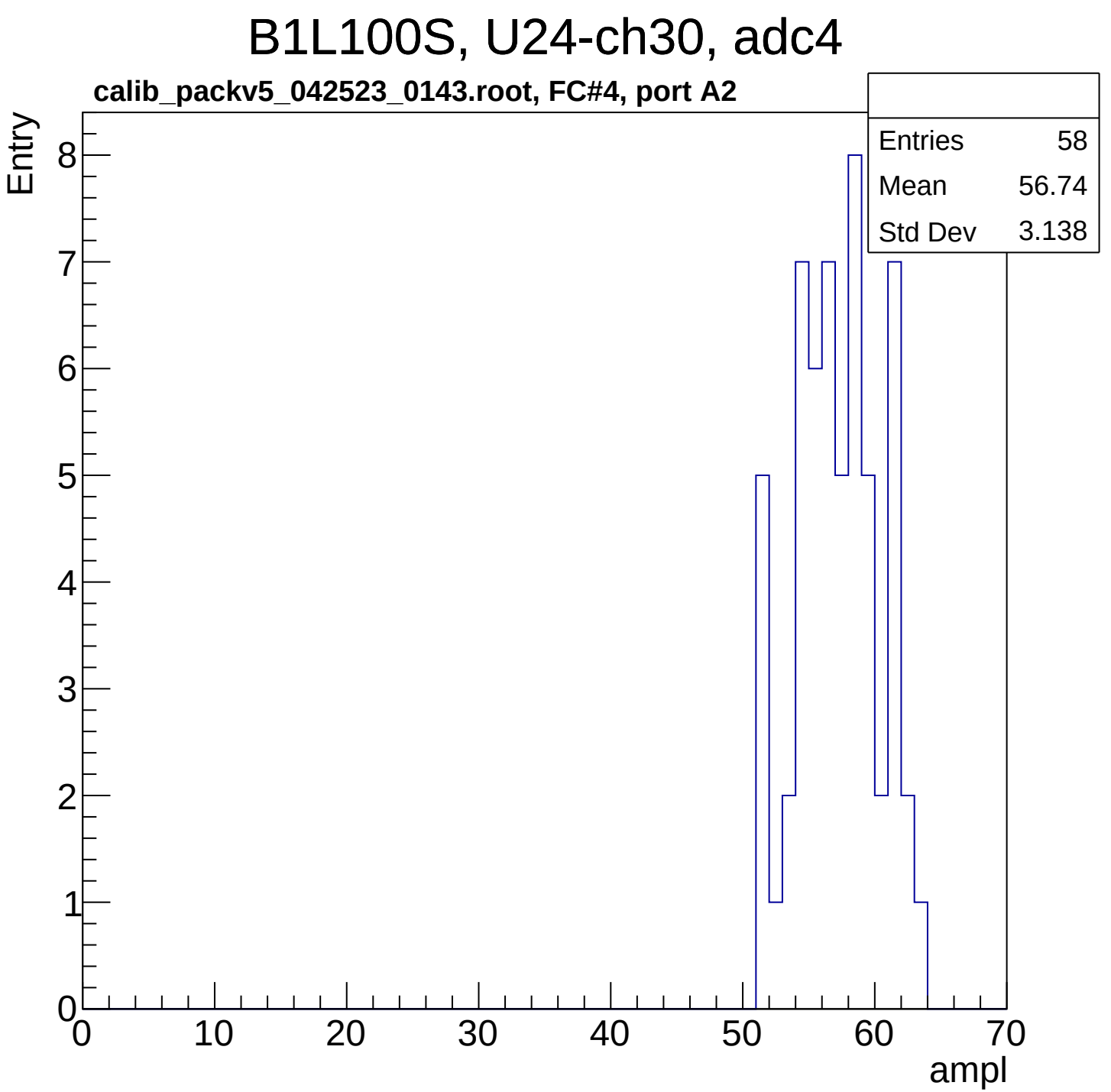
B1L100S, U24-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.74
Std Dev 3.138
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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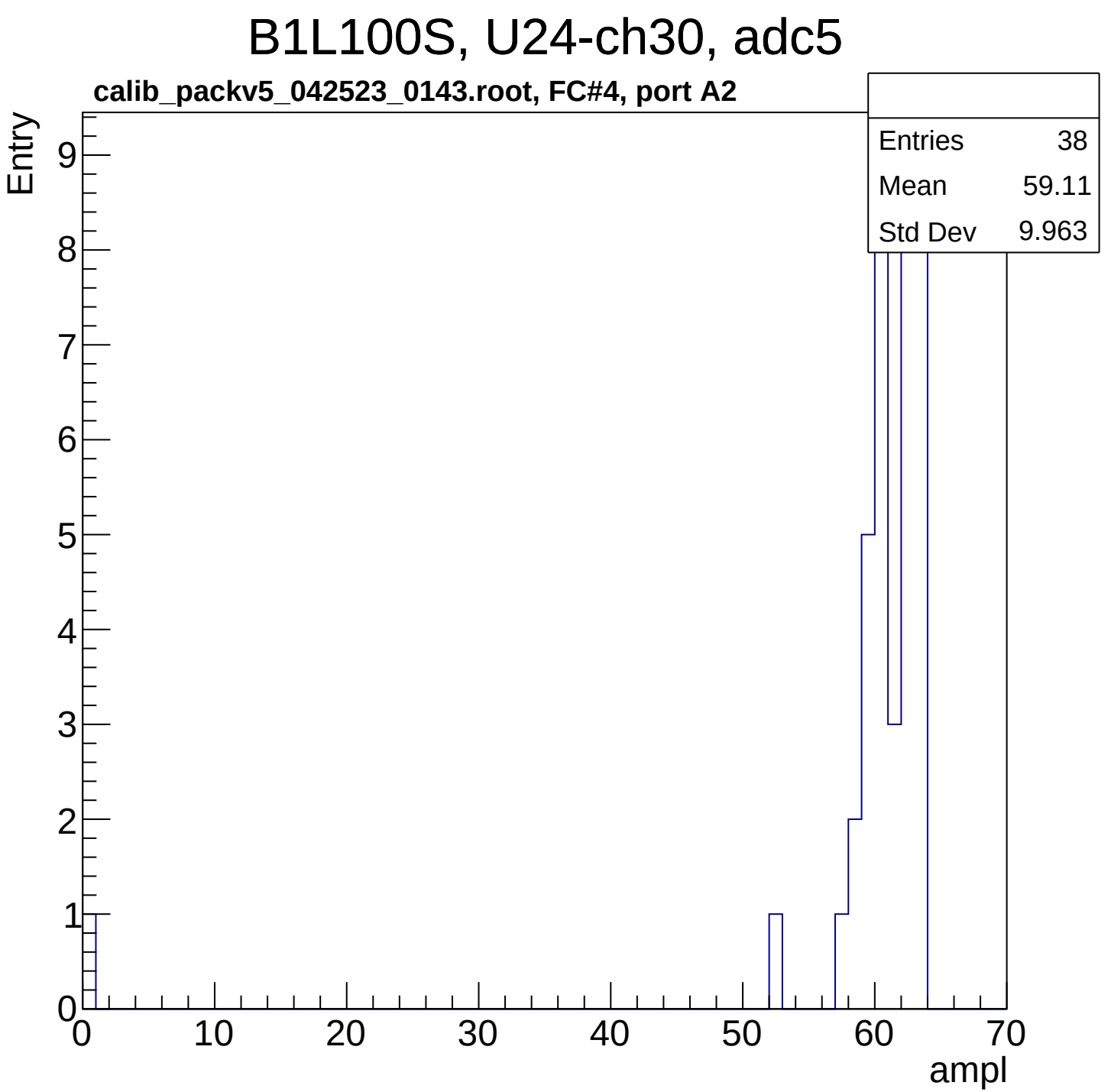
B1L100S, U24-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.11
Std Dev 9.963
Entry
ampl
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2
3
4
5
6
7
8
9
0
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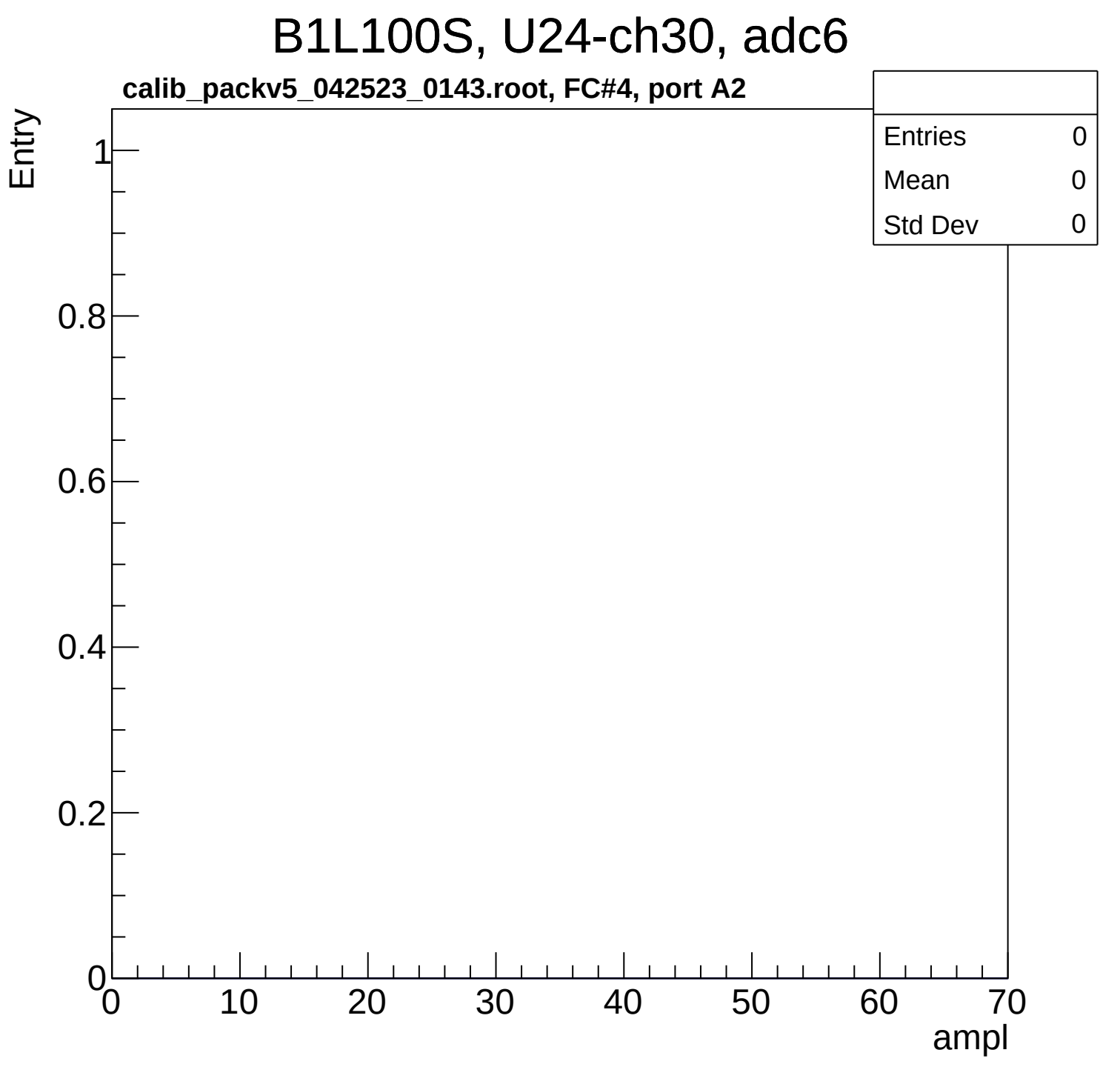
B1L100S, U24-ch30, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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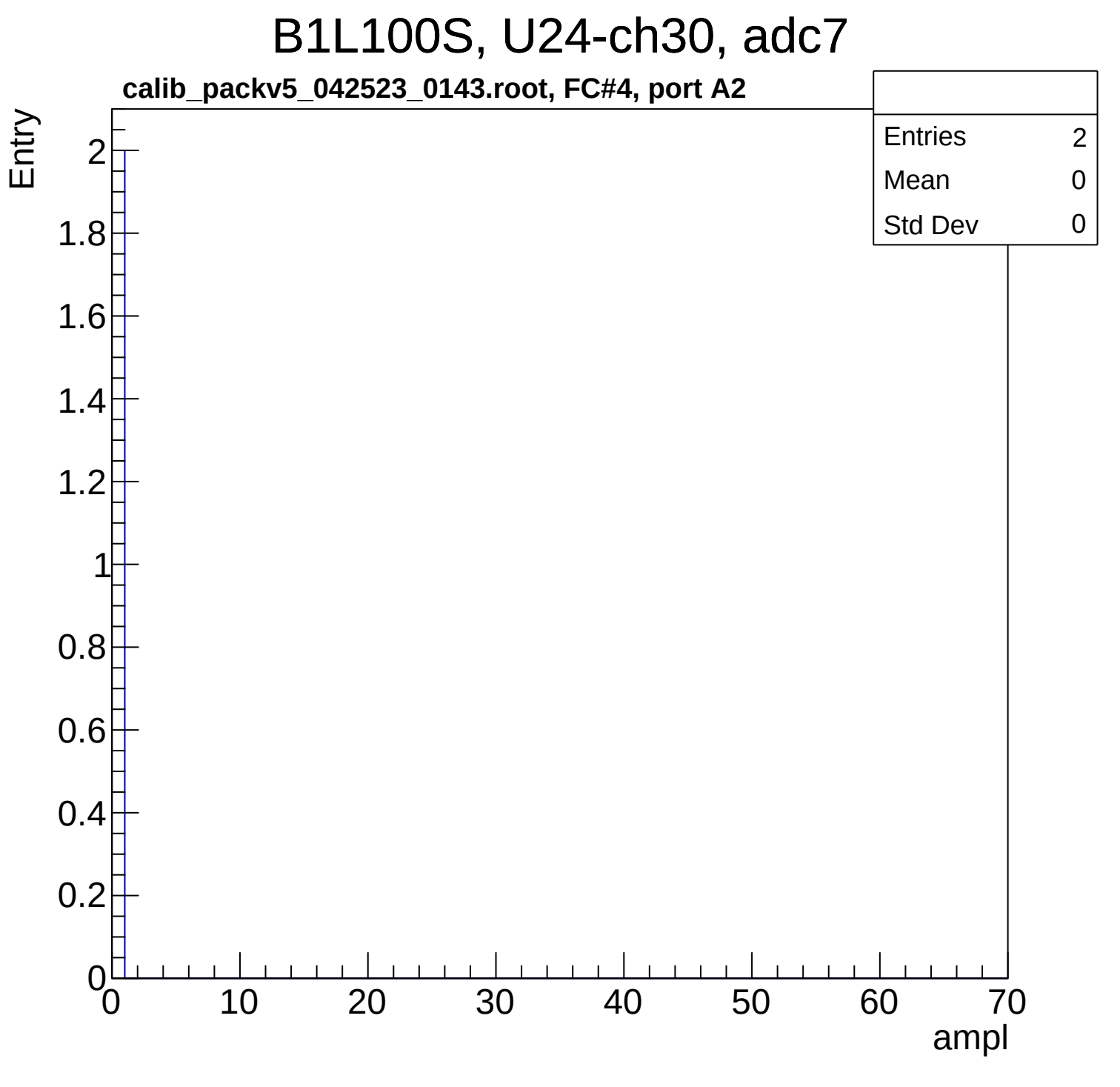
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
20
30
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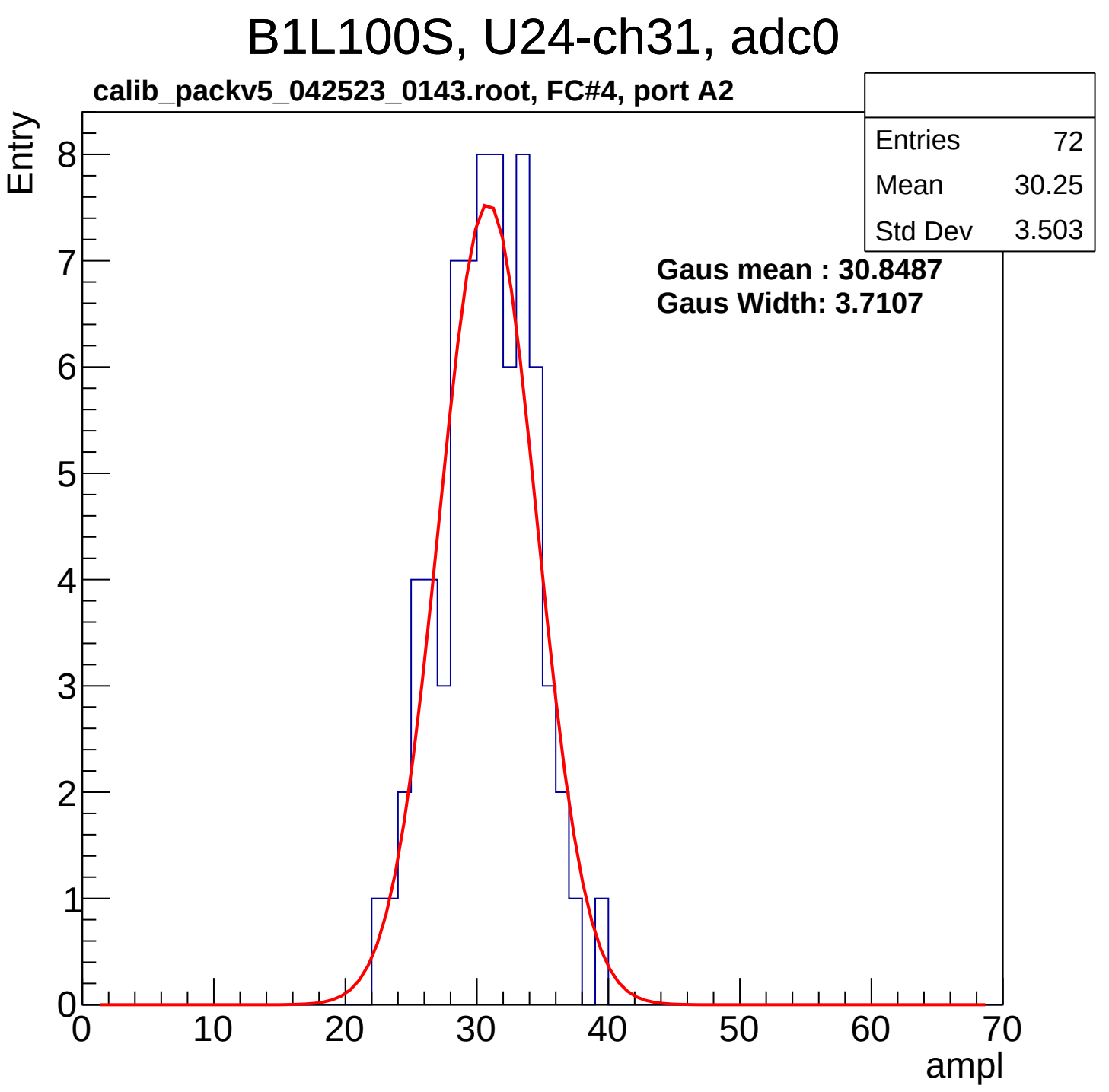
50
60
70
ampl


B1L100S, U24-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.25
Std Dev 3.503
Gaus mean : 30.8487
Gaus Width: 3.7107
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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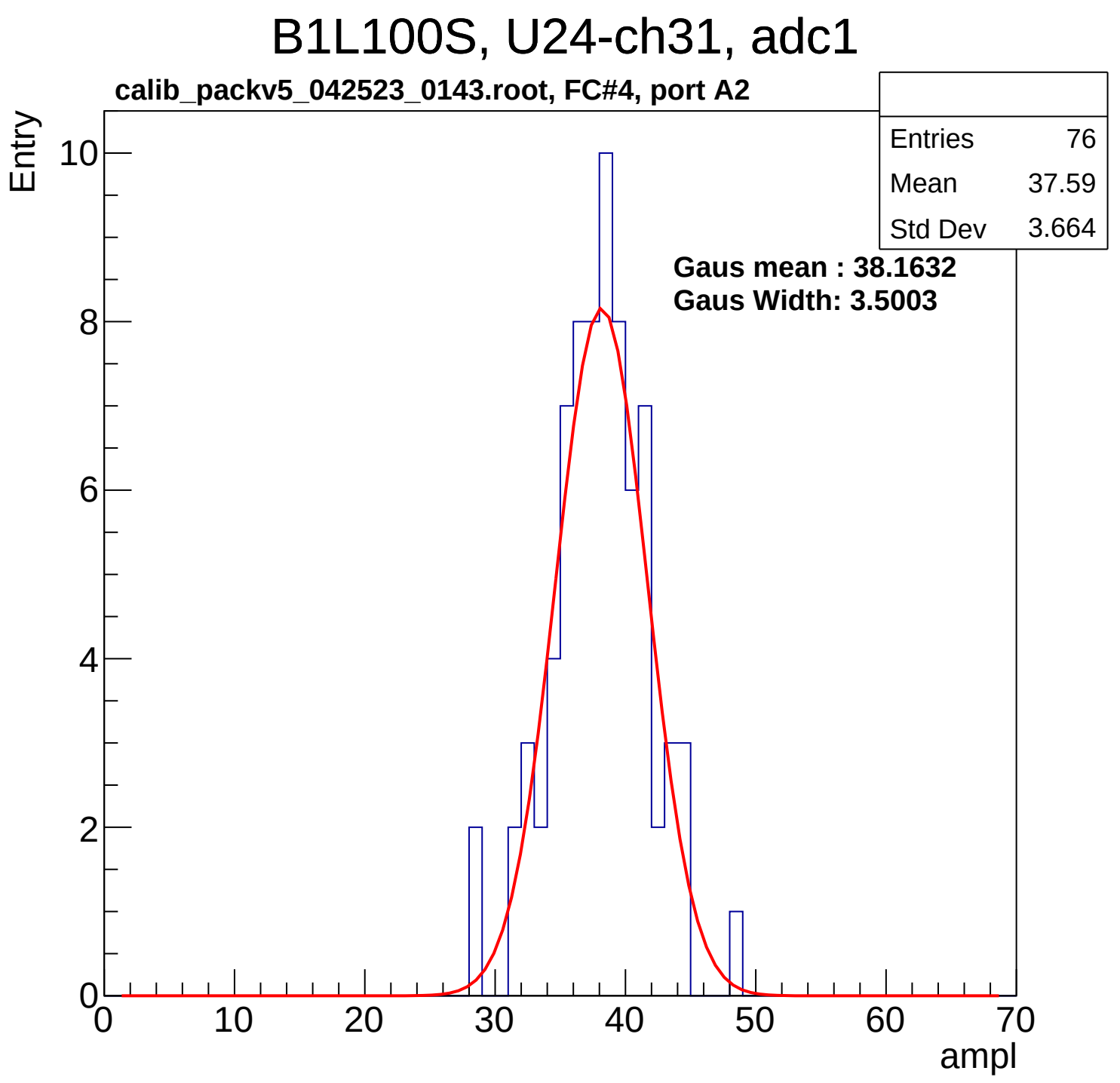
B1L100S, U24-ch31, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.59
Std Dev 3.664
Gaus mean : 38.1632
Gaus Width: 3.5003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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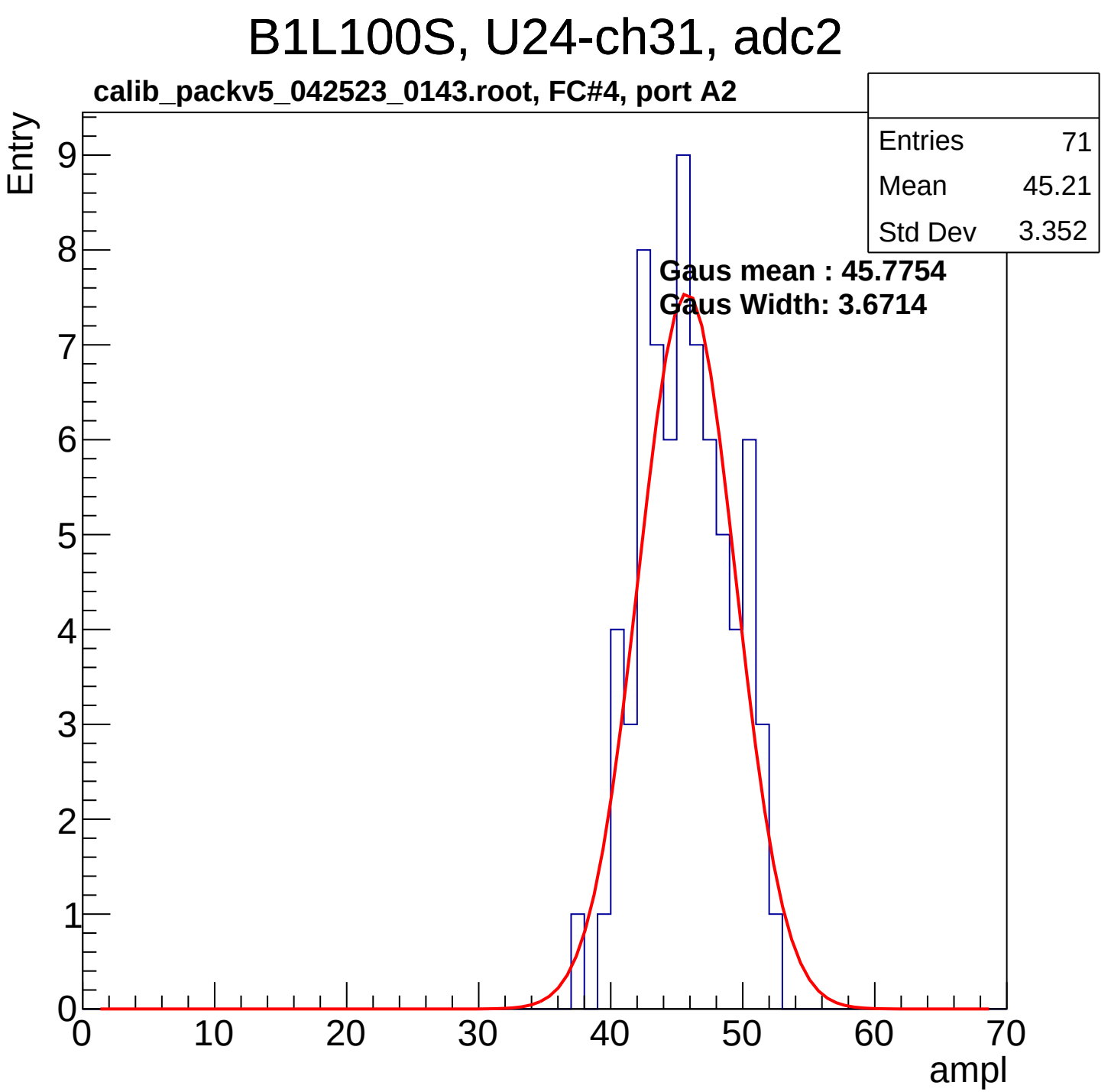
B1L100S, U24-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 45.21
Std Dev 3.352
Gaus mean : 45.7754
Gaus Width: 3.6714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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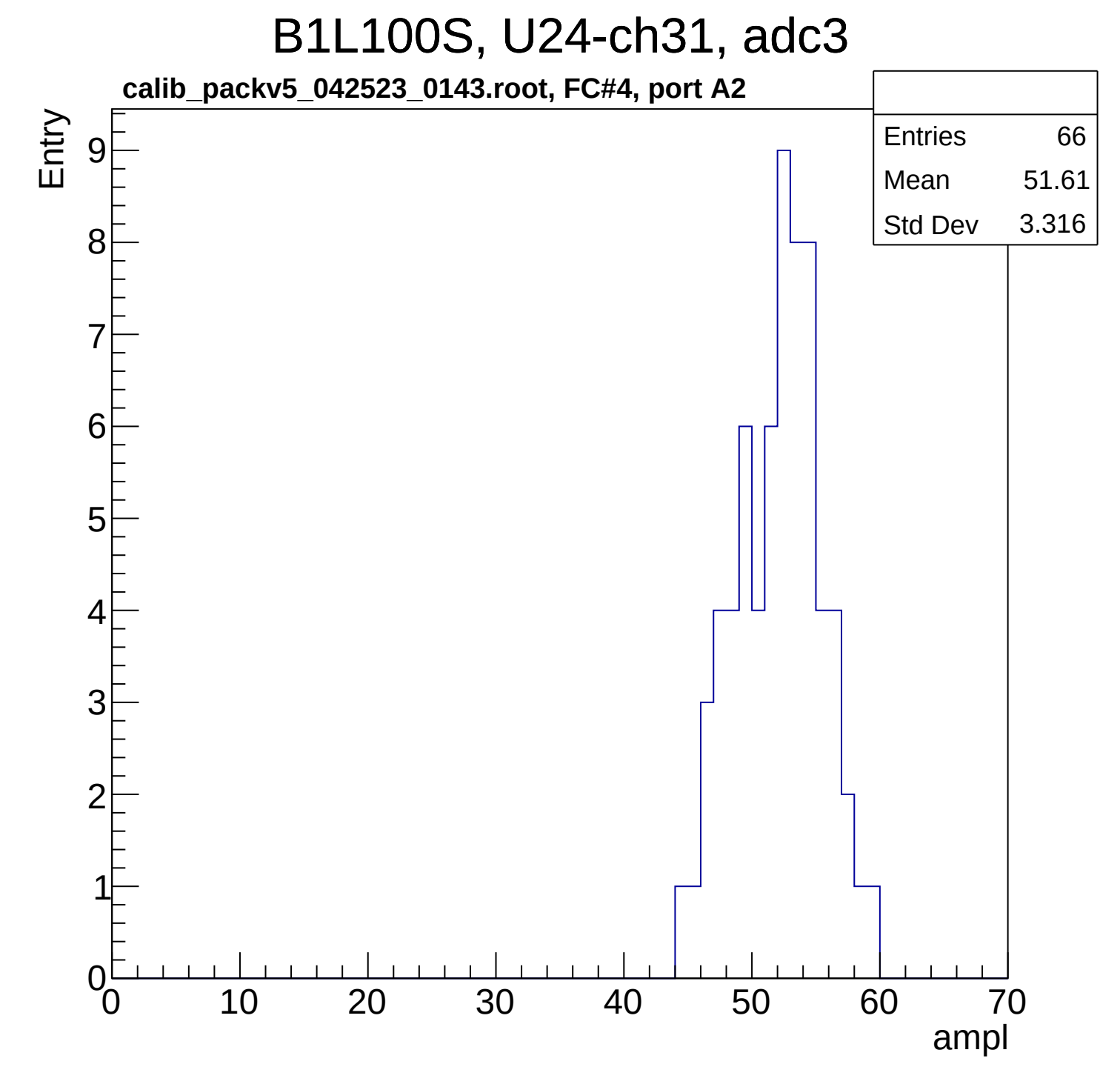
B1L100S, U24-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 51.61
Std Dev 3.316
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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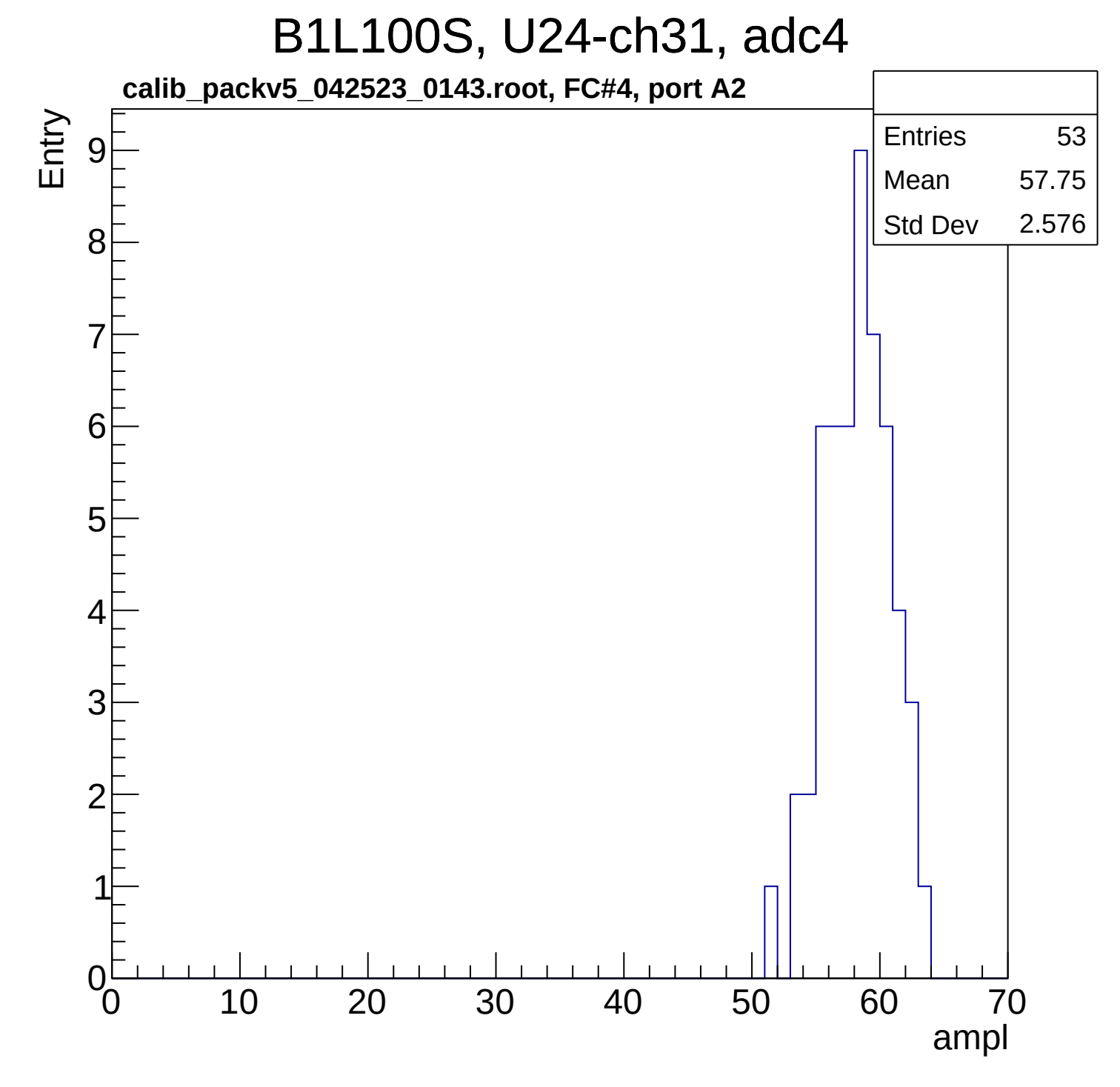
B1L100S, U24-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 57.75
Std Dev 2.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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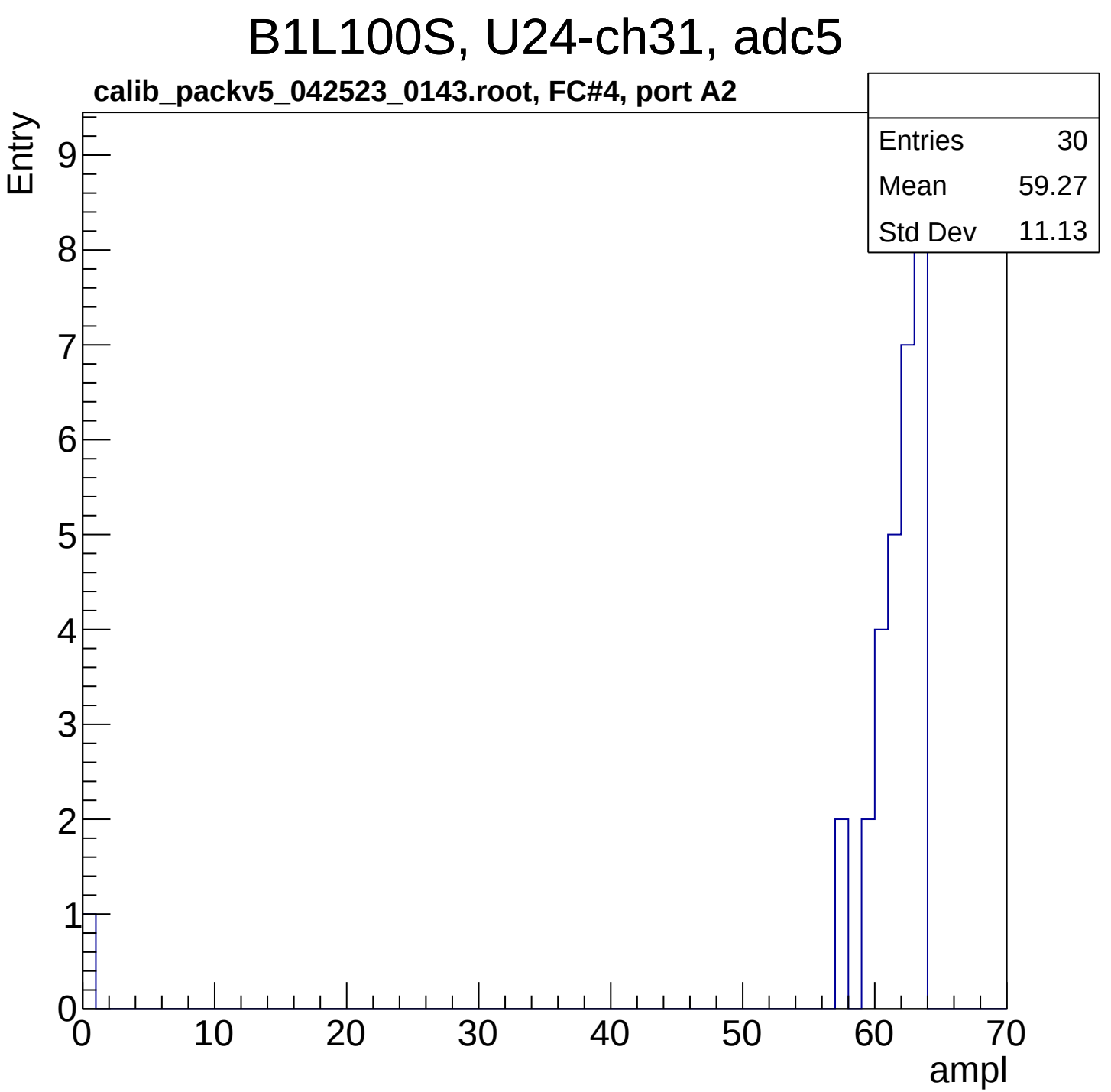
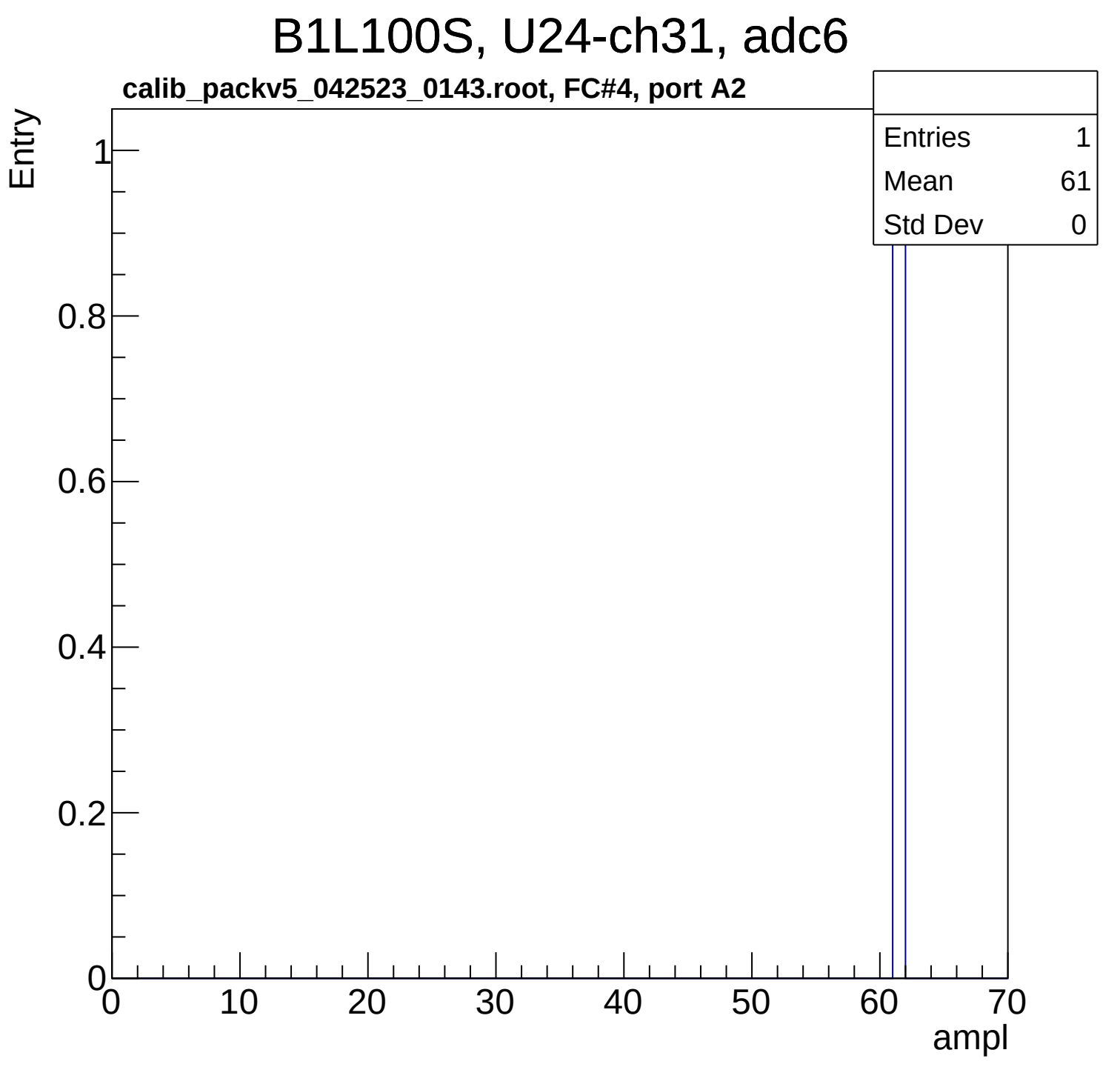

B1L100S, U24-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 59.27
Std Dev 11.13
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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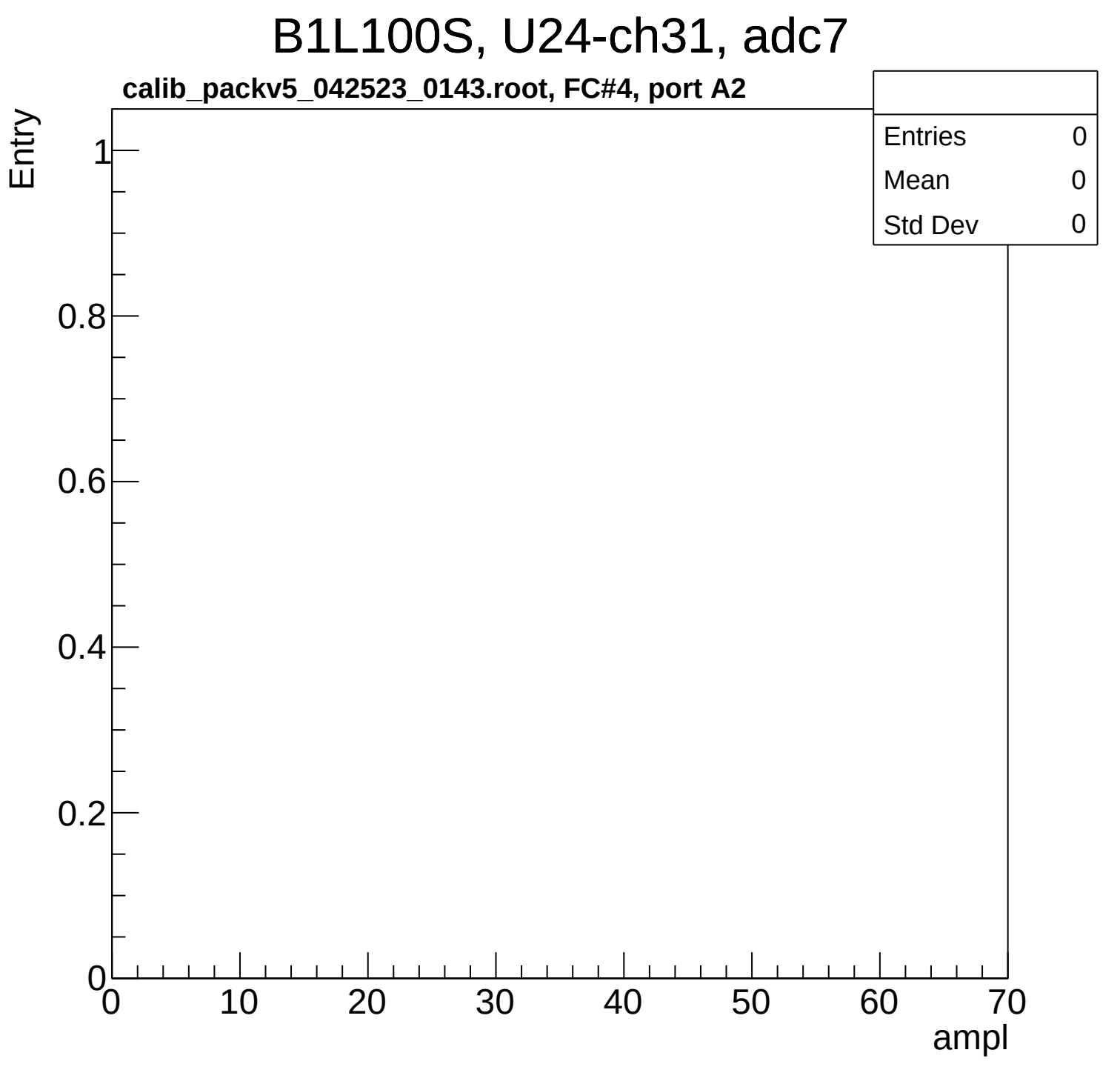
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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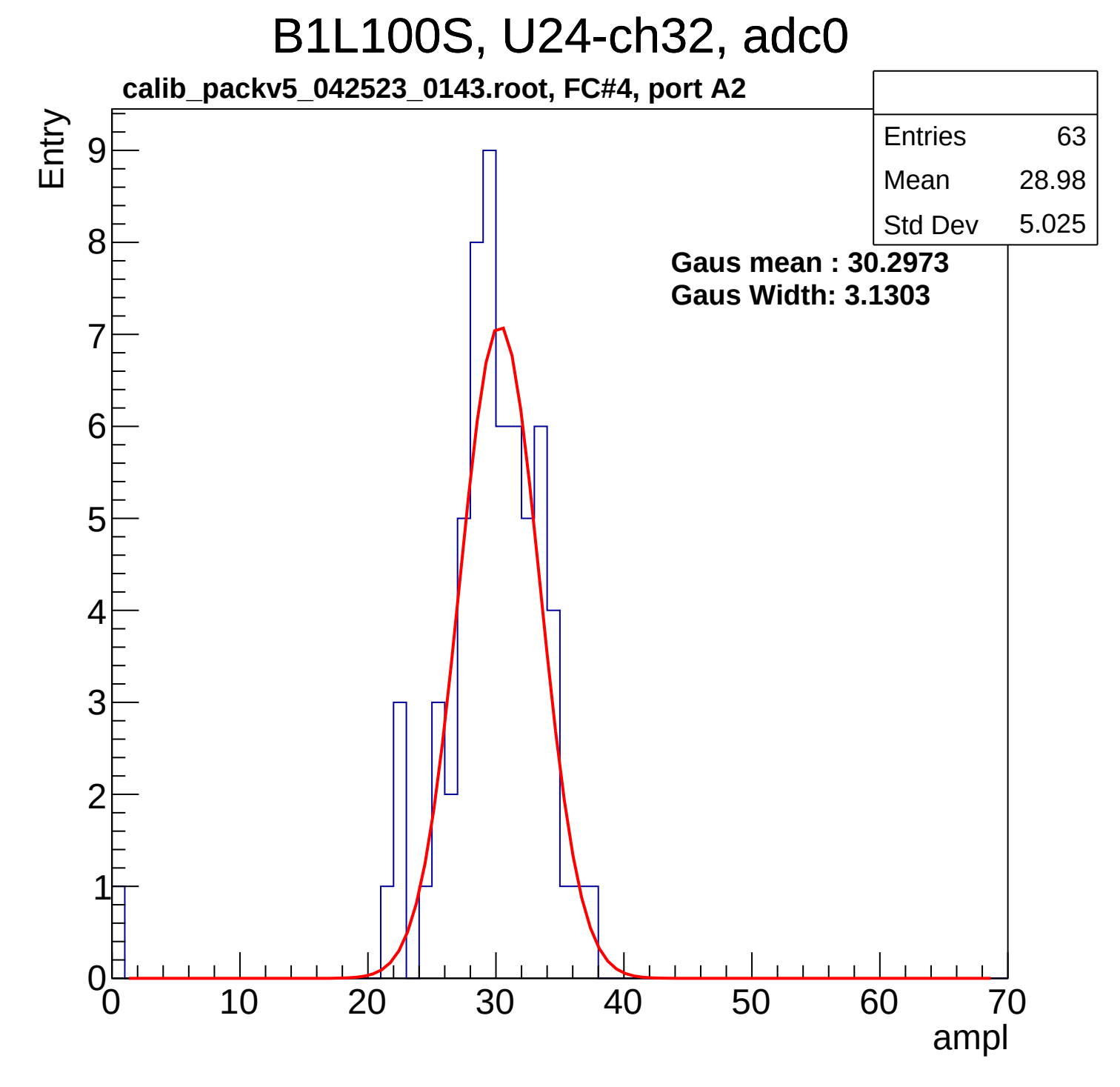
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.98
Std Dev 5.025
Gaus mean : 30.2973
Gaus Width: 3.1303
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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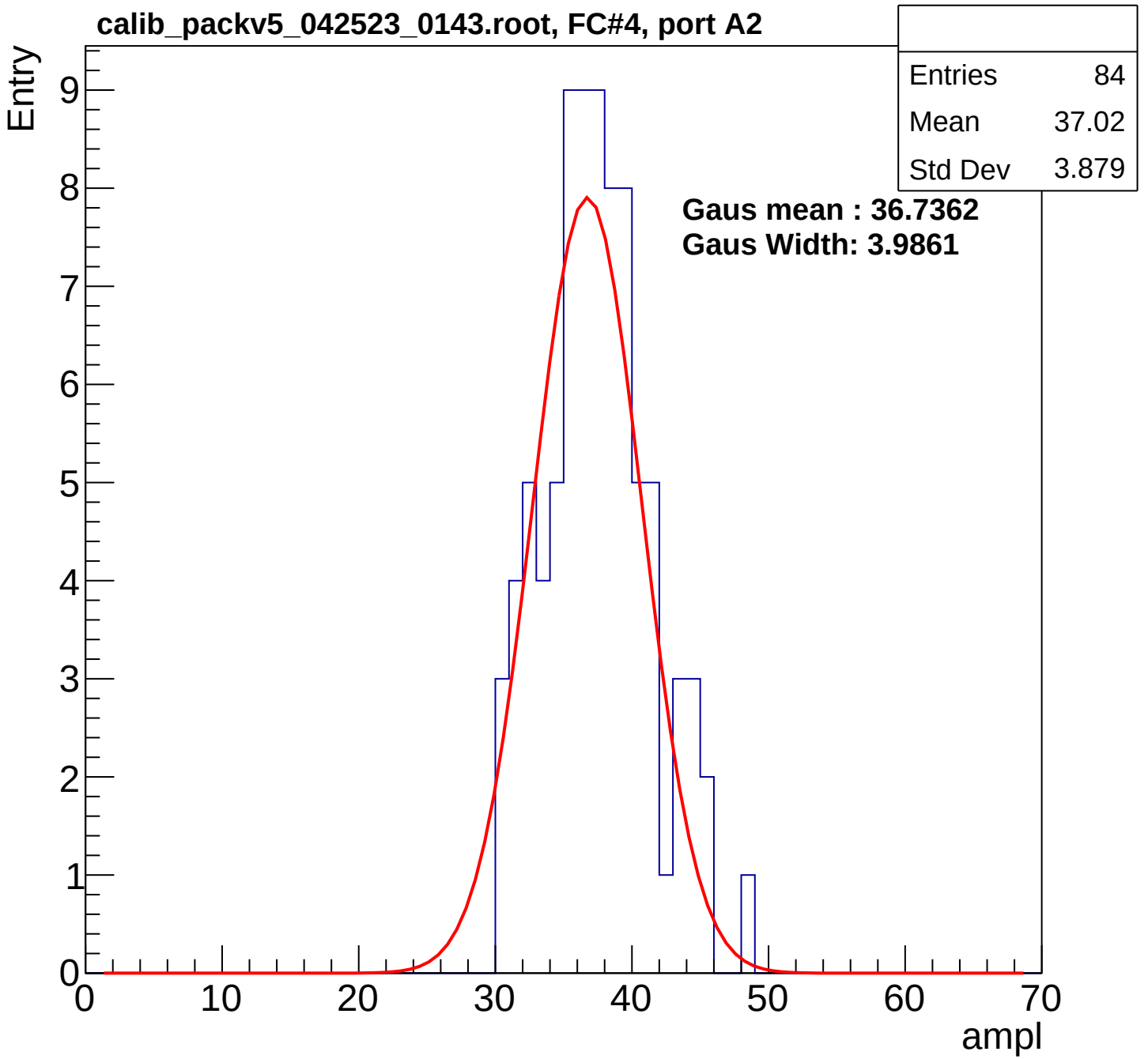
B1L100S, U24-ch32, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.02
Std Dev 3.879
Gaus mean : 36.7362
Gaus Width: 3.9861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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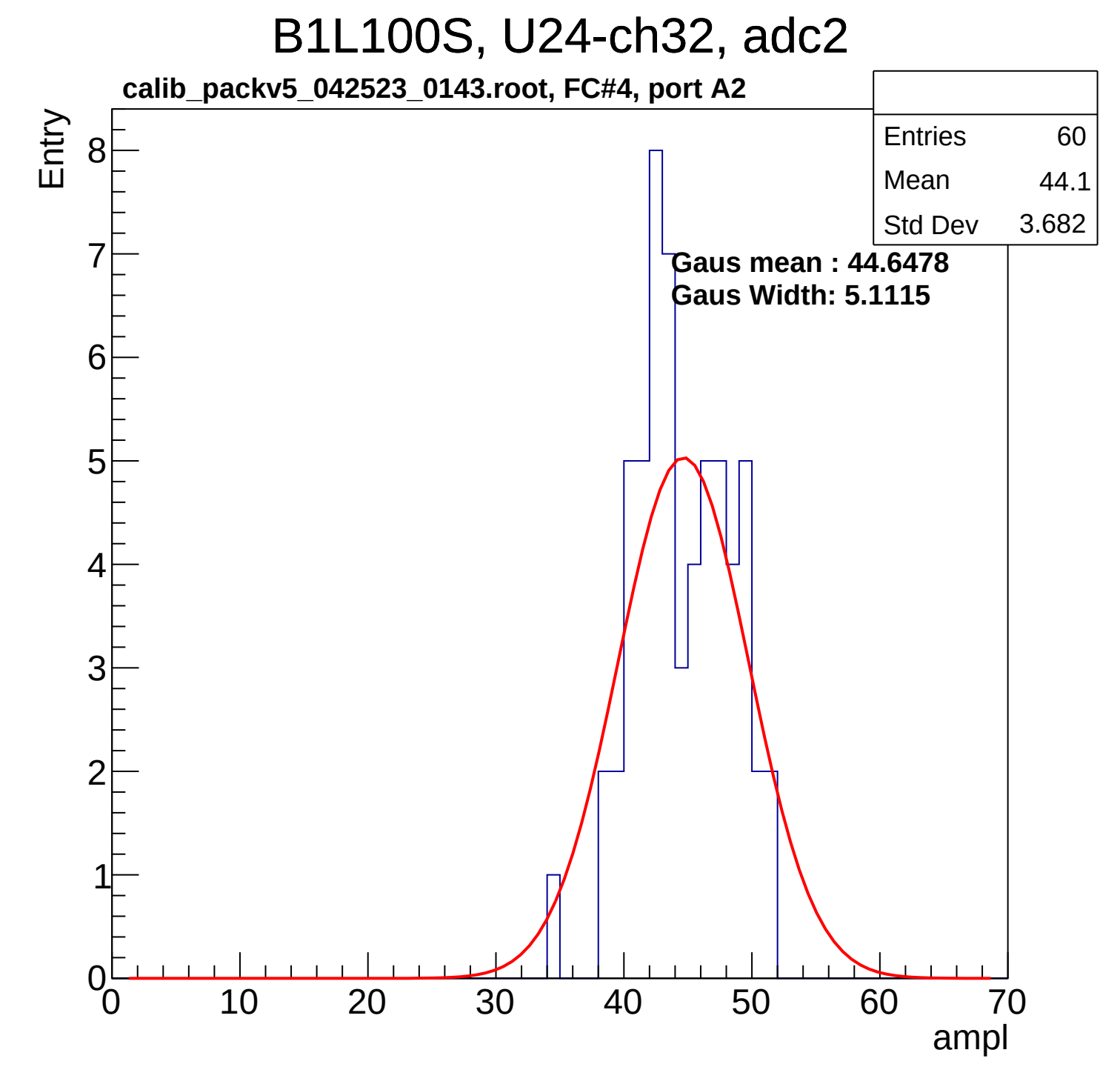
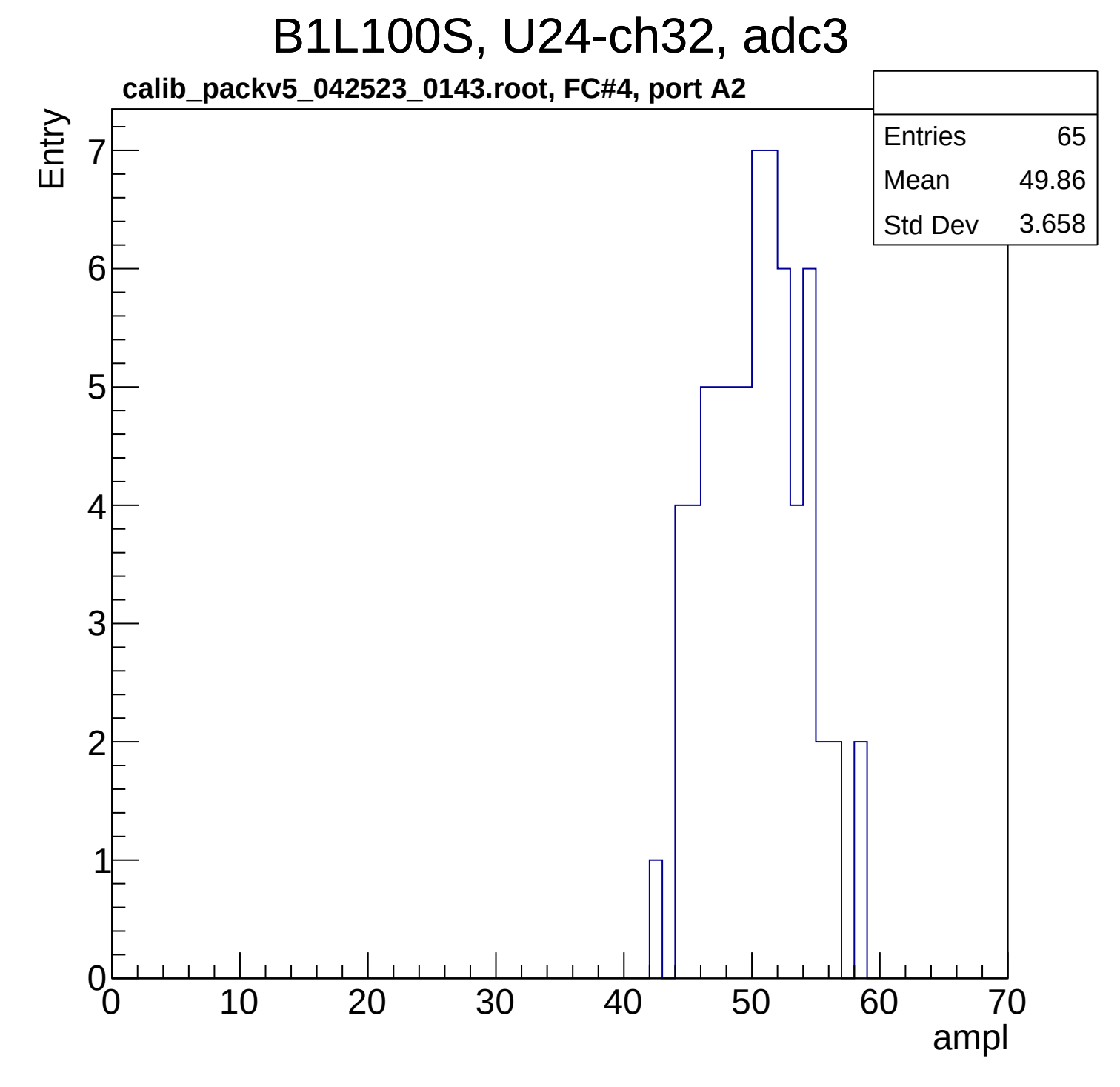

B1L100S, U24-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.1
Std Dev 3.682
Gaus mean : 44.6478
Gaus Width: 5.1115
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.86
Std Dev 3.658
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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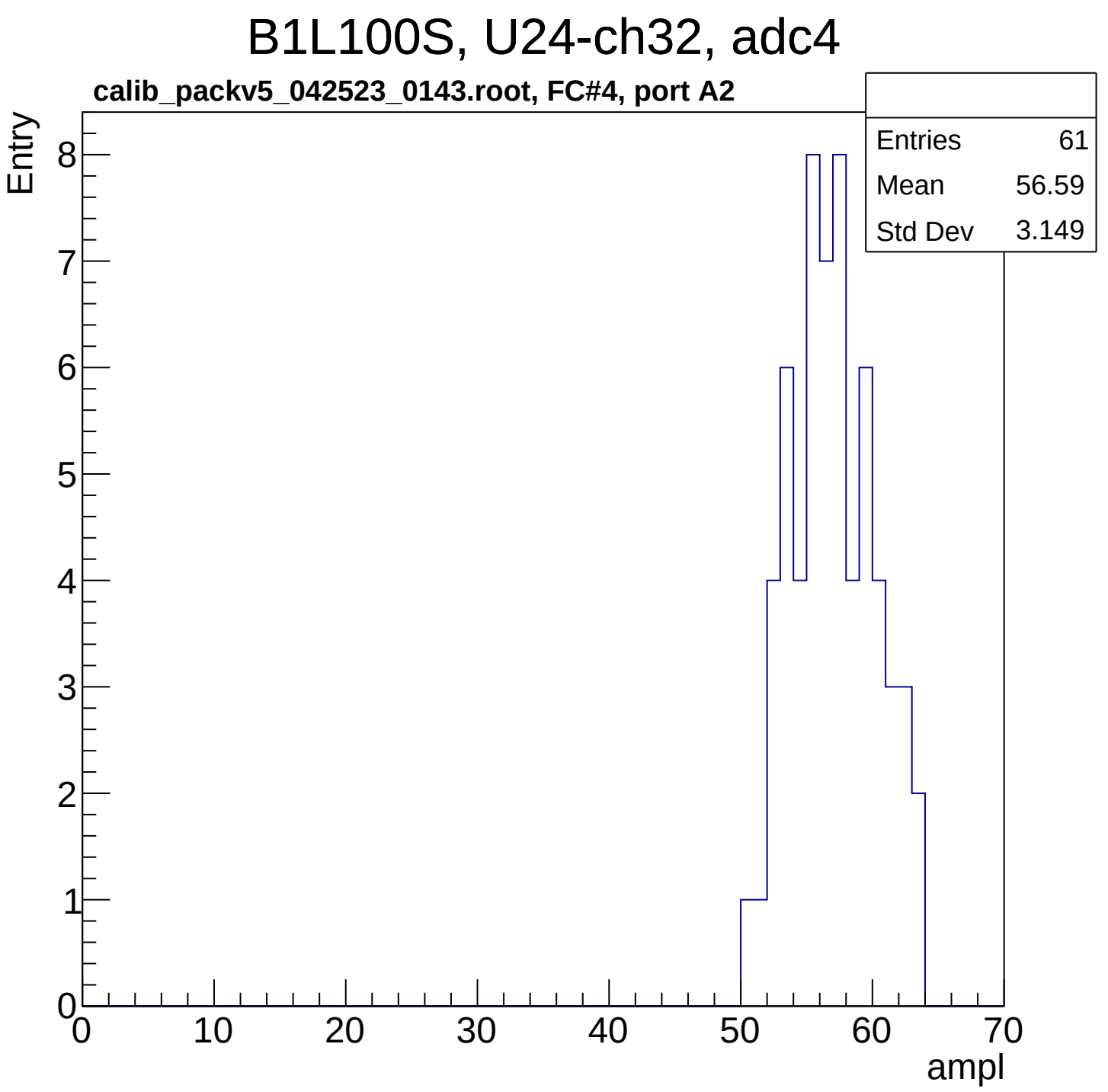
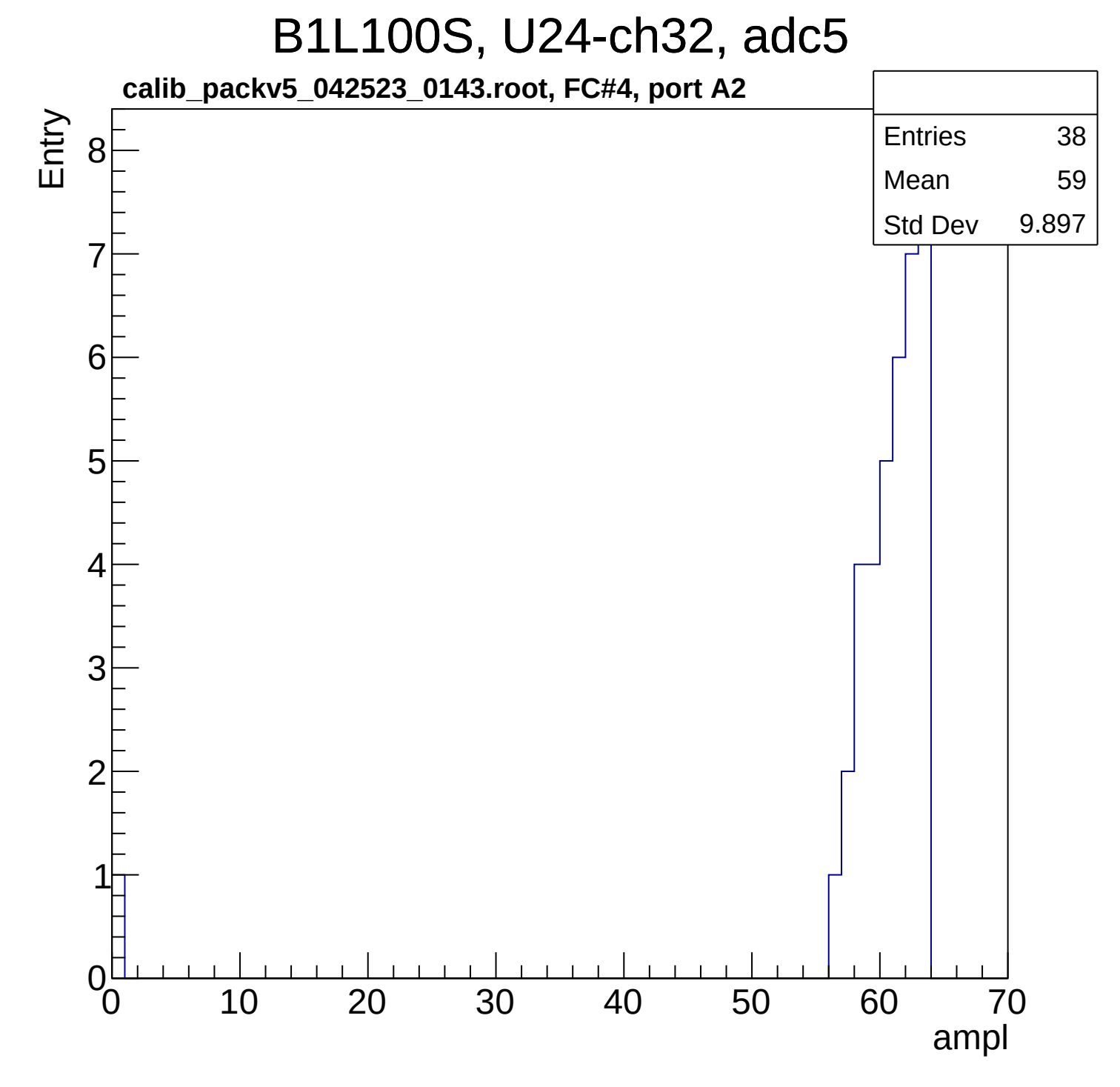

B1L100S, U24-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 56.59
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59
Std Dev 9.897
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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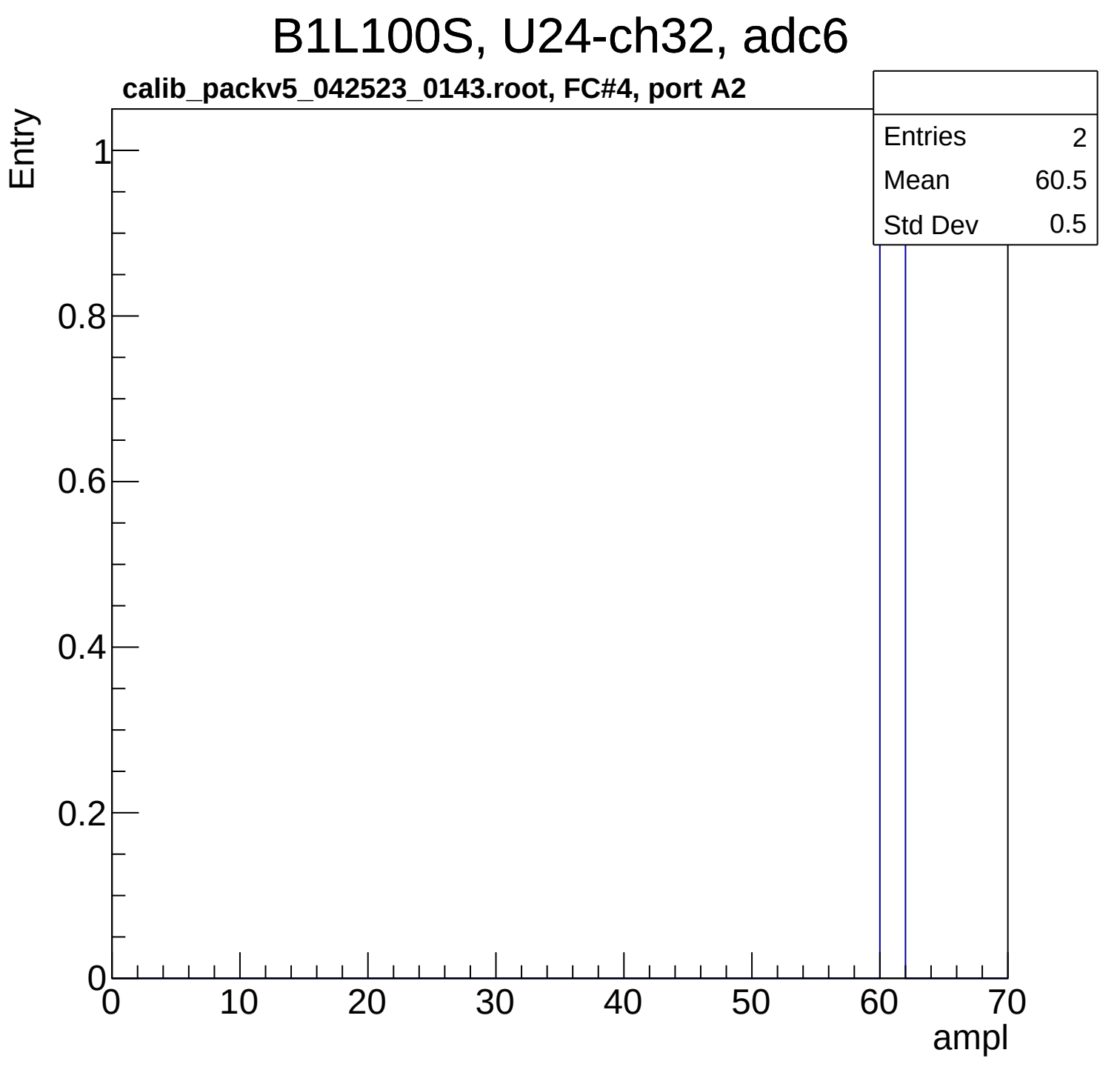
B1L100S, U24-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 60.5
Std Dev 0.5
Entry
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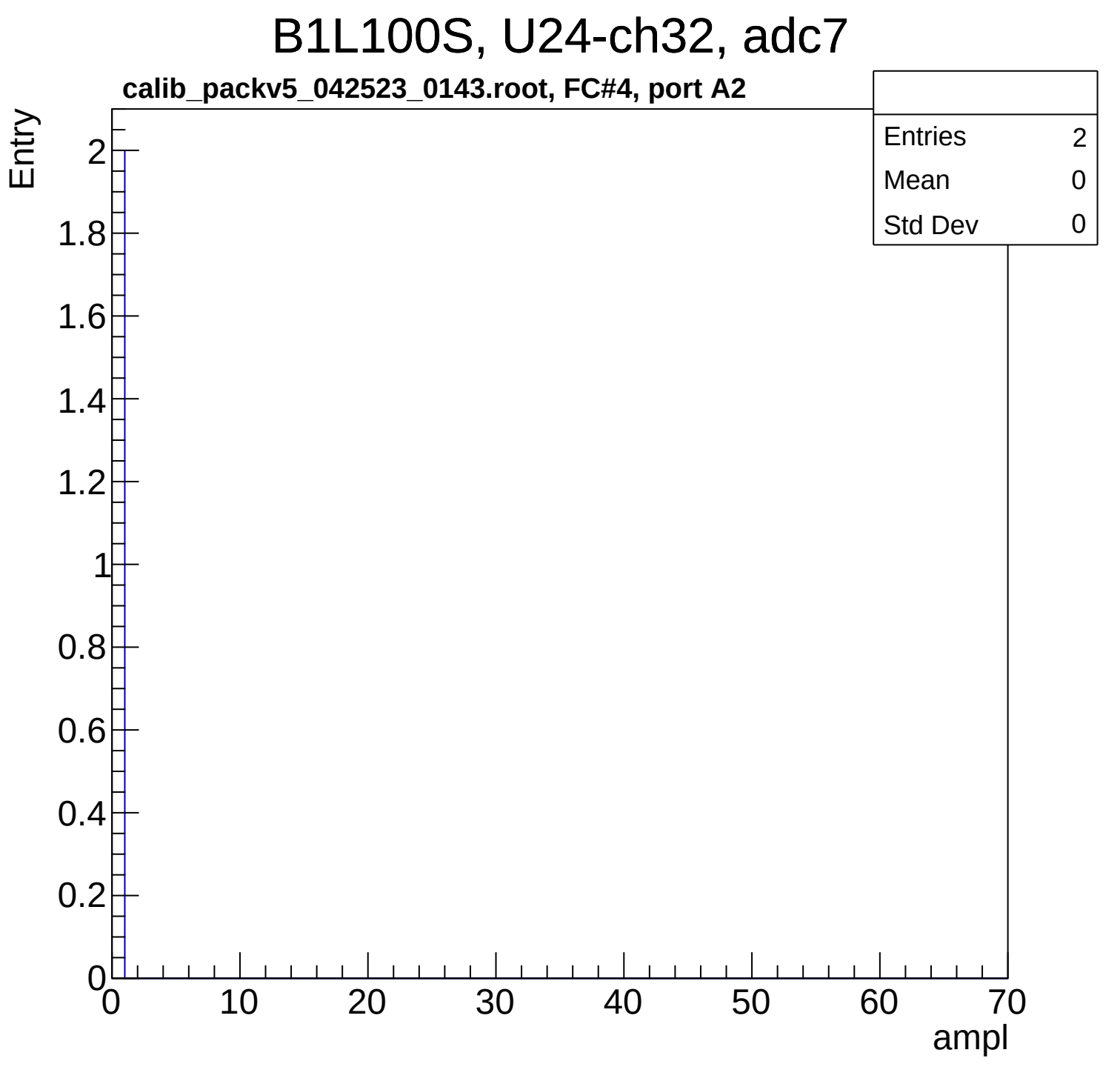
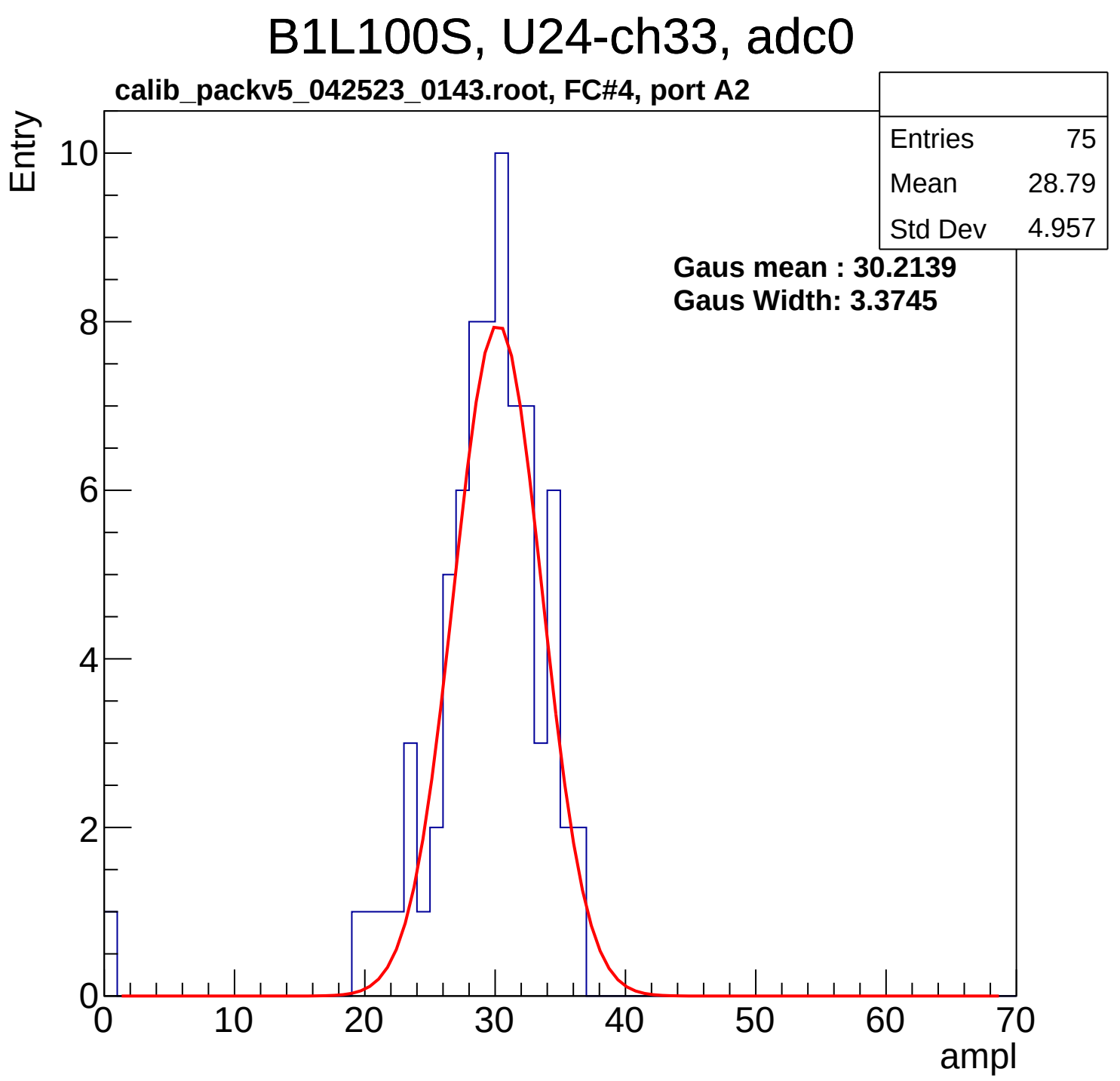
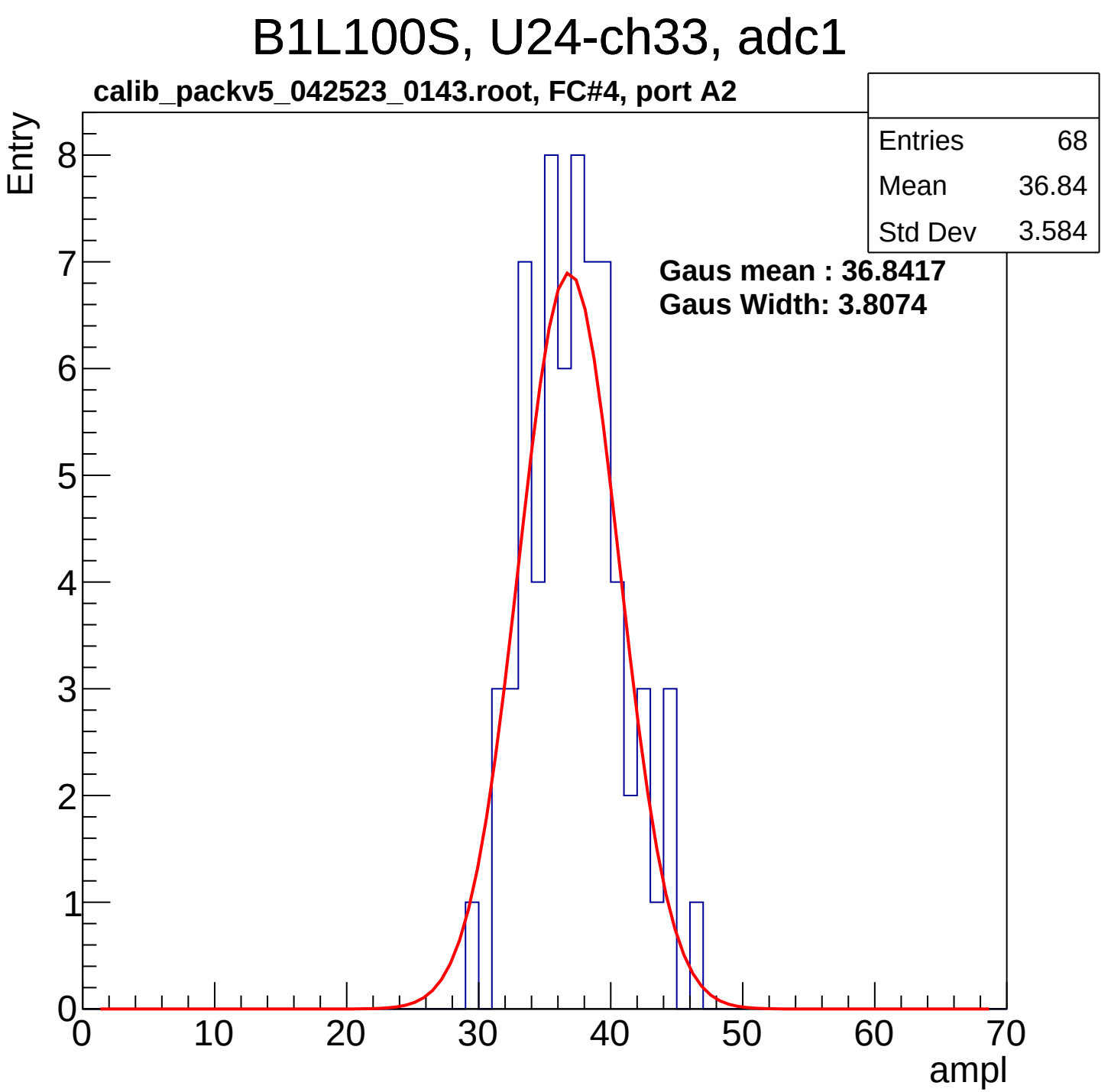
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.79
Std Dev 4.957
Gaus mean : 30.2139
Gaus Width: 3.3745
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch33, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.84
Std Dev 3.584
Gaus mean : 36.8417
Gaus Width: 3.8074
Entry
ampl
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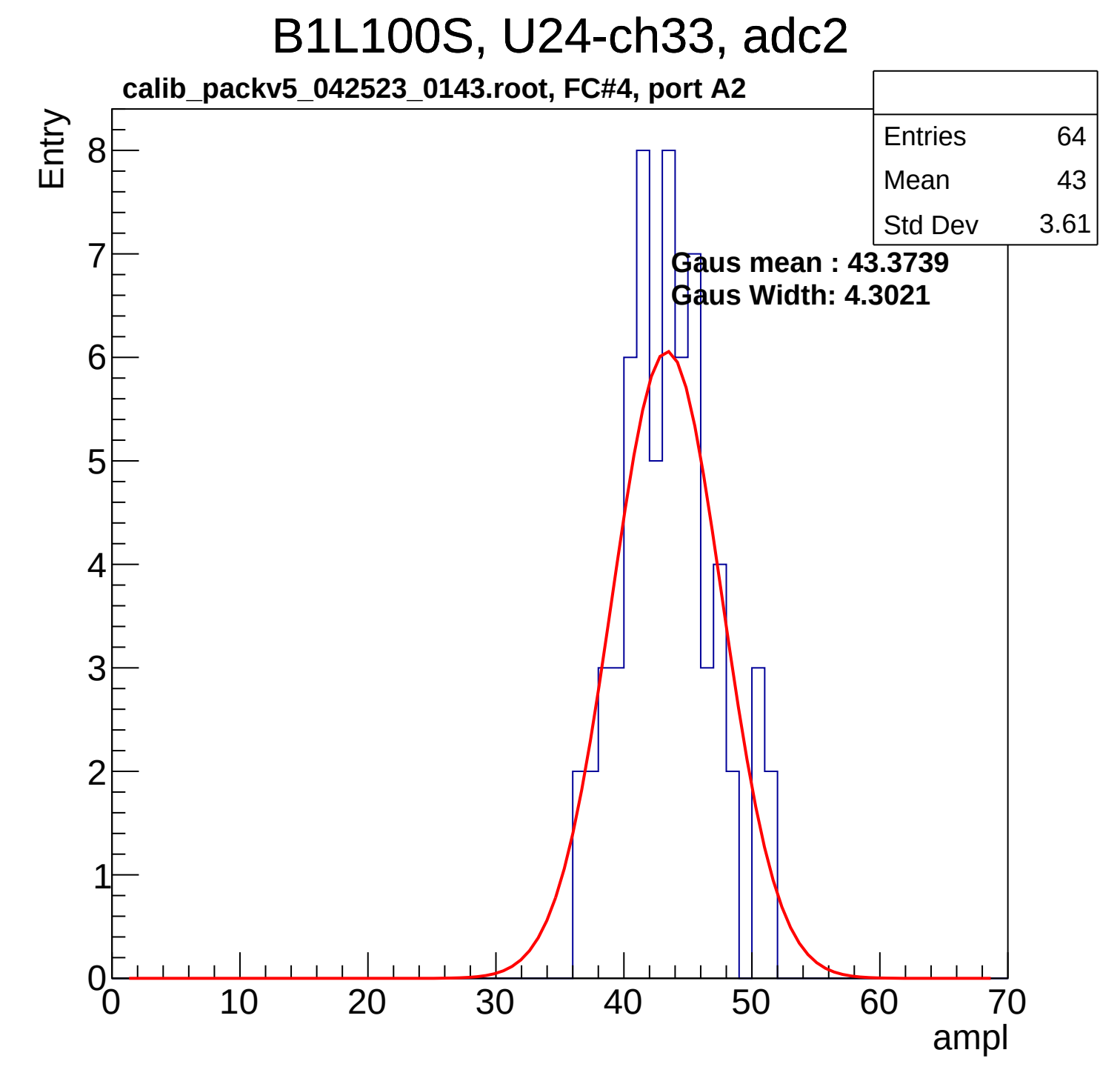
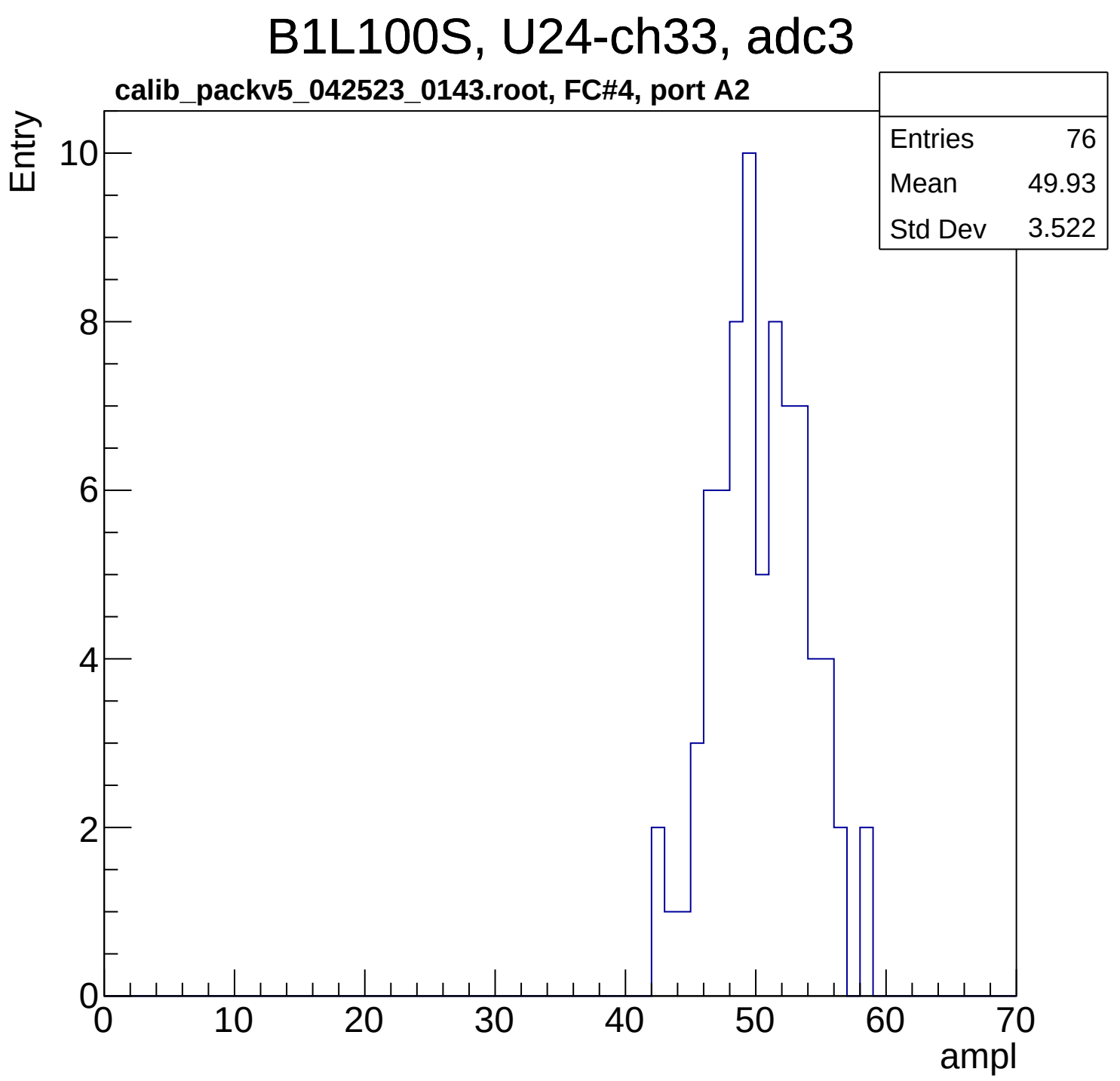
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43
Std Dev 3.61
Gaus mean : 43.3739
Gaus Width: 4.3021
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.93
Std Dev 3.522
Entry
0
2
4
6
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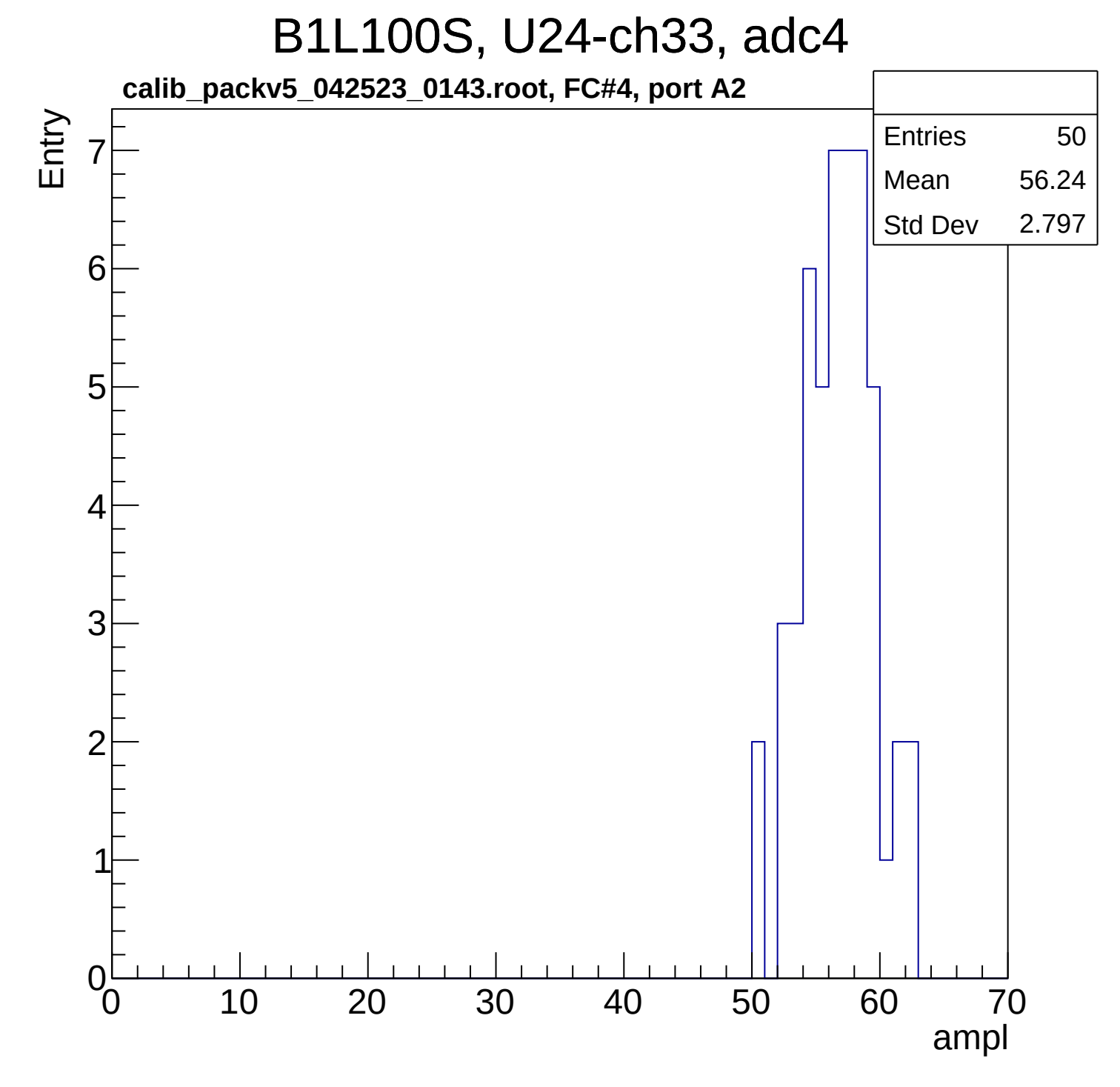
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 56.24
Std Dev 2.797
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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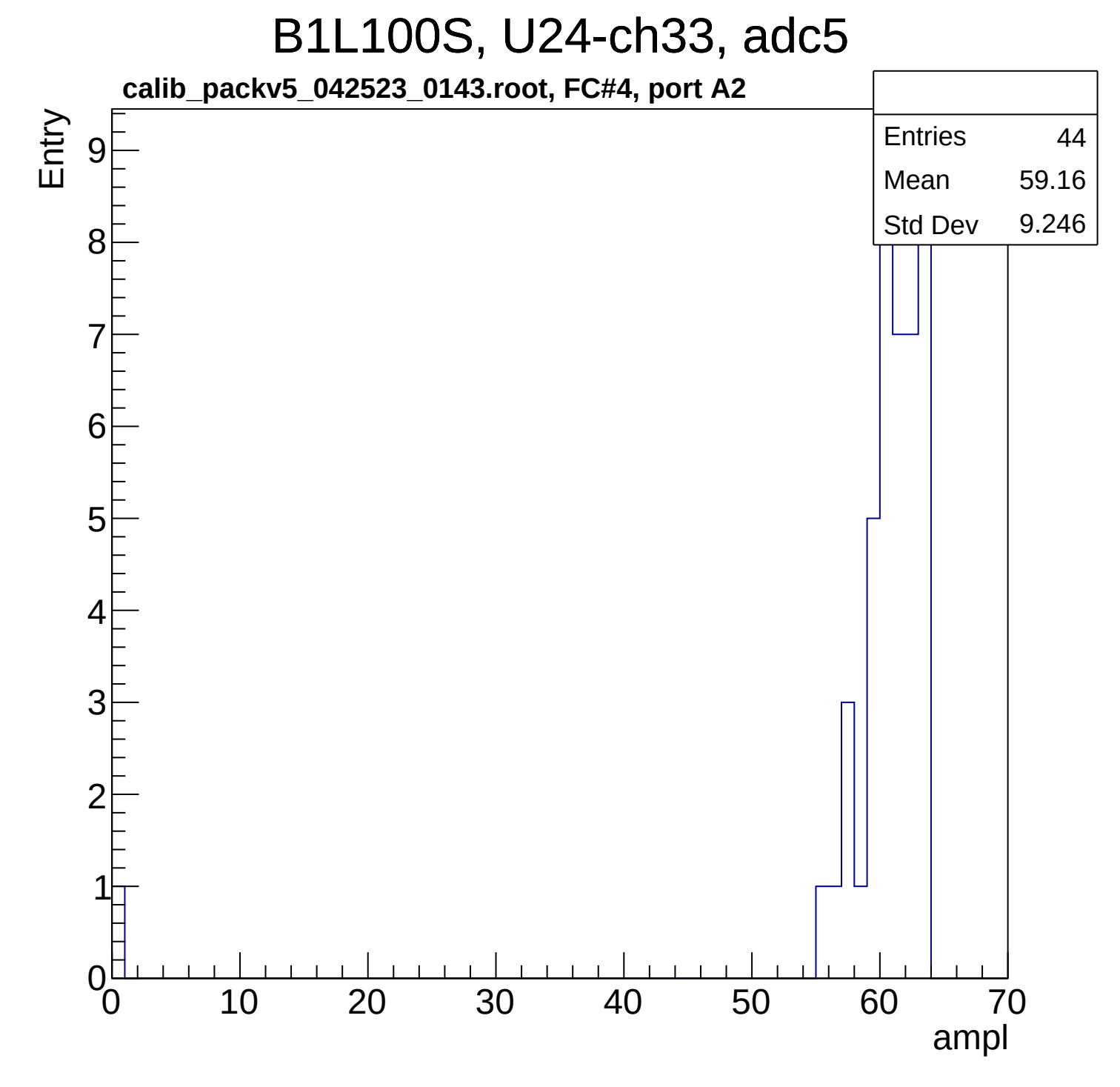
40
50
60
70


B1L100S, U24-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 59.16
Std Dev 9.246
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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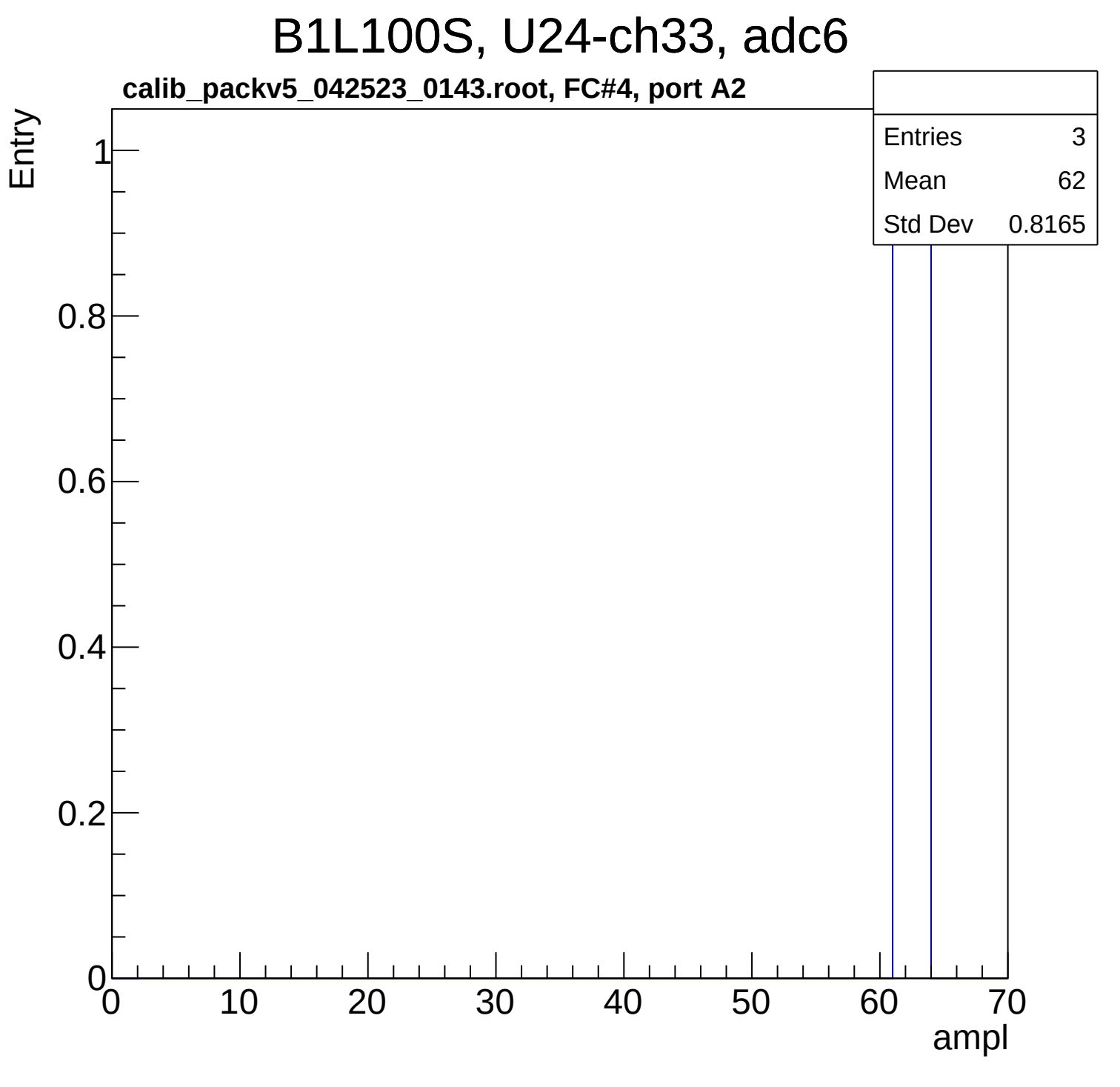
B1L100S, U24-ch33, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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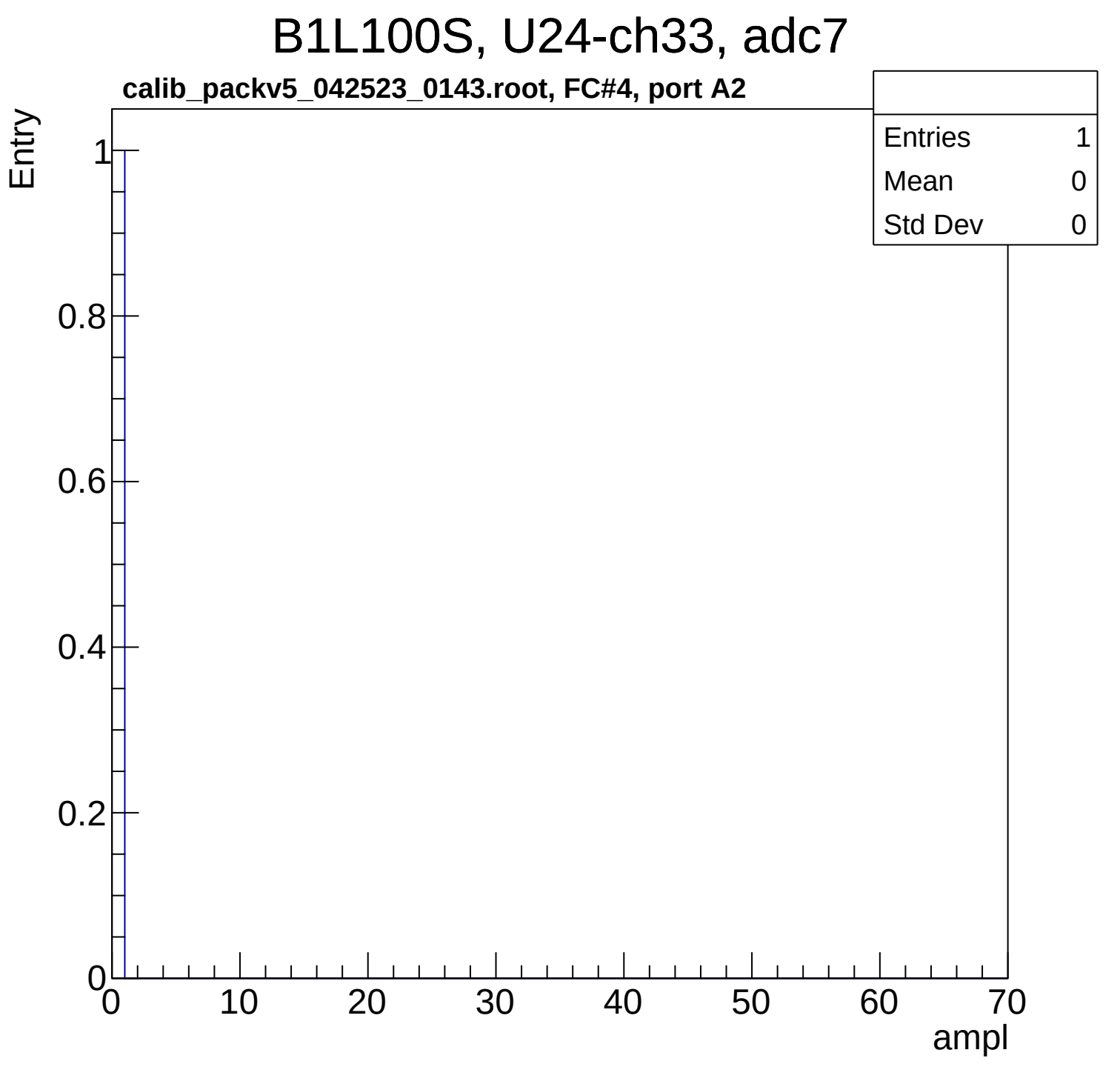
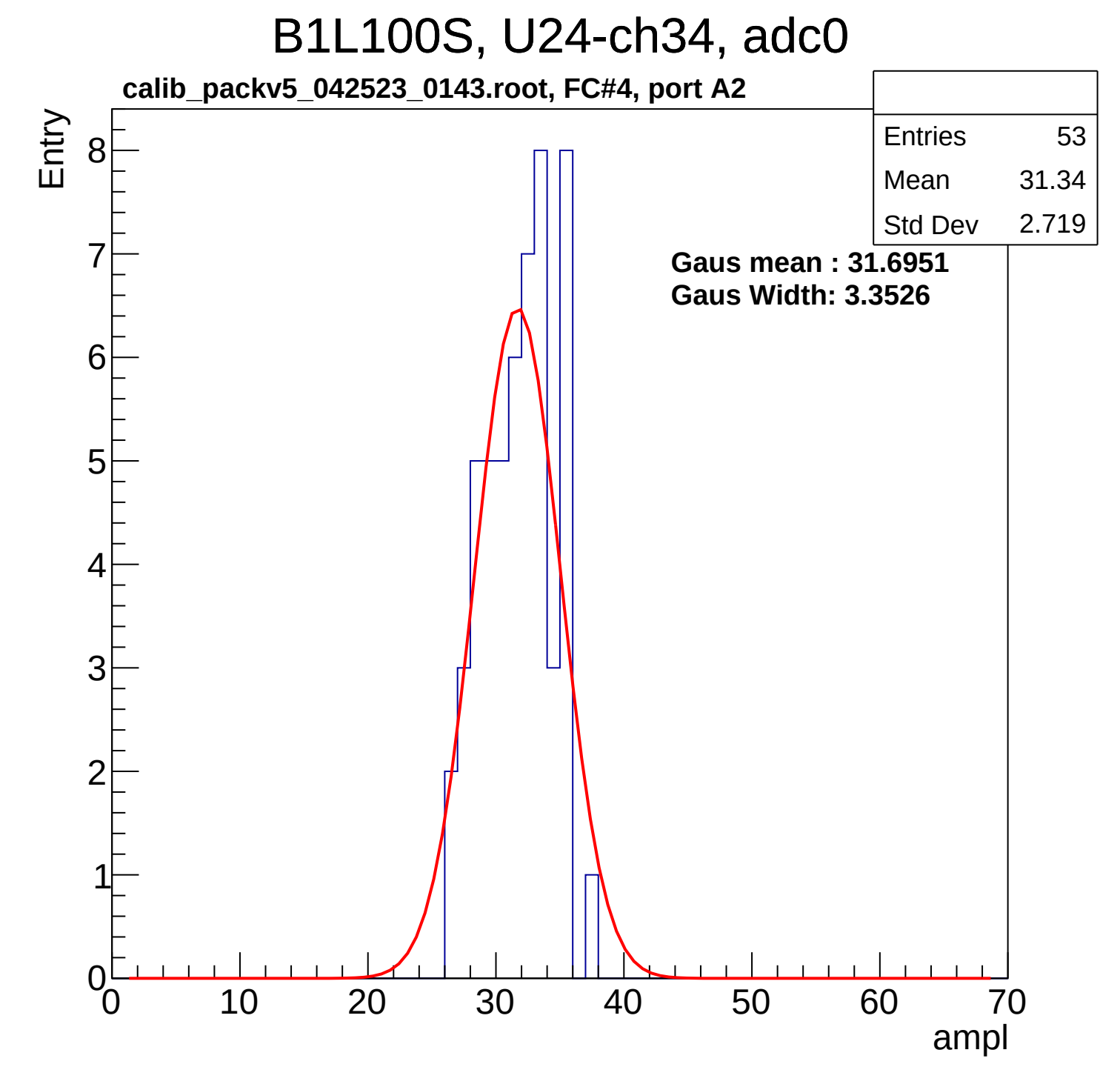
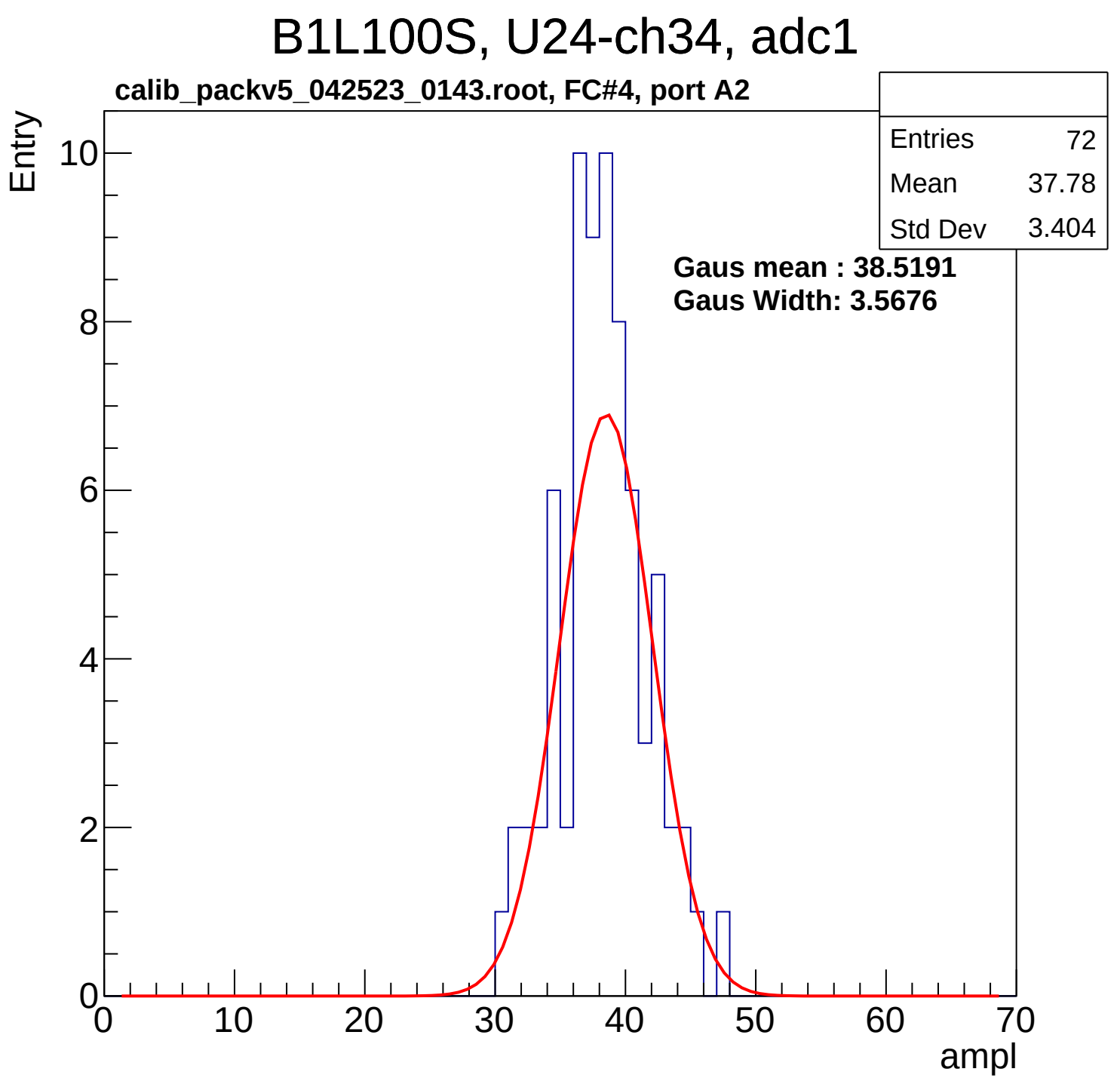
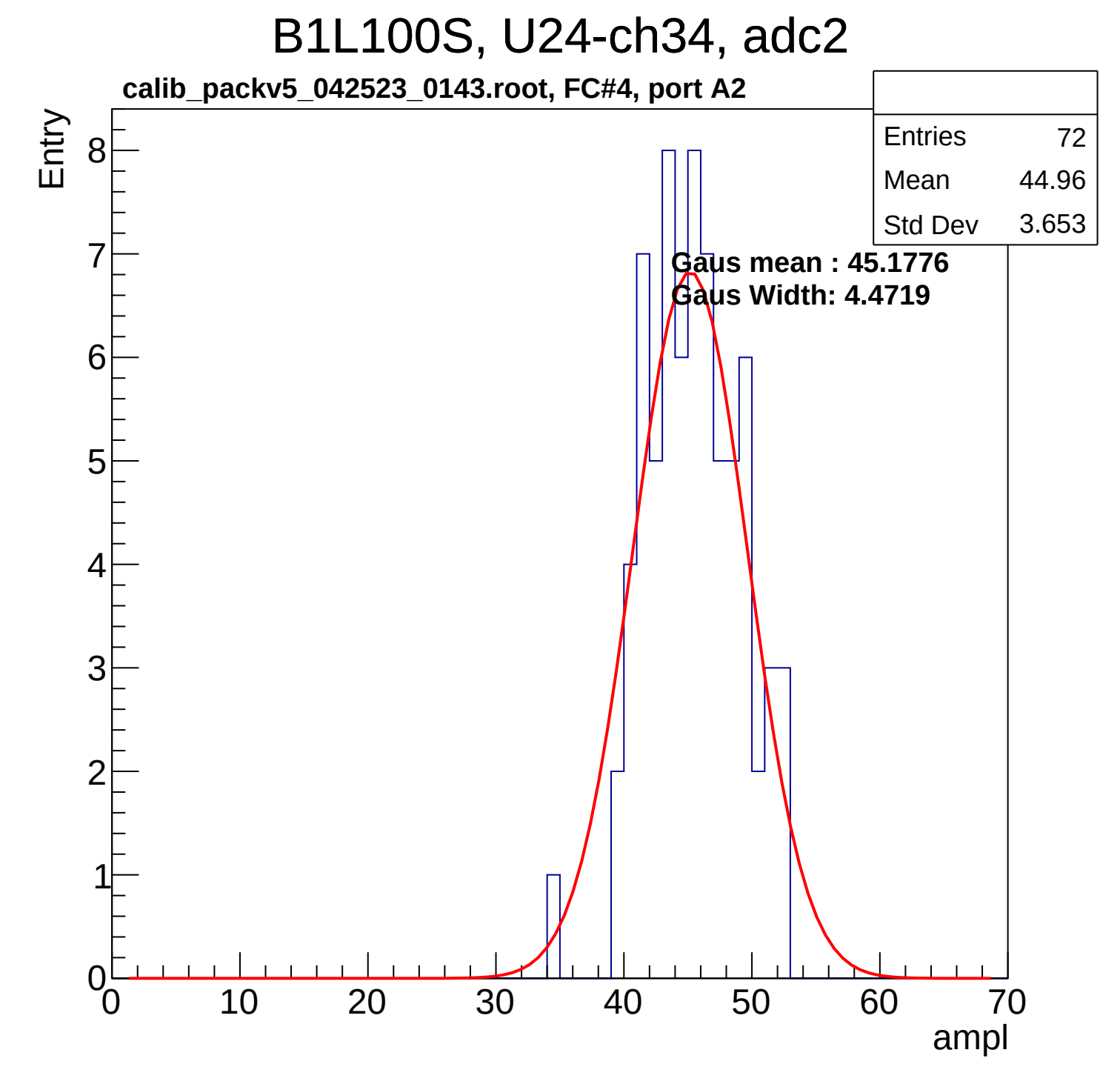
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch33, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 31.34
Std Dev 2.719
Gaus mean : 31.6951
Gaus Width: 3.3526
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.78
Std Dev 3.404
Gaus mean : 38.5191
Gaus Width: 3.5676
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.96
Std Dev 3.653
Gaus mean : 45.1776
Gaus Width: 4.4719
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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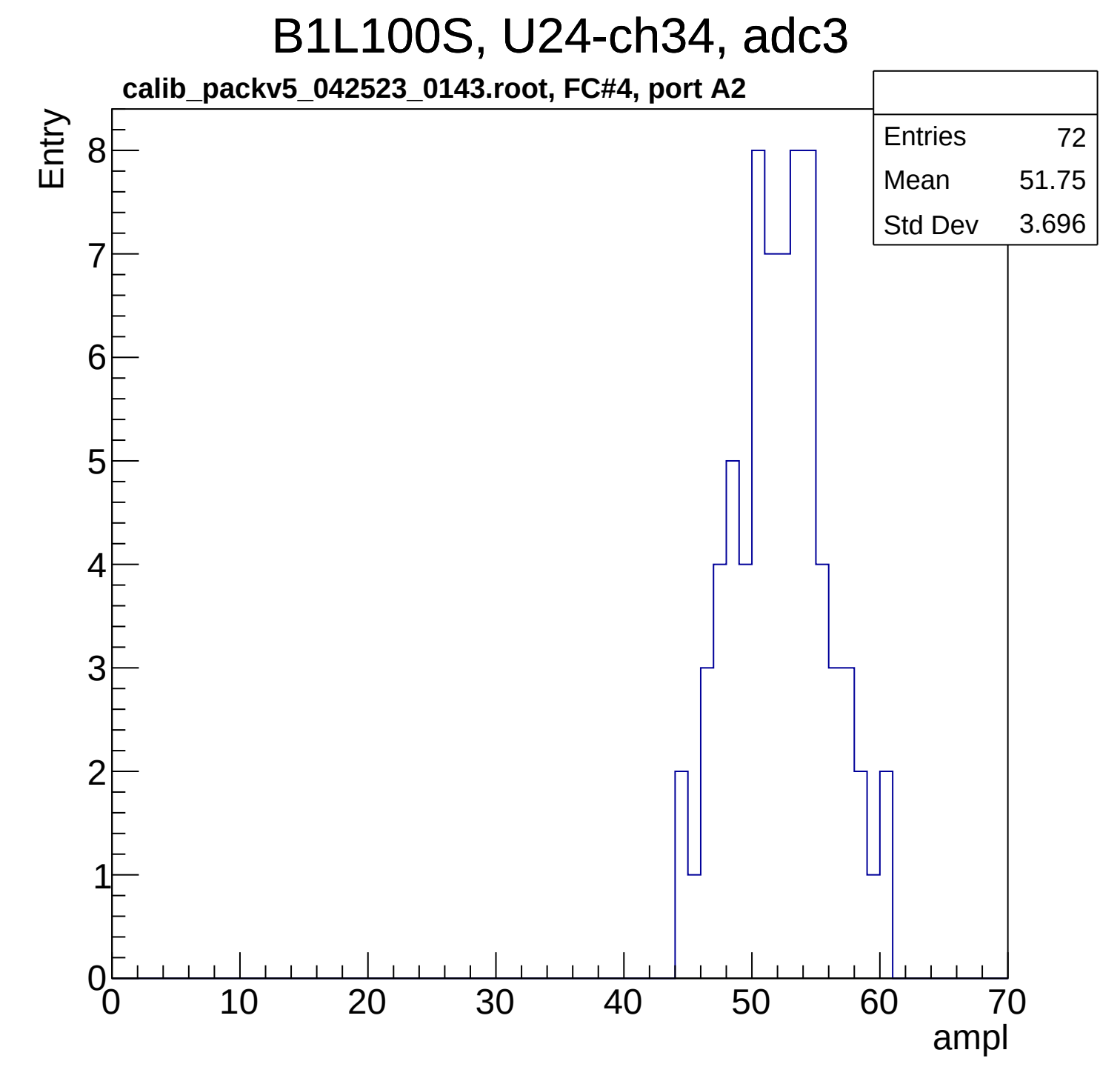
B1L100S, U24-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 51.75
Std Dev 3.696
Entry
ampl
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4
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8
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60
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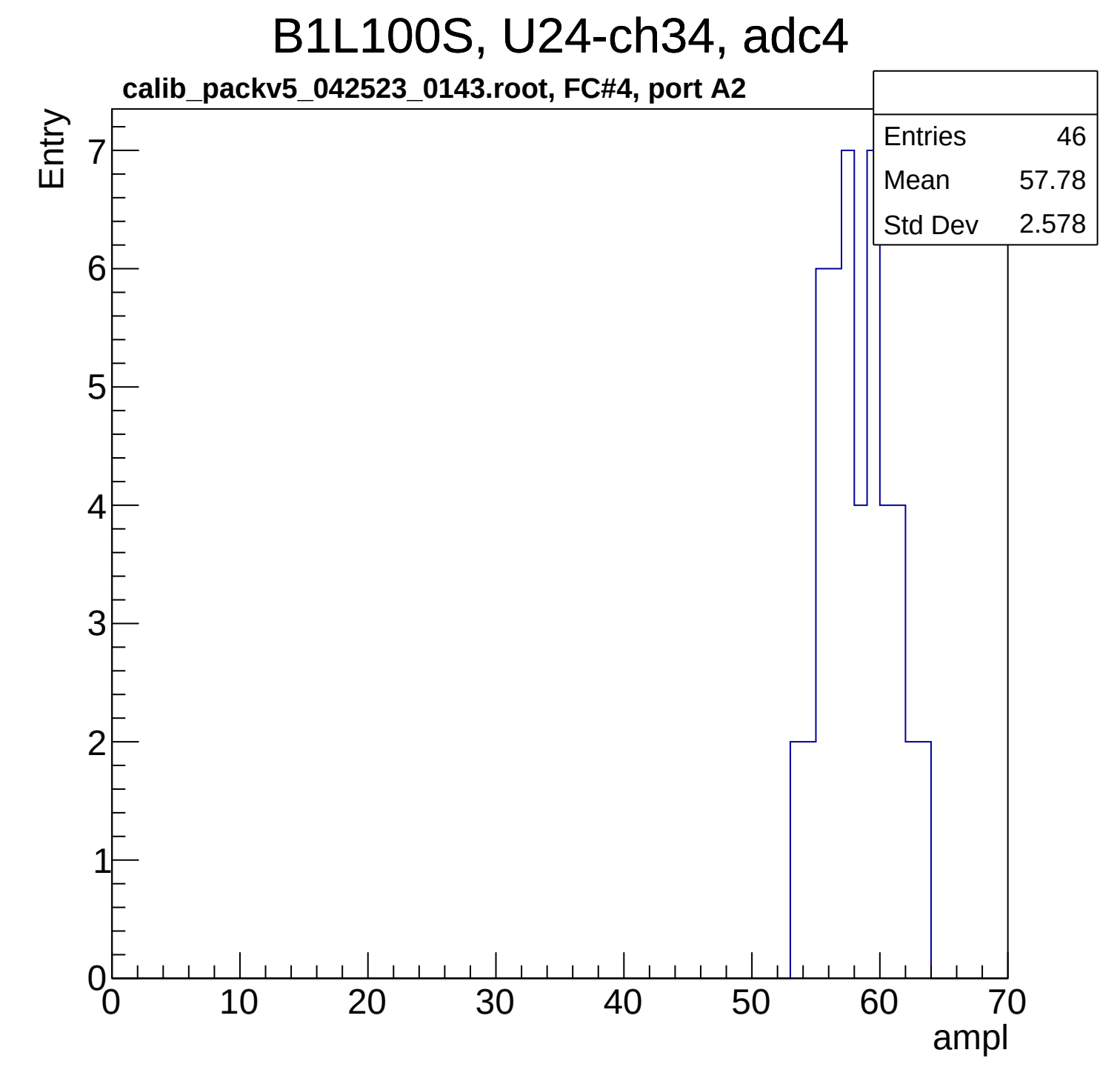
B1L100S, U24-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 57.78
Std Dev 2.578
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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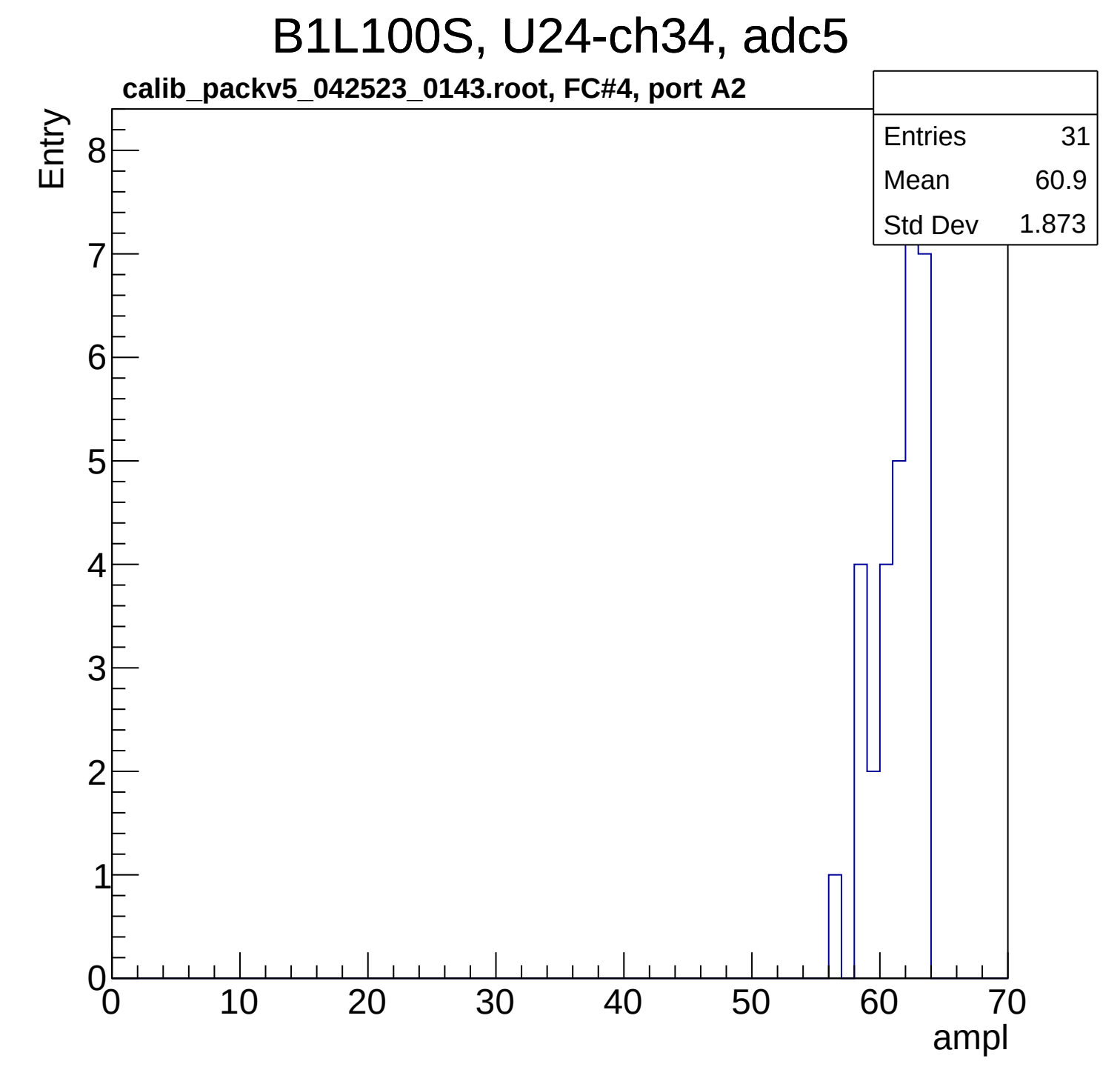
B1L100S, U24-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.9
Std Dev 1.873
Entry
ampl
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3
4
5
6
7
8
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40
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60
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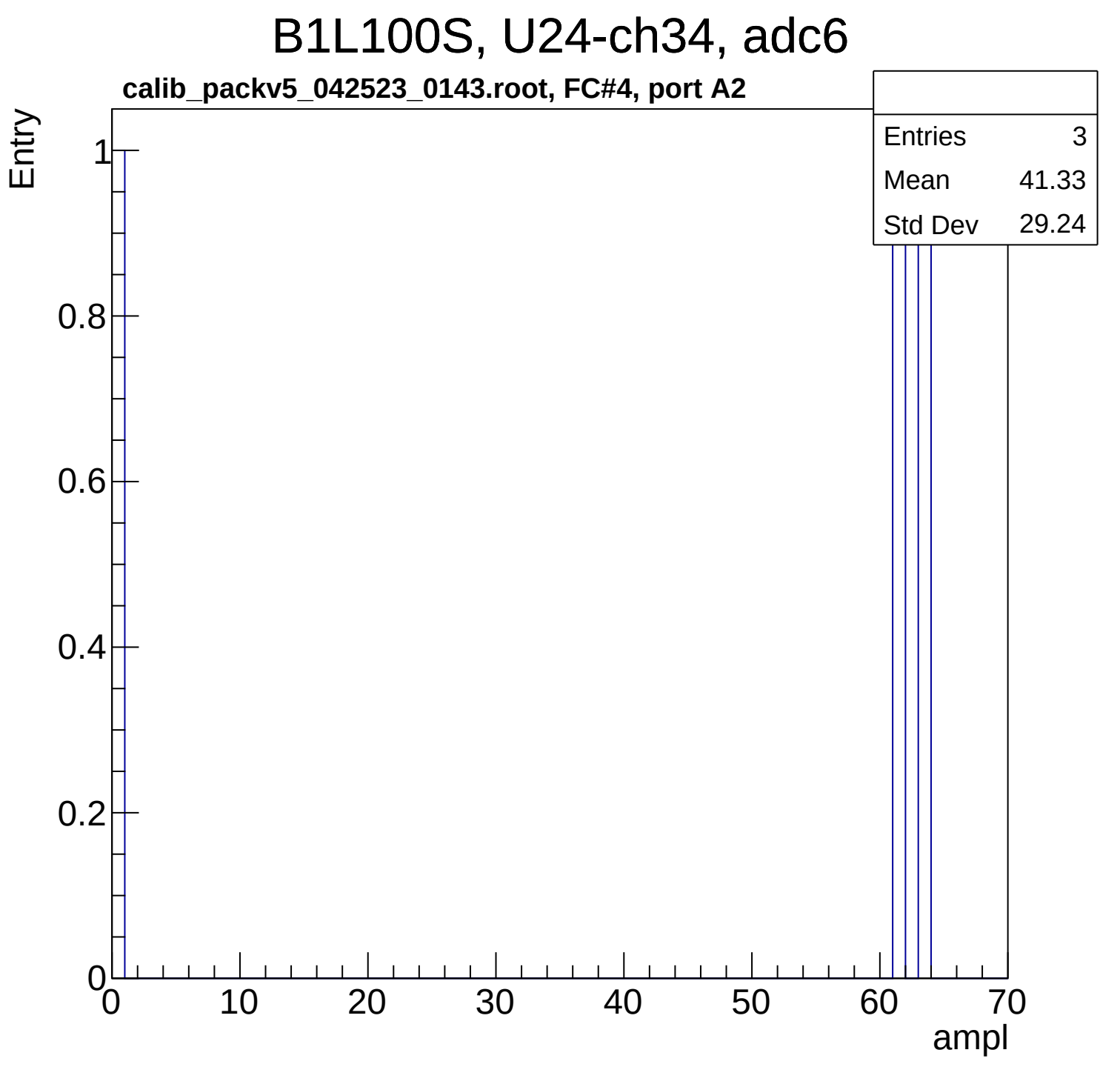
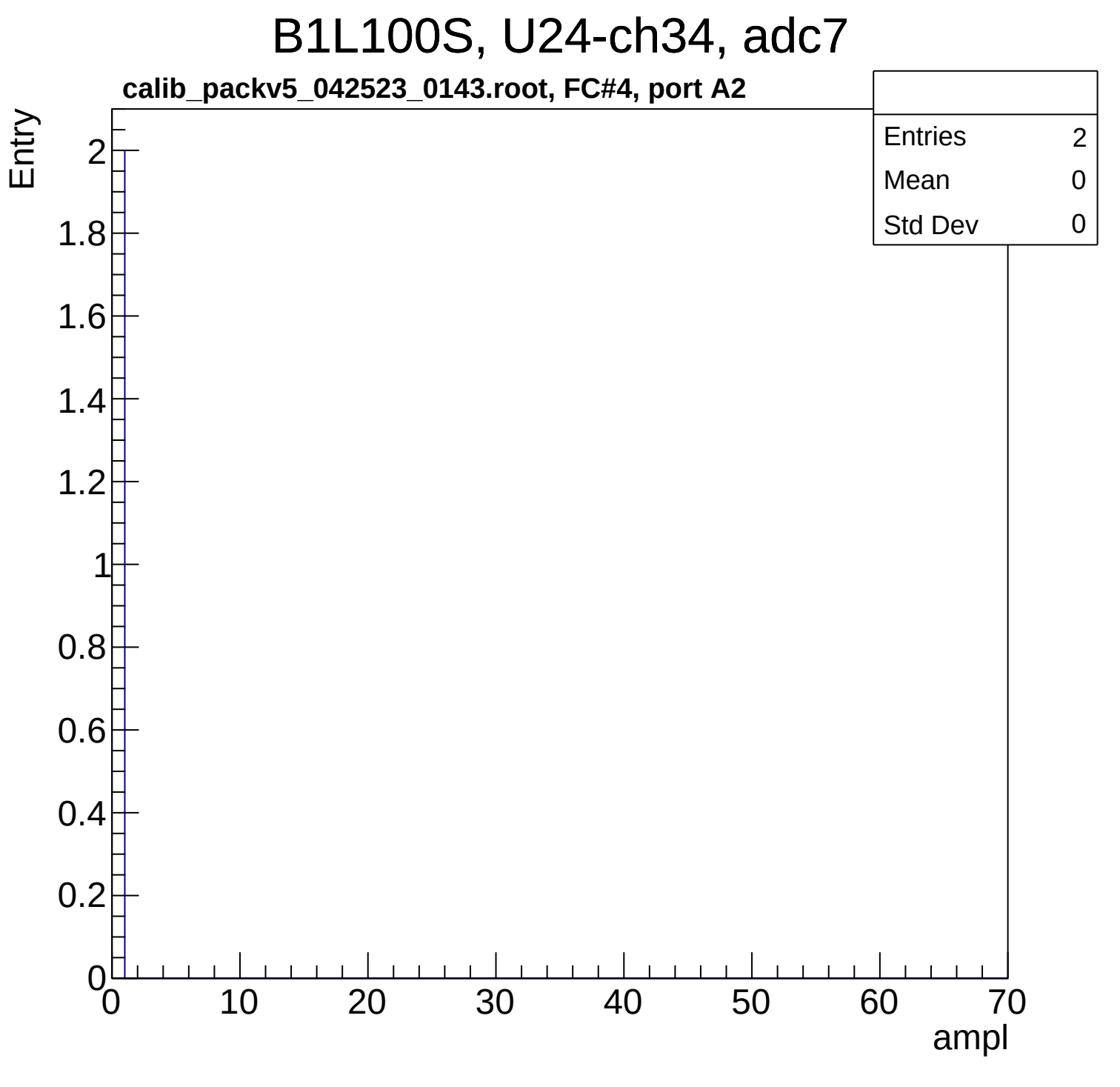

B1L100S, U24-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U24-ch34, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
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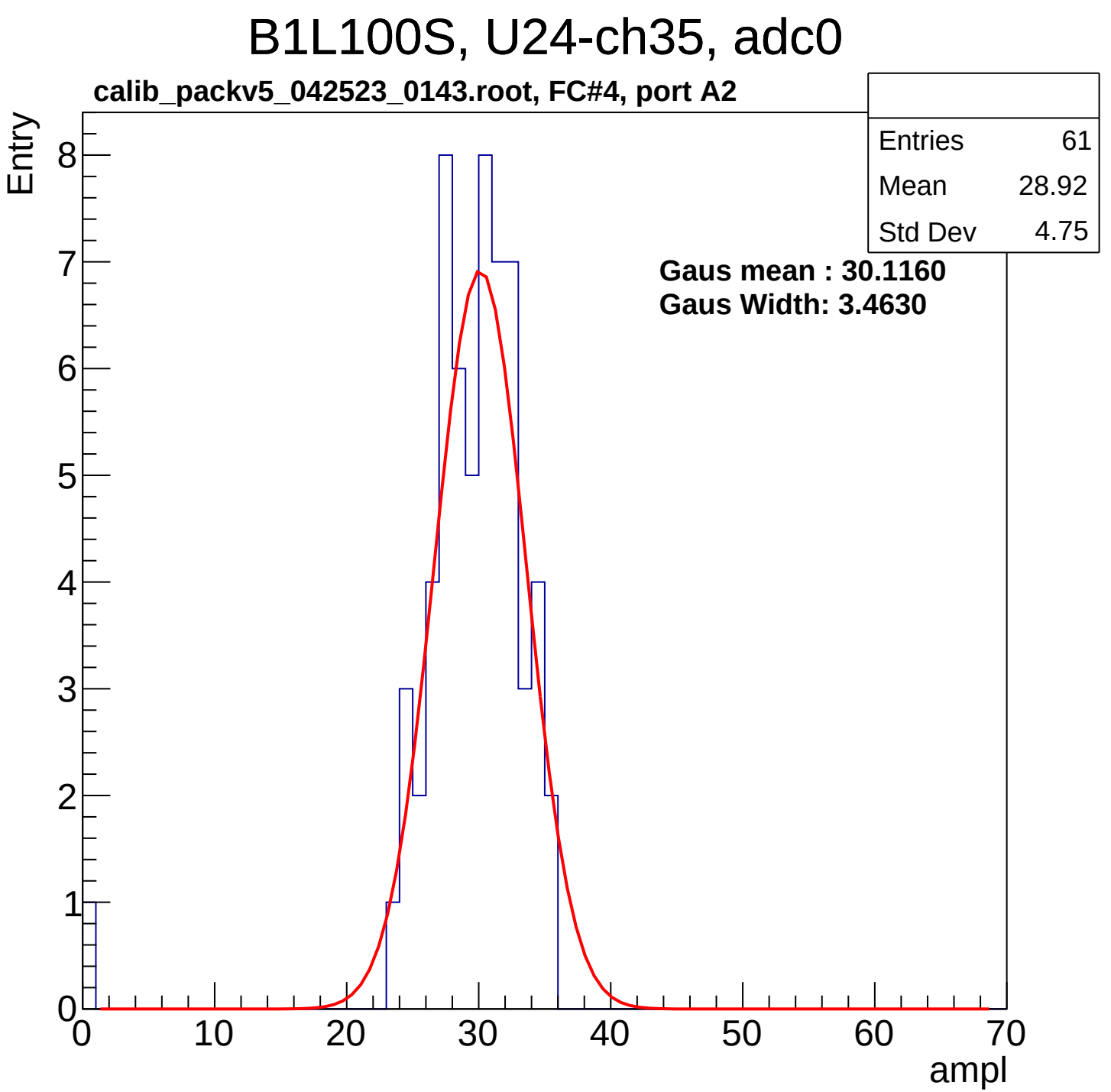
50
60
70
ampl


B1L100S, U24-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.92
Std Dev 4.75
Gaus mean : 30.1160
Gaus Width: 3.4630
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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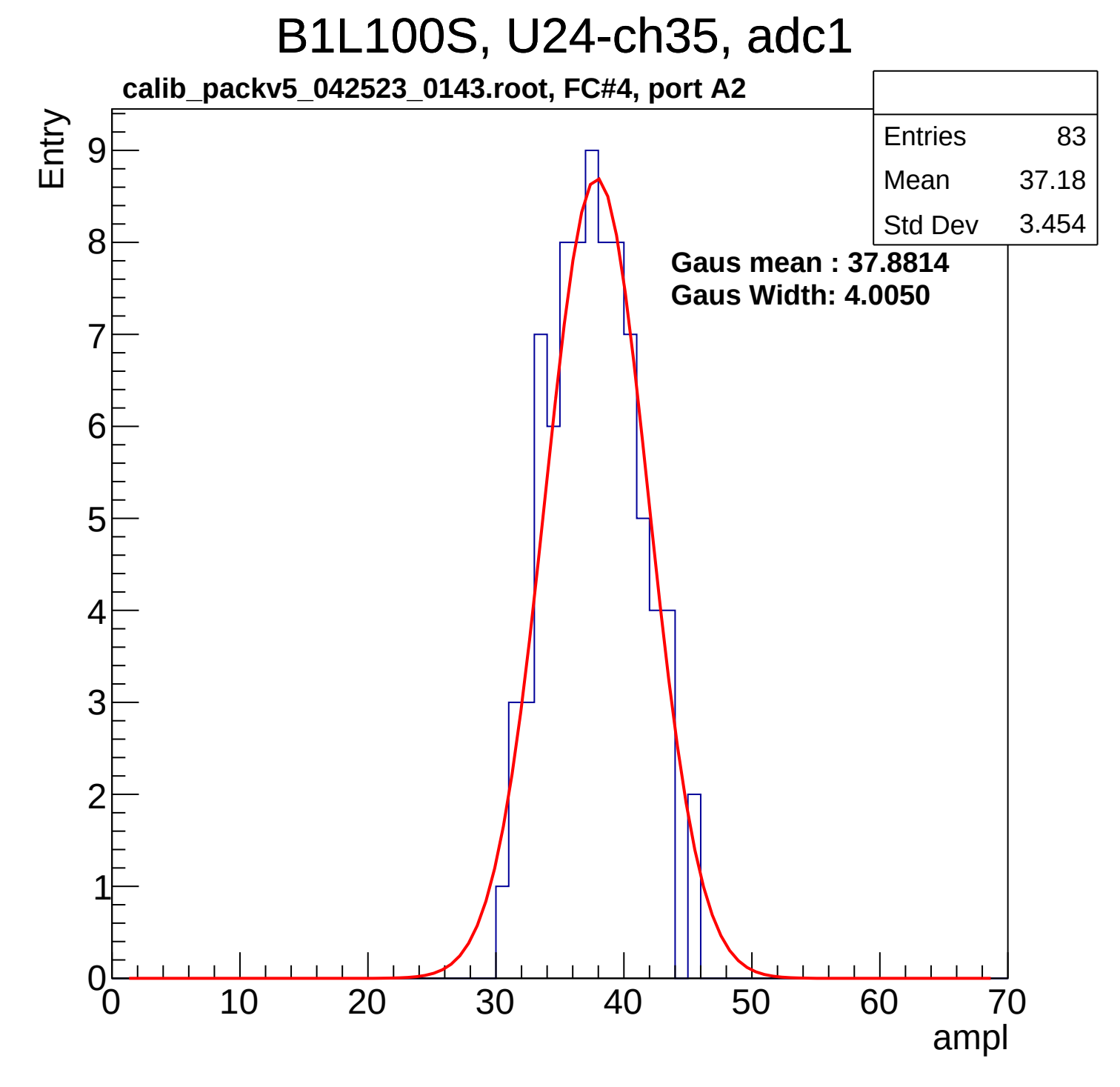
B1L100S, U24-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 37.18
Std Dev 3.454
Gaus mean : 37.8814
Gaus Width: 4.0050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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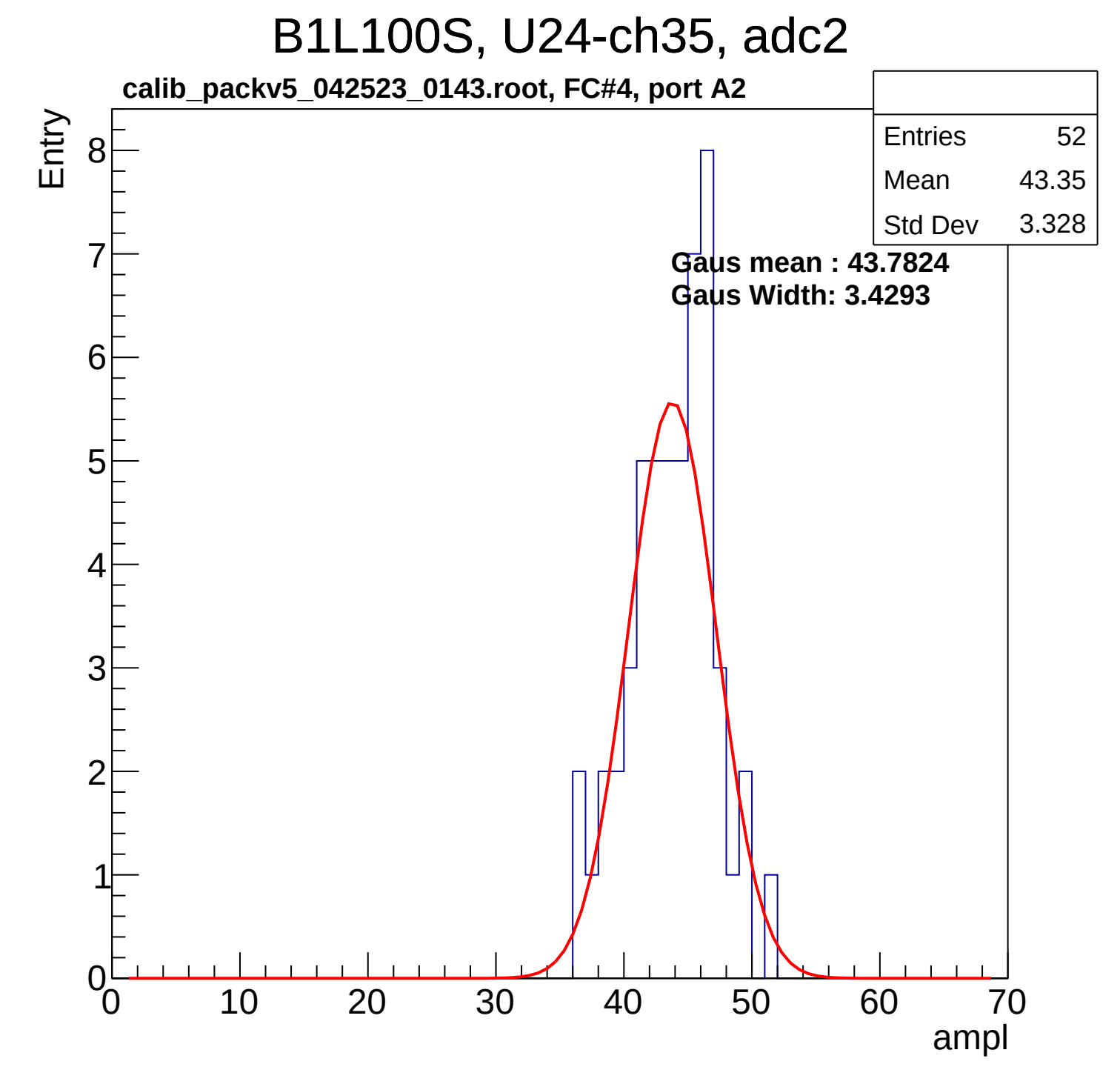
B1L100S, U24-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.35
Std Dev 3.328
Gaus mean : 43.7824
Gaus Width: 3.4293
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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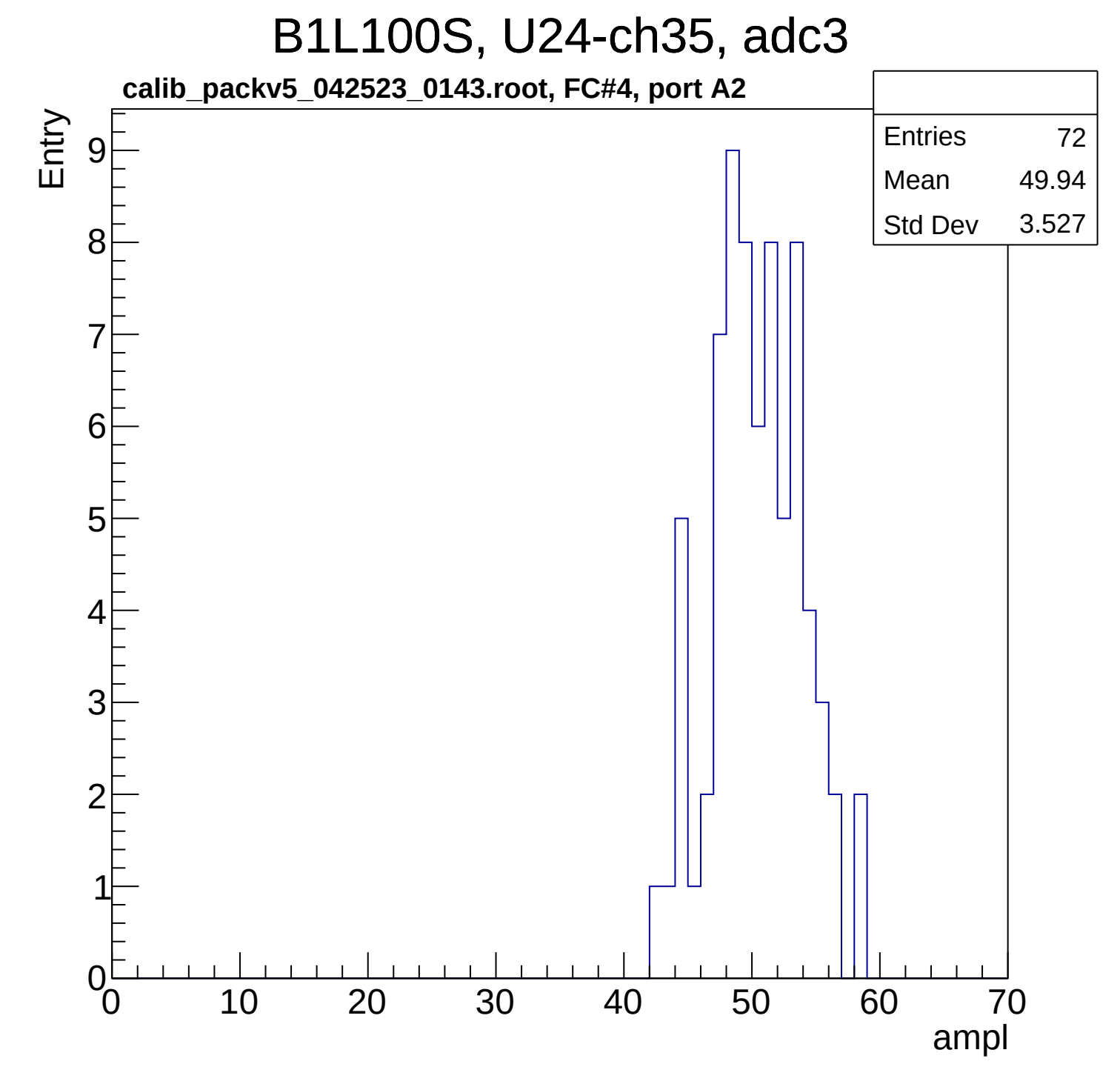
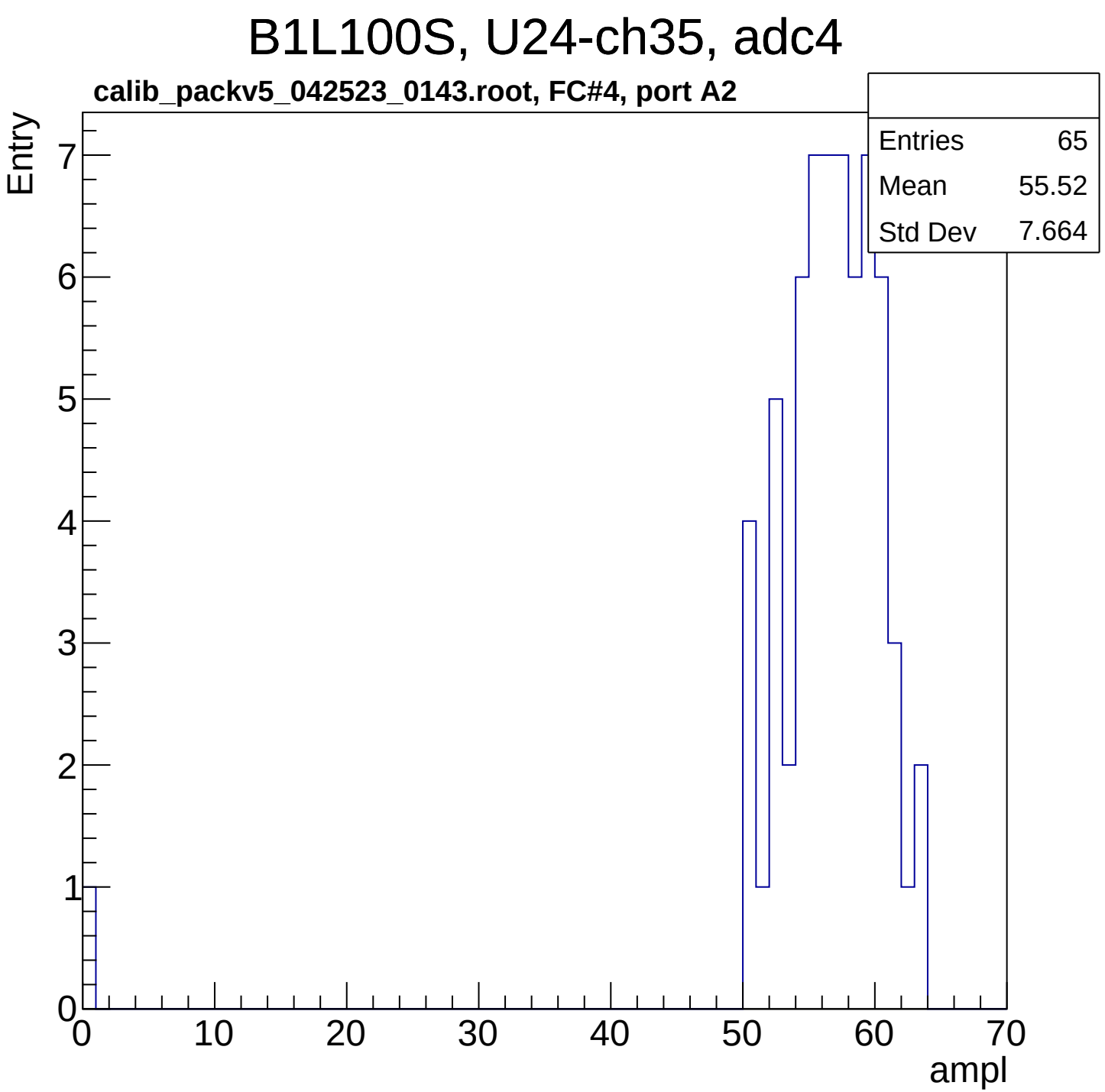
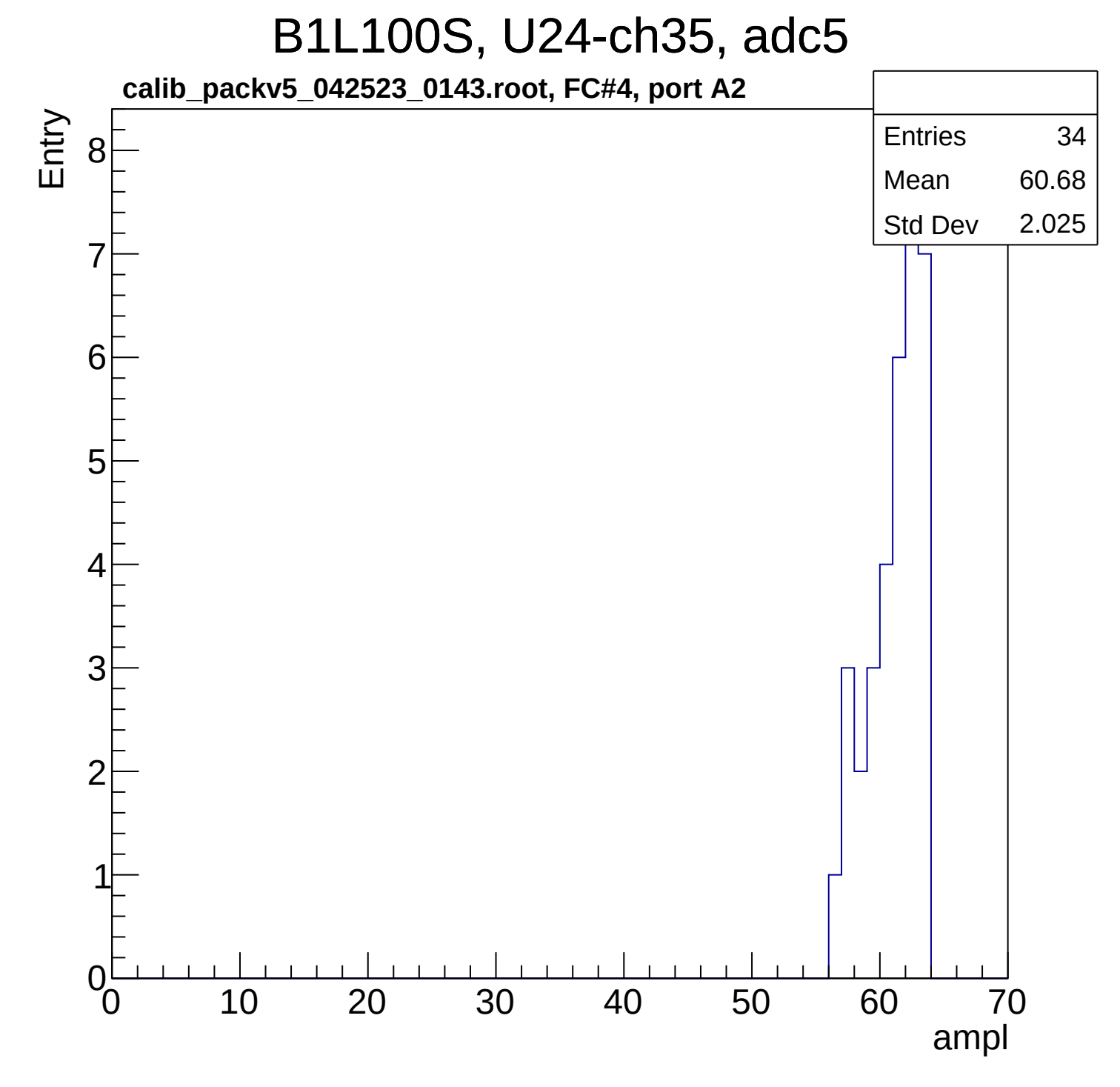

B1L100S, U24-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 49.94
Std Dev 3.527
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch35, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.52
Std Dev 7.664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 60.68
Std Dev 2.025
Entry
ampl
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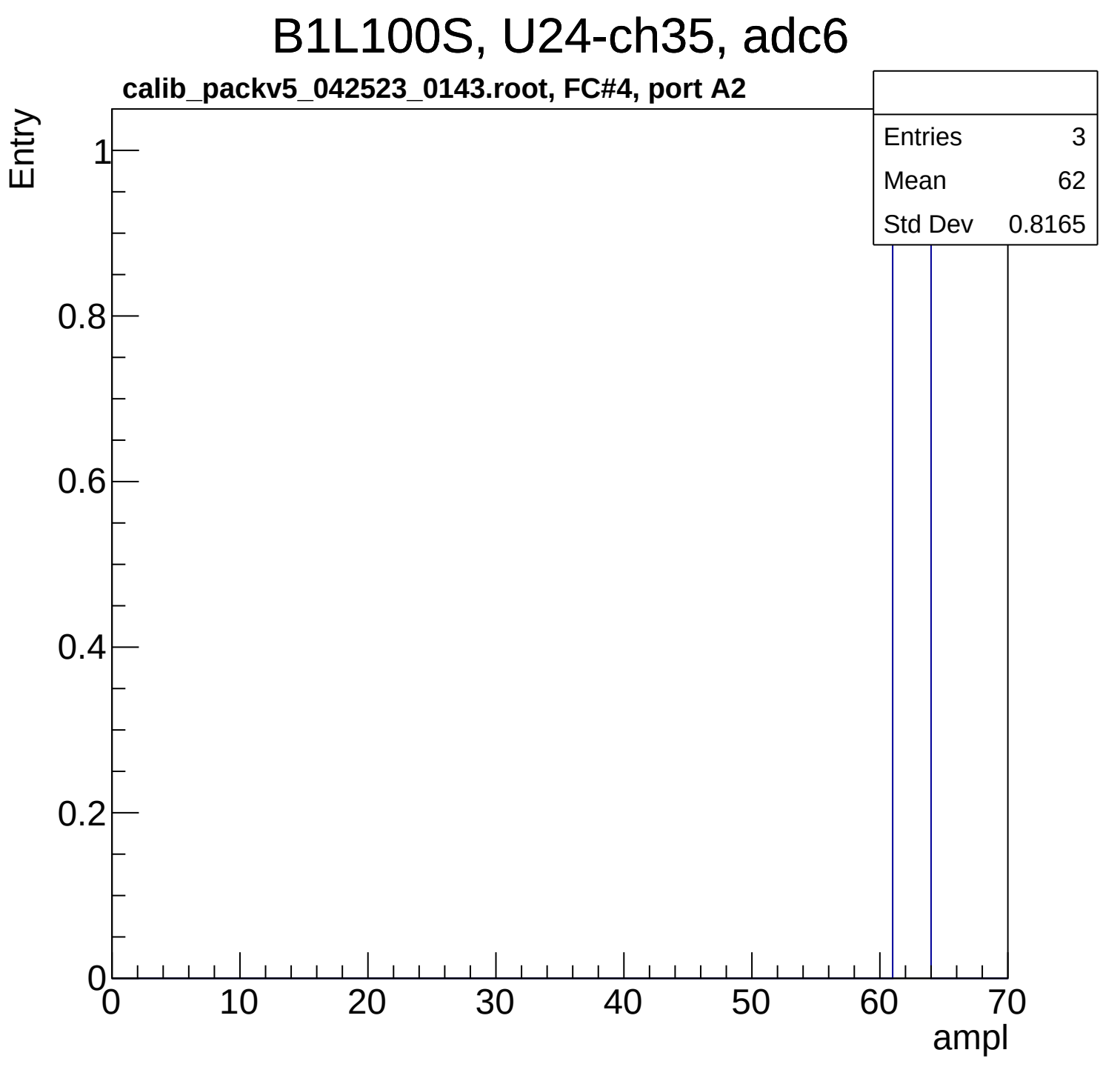
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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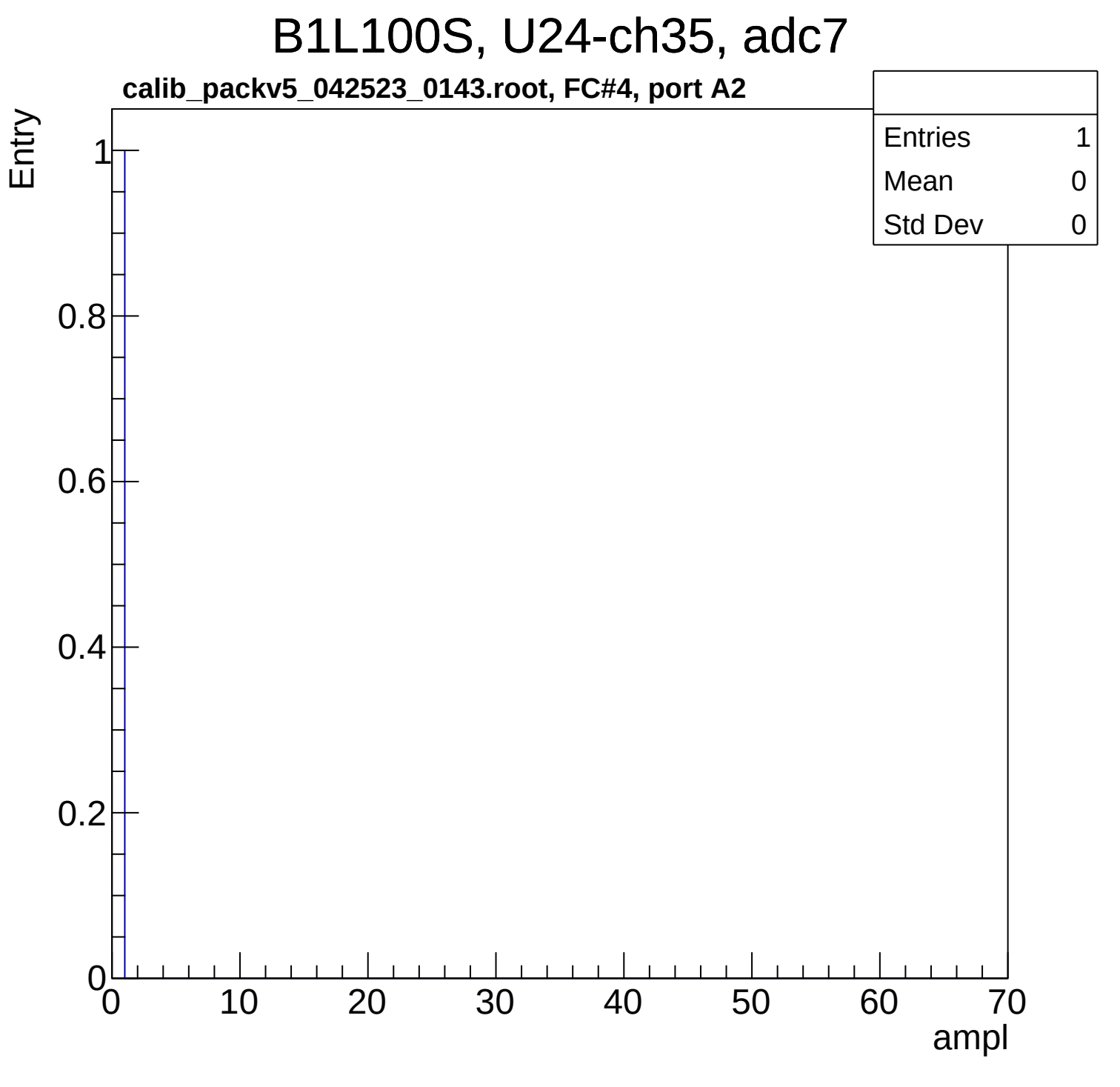
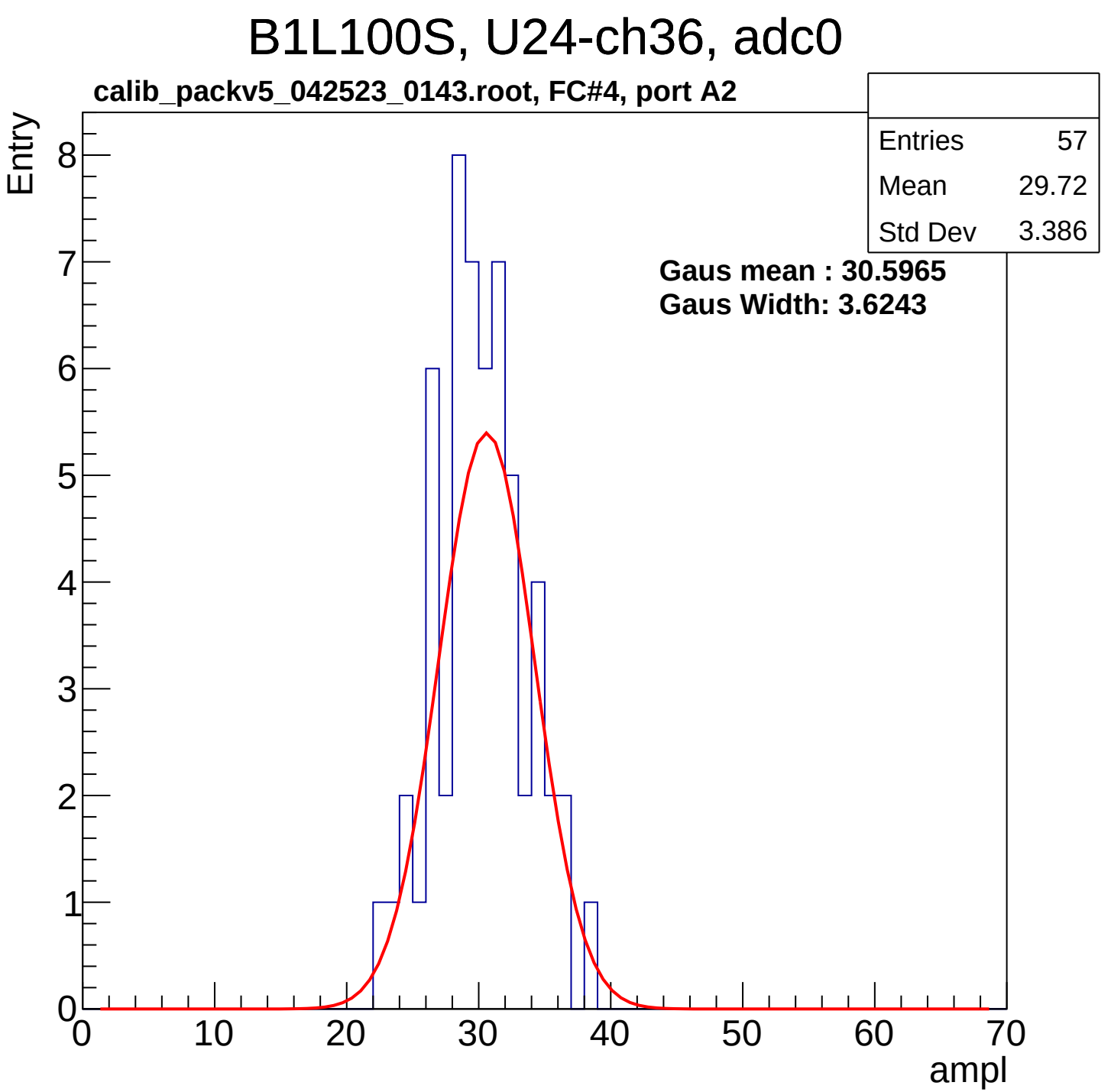
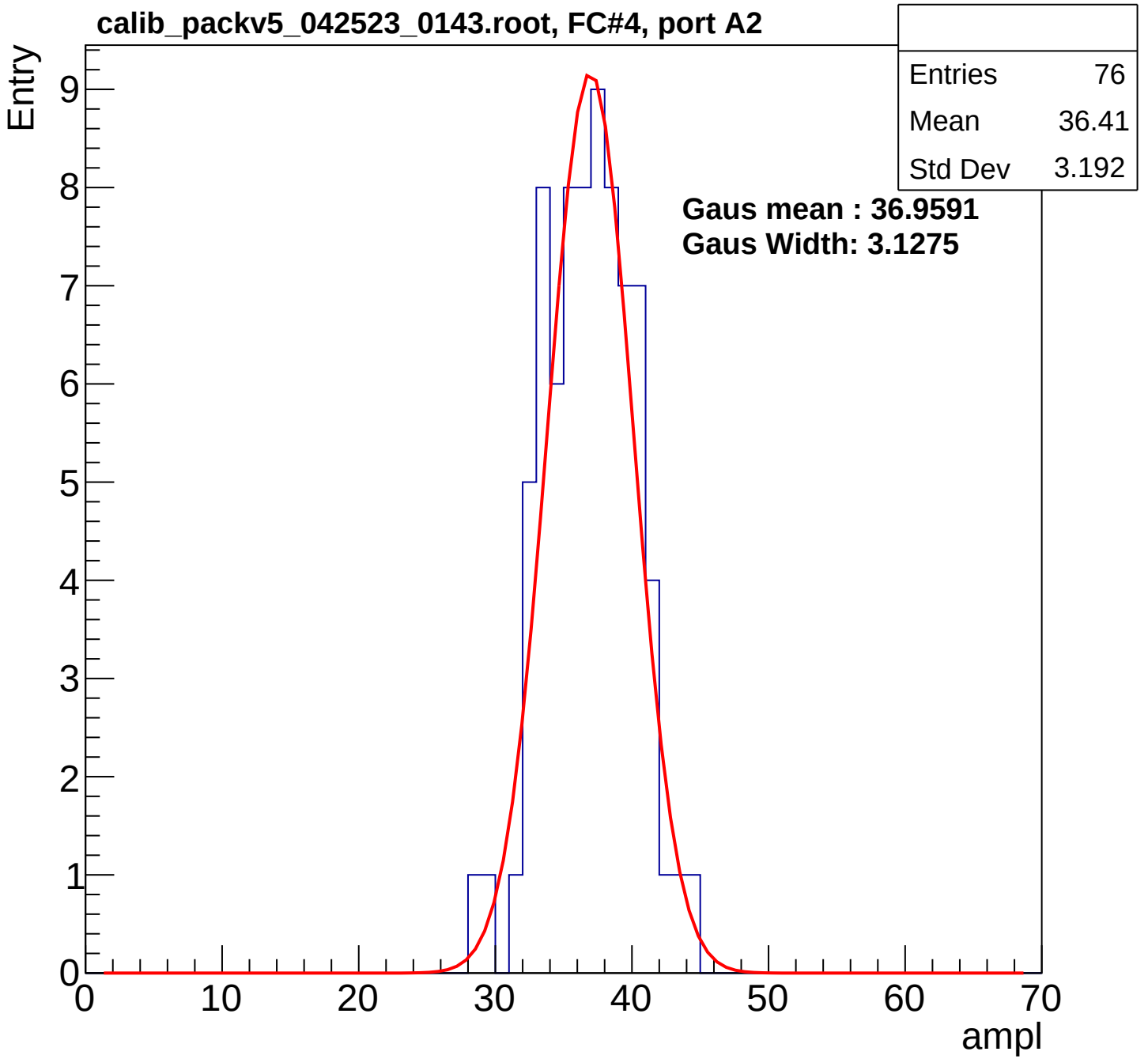
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch35, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.72
Std Dev 3.386
Gaus mean : 30.5965
Gaus Width: 3.6243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.41
Std Dev 3.192
Gaus mean : 36.9591
Gaus Width: 3.1275
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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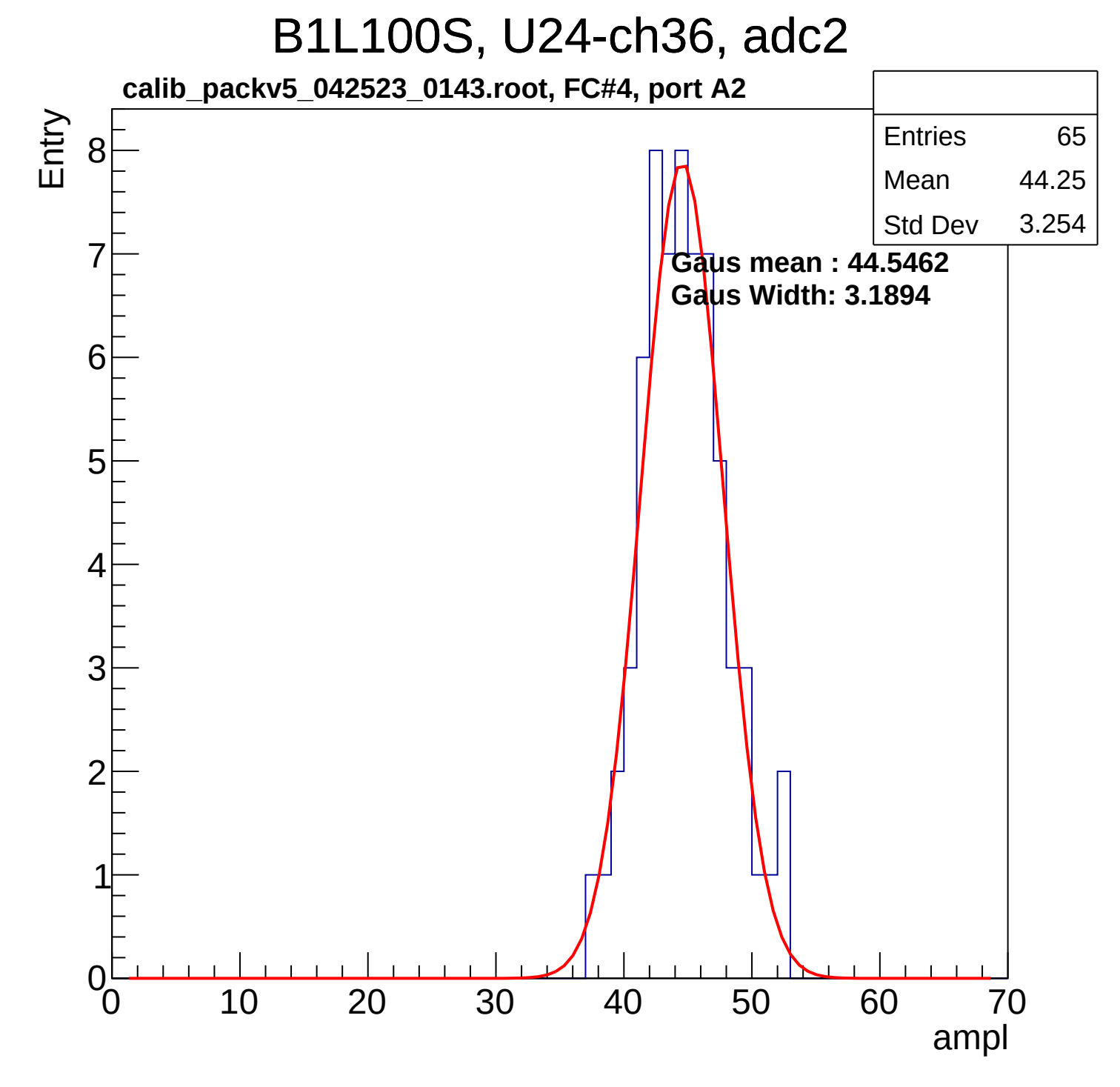
B1L100S, U24-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.25
Std Dev 3.254
Gaus mean : 44.5462
Gaus Width: 3.1894
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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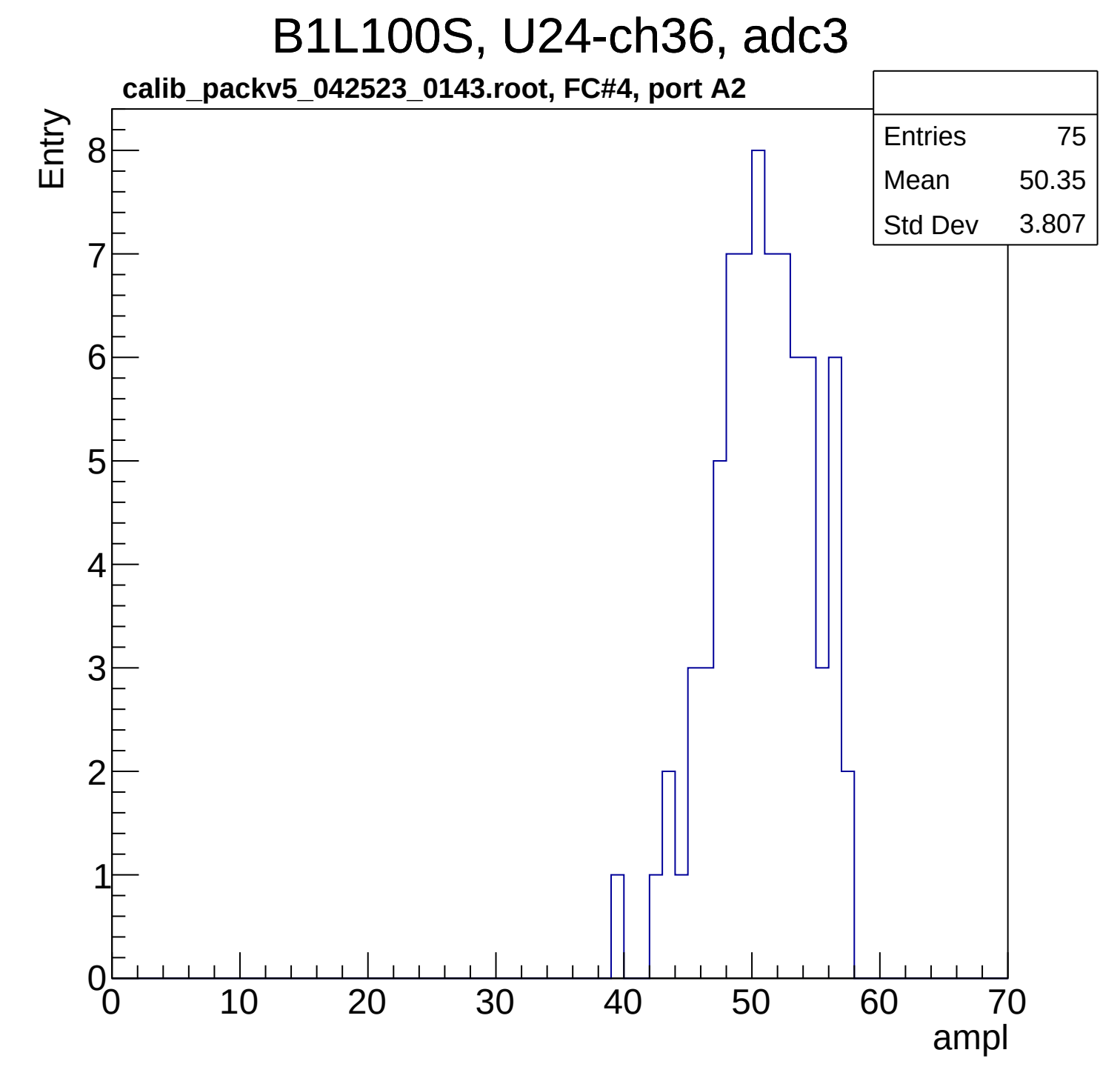
B1L100S, U24-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.35
Std Dev 3.807
Entry
ampl
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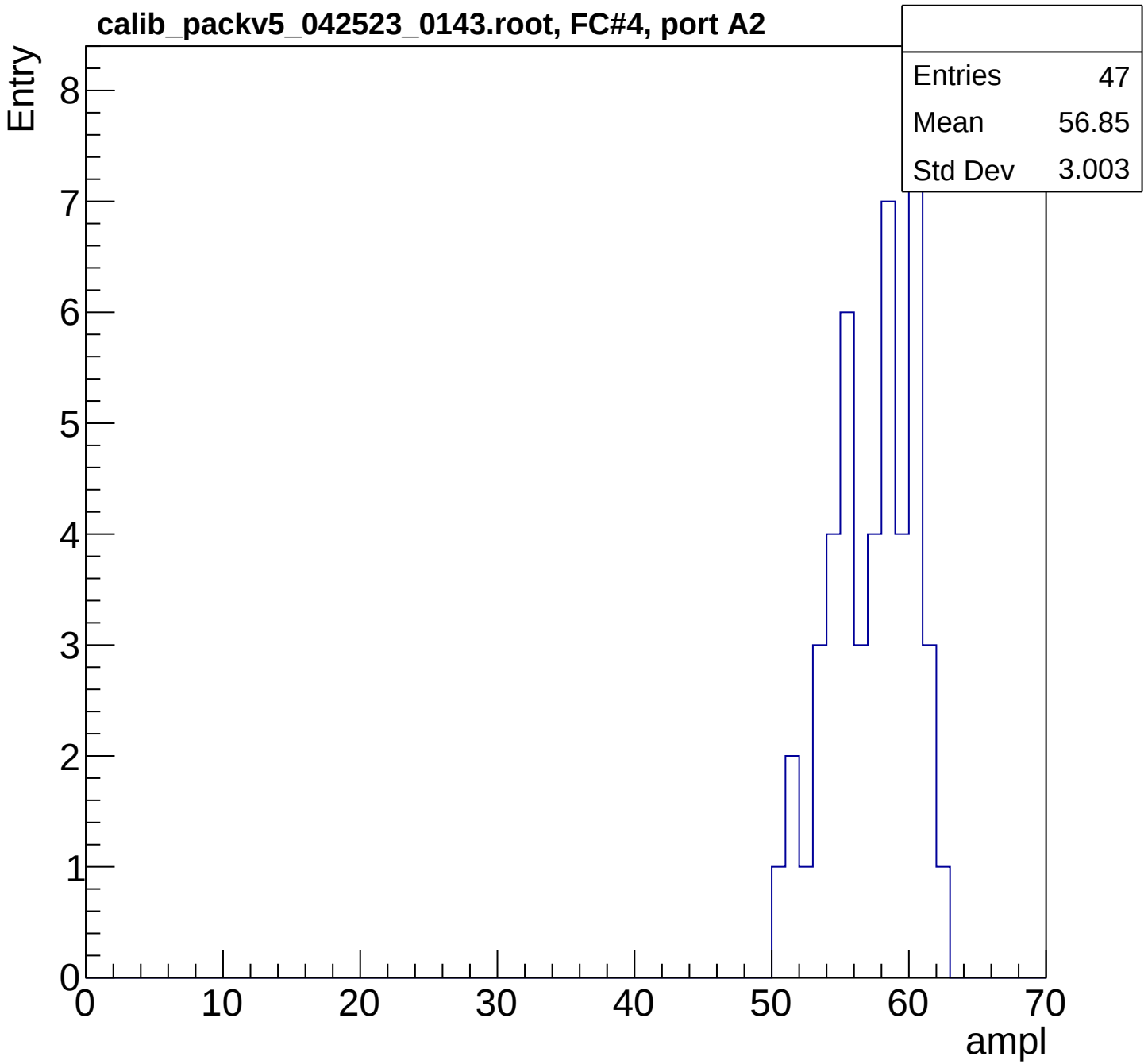
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 56.85
Std Dev 3.003
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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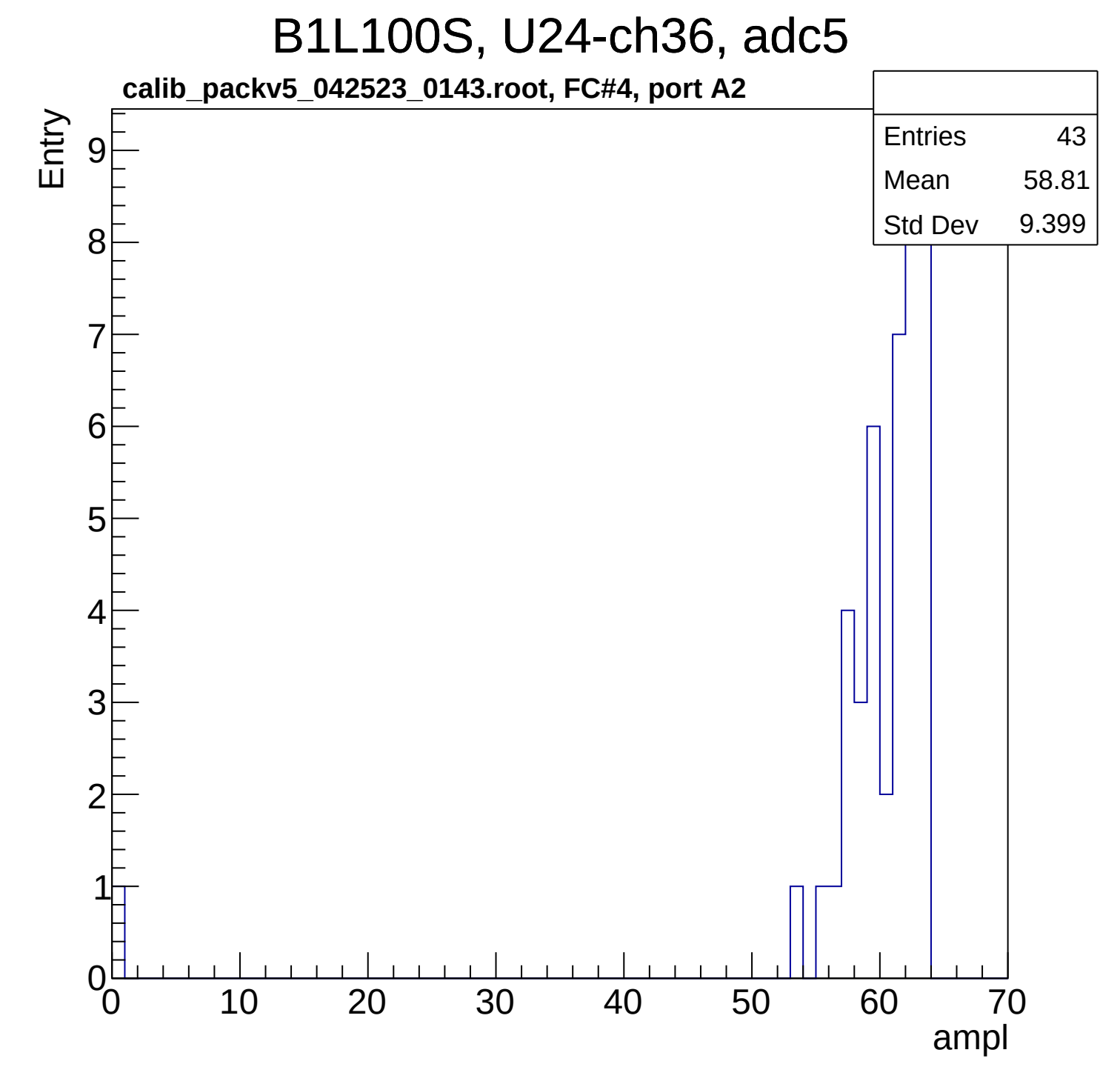
B1L100S, U24-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.81
Std Dev 9.399
Entry
ampl
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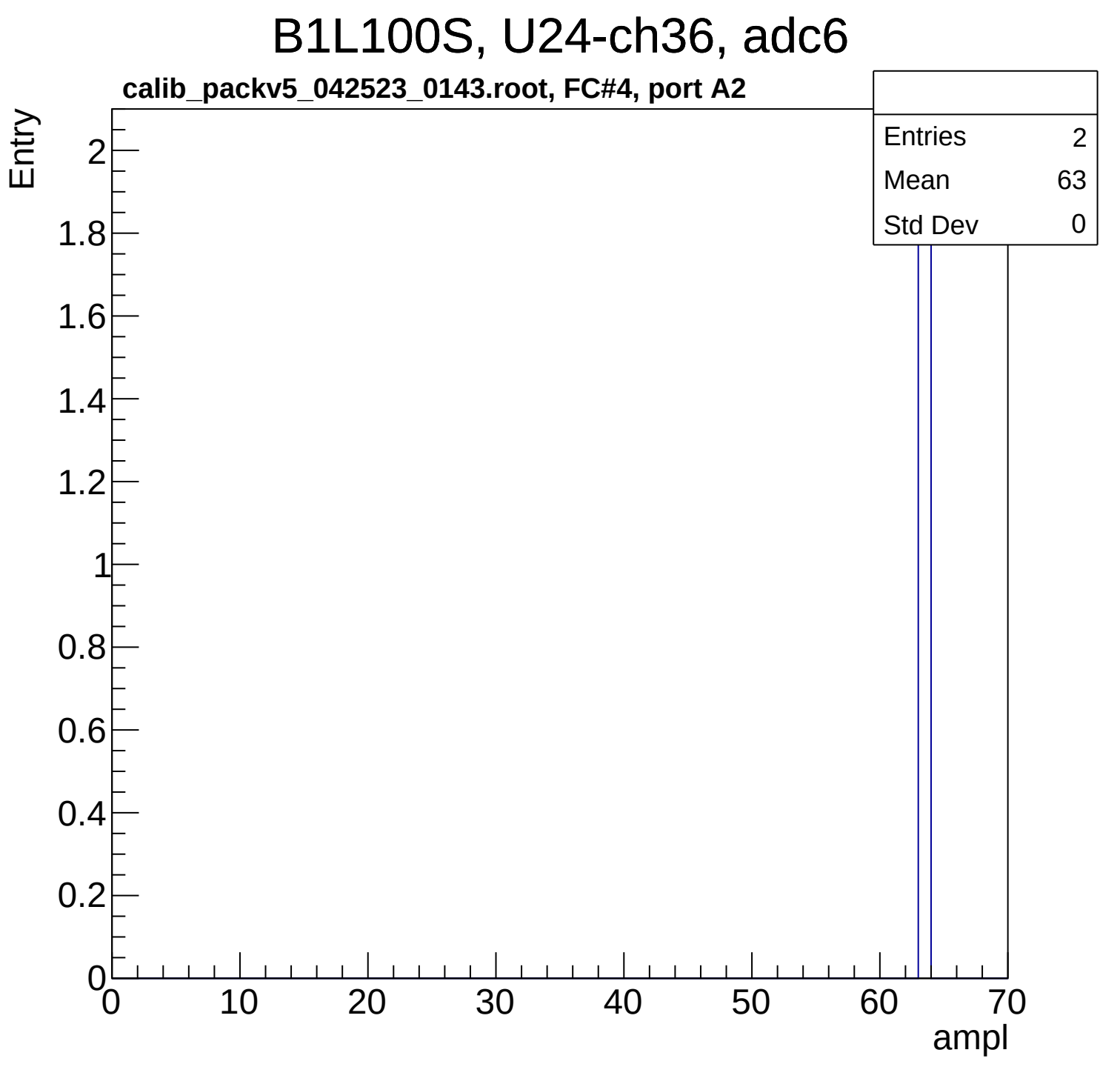
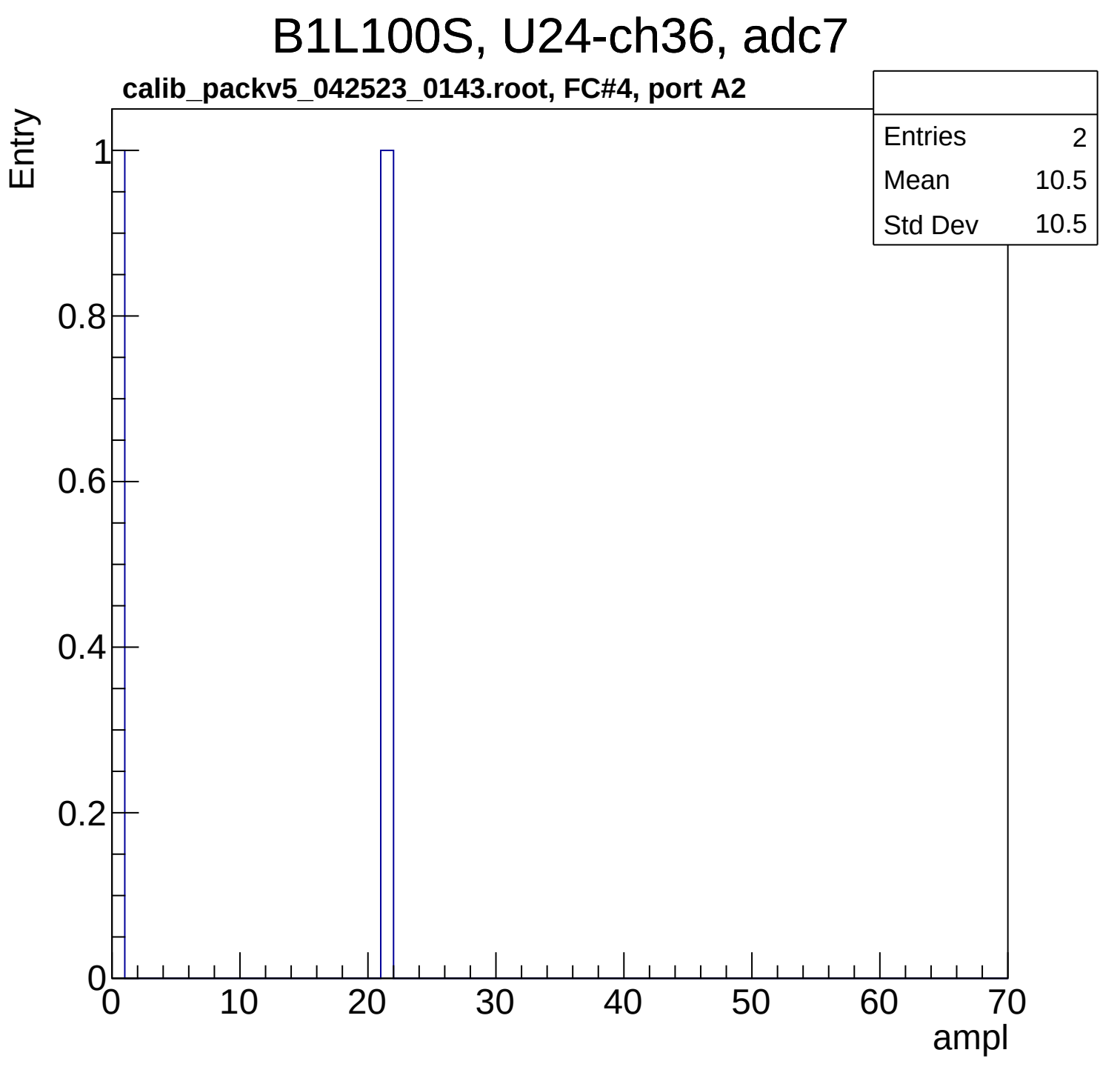
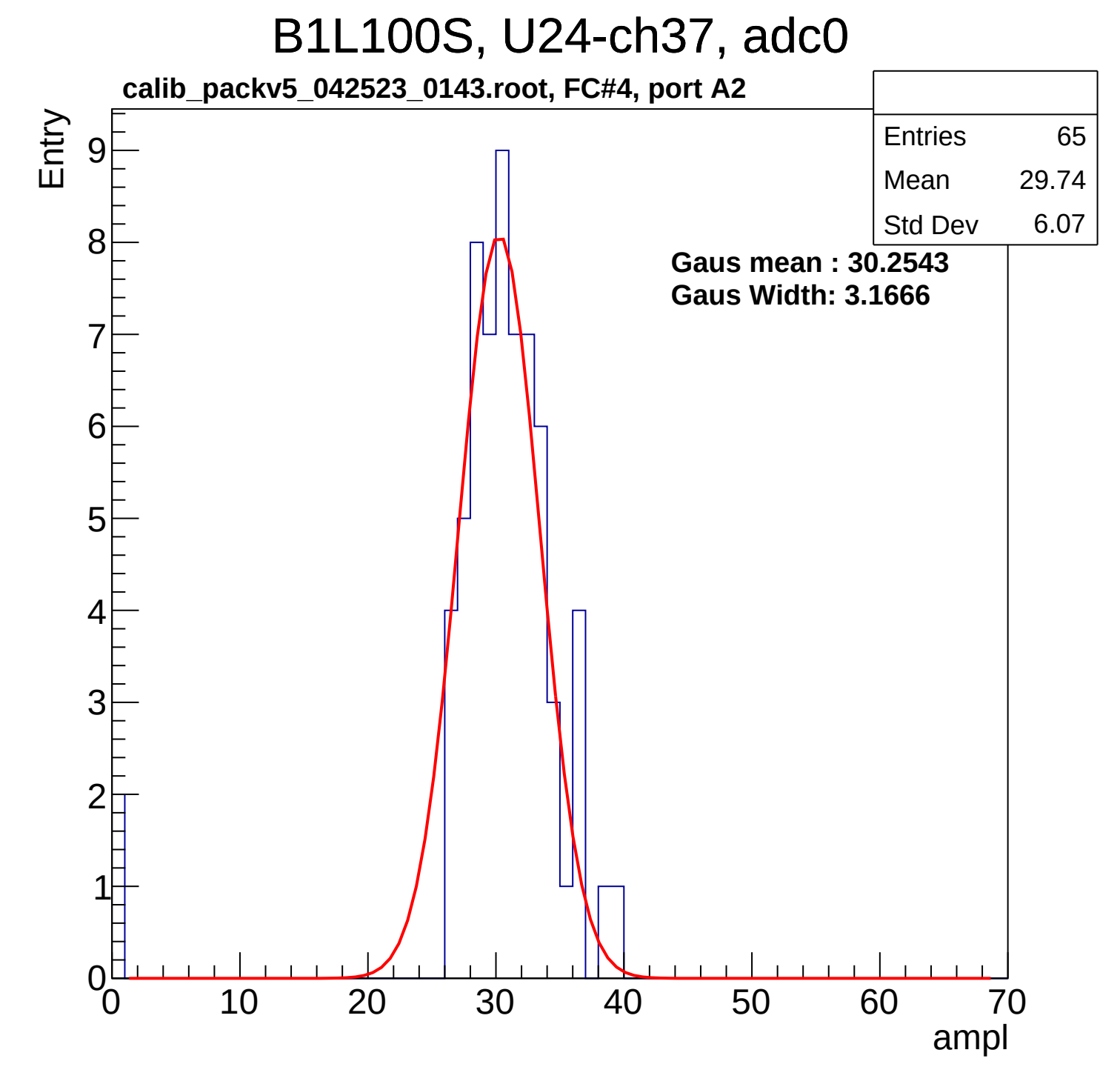
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.74
Std Dev 6.07
Gaus mean : 30.2543
Gaus Width: 3.1666
Entry
ampl
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2
3
4
5
6
7
8
9
0
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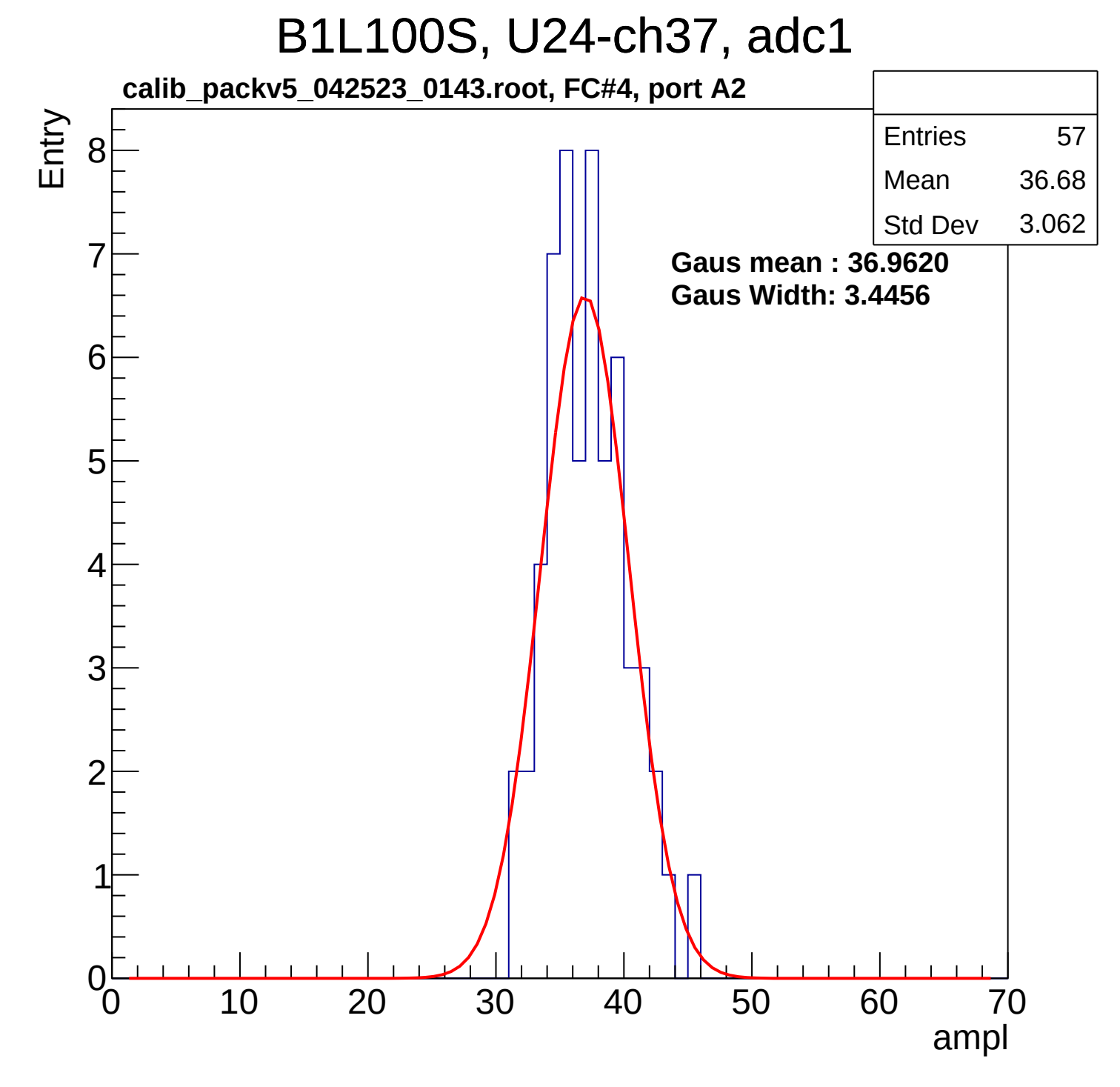
20
30
40
50
60
70


B1L100S, U24-ch37, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 36.68
Std Dev 3.062
Gaus mean : 36.9620
Gaus Width: 3.4456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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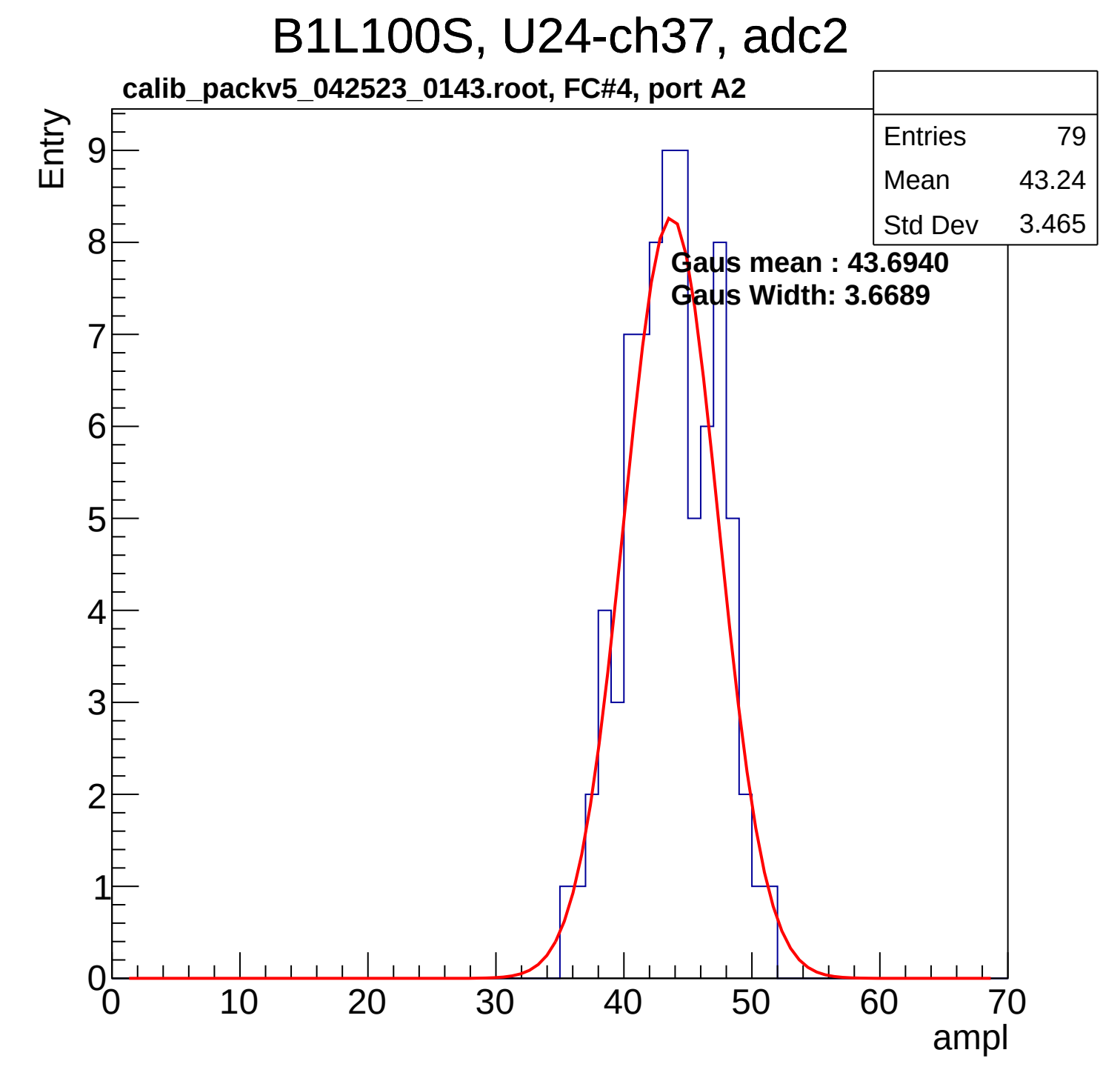
60
70


B1L100S, U24-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 43.24
Std Dev 3.465
Gaus mean : 43.6940
Gaus Width: 3.6689
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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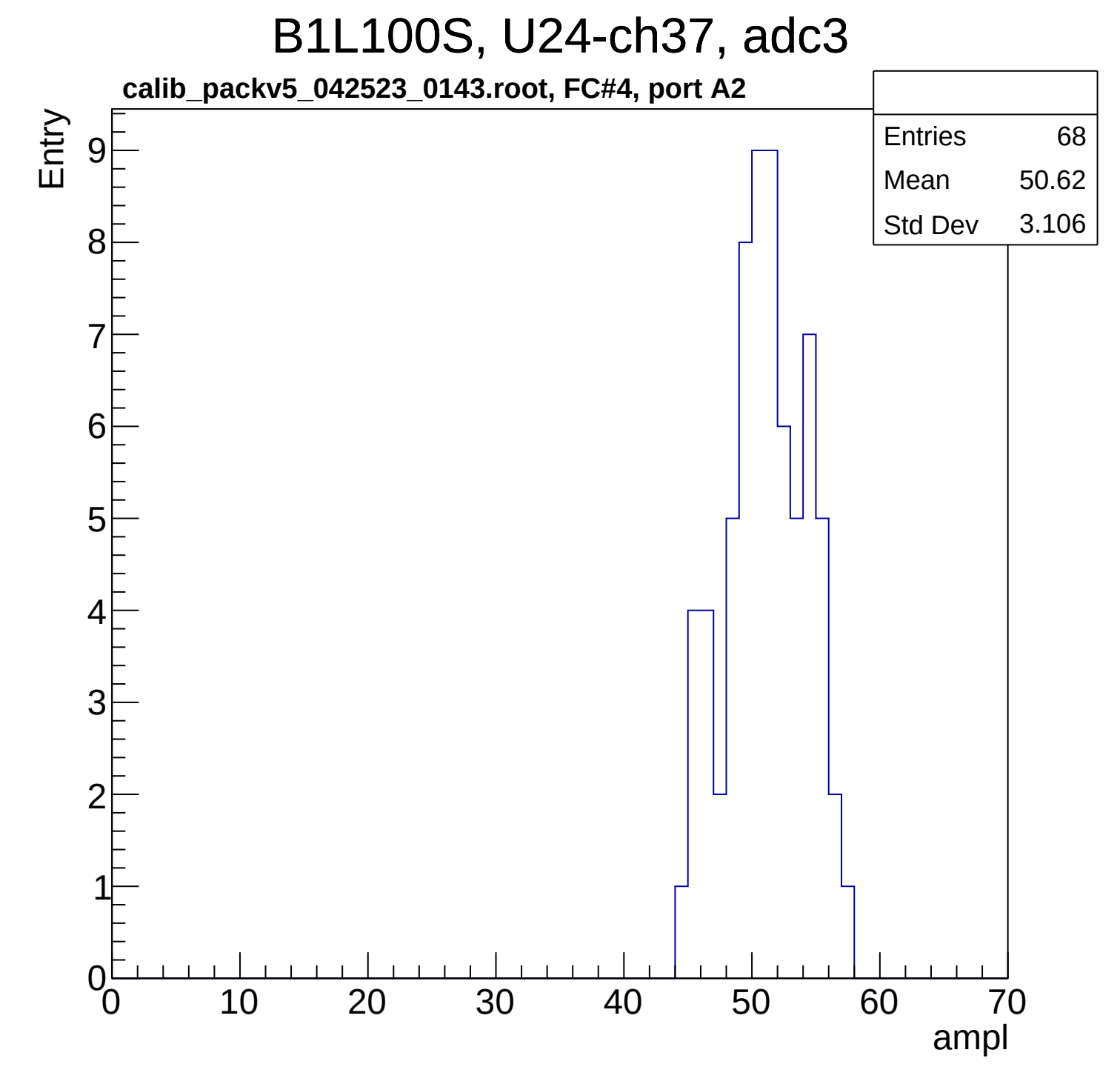
B1L100S, U24-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.62
Std Dev 3.106
Entry
ampl
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60
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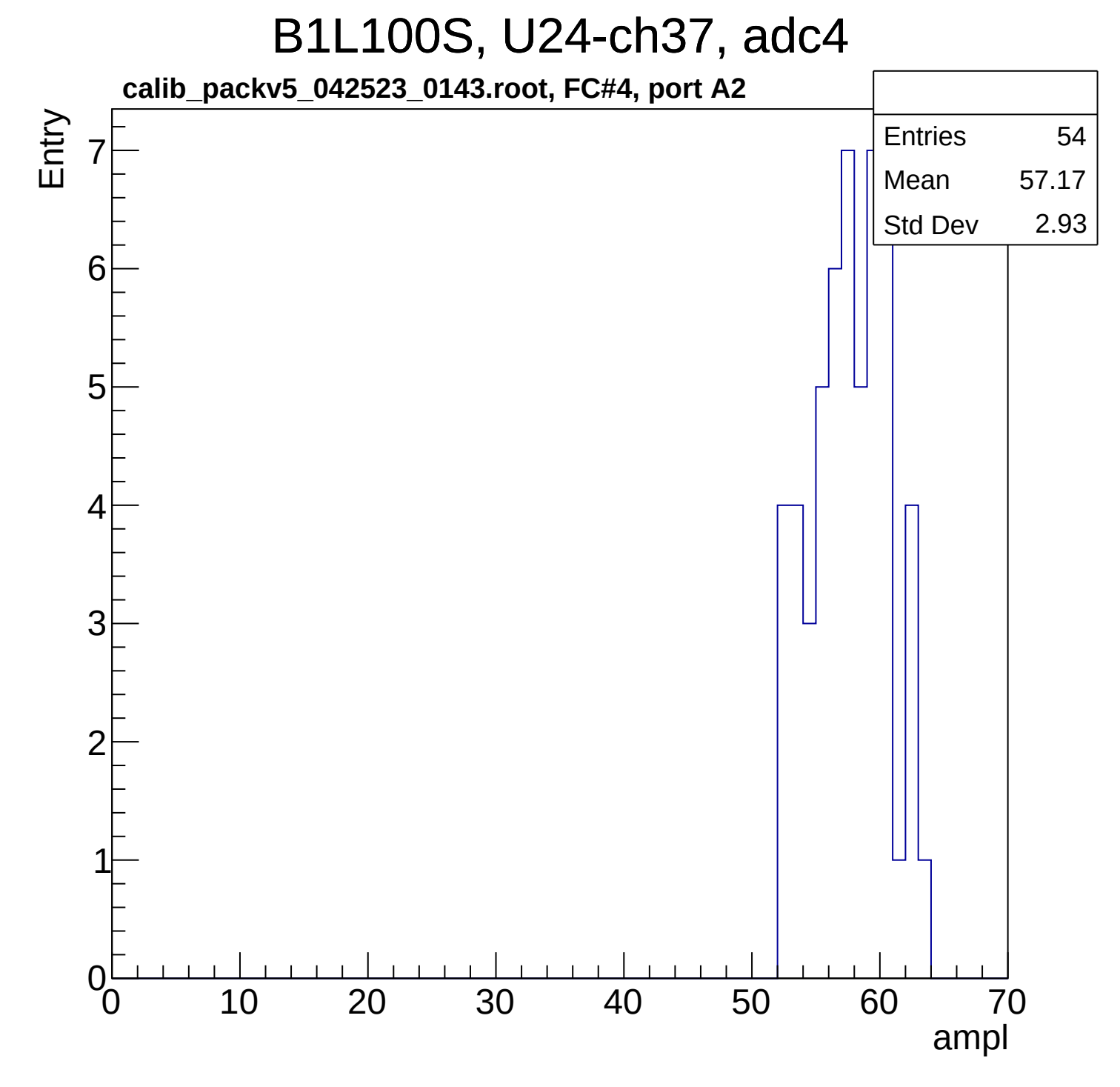
B1L100S, U24-ch37, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 57.17
Std Dev 2.93
Entry
ampl
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0
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20
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40
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60
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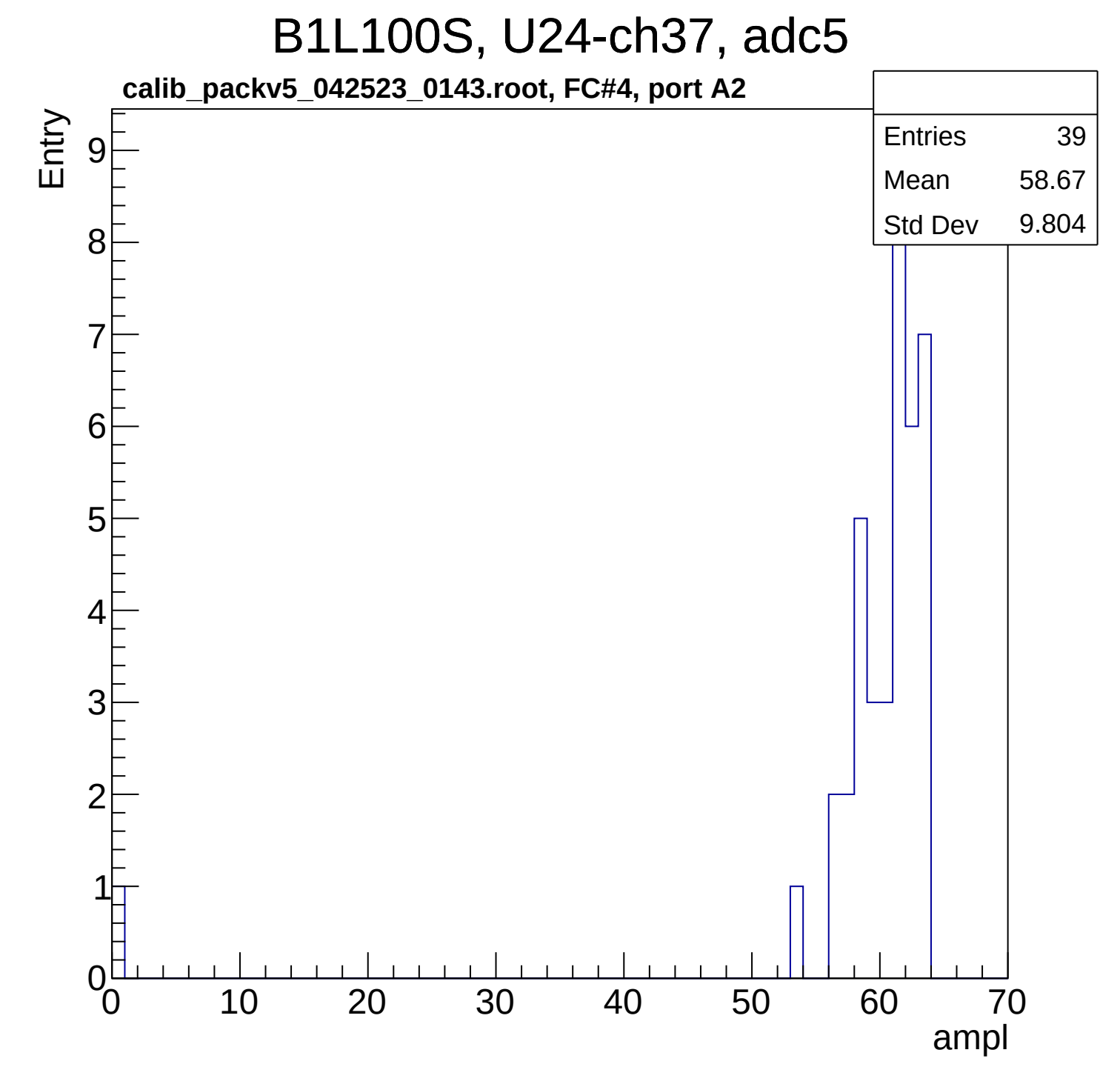
B1L100S, U24-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.67
Std Dev 9.804
Entry
ampl
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3
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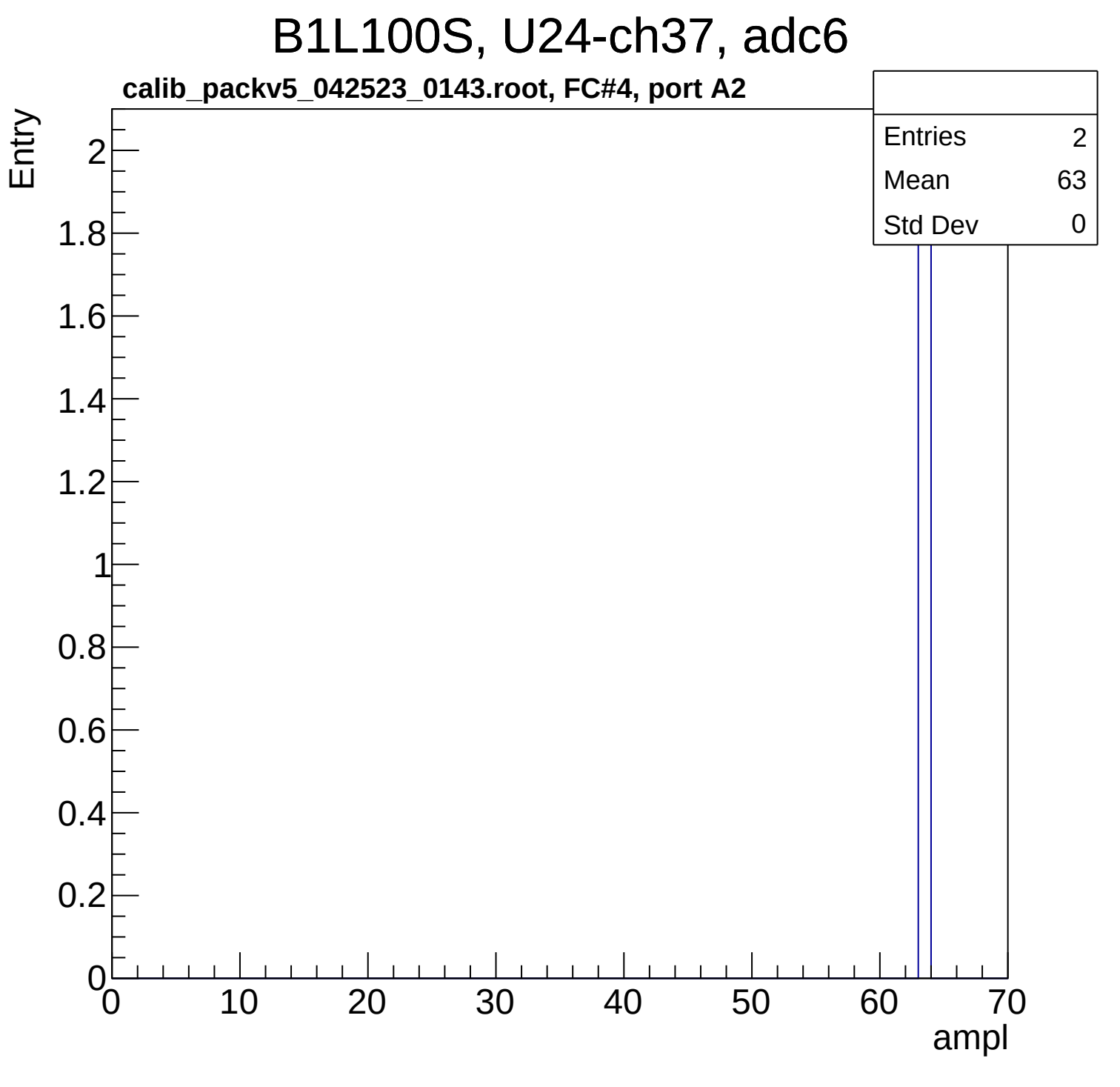
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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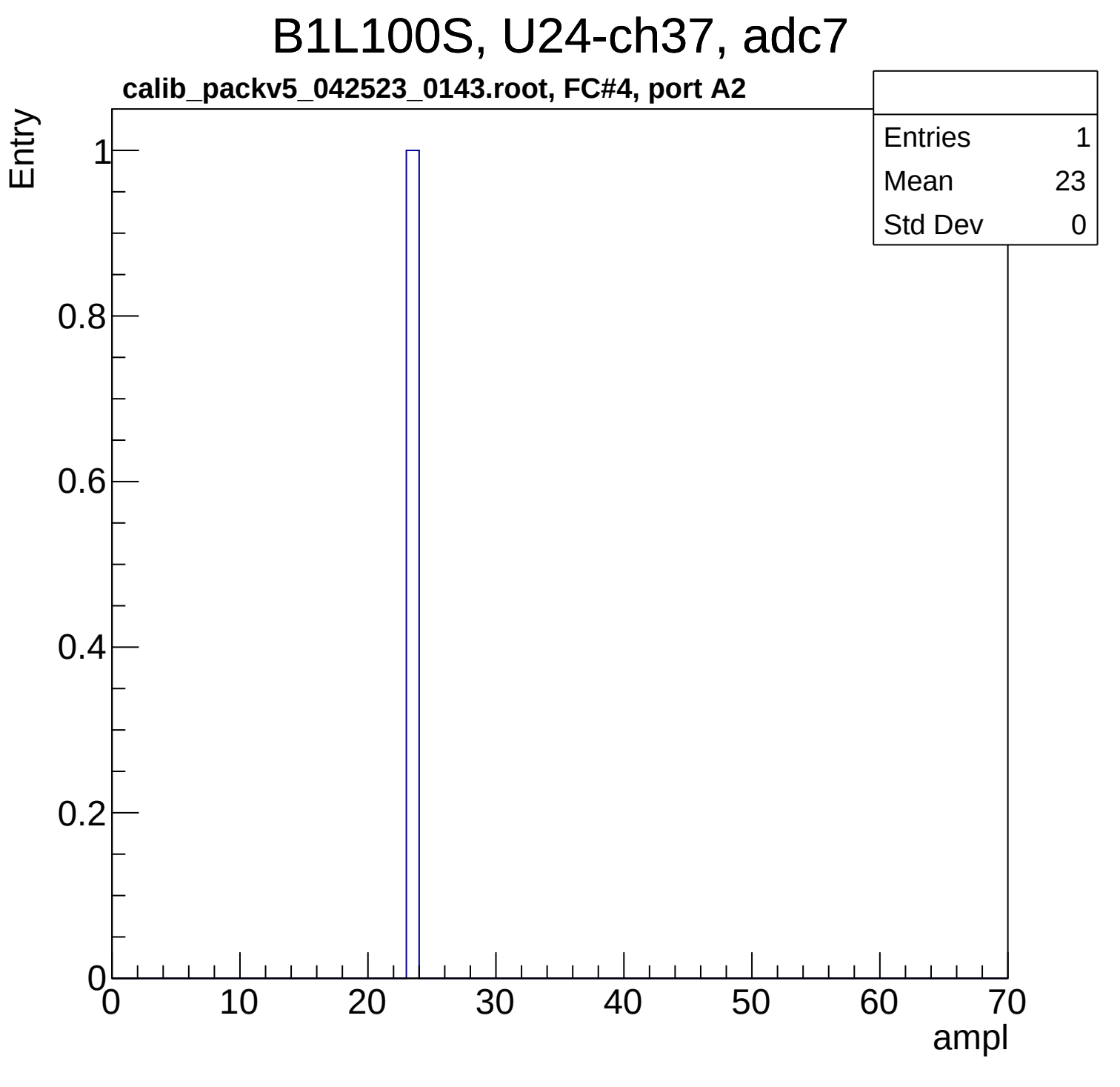
B1L100S, U24-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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0
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10
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30
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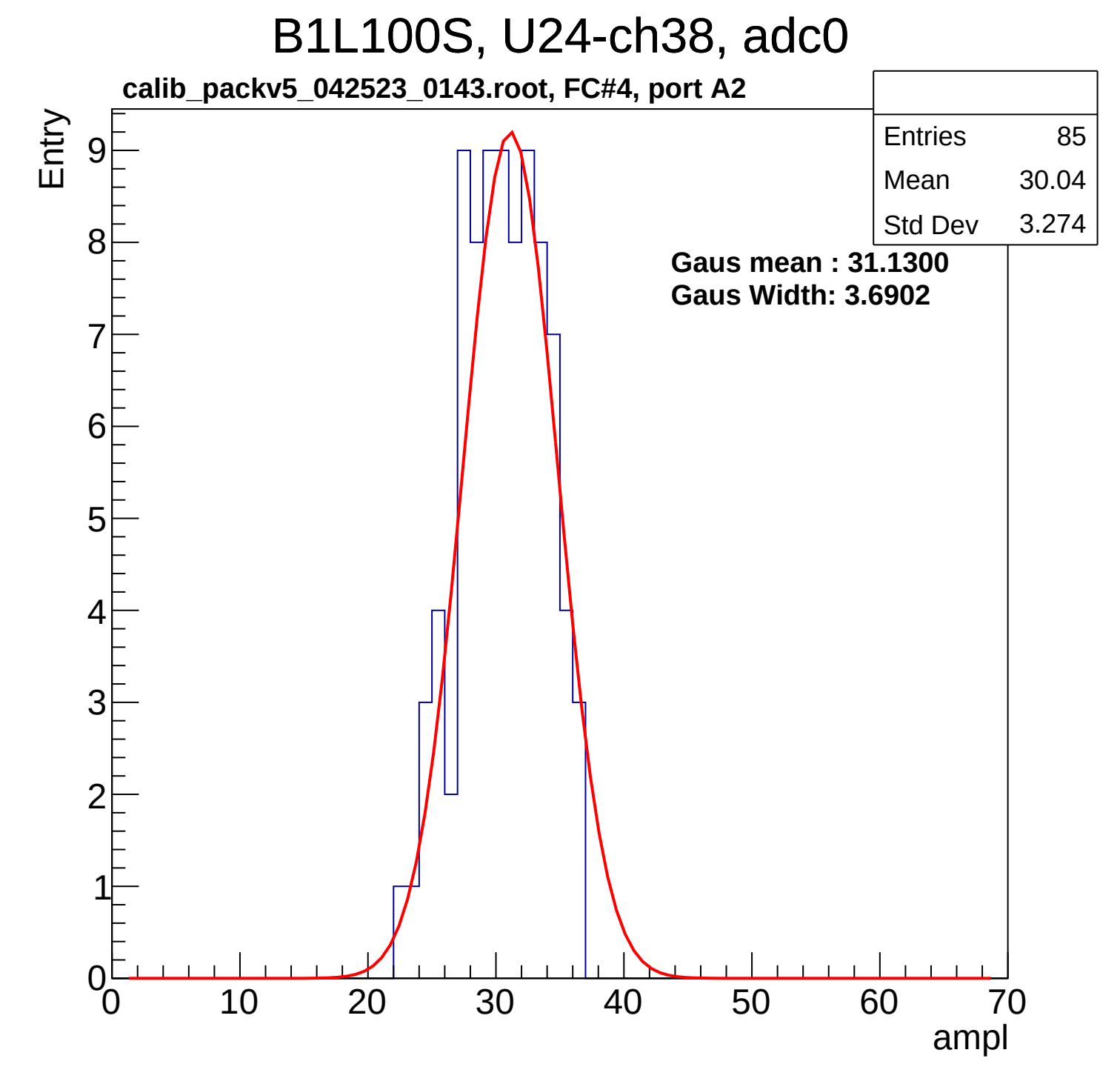
50
60
70
ampl


B1L100S, U24-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 30.04
Std Dev 3.274
Gaus mean : 31.1300
Gaus Width: 3.6902
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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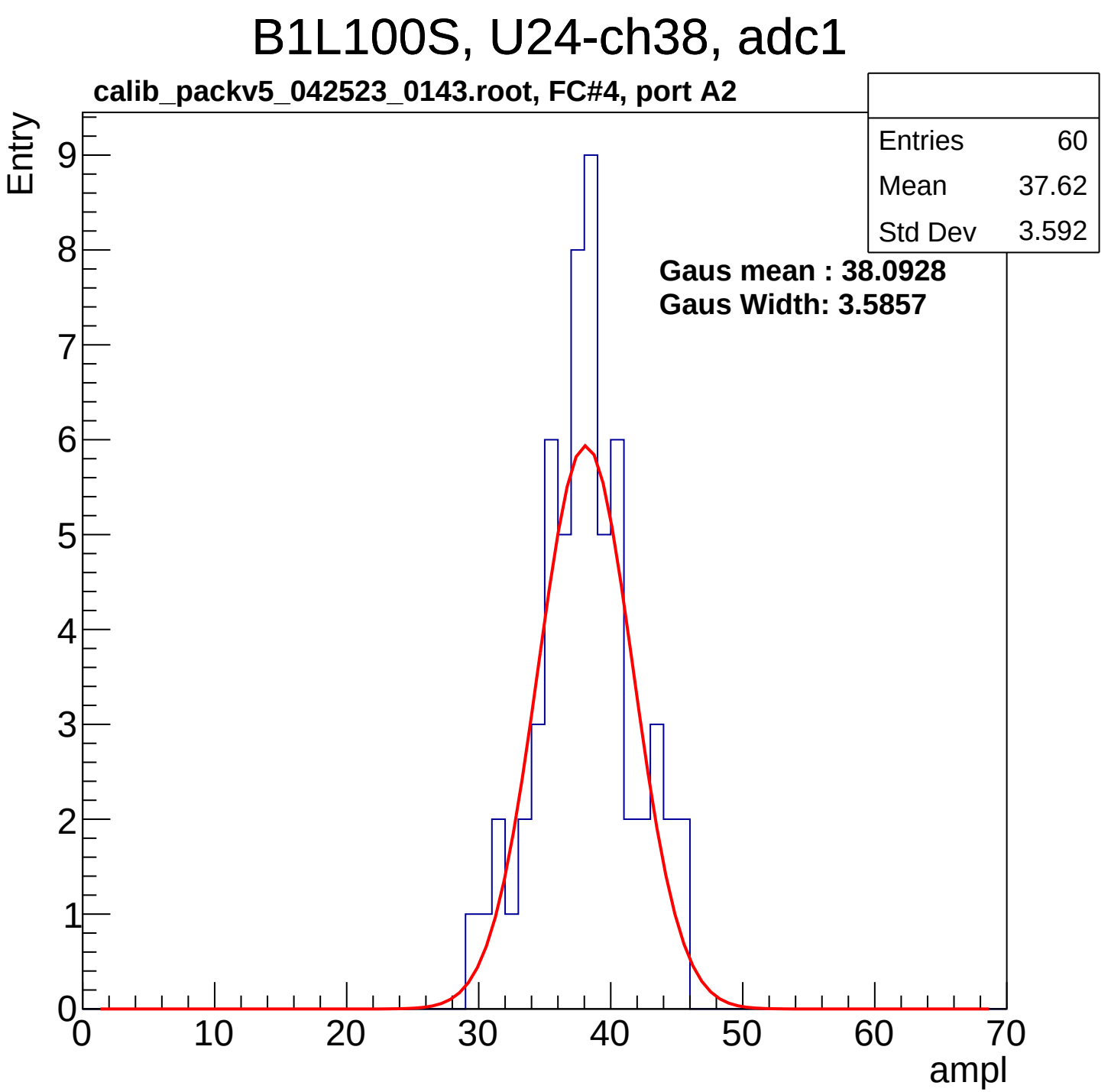
B1L100S, U24-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 37.62
Std Dev 3.592
Gaus mean : 38.0928
Gaus Width: 3.5857
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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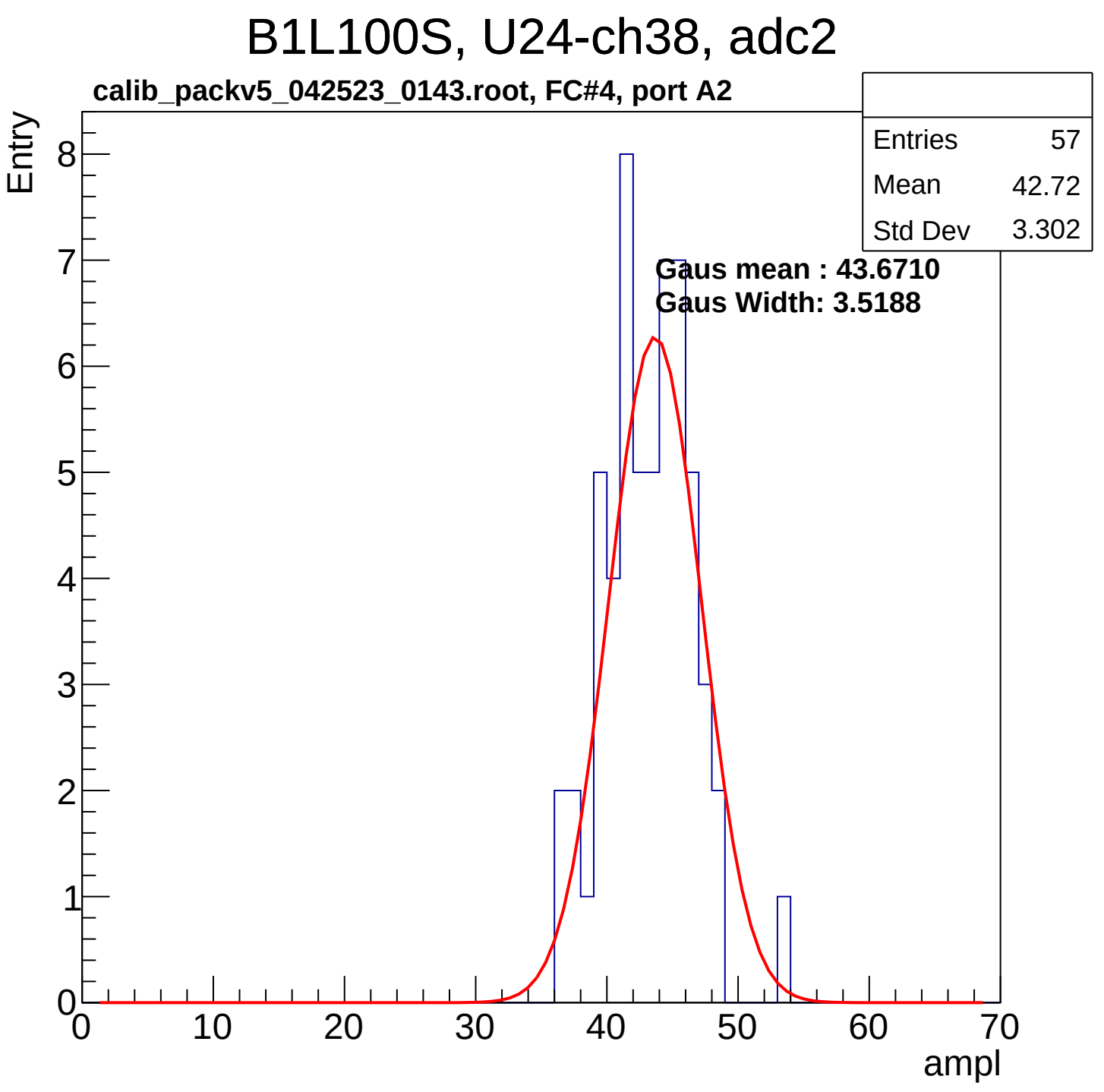
B1L100S, U24-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.72
Std Dev 3.302
Gaus mean : 43.6710
Gaus Width: 3.5188
Entry
ampl
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2
3
4
5
6
7
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0
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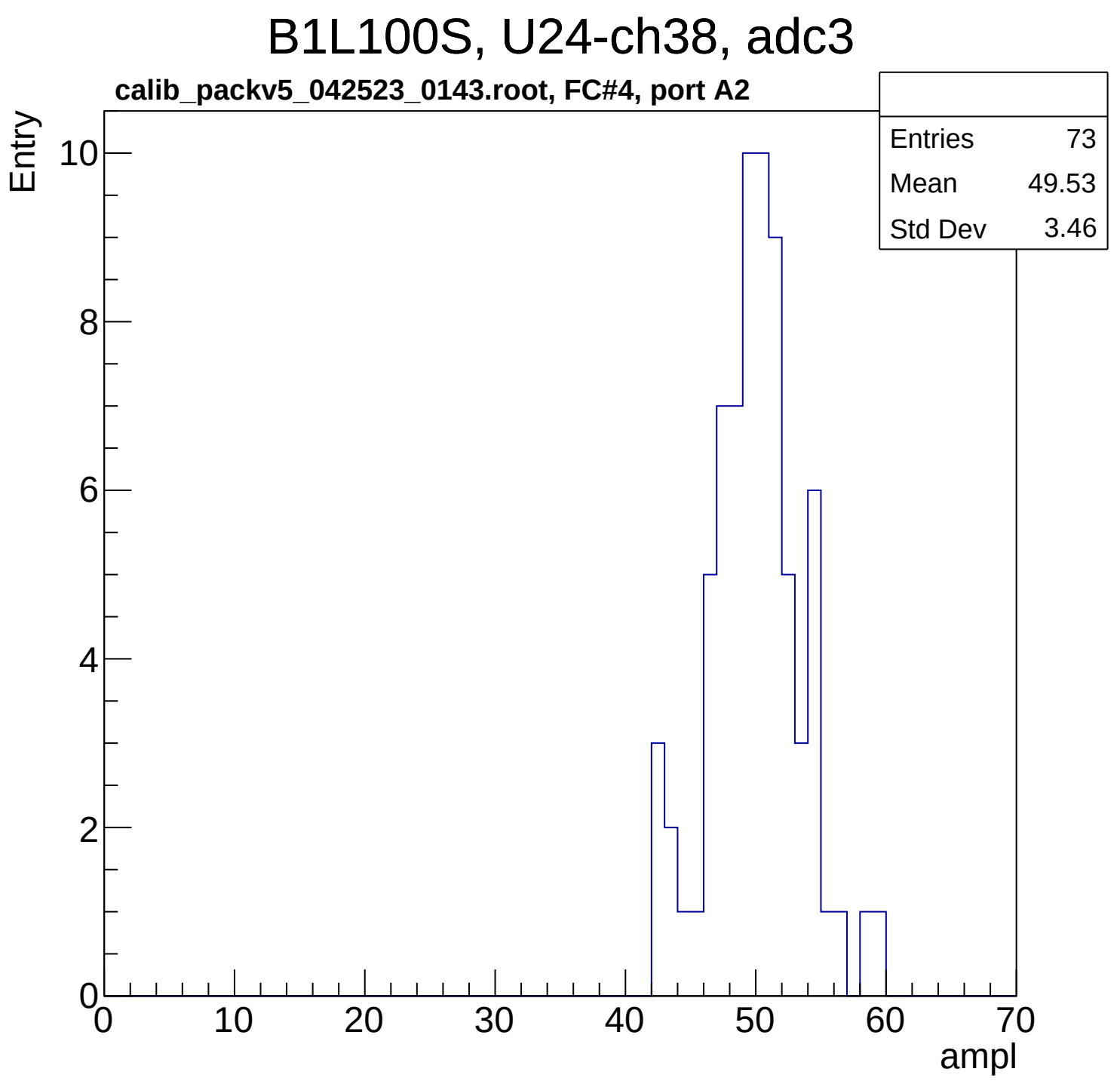
B1L100S, U24-ch38, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 49.53
Std Dev 3.46
Entry
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10
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10
20
30
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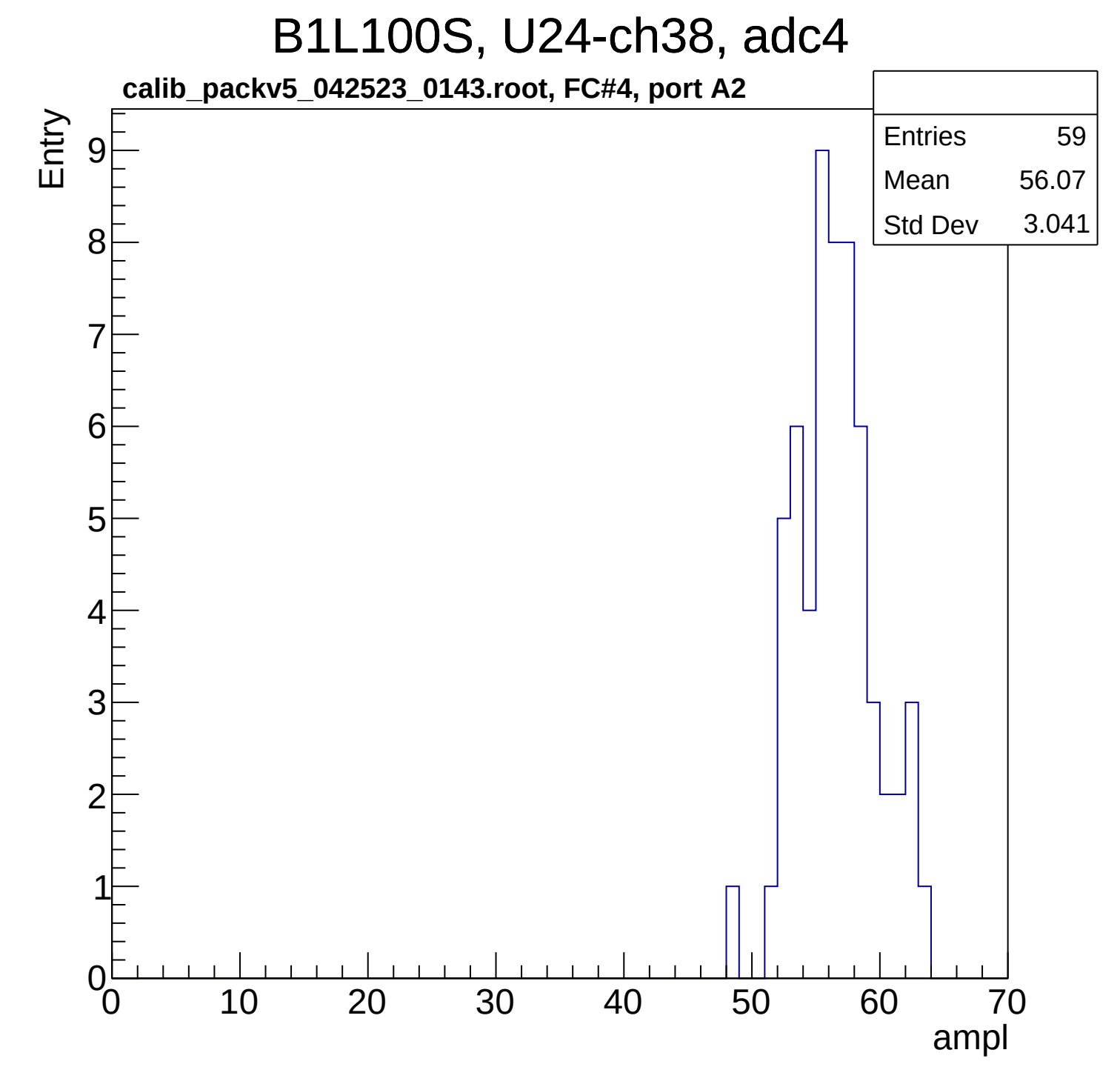
50
60
70
ampl


B1L100S, U24-ch38, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 56.07
Std Dev 3.041
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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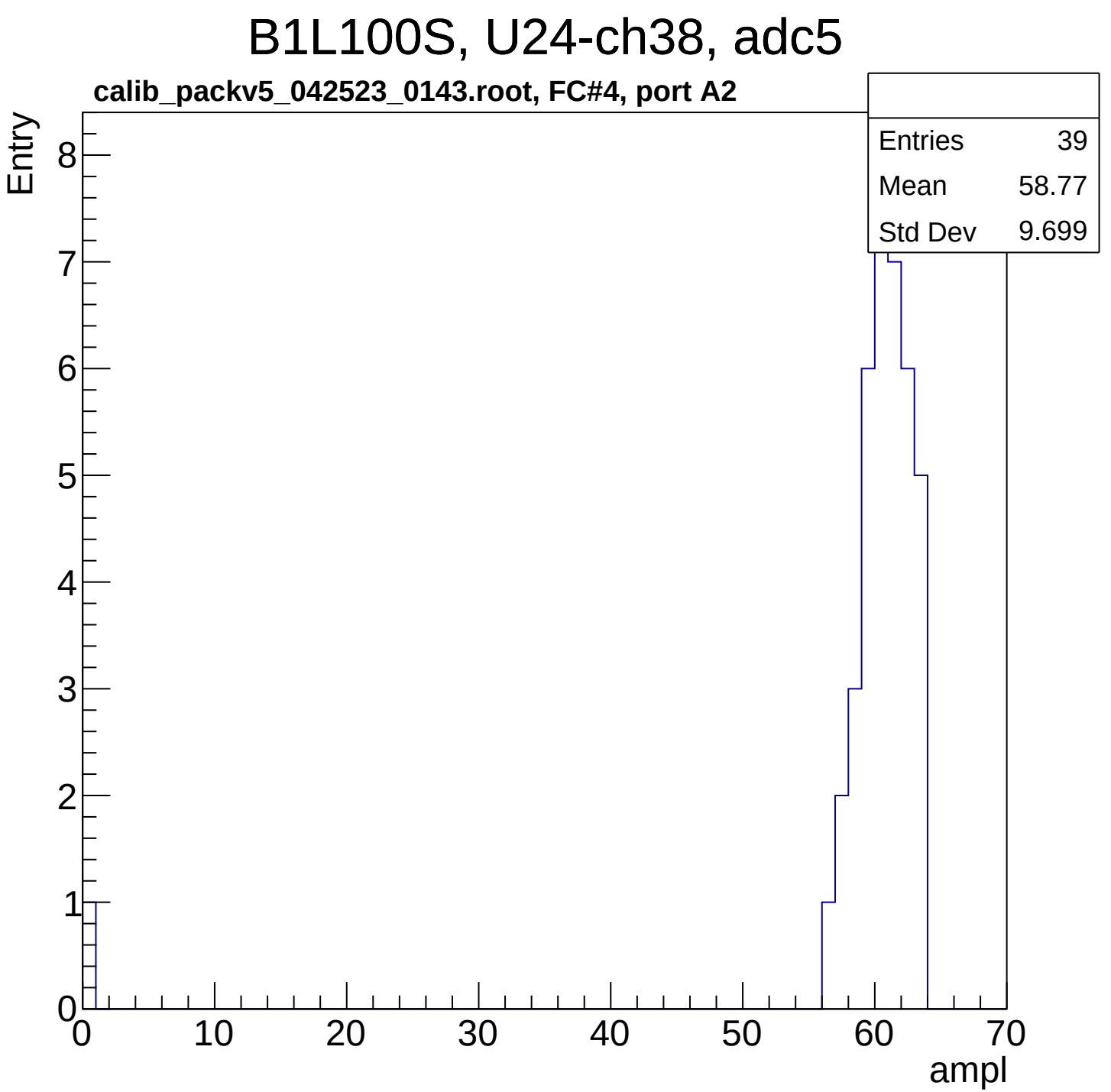
B1L100S, U24-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.77
Std Dev 9.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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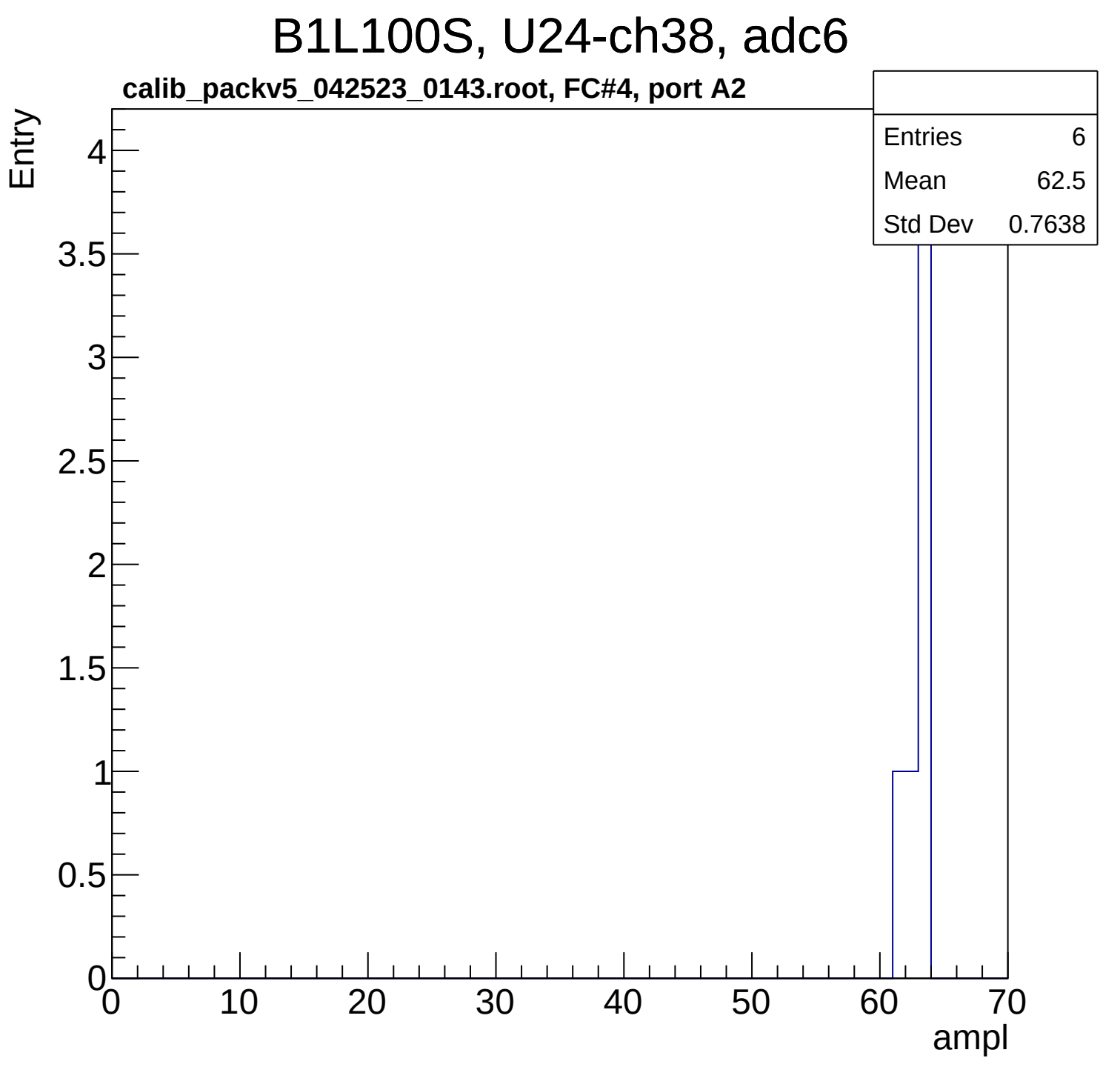
B1L100S, U24-ch38, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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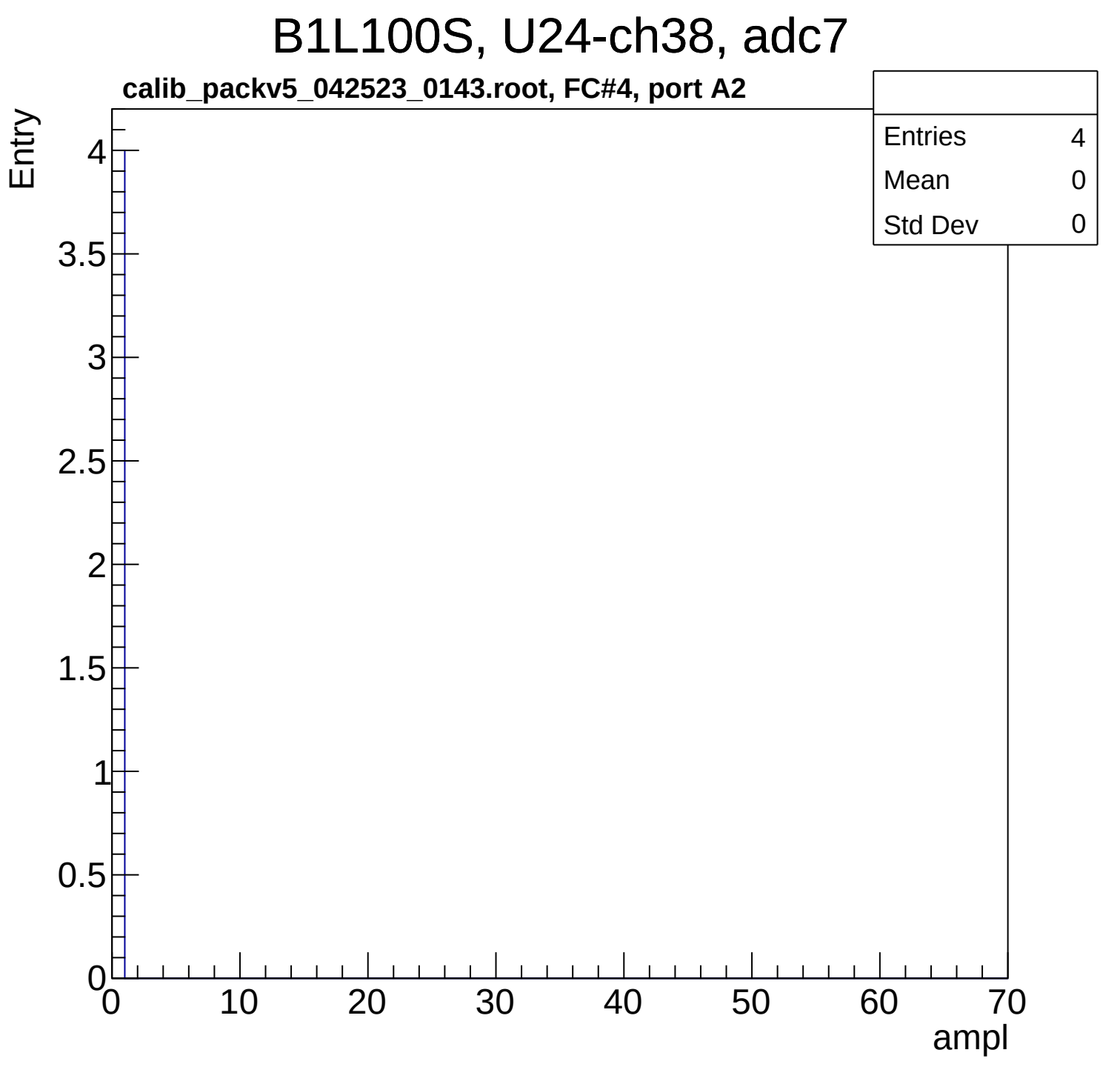
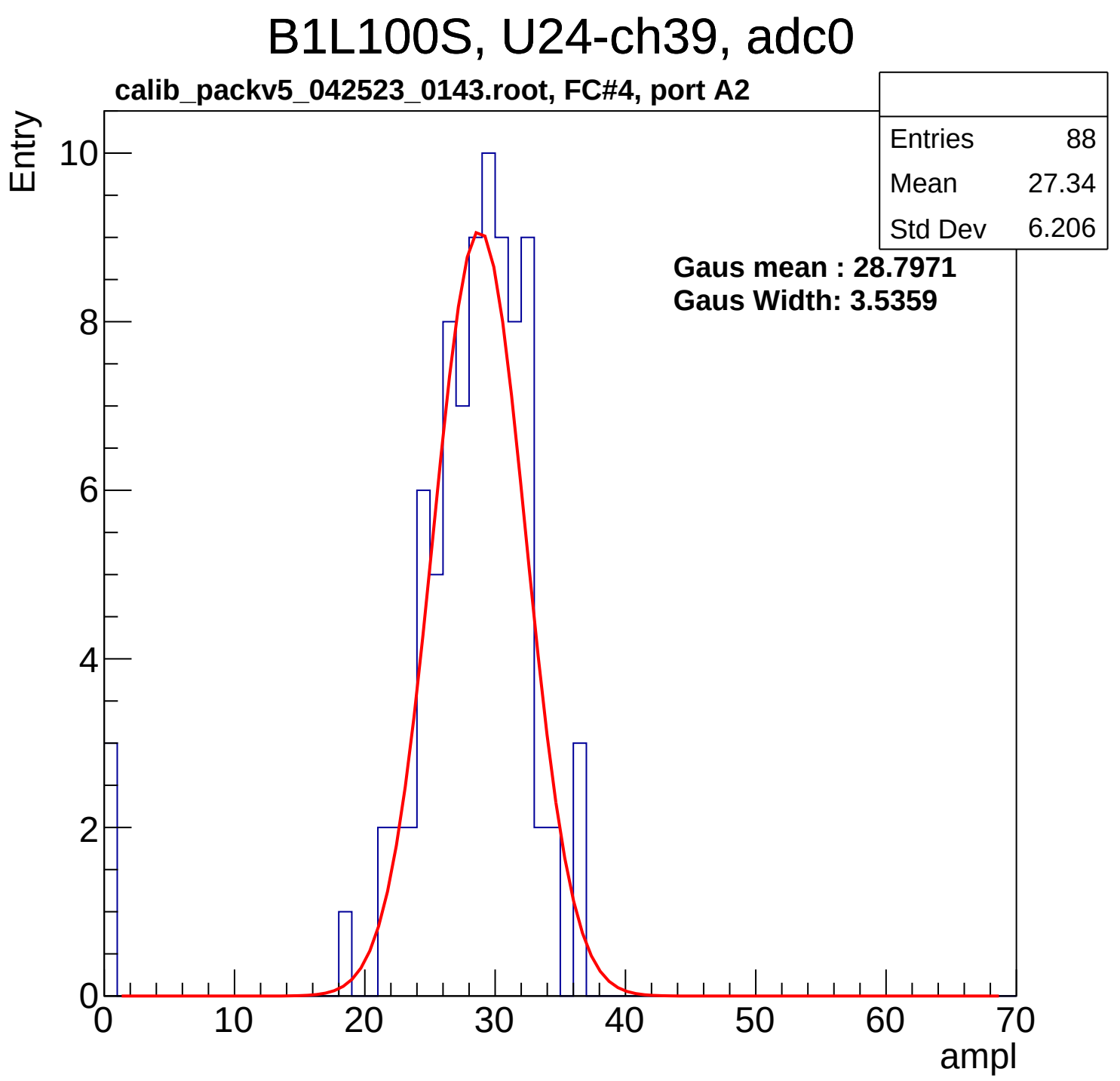
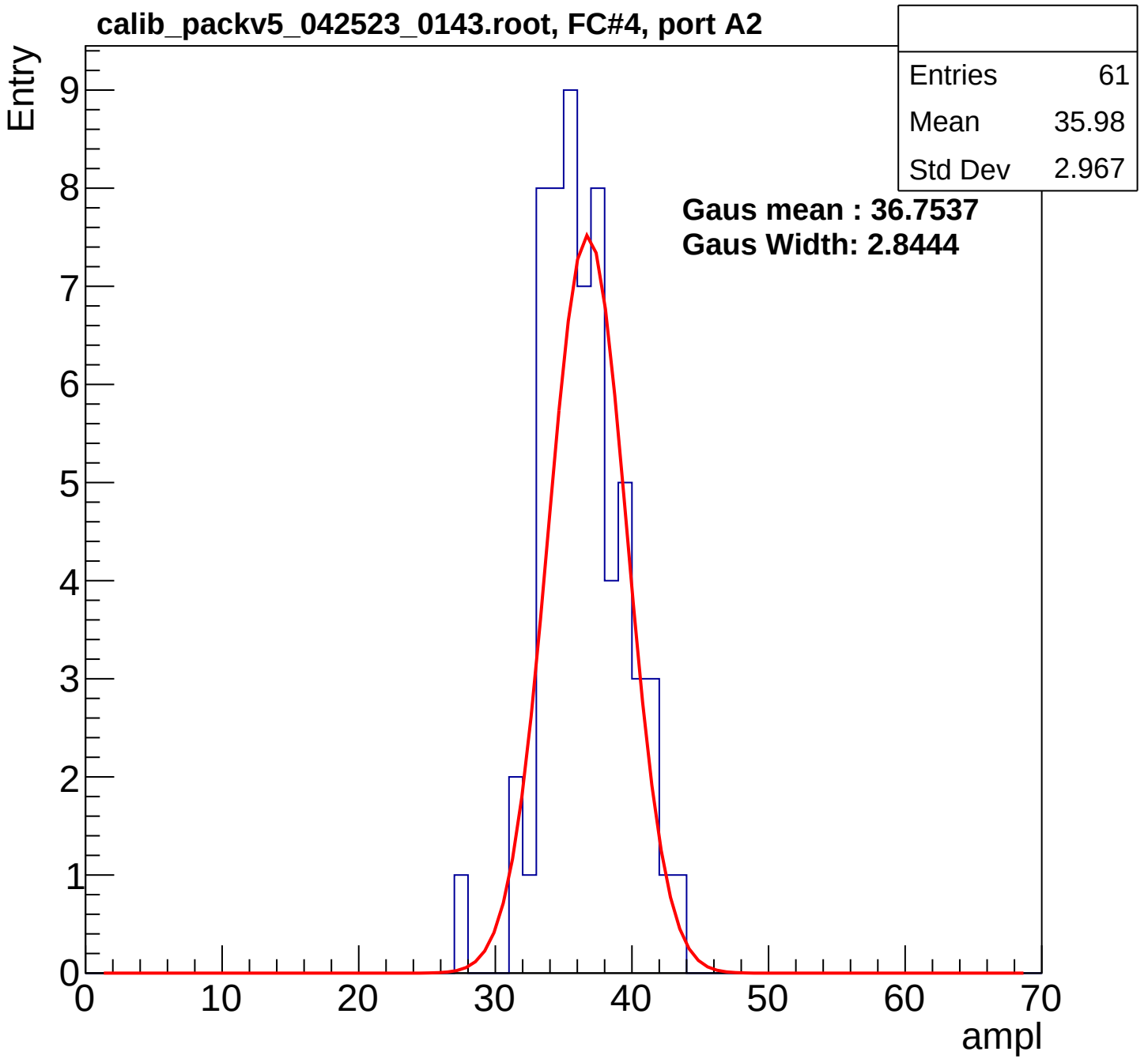

B1L100S, U24-ch38, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 0
Std Dev 0
Entry
4
3.5
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 27.34
Std Dev 6.206
Gaus mean : 28.7971
Gaus Width: 3.5359
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 35.98
Std Dev 2.967
Gaus mean : 36.7537
Gaus Width: 2.8444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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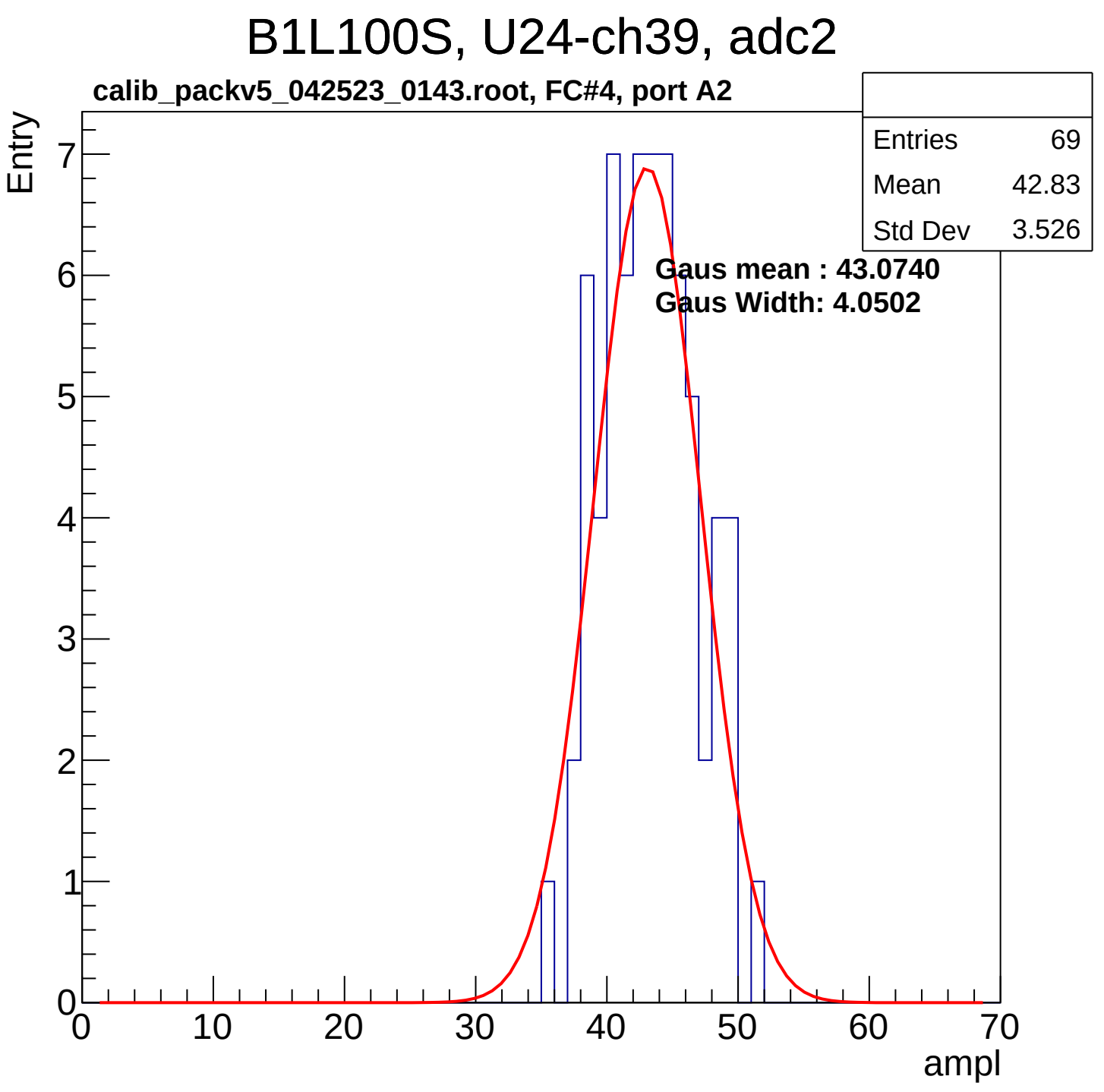
B1L100S, U24-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.83
Std Dev 3.526
Gaus mean : 43.0740
Gaus Width: 4.0502
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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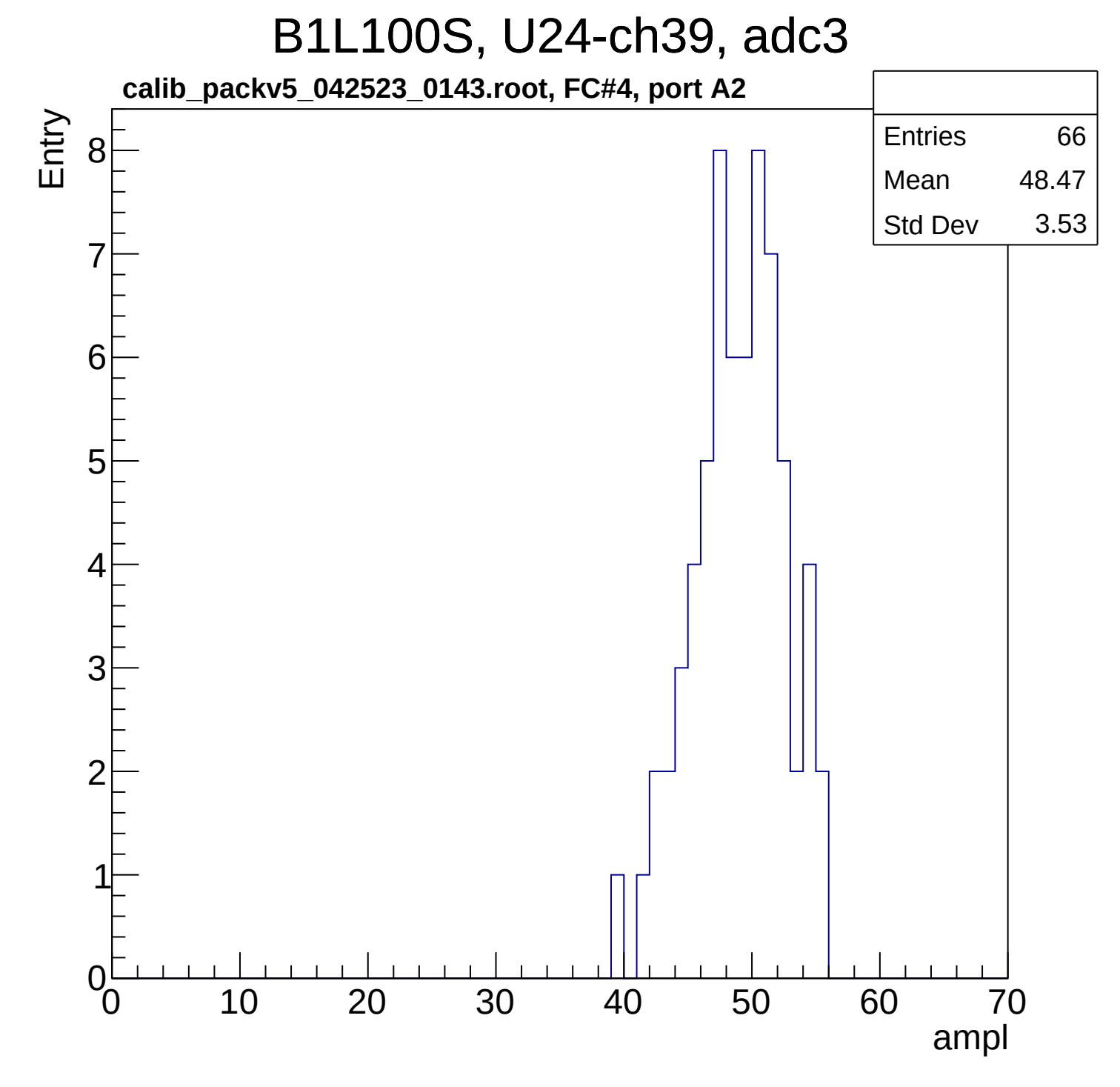
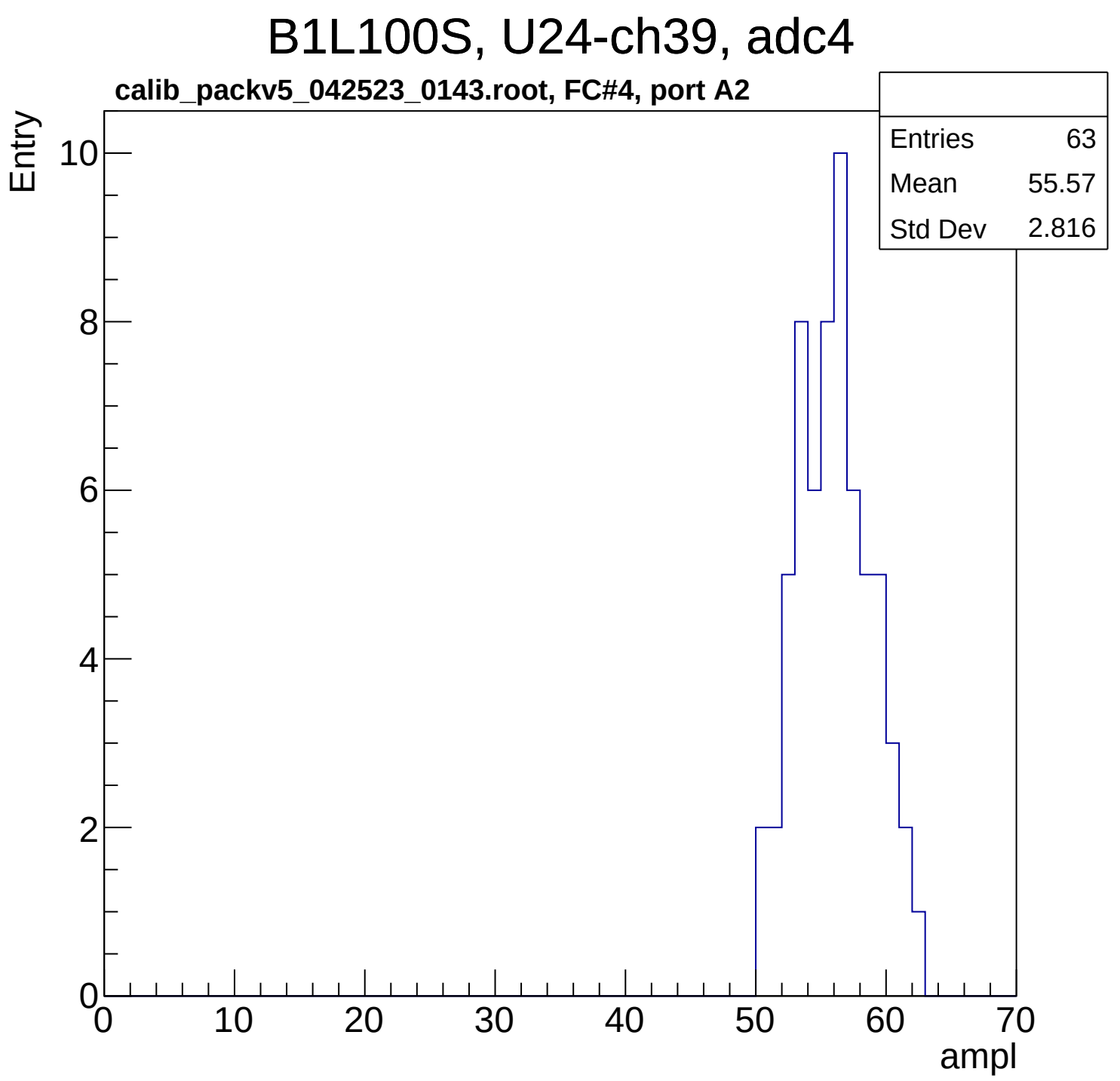
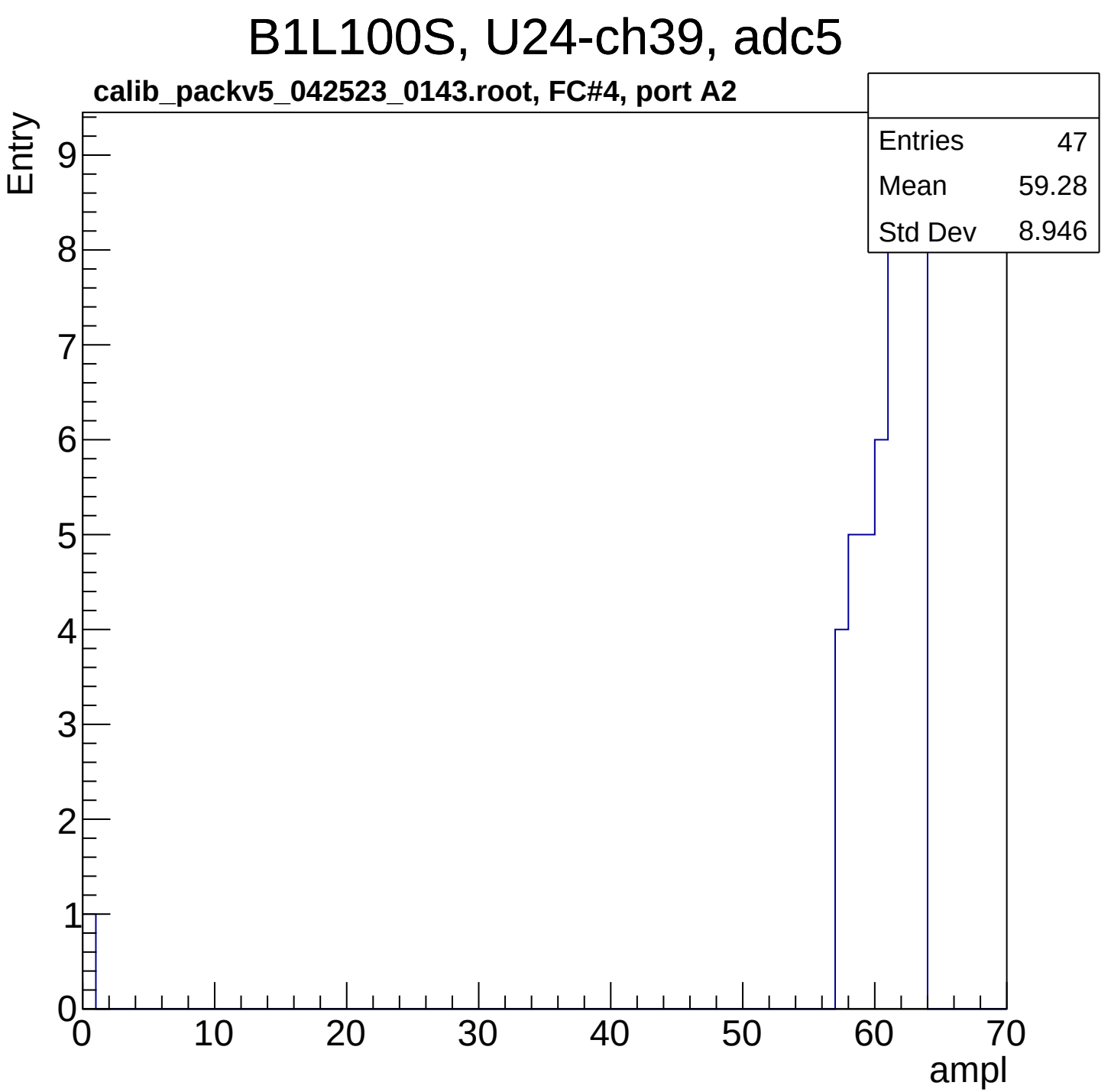

B1L100S, U24-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 48.47
Std Dev 3.53
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch39, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.57
Std Dev 2.816
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.28
Std Dev 8.946
Entry
ampl
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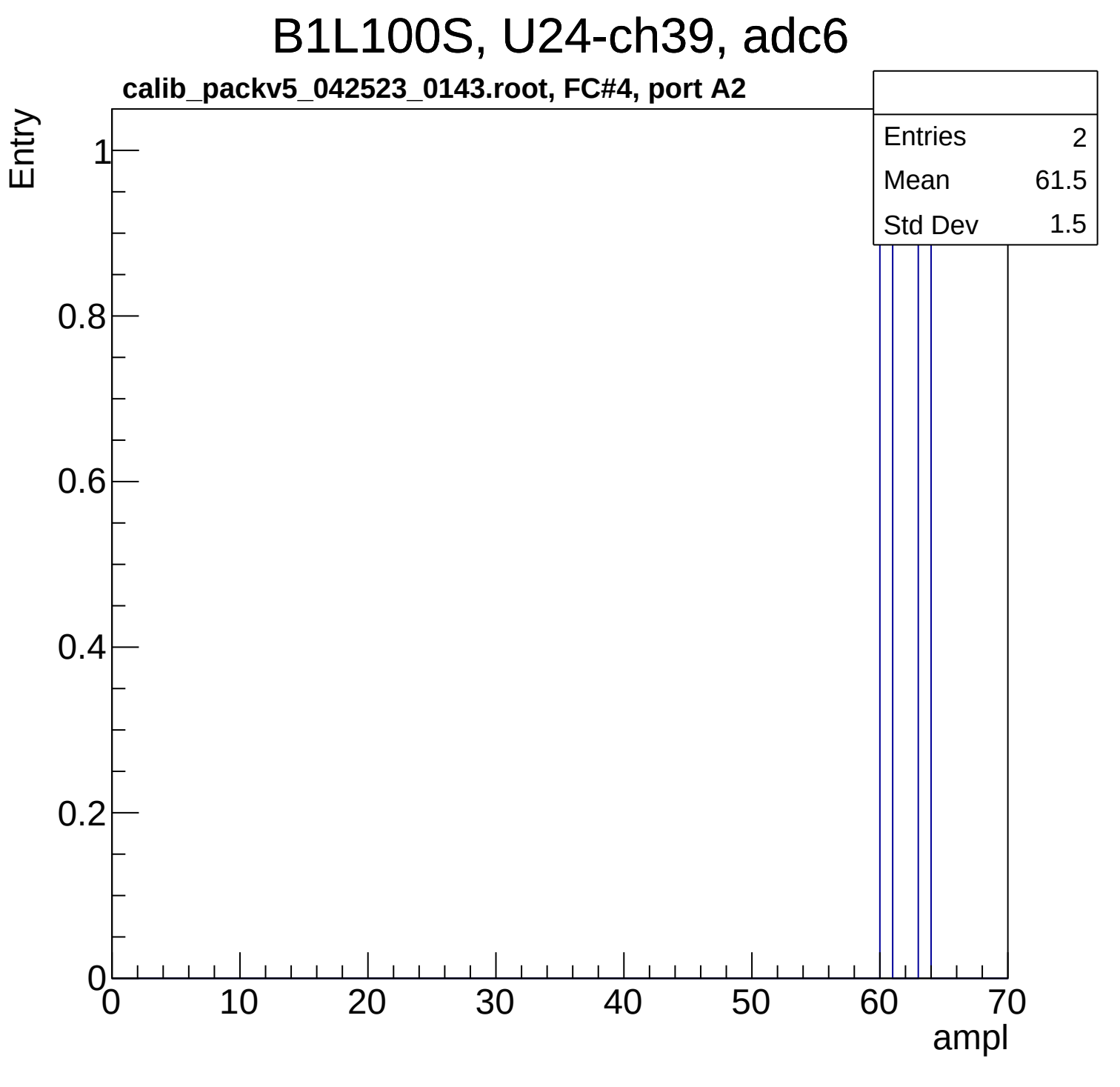
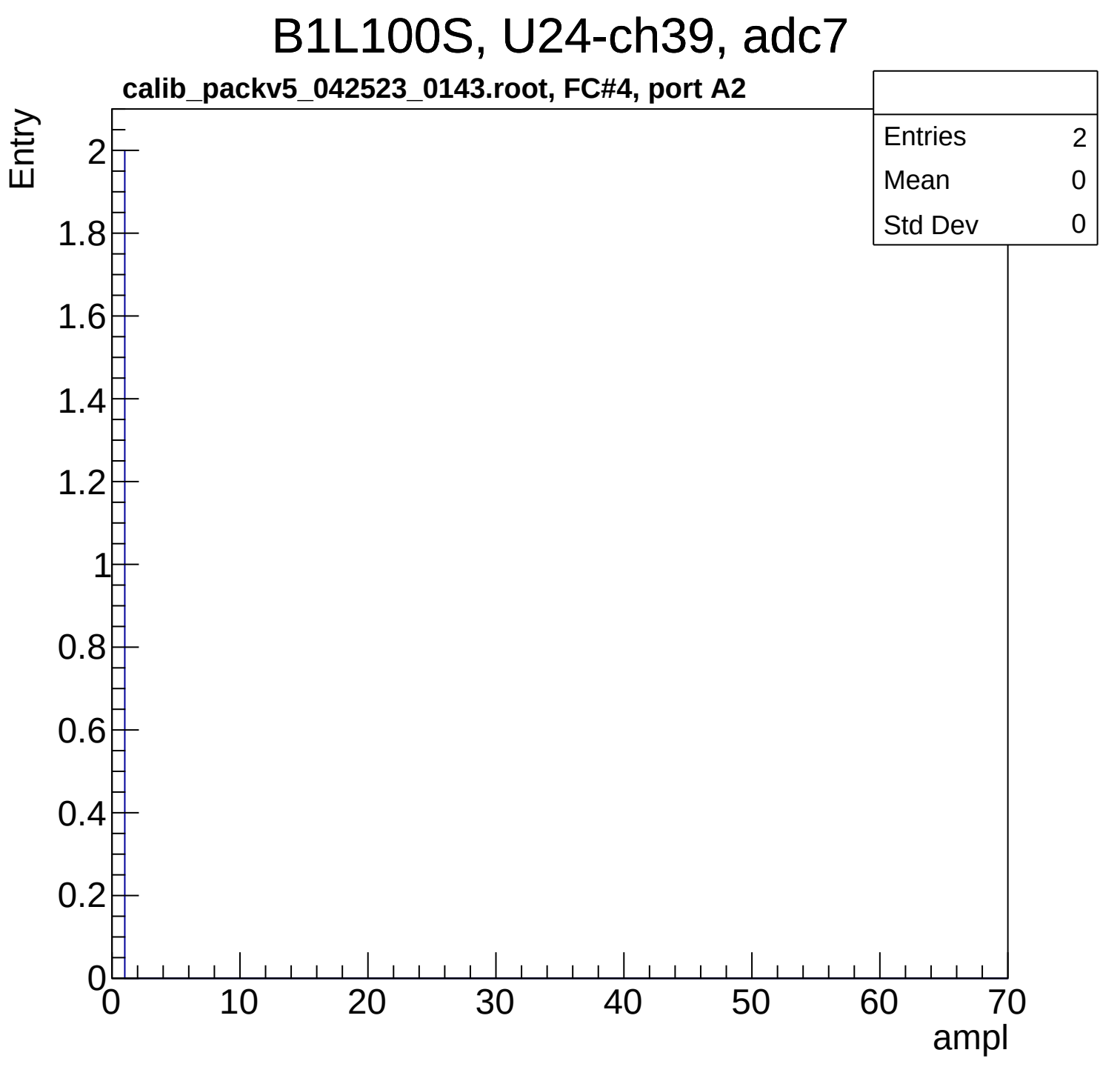
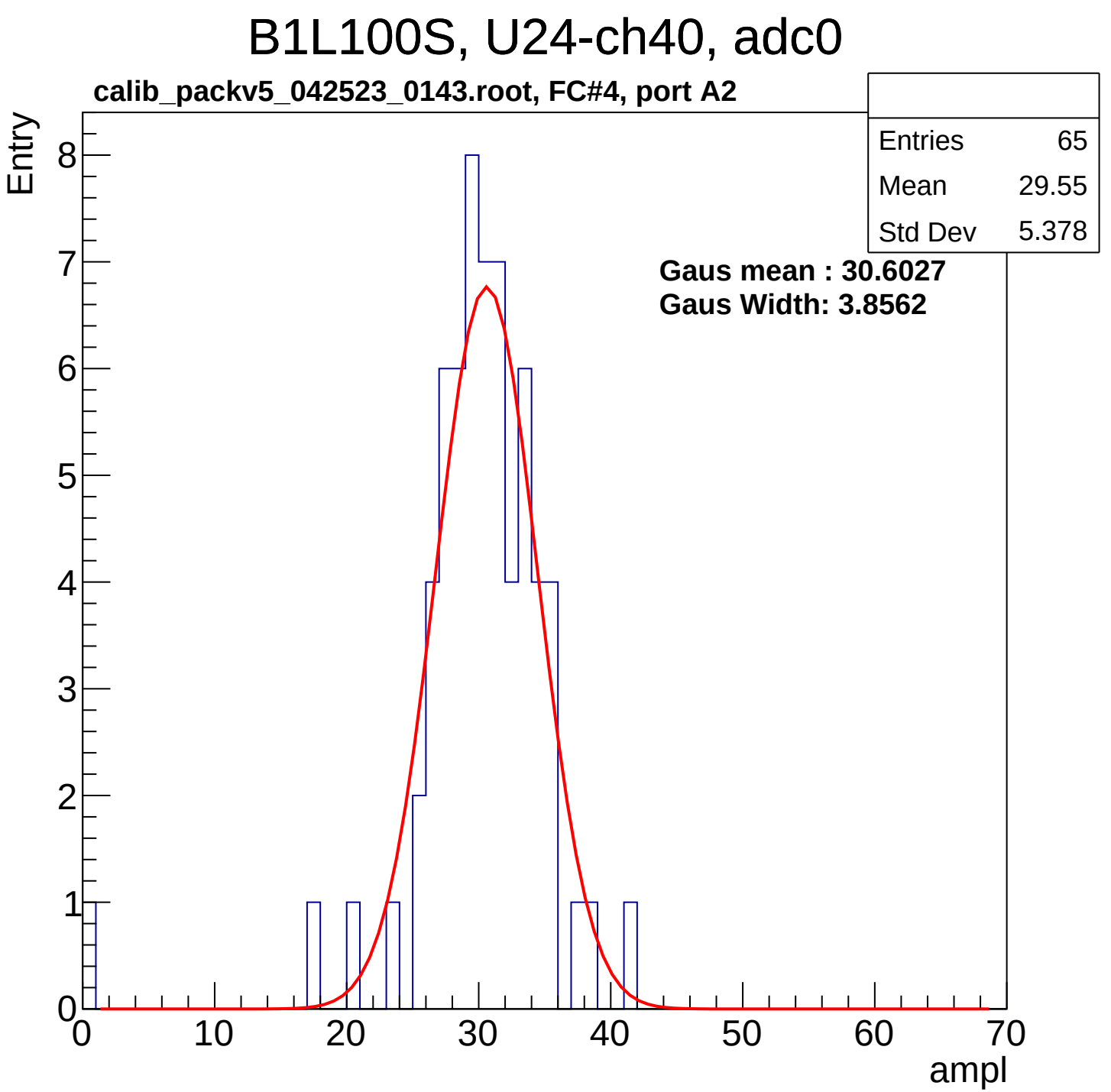
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.55
Std Dev 5.378
Gaus mean : 30.6027
Gaus Width: 3.8562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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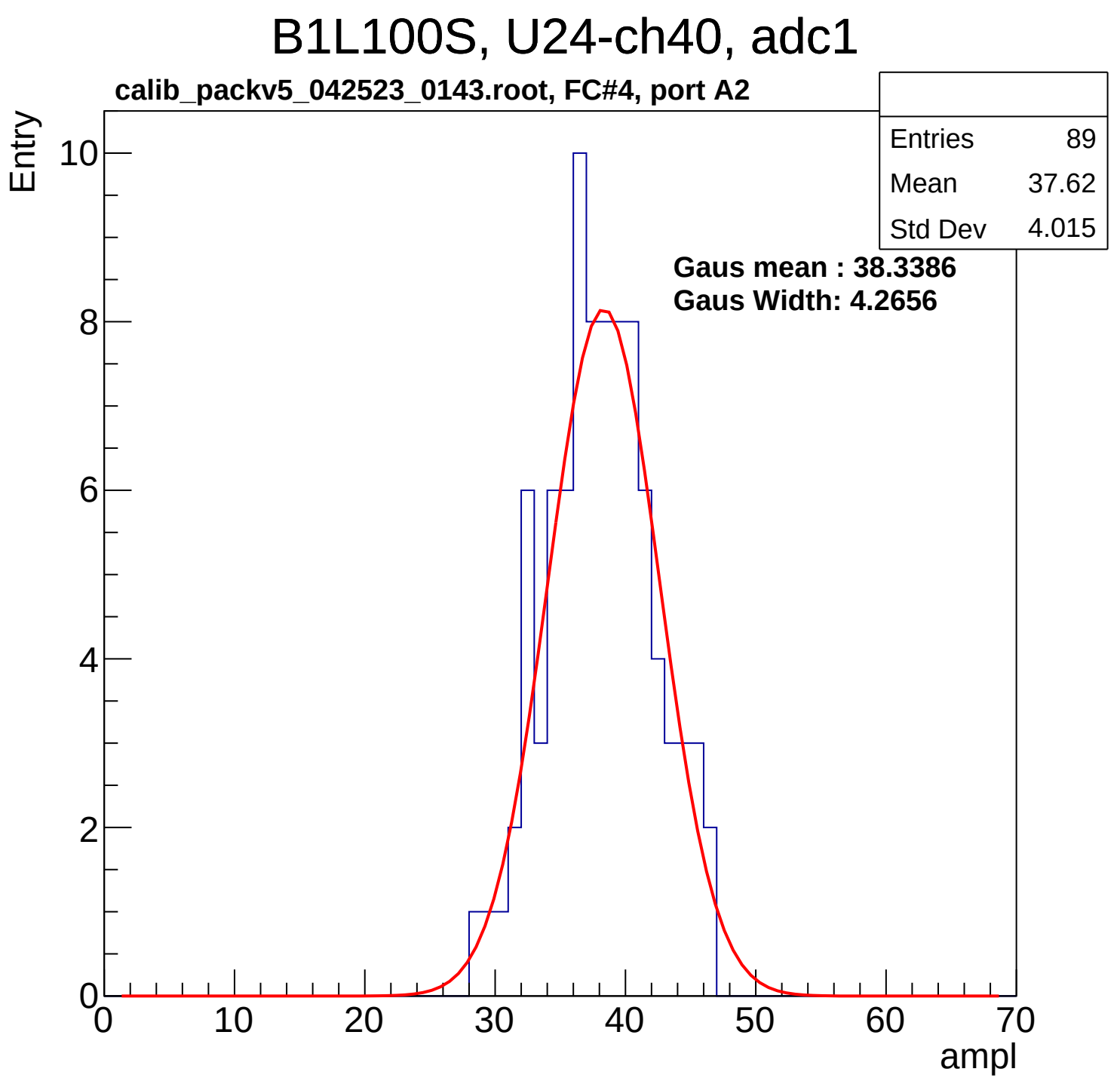
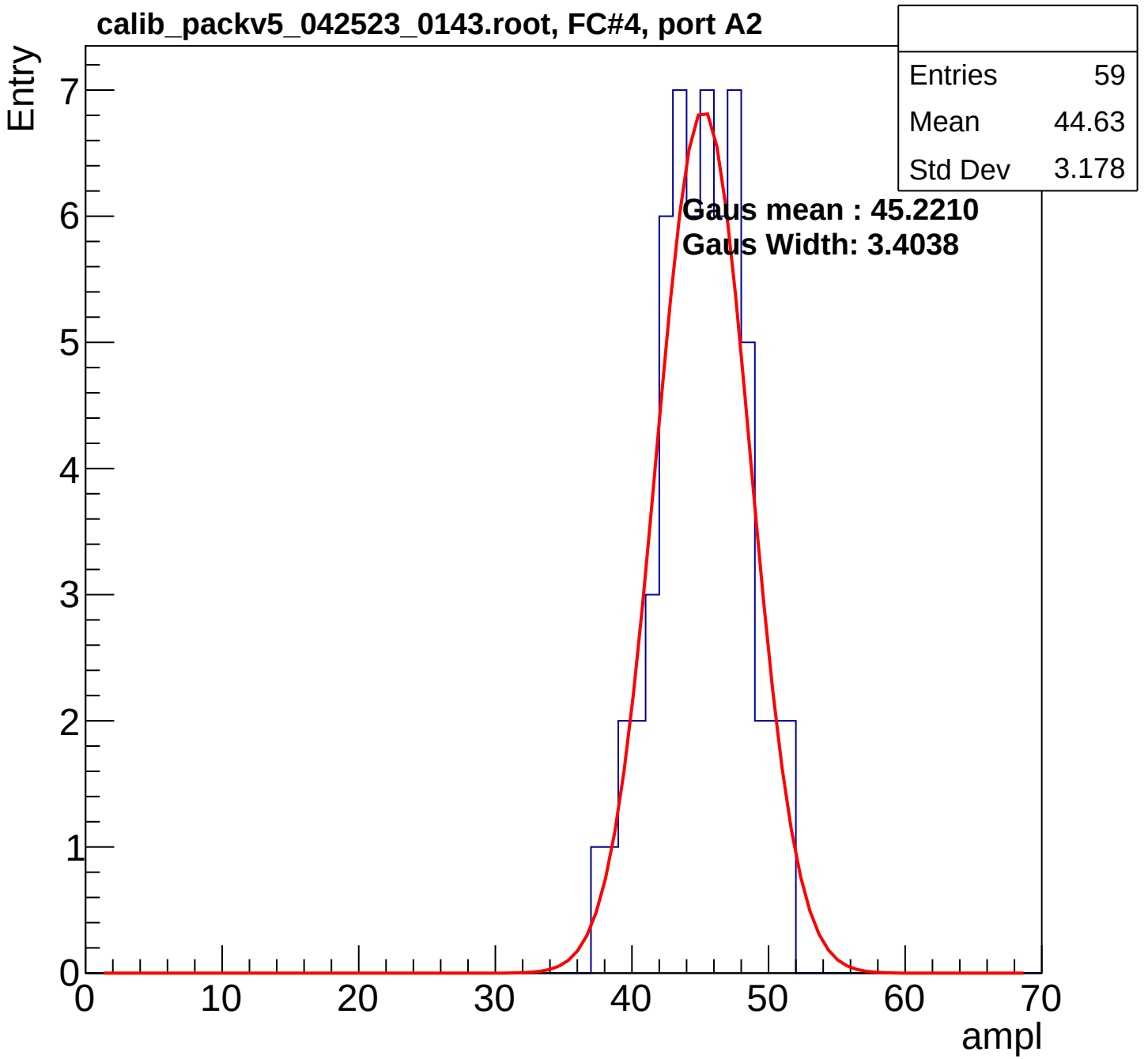

B1L100S, U24-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 37.62
Std Dev 4.015
Gaus mean : 38.3386
Gaus Width: 4.2656
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44.63
Std Dev 3.178
Gaus mean : 45.2210
Gaus Width: 3.4038
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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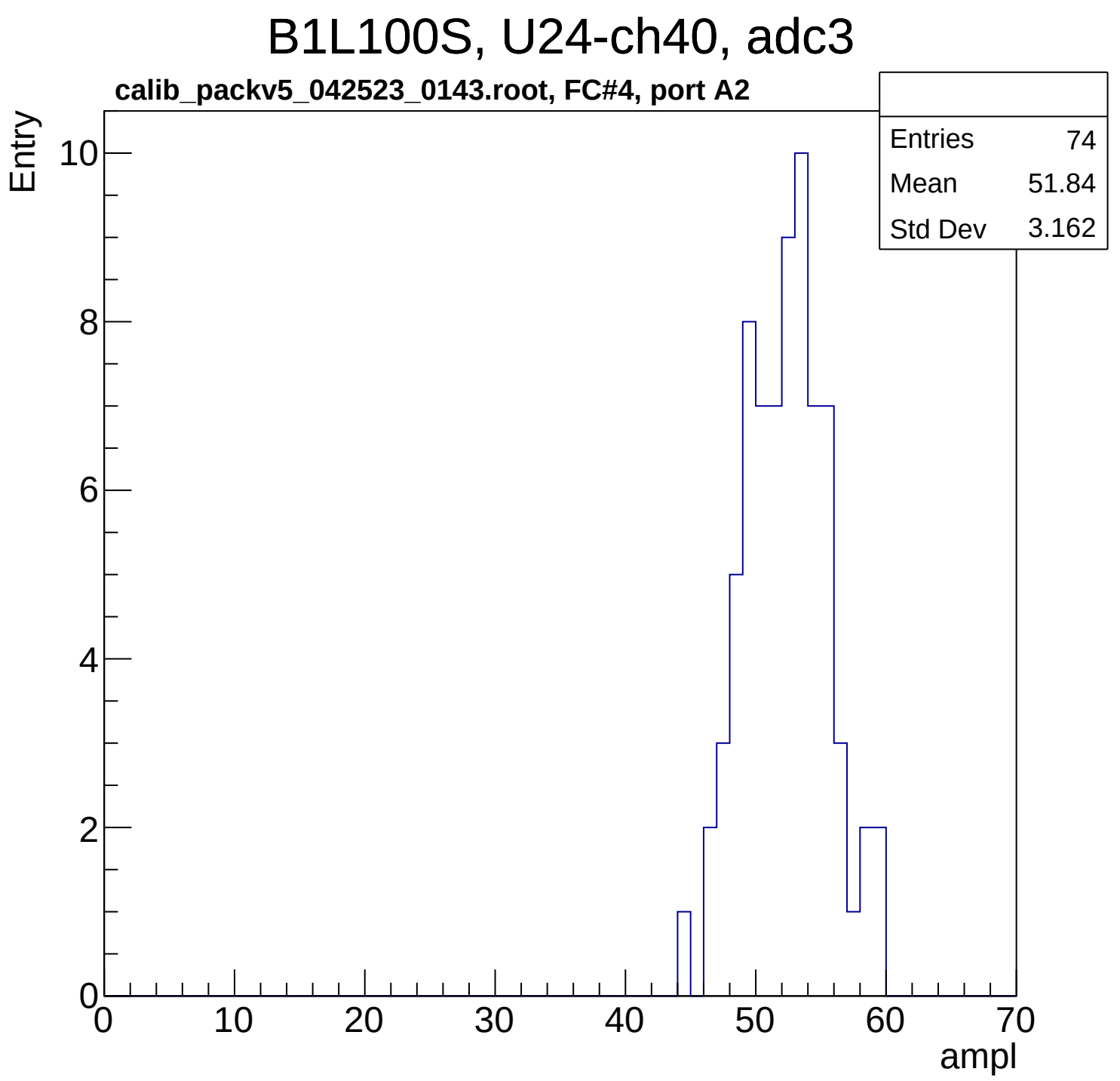
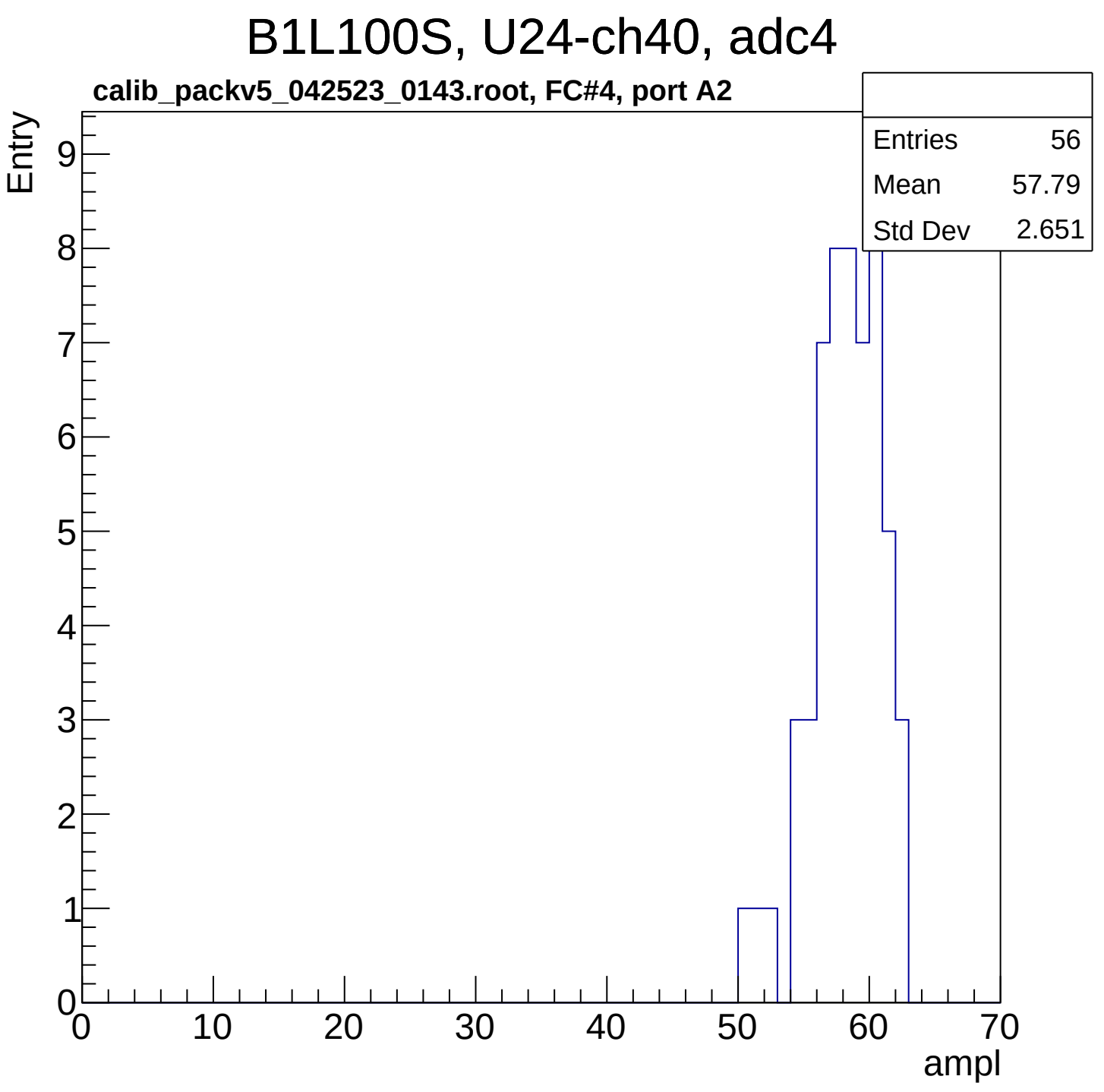
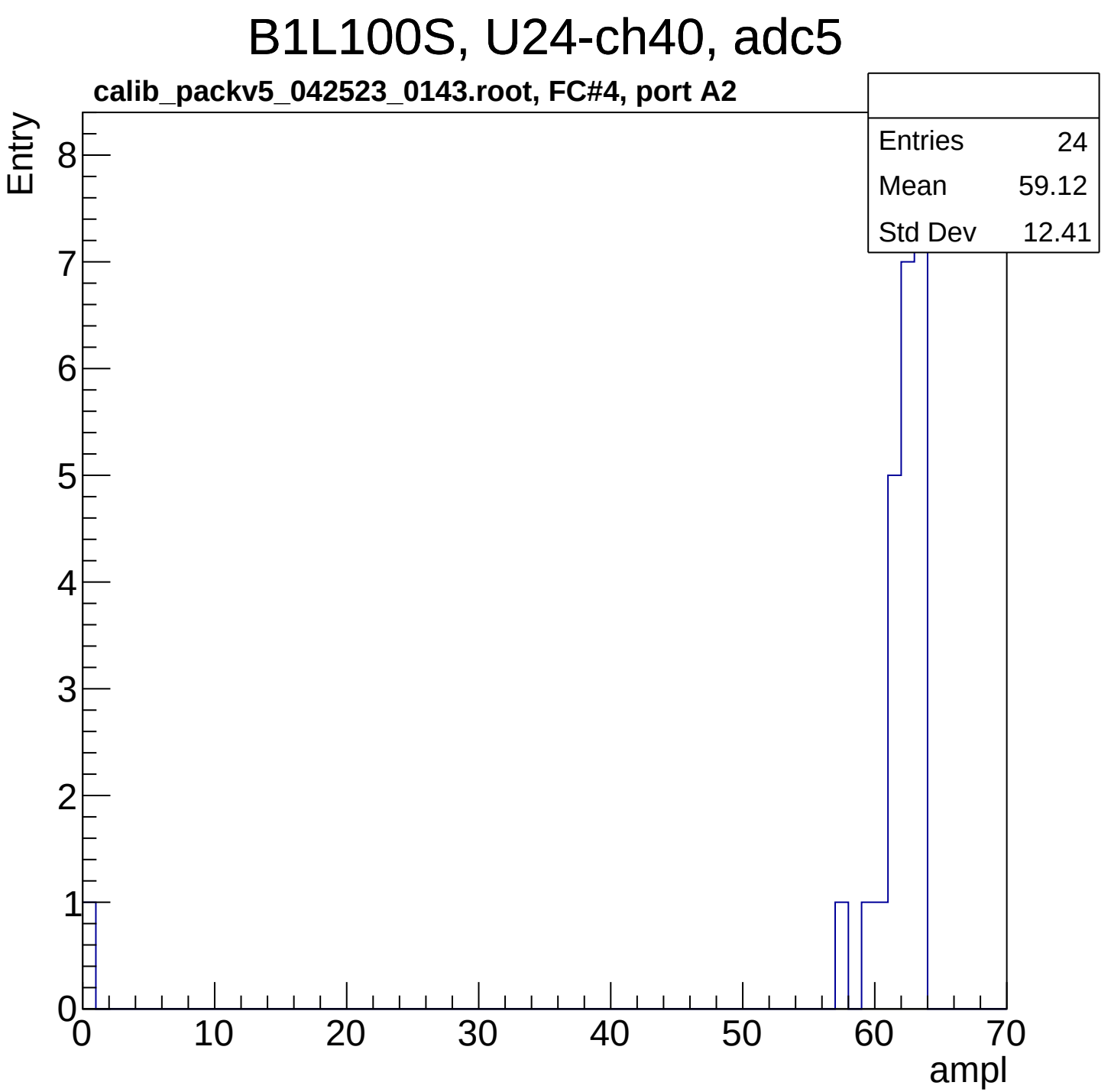

B1L100S, U24-ch40, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.84
Std Dev 3.162
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.79
Std Dev 2.651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 59.12
Std Dev 12.41
Entry
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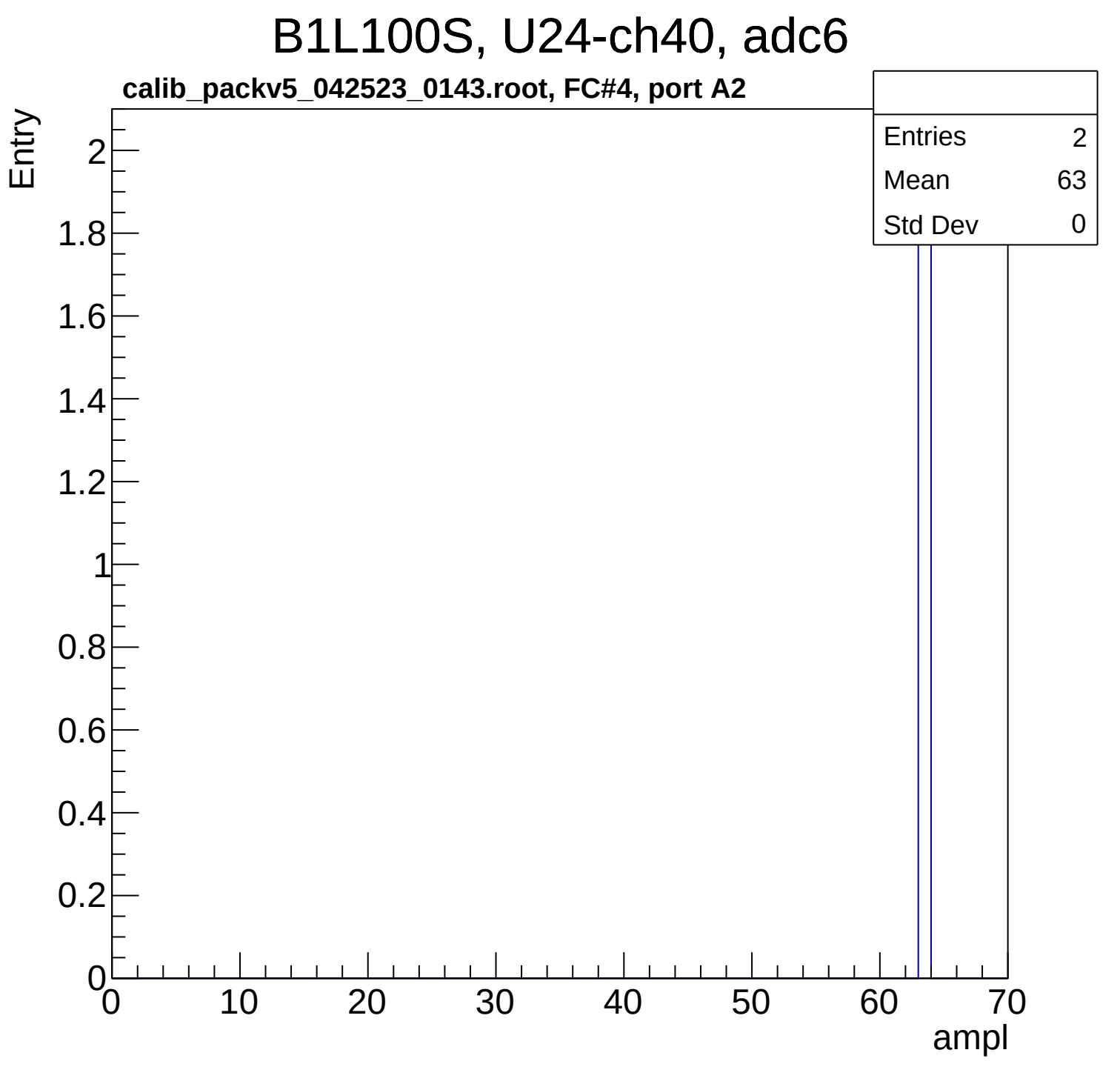
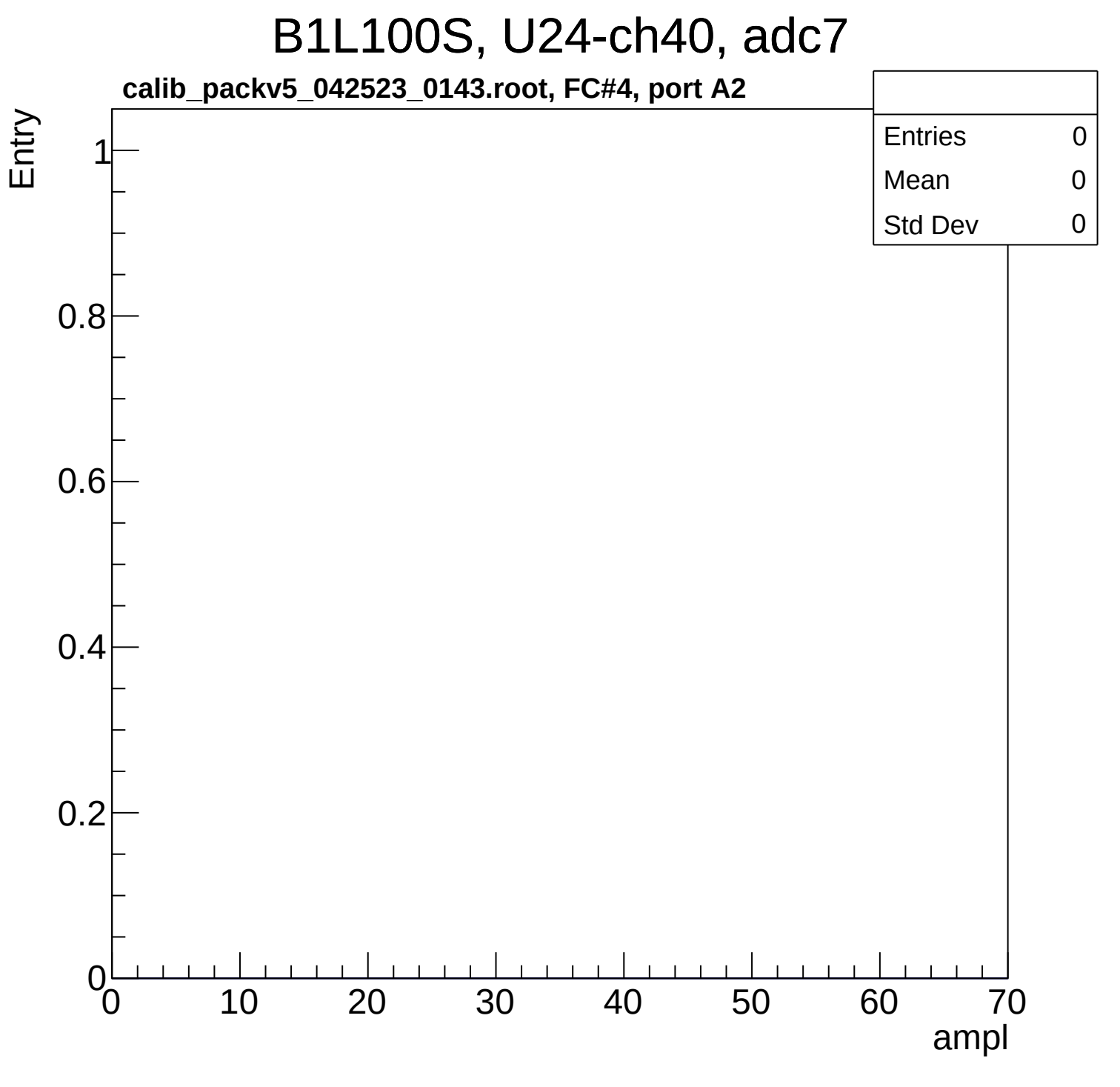
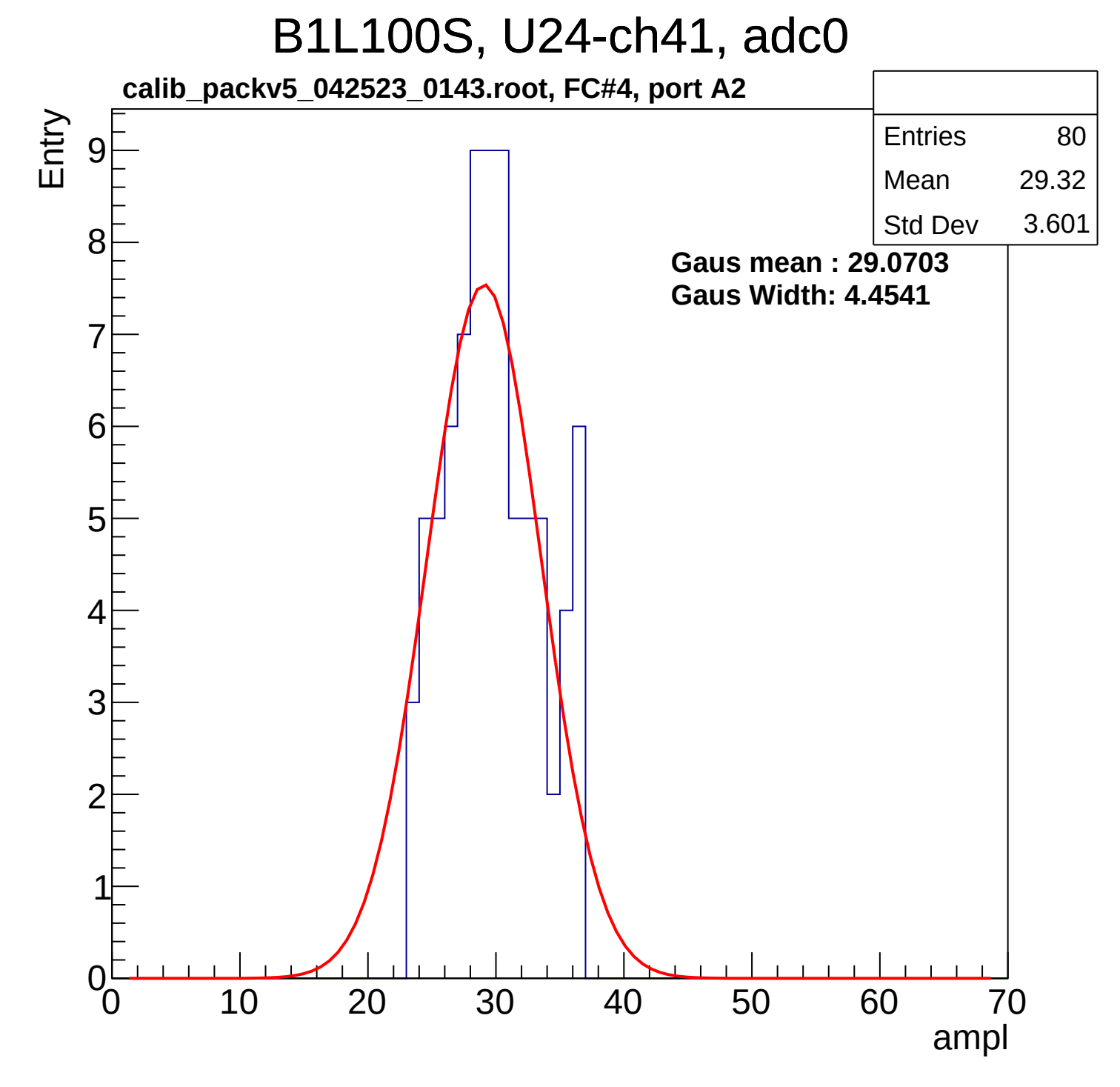
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch40, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch41, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.32
Std Dev 3.601
Gaus mean : 29.0703
Gaus Width: 4.4541
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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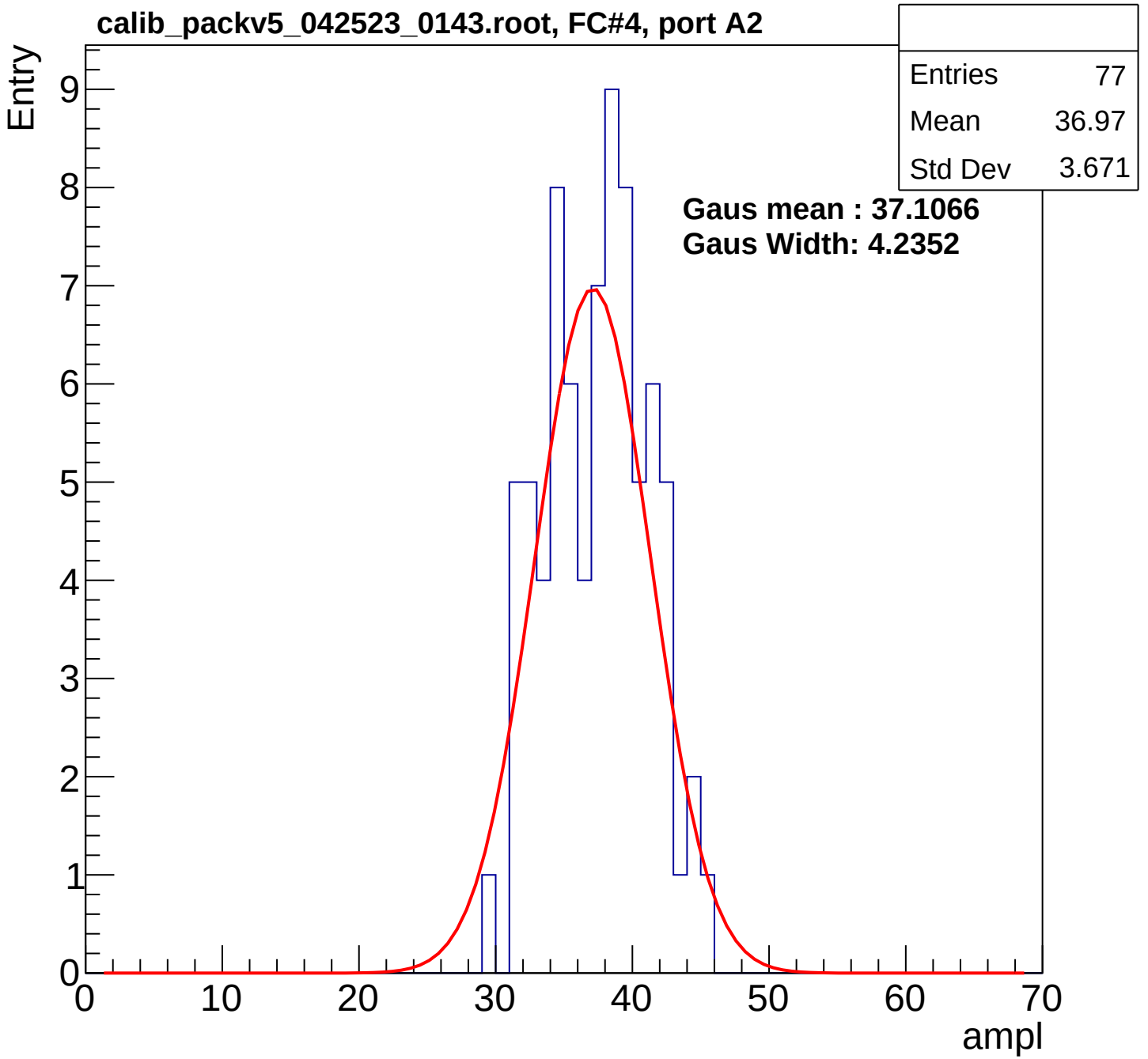
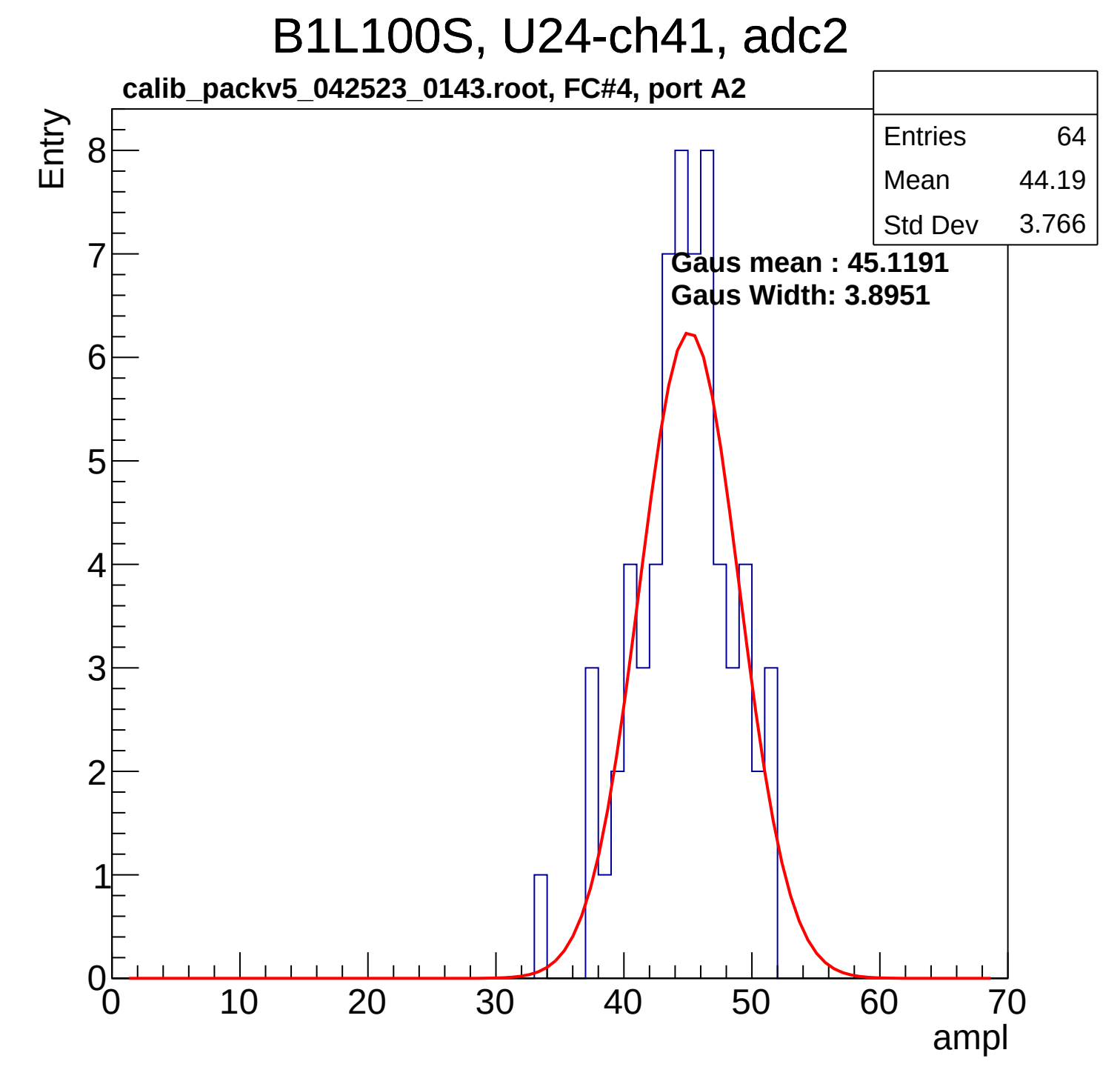

B1L100S, U24-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.97
Std Dev 3.671
Gaus mean : 37.1066
Gaus Width: 4.2352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 44.19
Std Dev 3.766
Gaus mean : 45.1191
Gaus Width: 3.8951
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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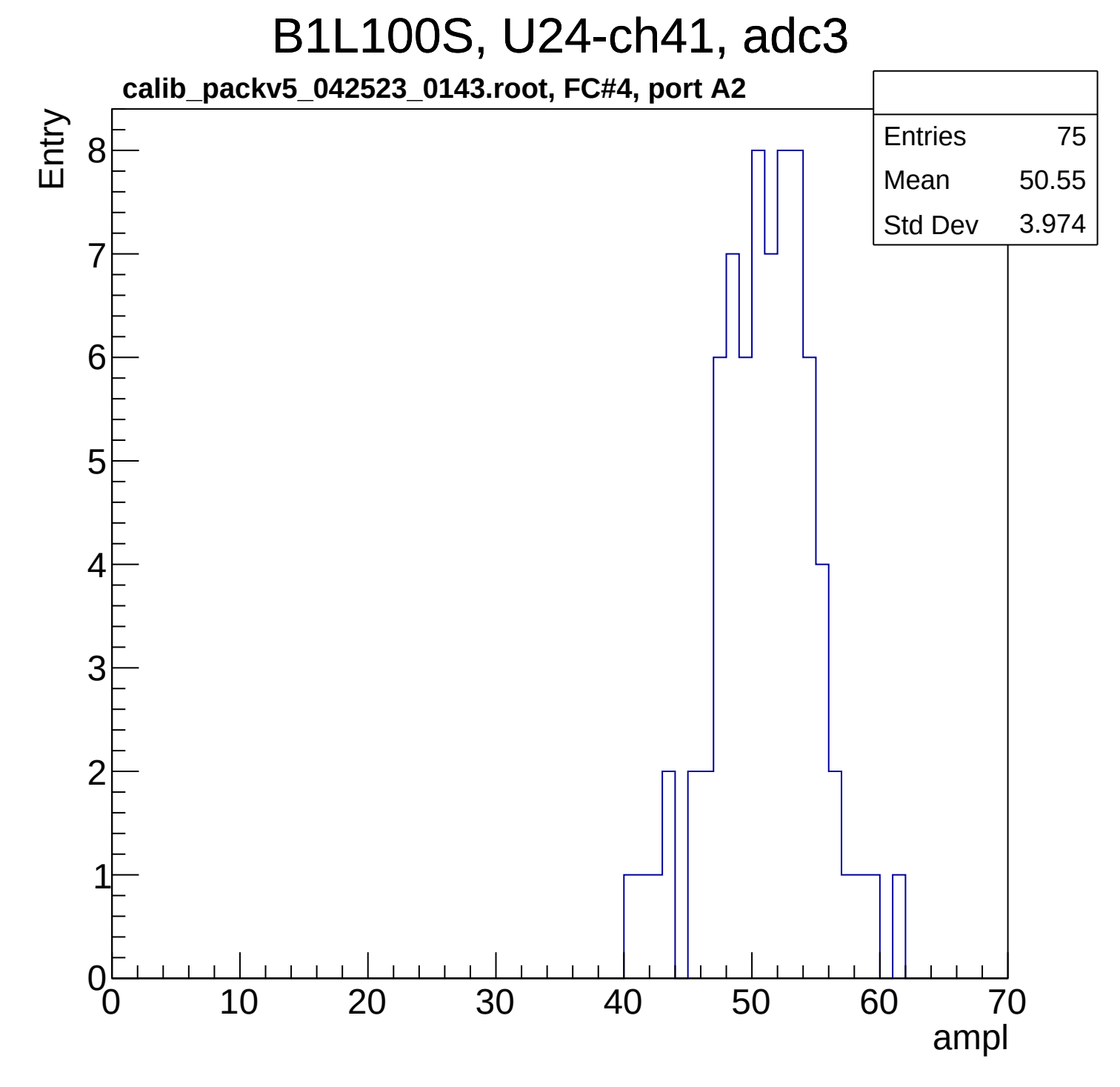
B1L100S, U24-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.55
Std Dev 3.974
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
50
60
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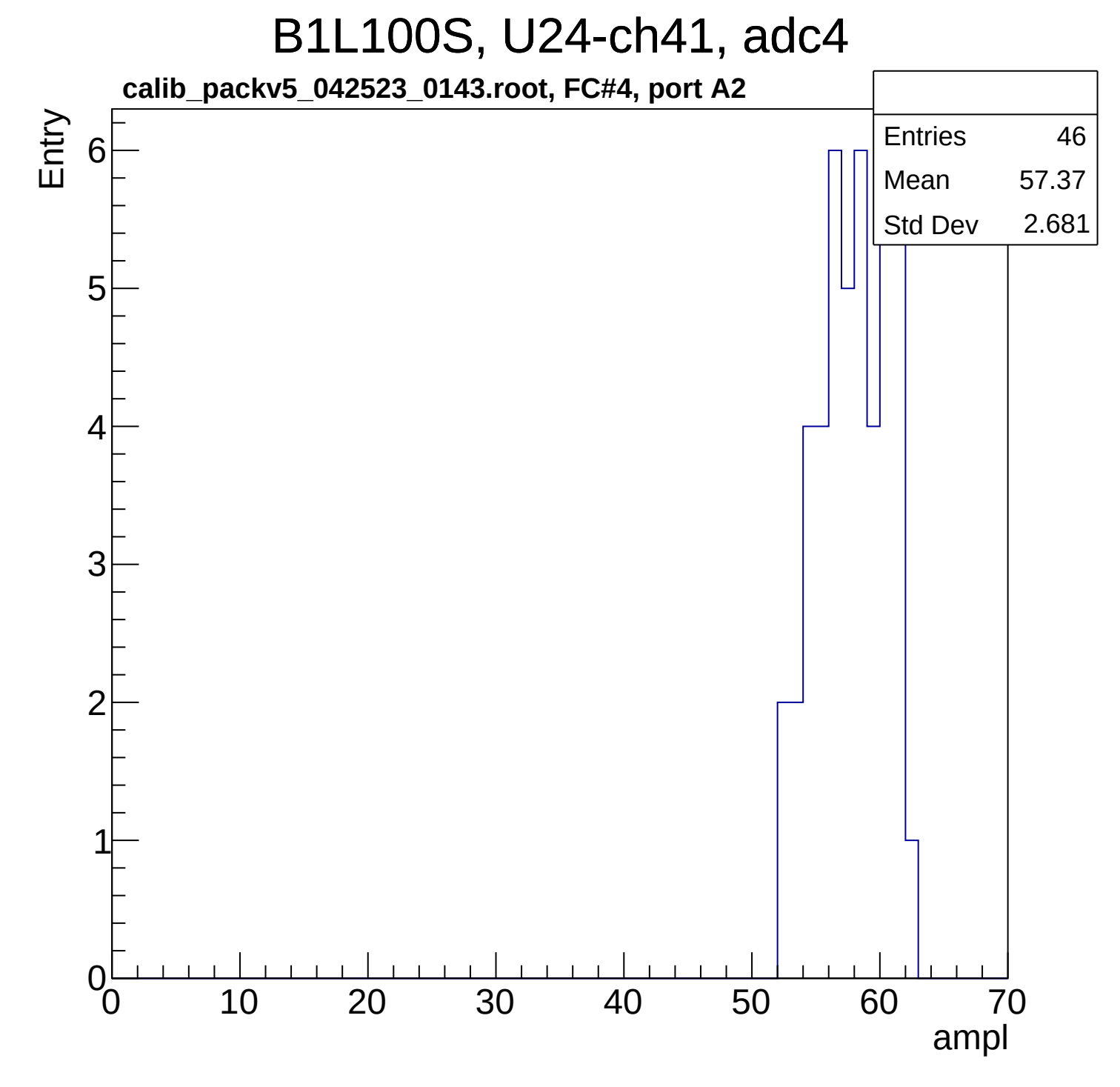
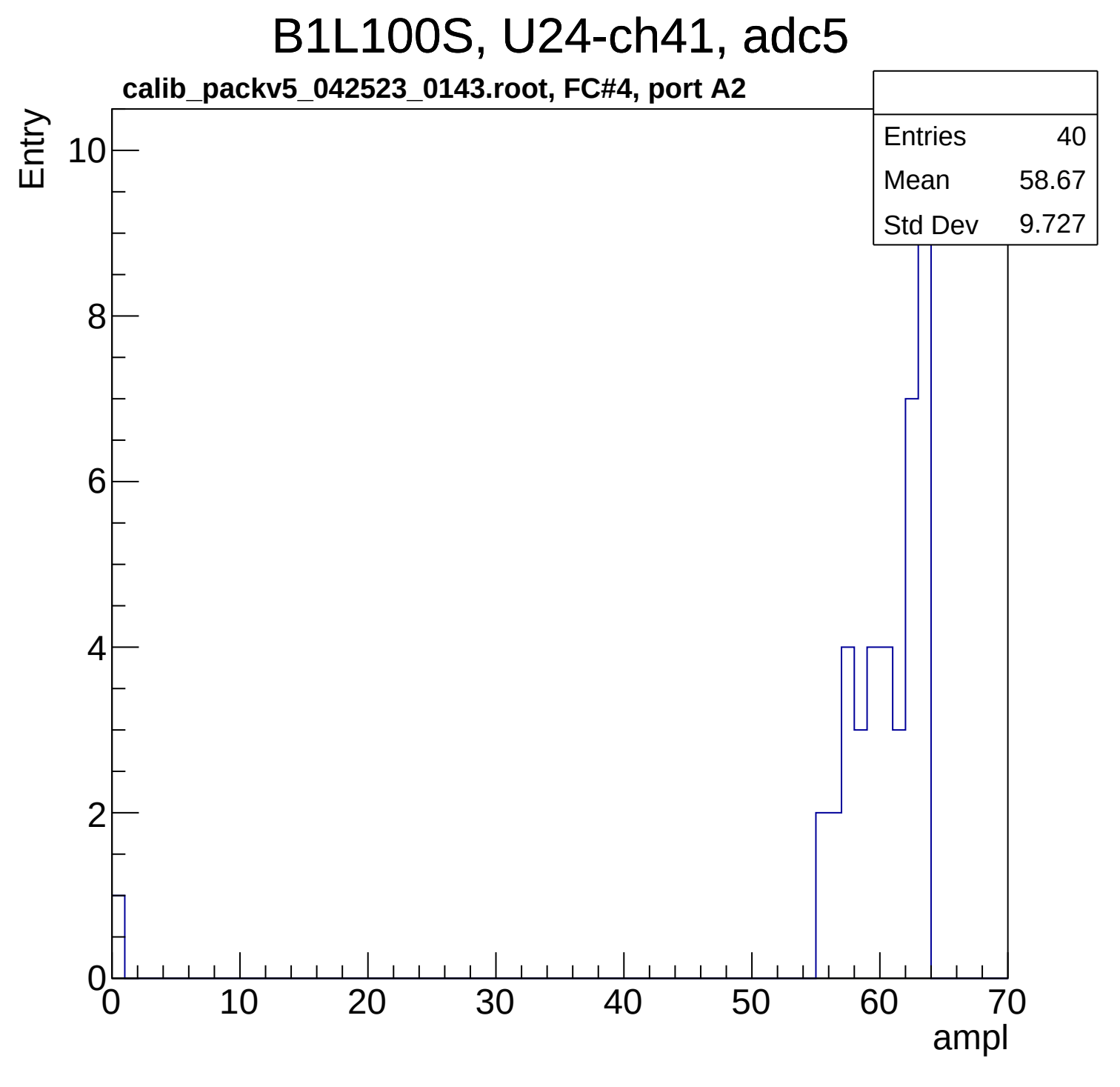

B1L100S, U24-ch41, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 57.37
Std Dev 2.681
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.67
Std Dev 9.727
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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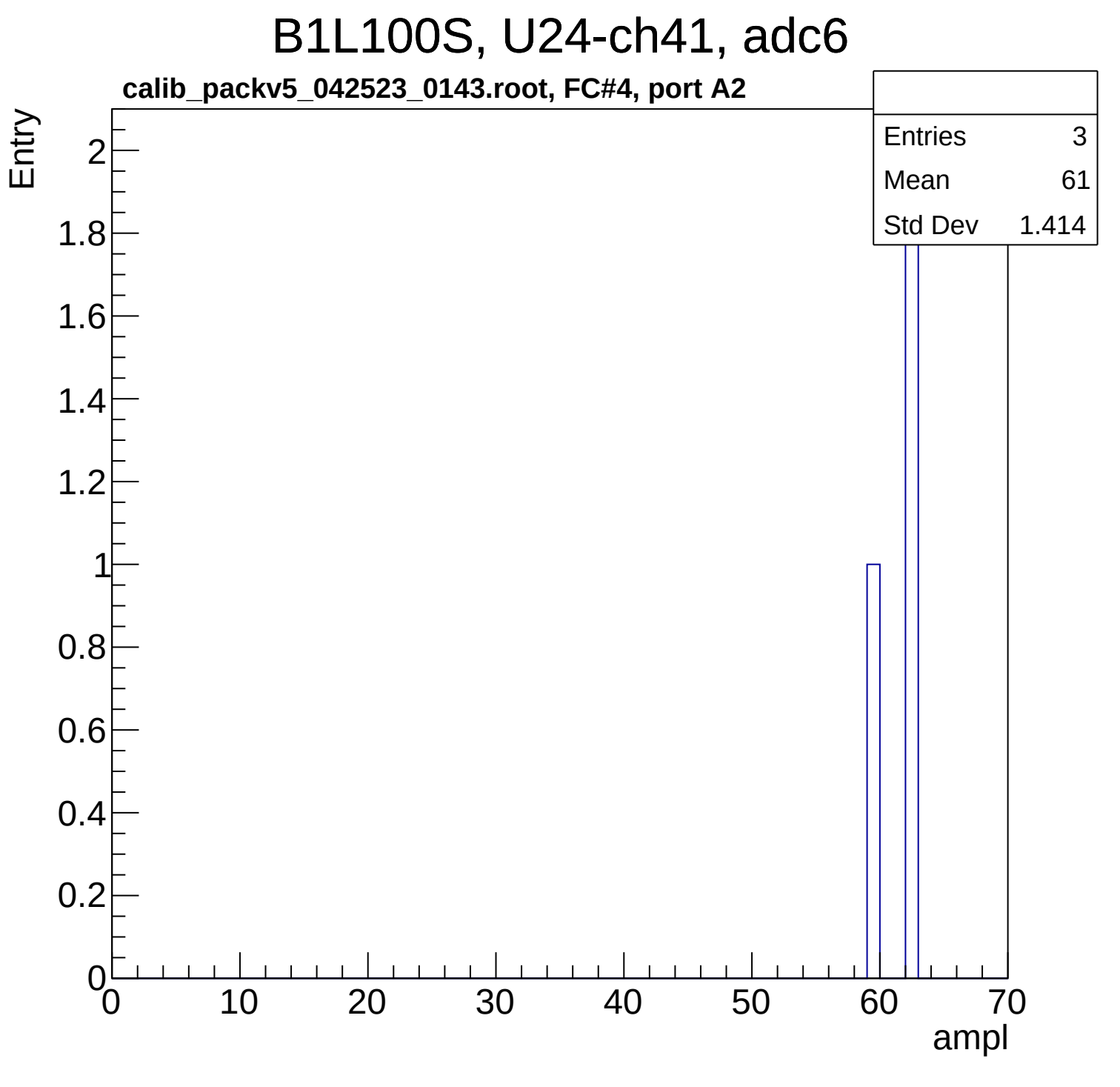
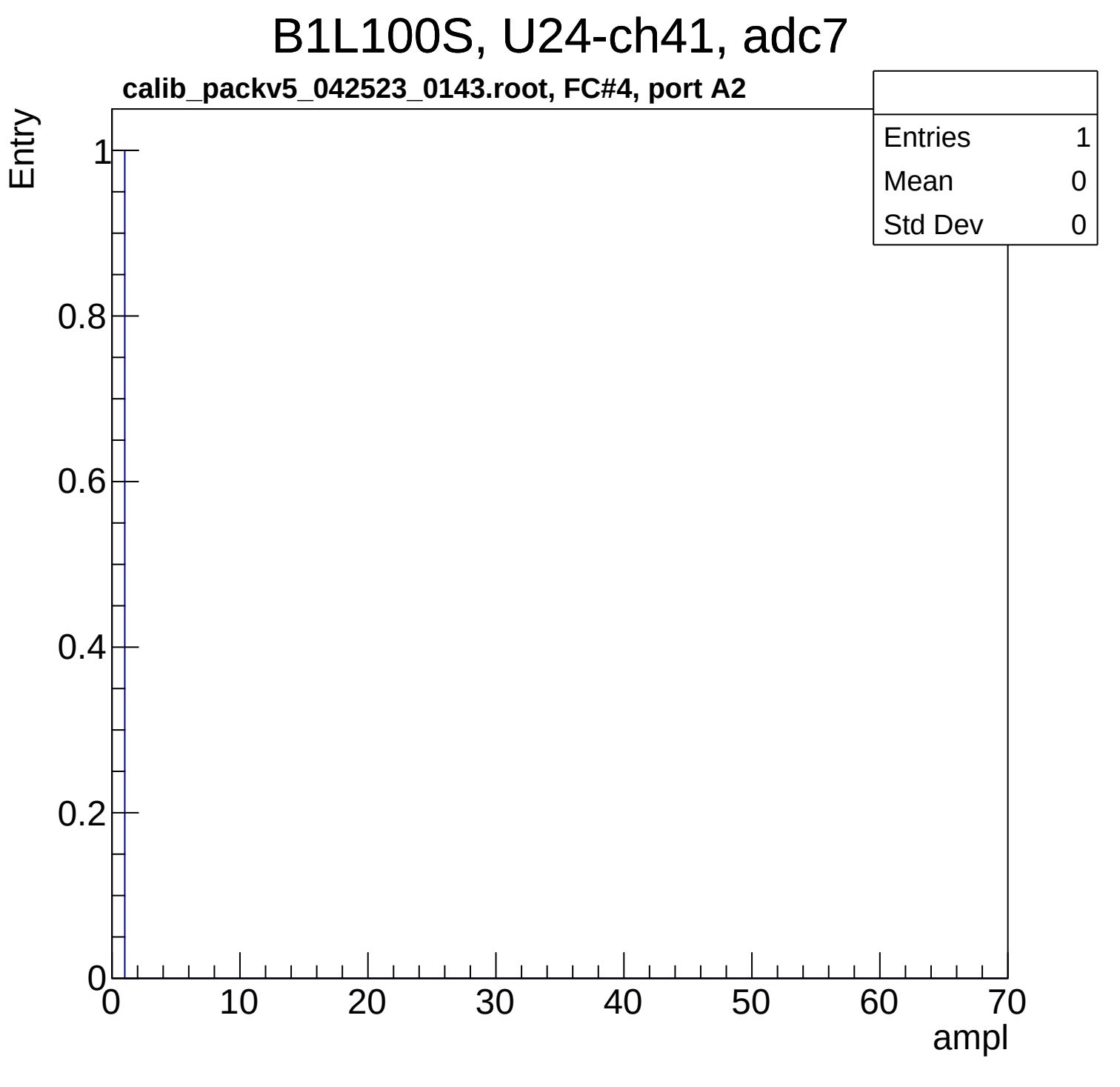

B1L100S, U24-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
30
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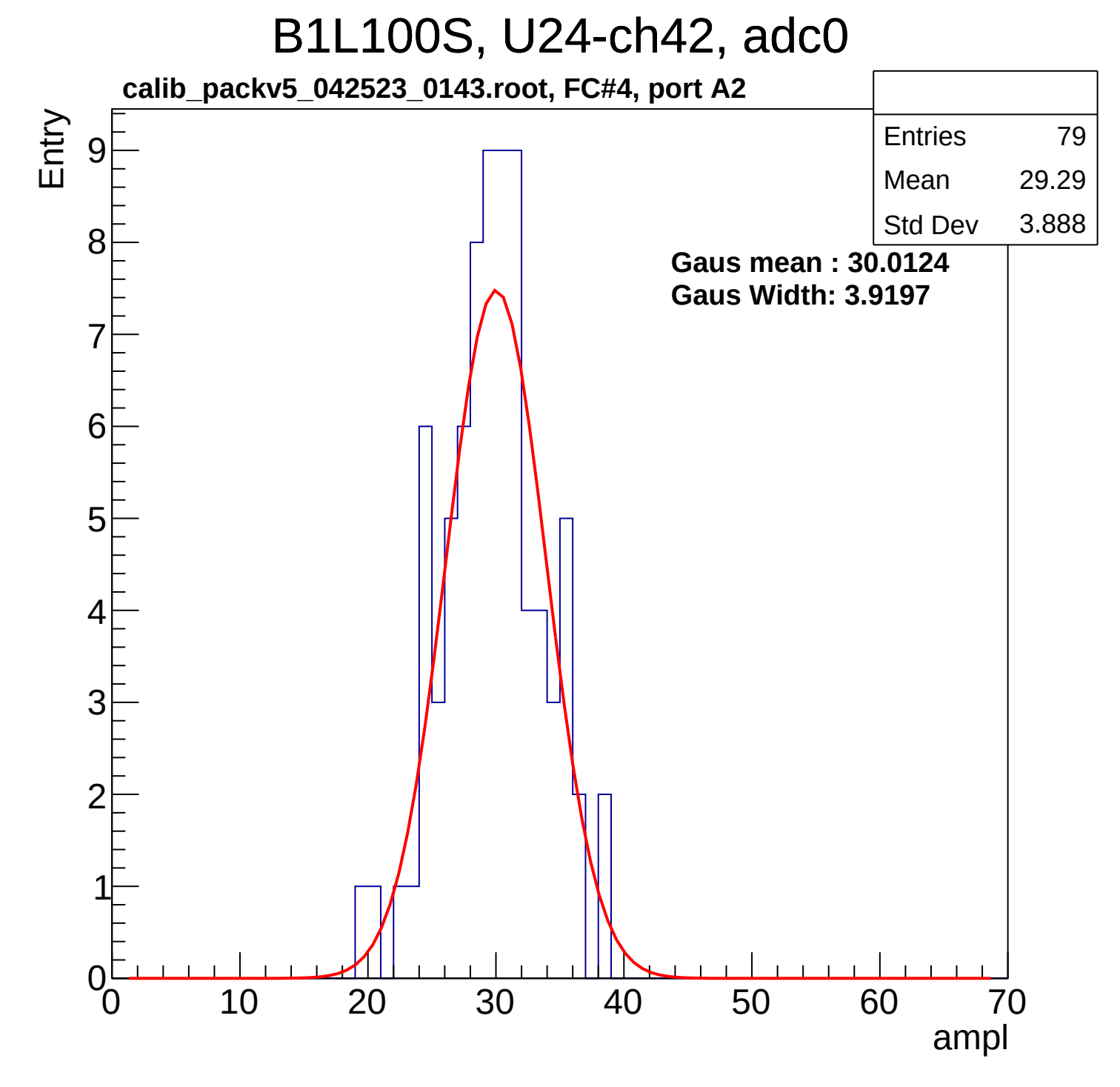
50
60
70
ampl


B1L100S, U24-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.29
Std Dev 3.888
Gaus mean : 30.0124
Gaus Width: 3.9197
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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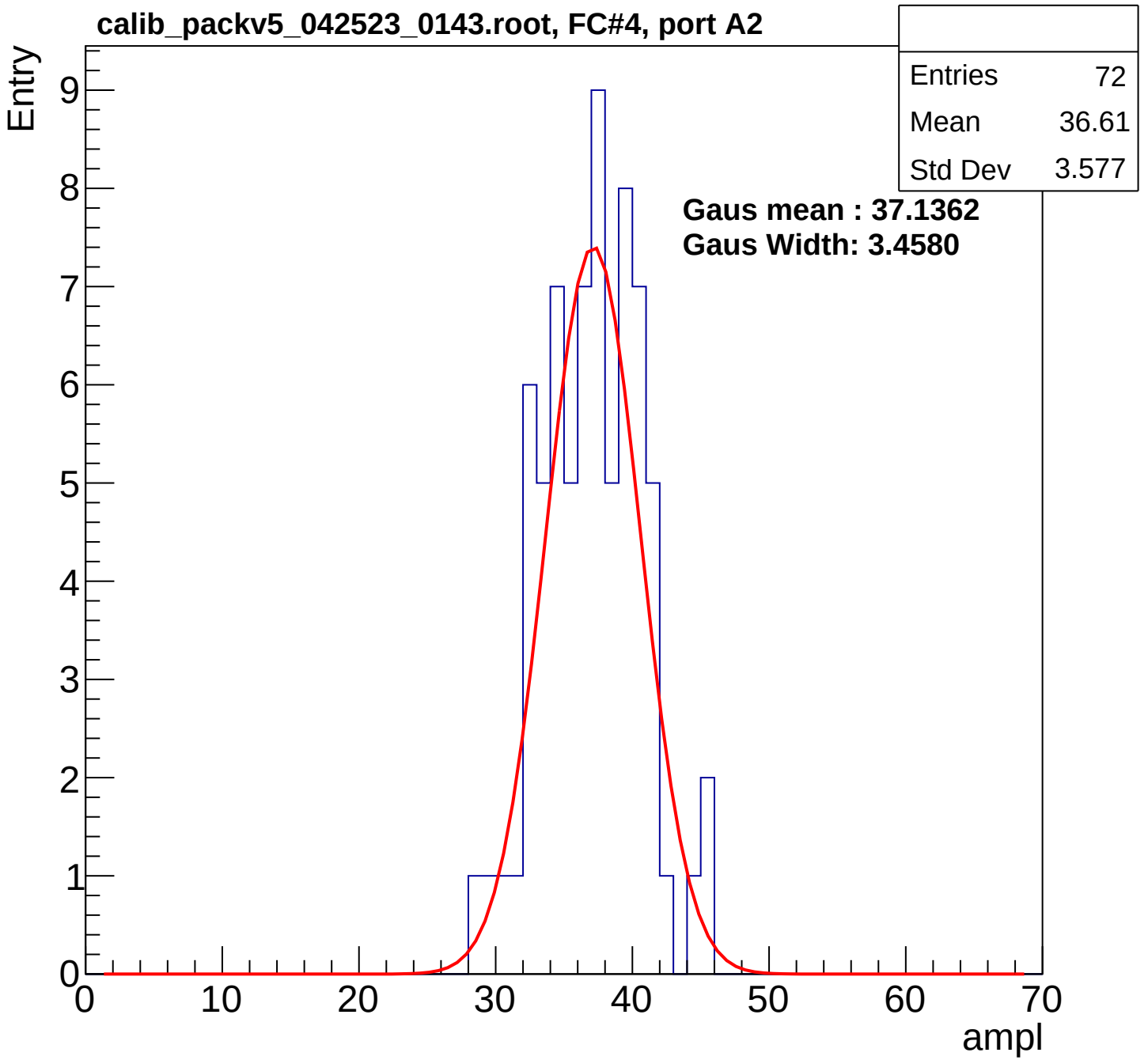
B1L100S, U24-ch42, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.61
Std Dev 3.577
Gaus mean : 37.1362
Gaus Width: 3.4580
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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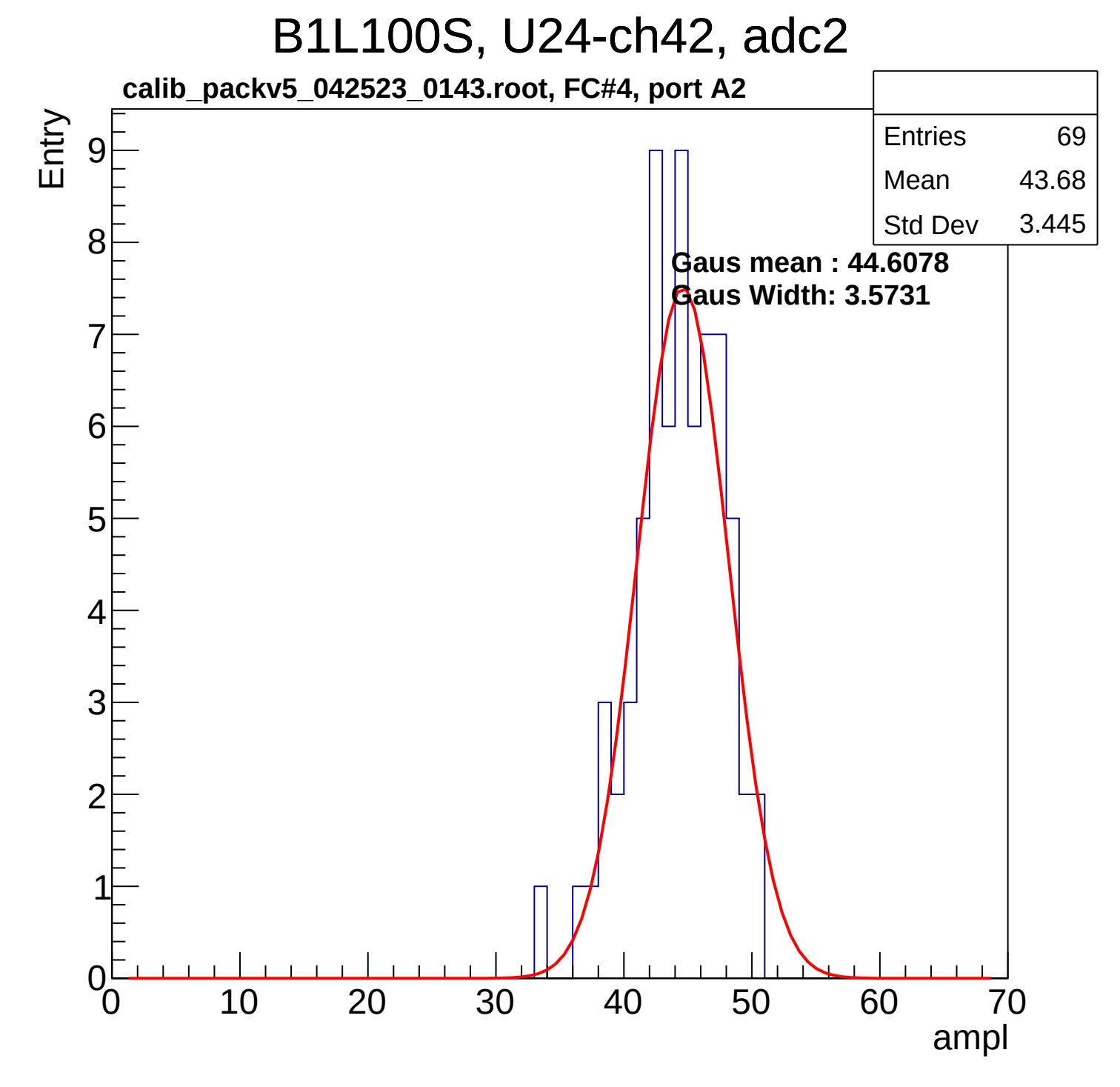
B1L100S, U24-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.68
Std Dev 3.445
Gaus mean : 44.6078
Gaus Width: 3.5731
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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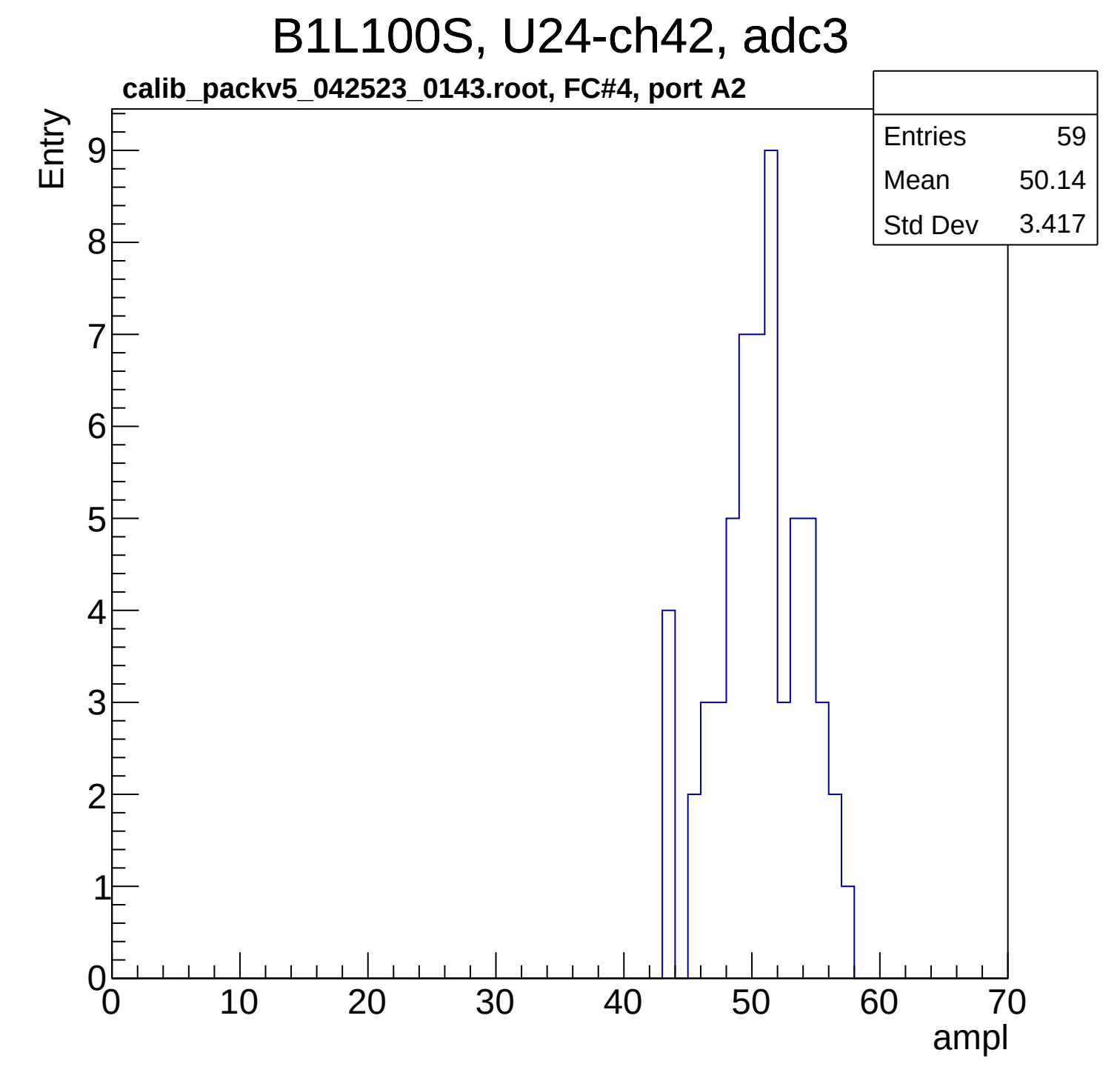
B1L100S, U24-ch42, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 50.14
Std Dev 3.417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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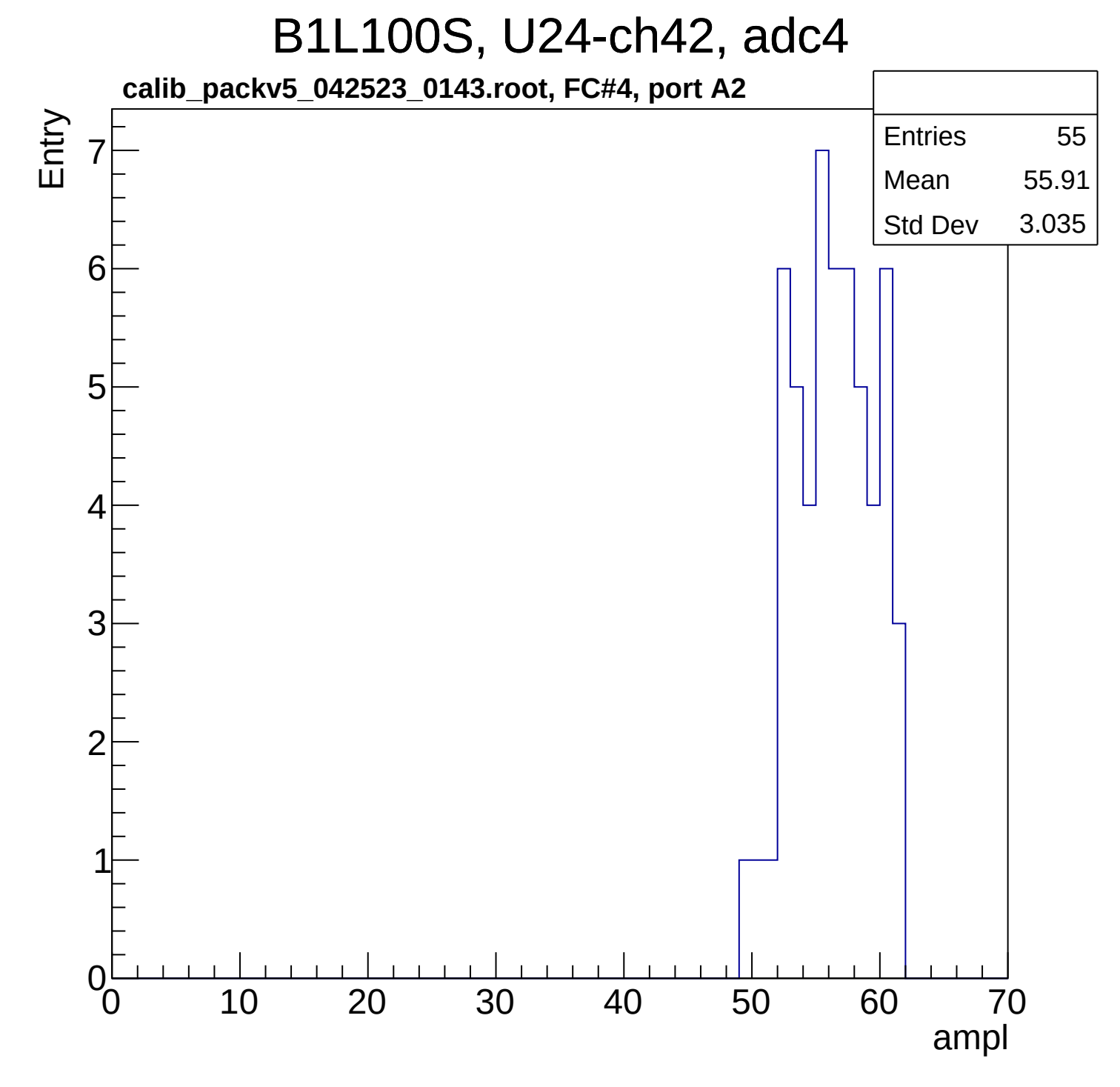
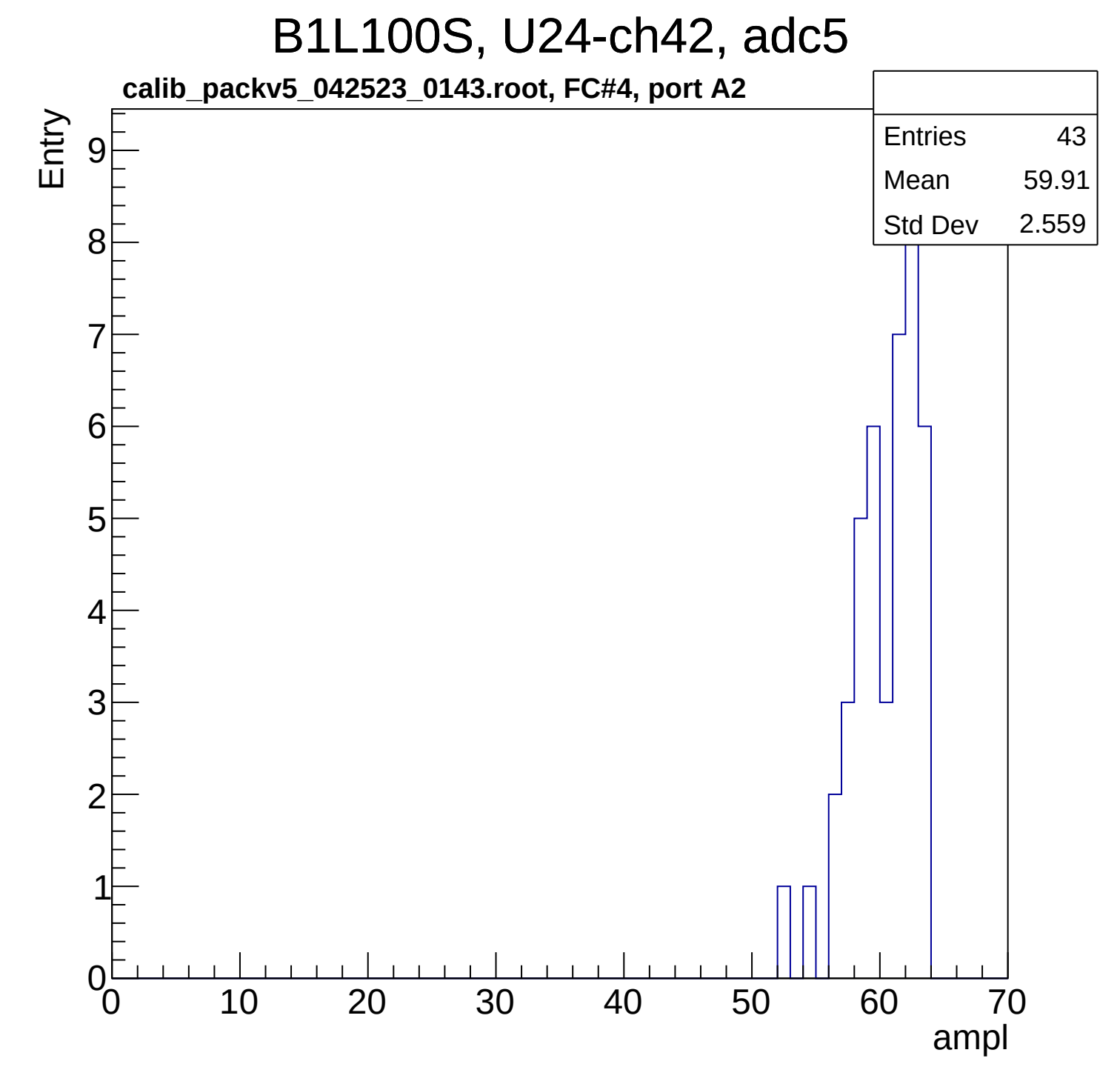

B1L100S, U24-ch42, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.91
Std Dev 3.035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59.91
Std Dev 2.559
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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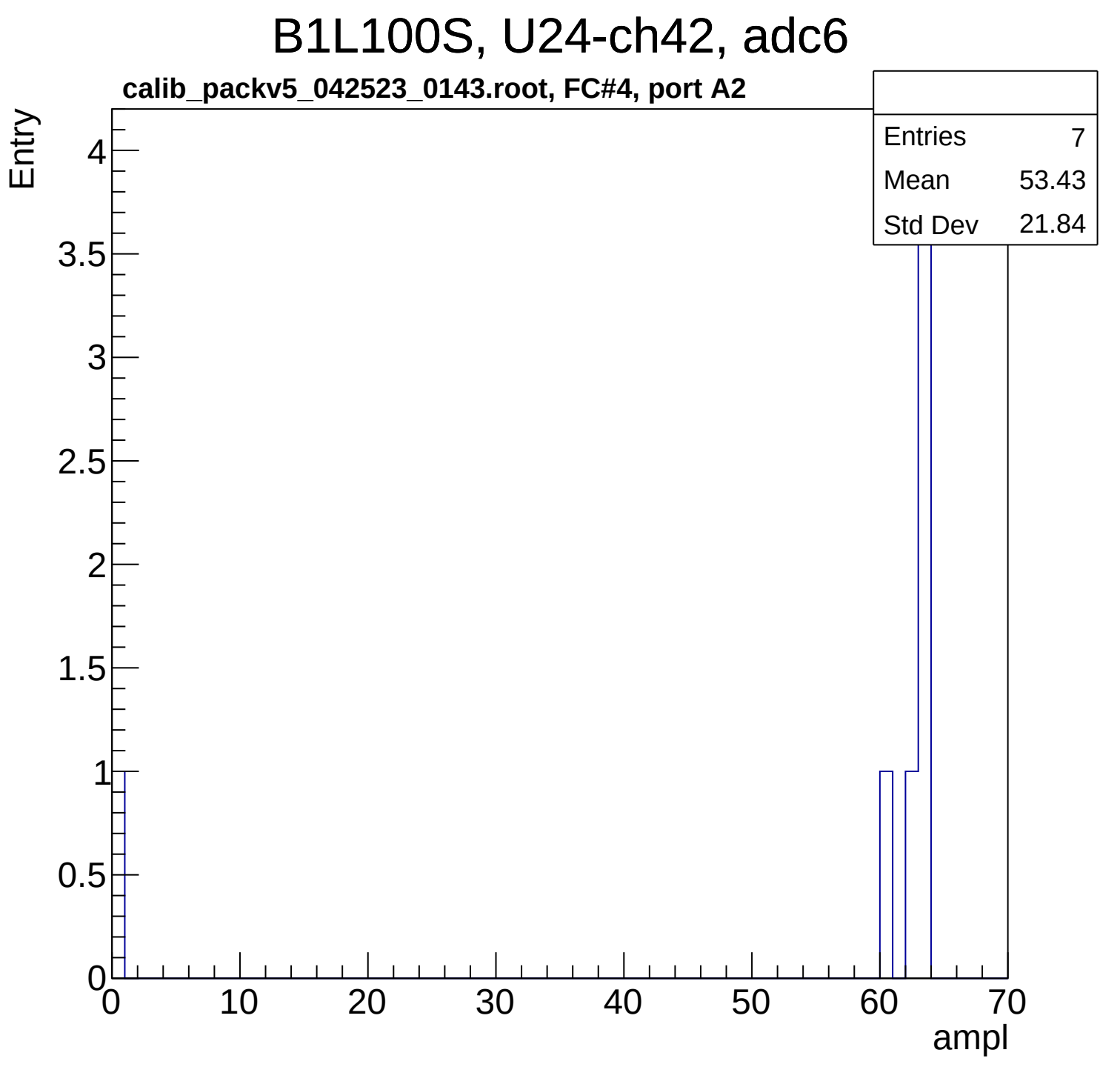
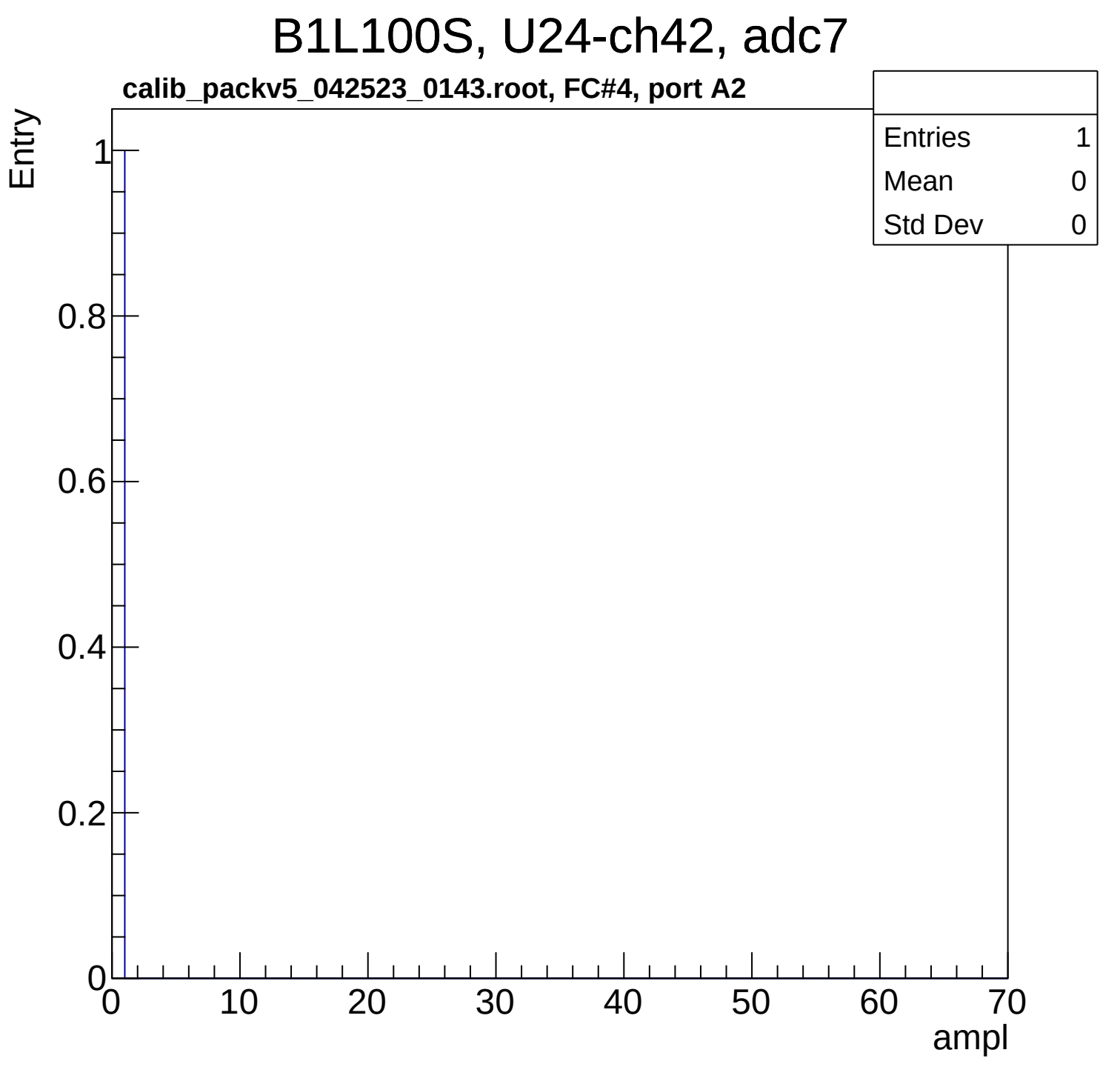
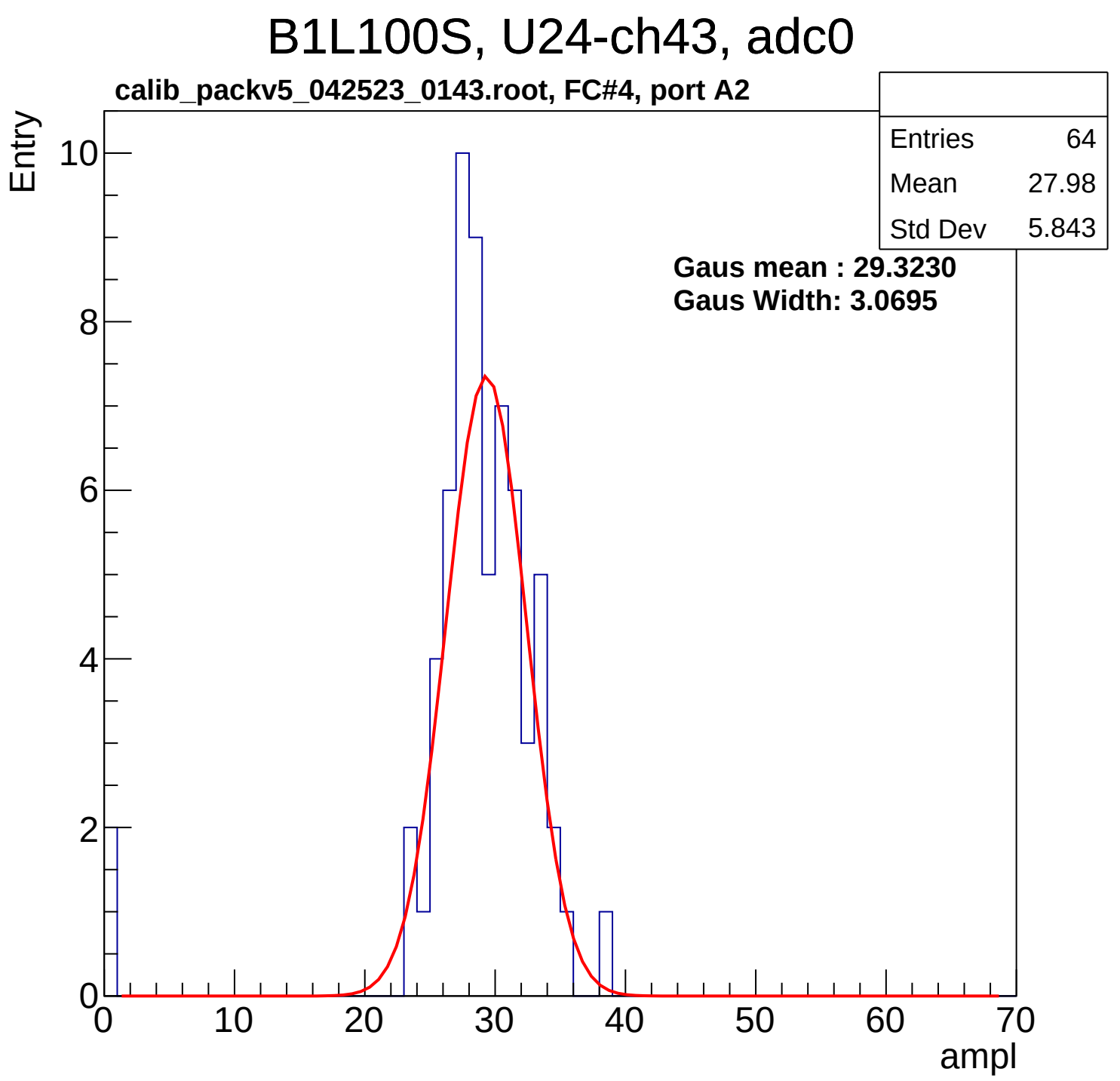
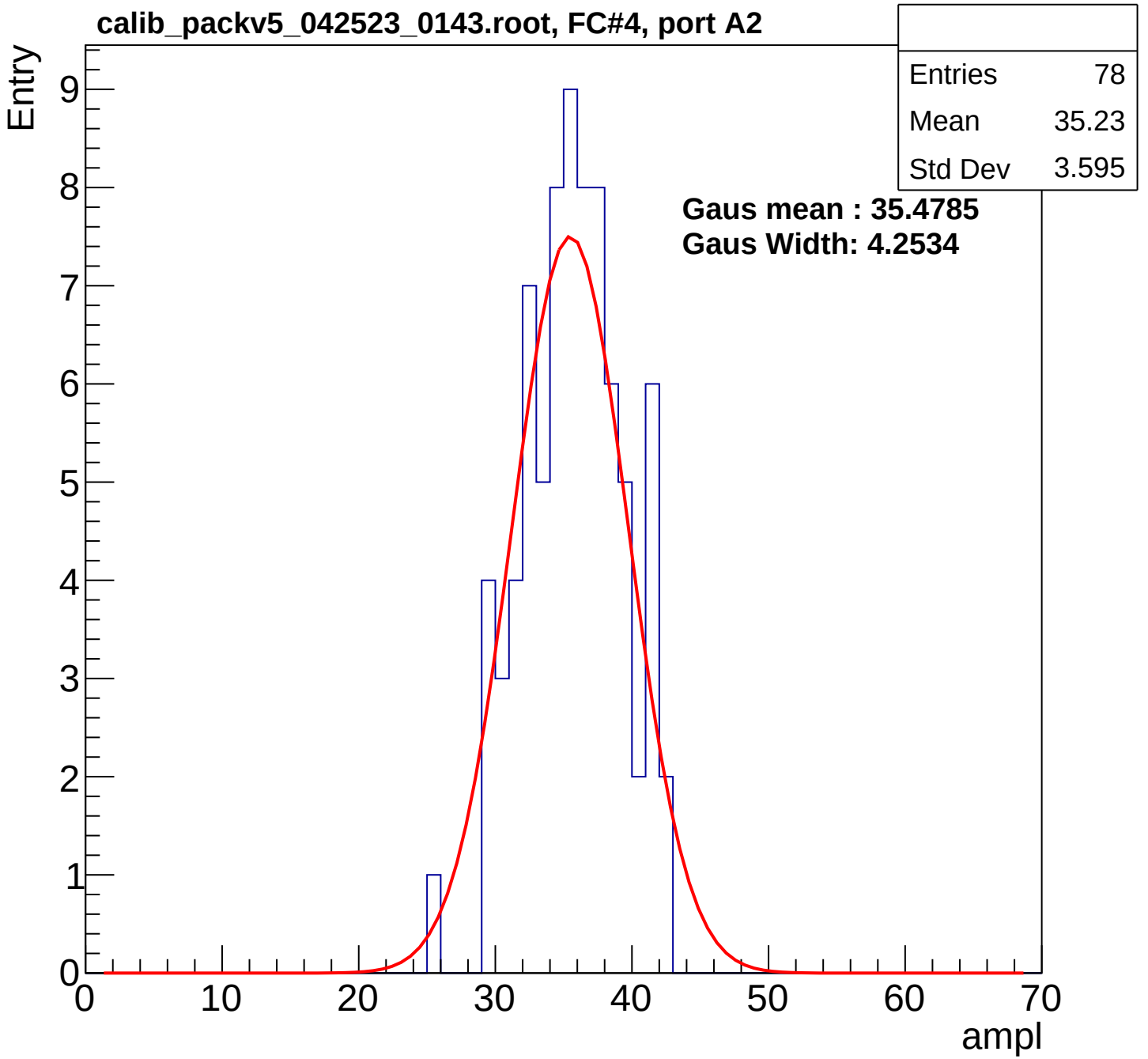

B1L100S, U24-ch42, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.43
Std Dev 21.84
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U24-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch43, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 27.98
Std Dev 5.843
Gaus mean : 29.3230
Gaus Width: 3.0695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 35.23
Std Dev 3.595
Gaus mean : 35.4785
Gaus Width: 4.2534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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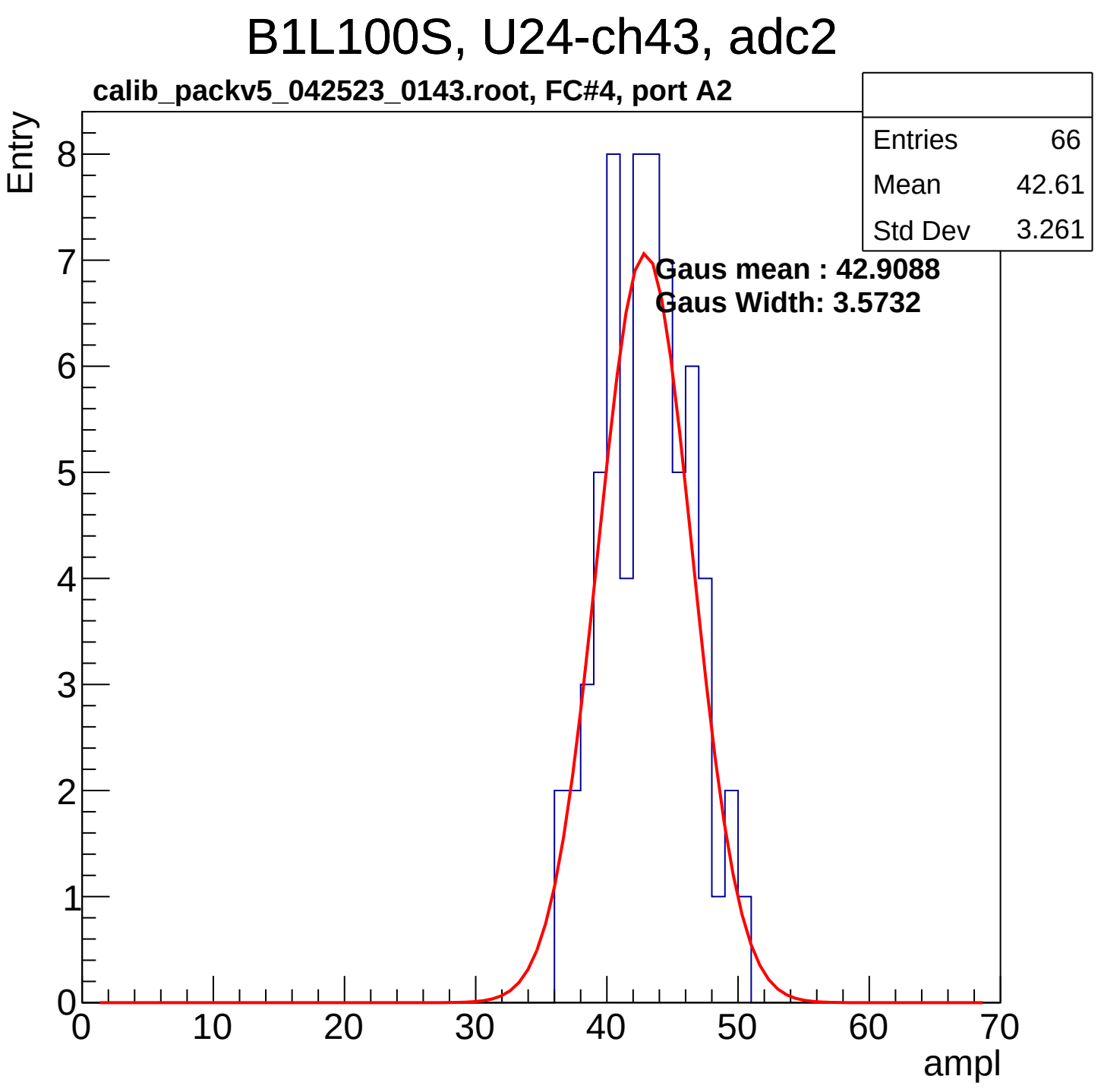
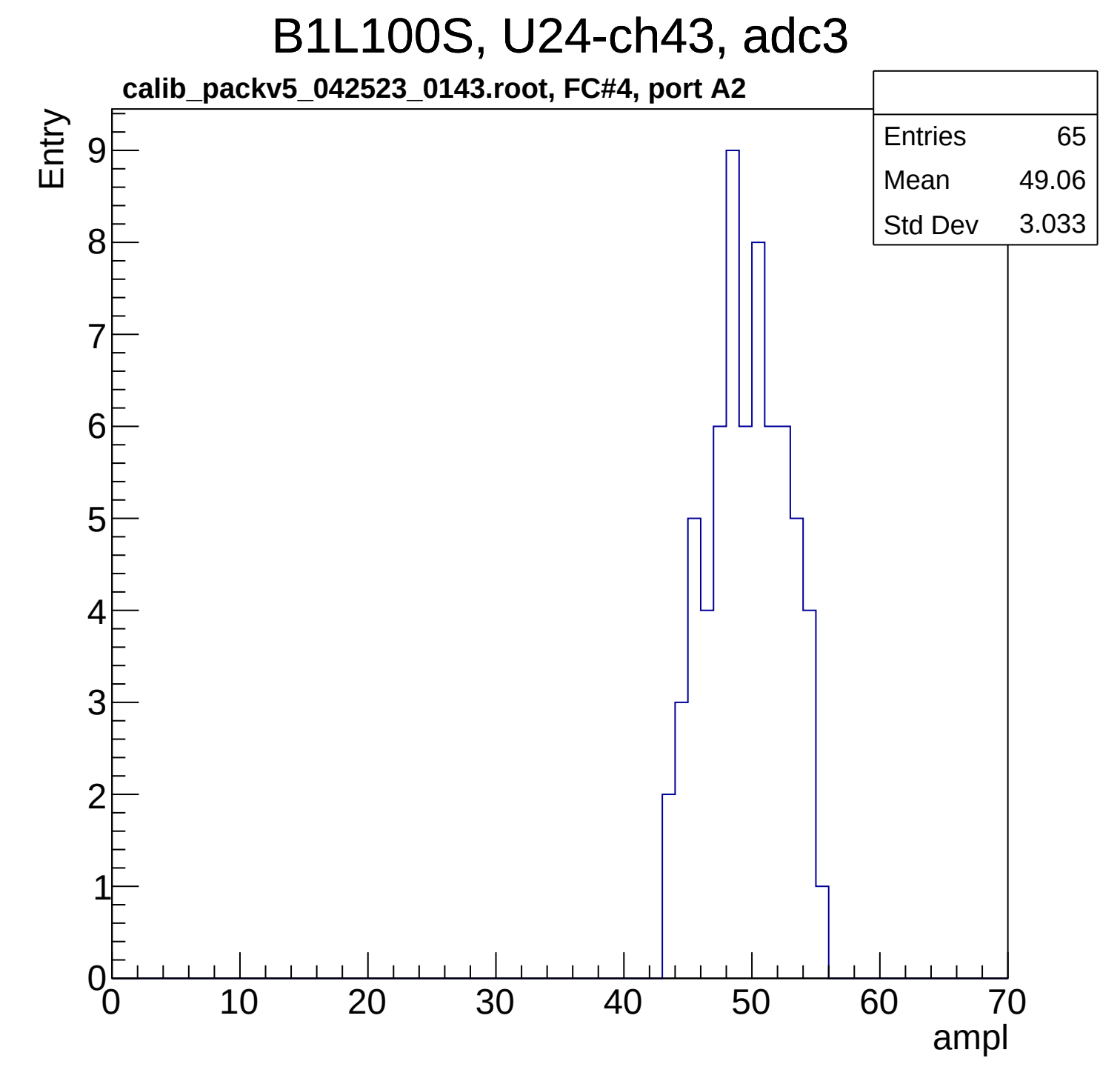

B1L100S, U24-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.61
Std Dev 3.261
Gaus mean : 42.9088
Gaus Width: 3.5732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.06
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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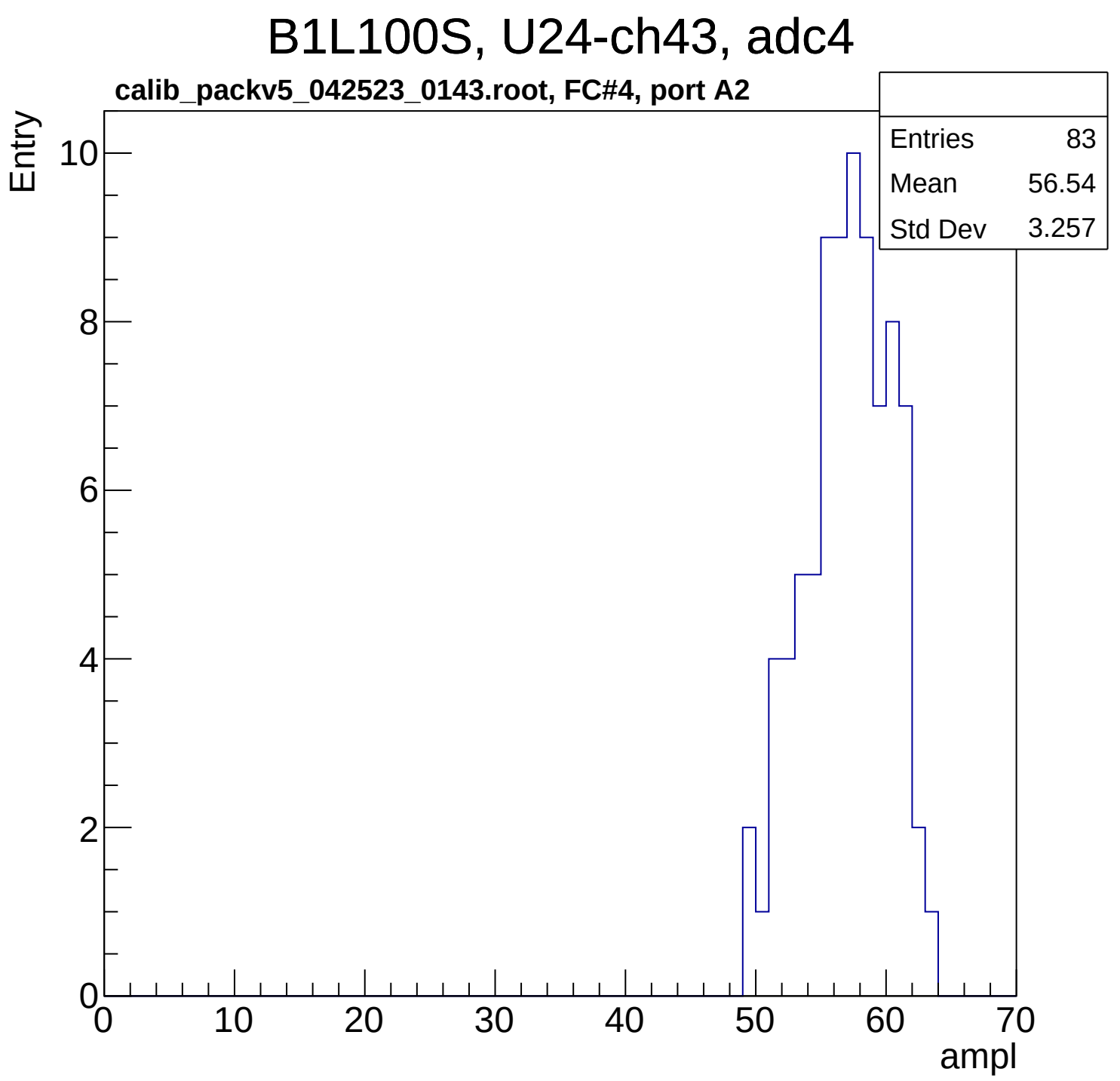
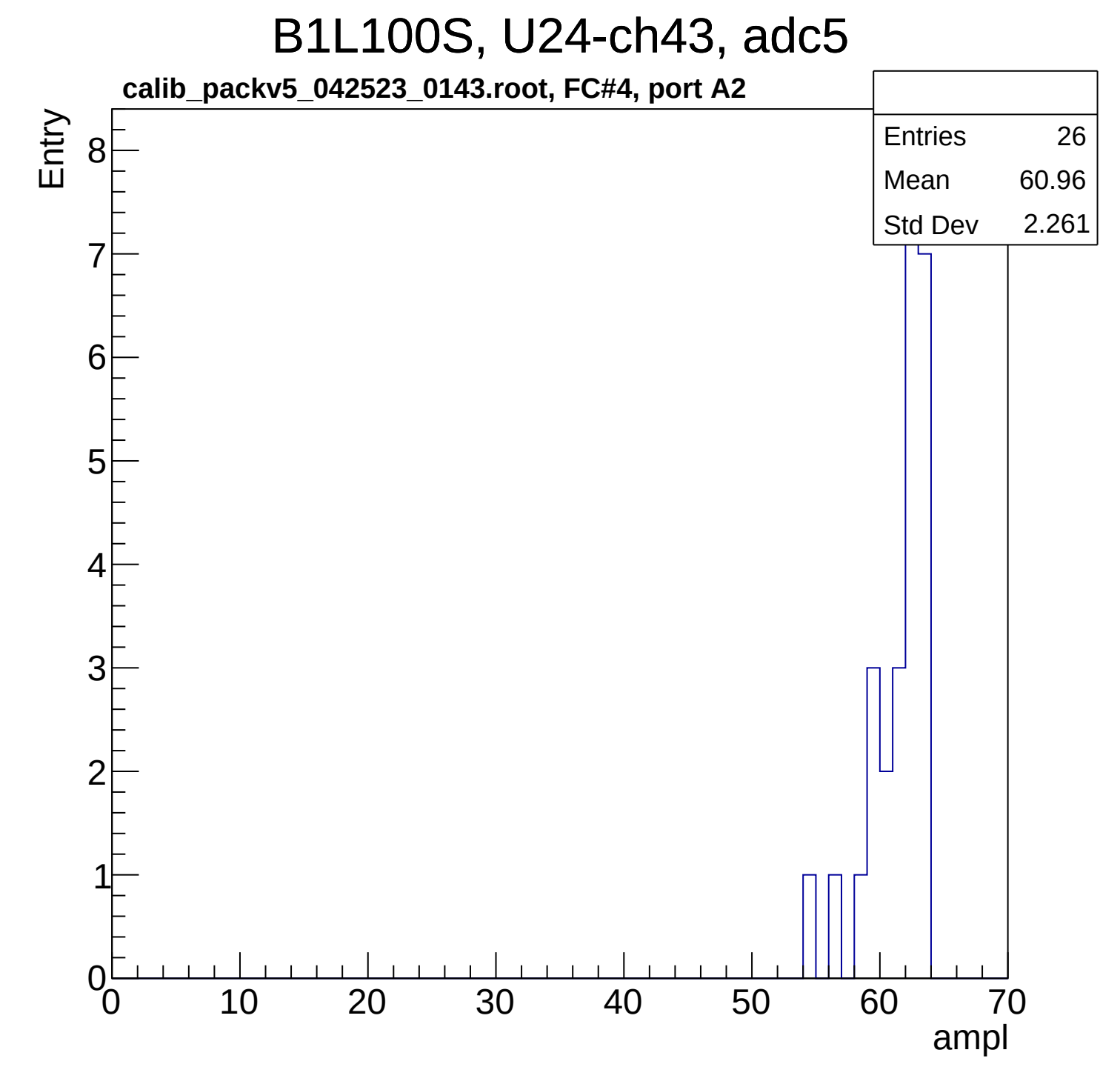
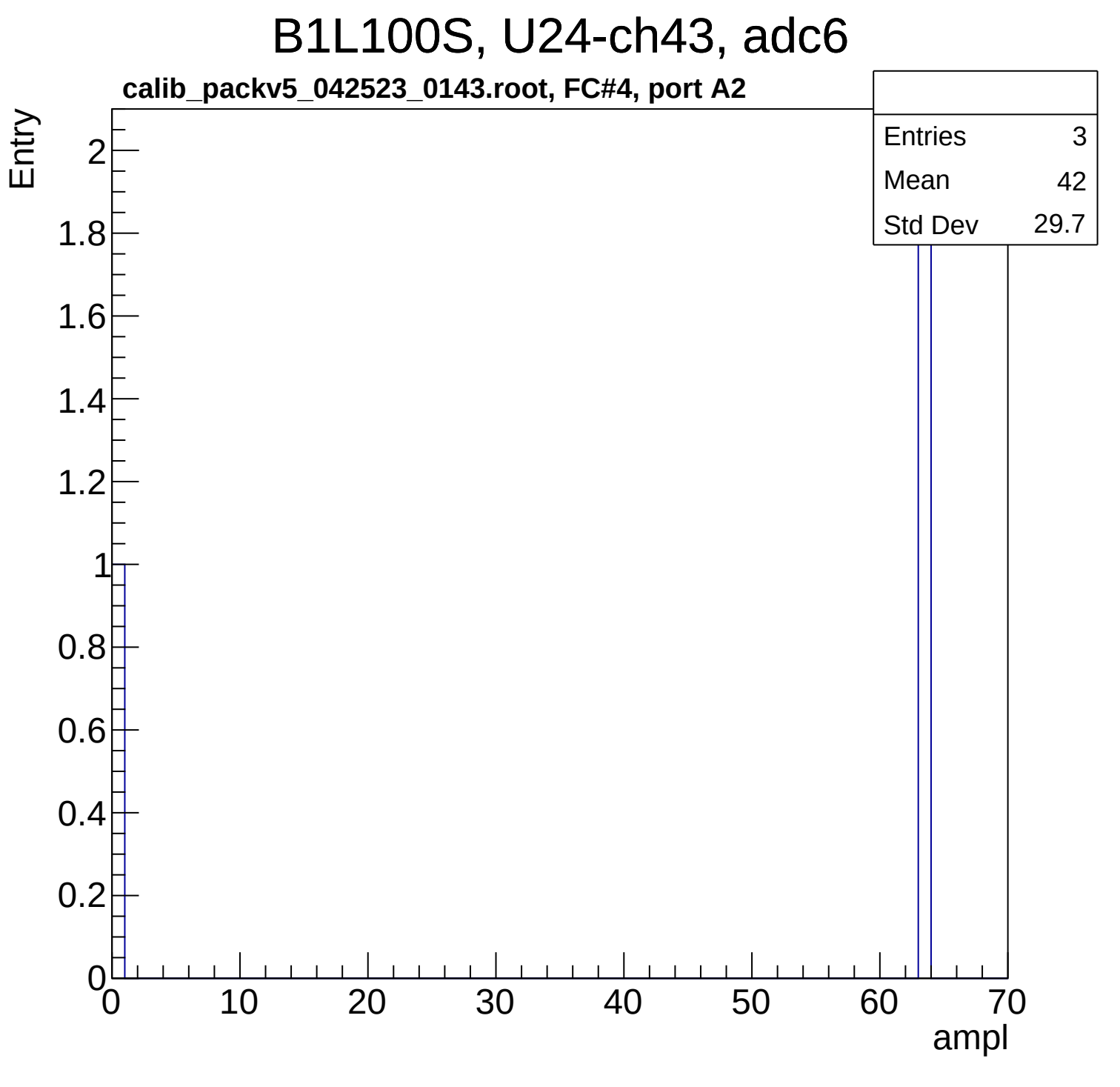
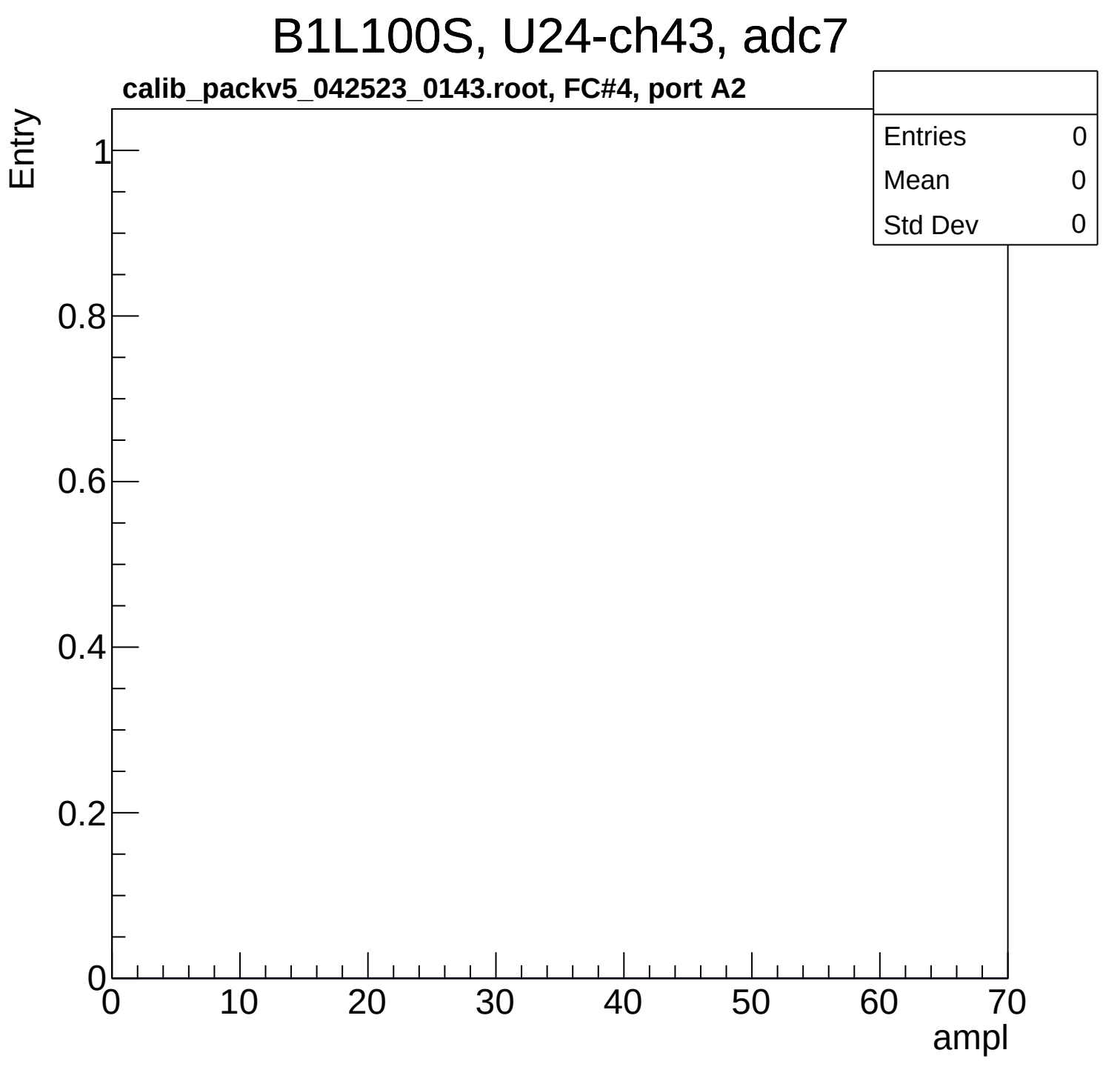
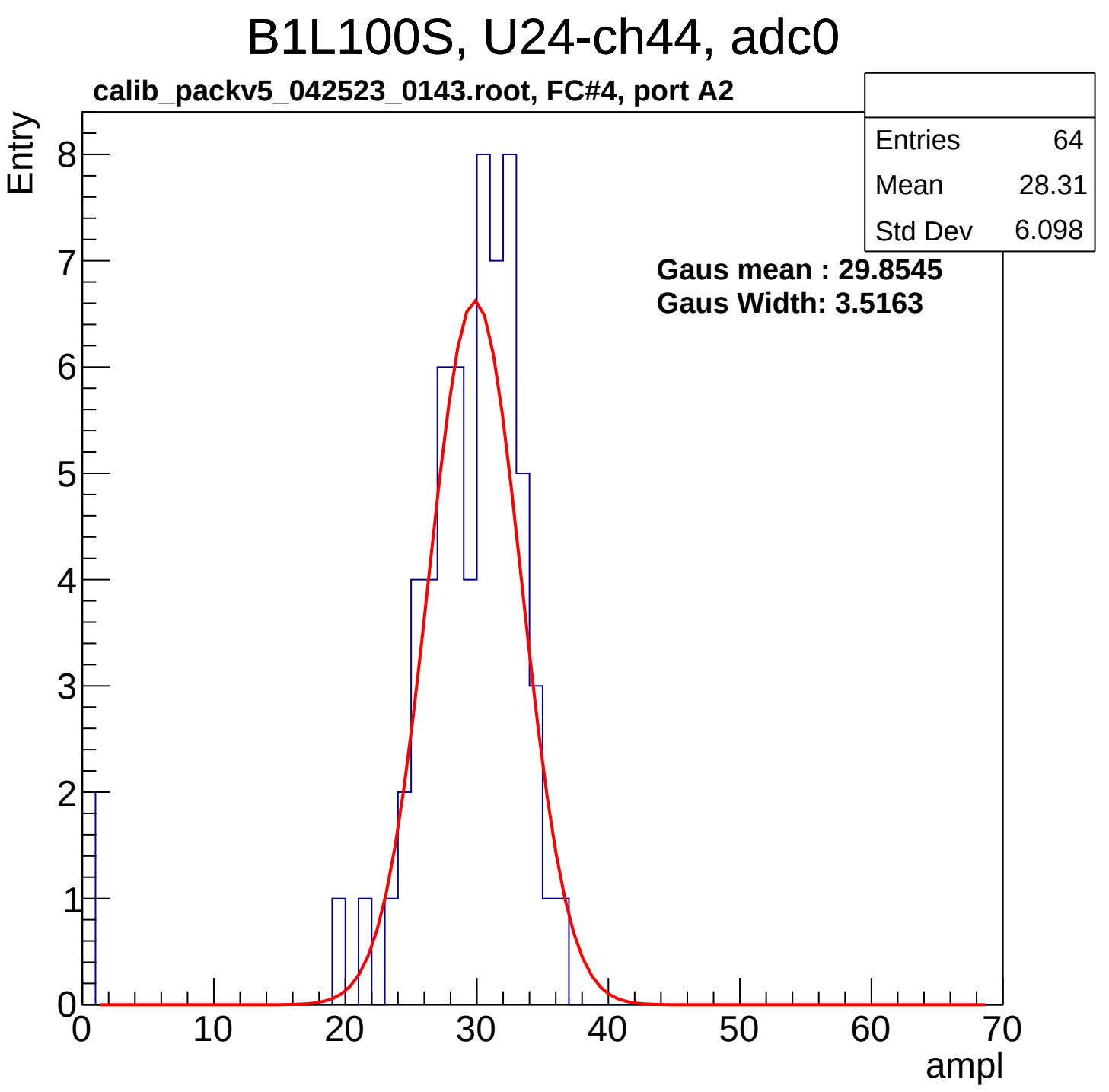
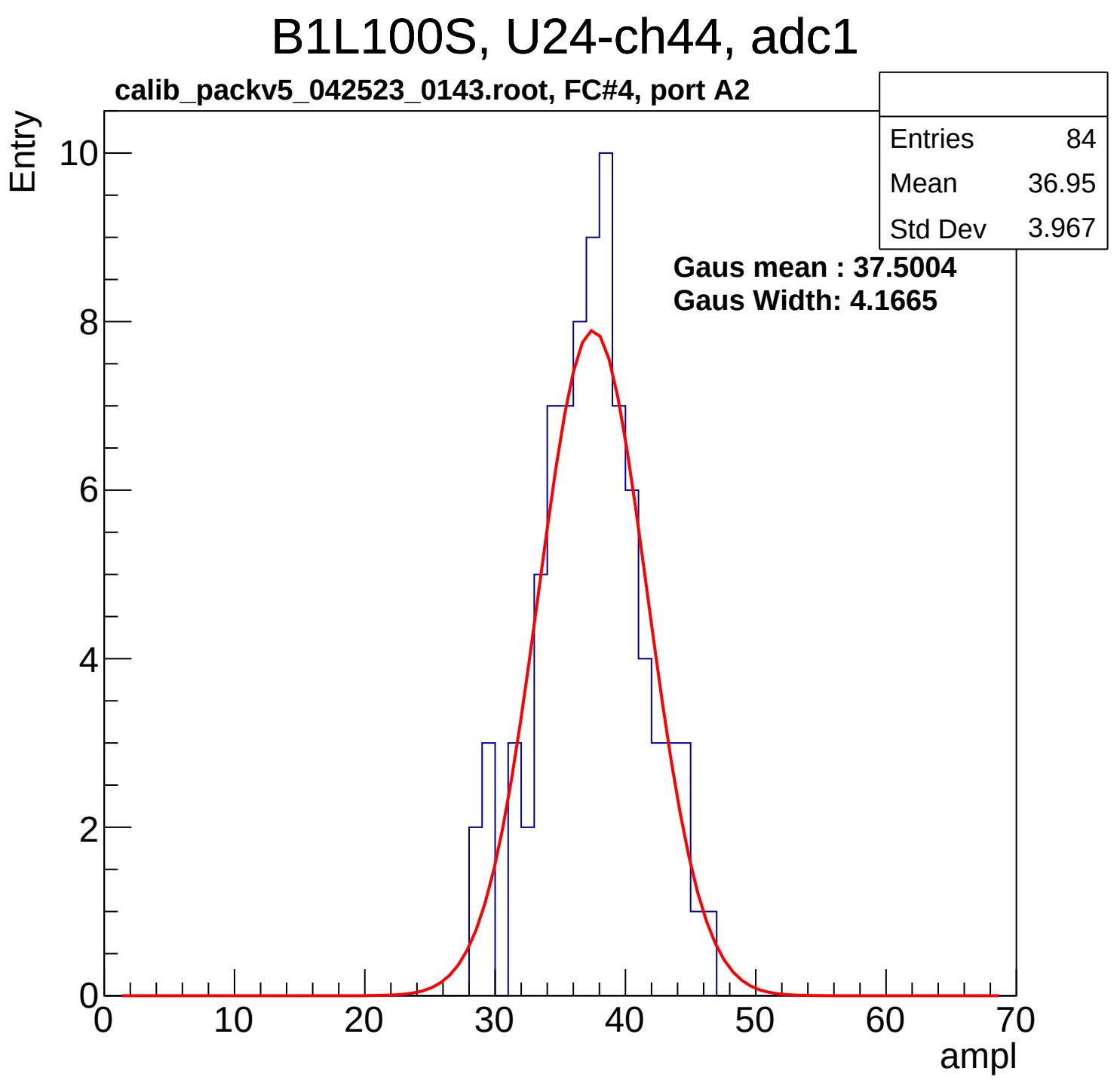

B1L100S, U24-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 56.54
Std Dev 3.257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

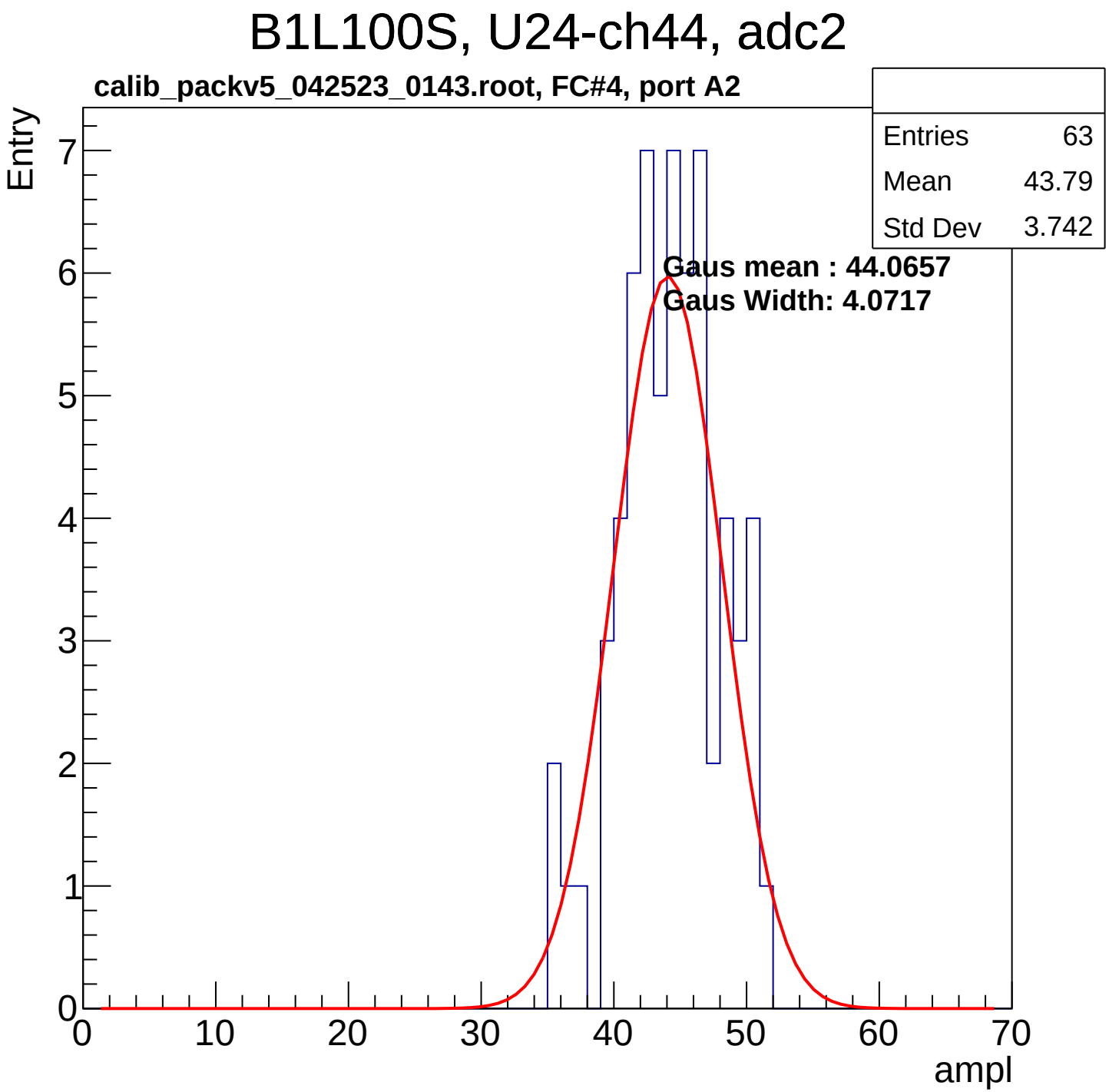

B1L100S, U24-ch43, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 60.96
Std Dev 2.261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

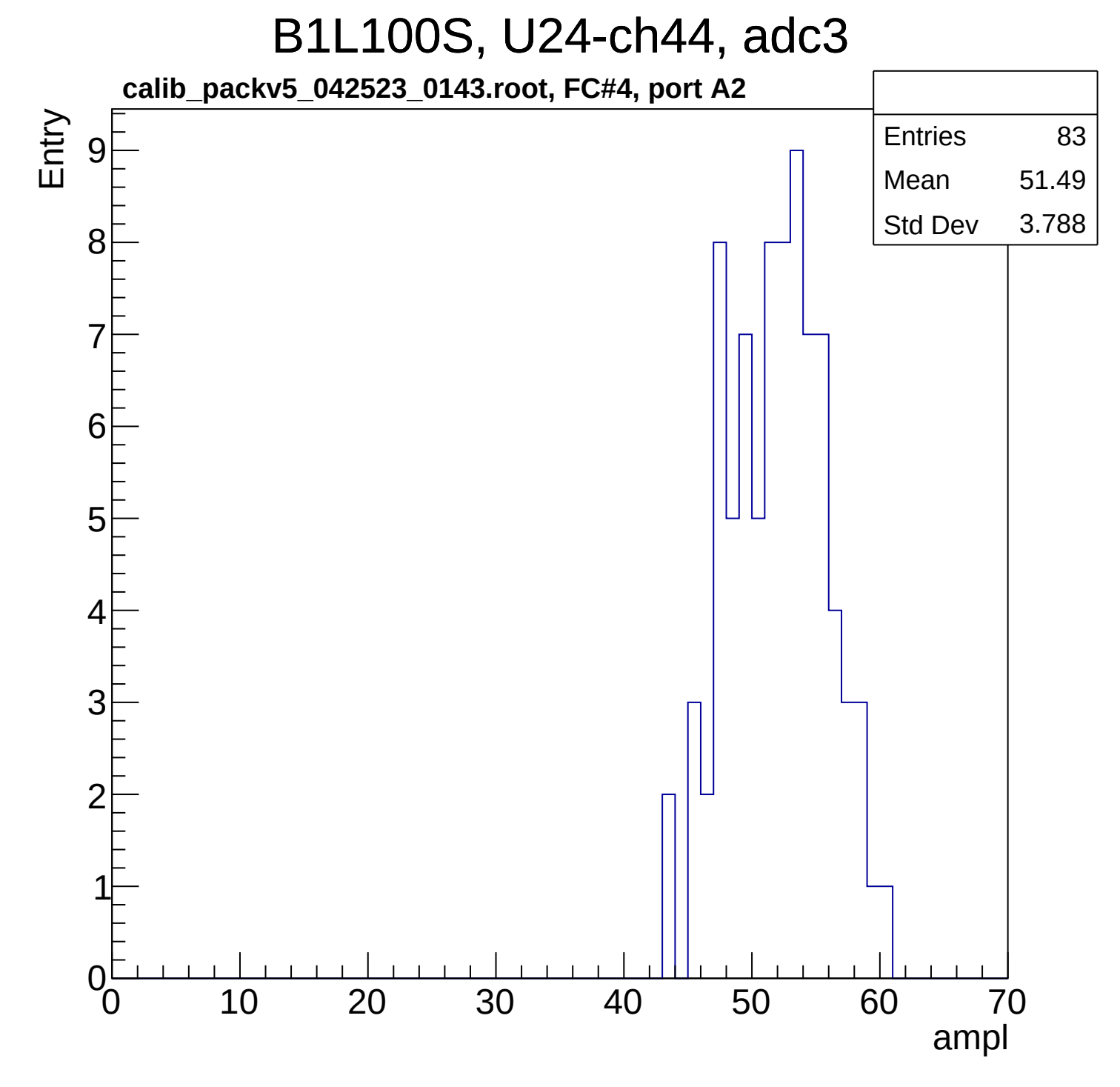

B1L100S, U24-ch43, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

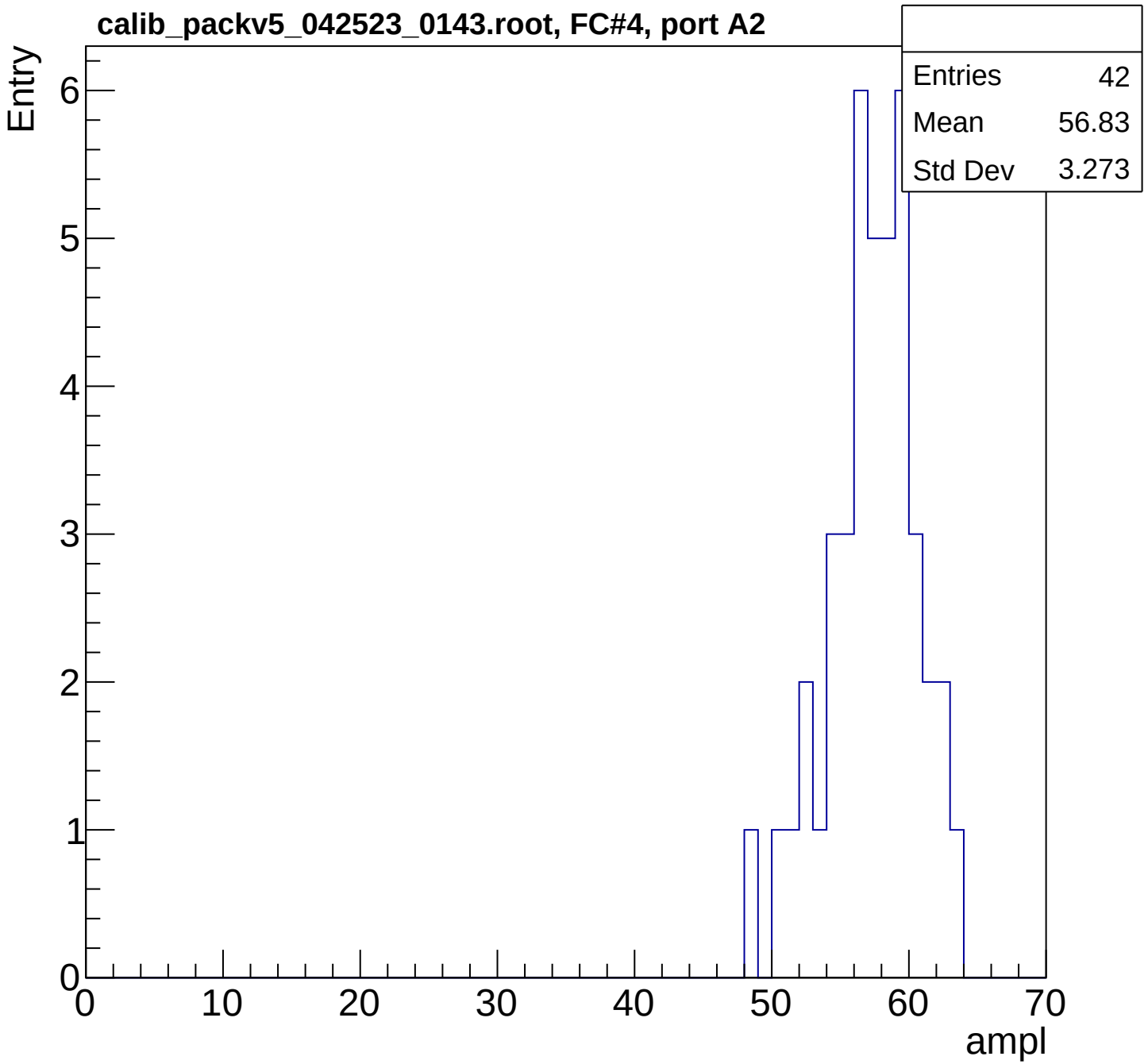

B1L100S, U24-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

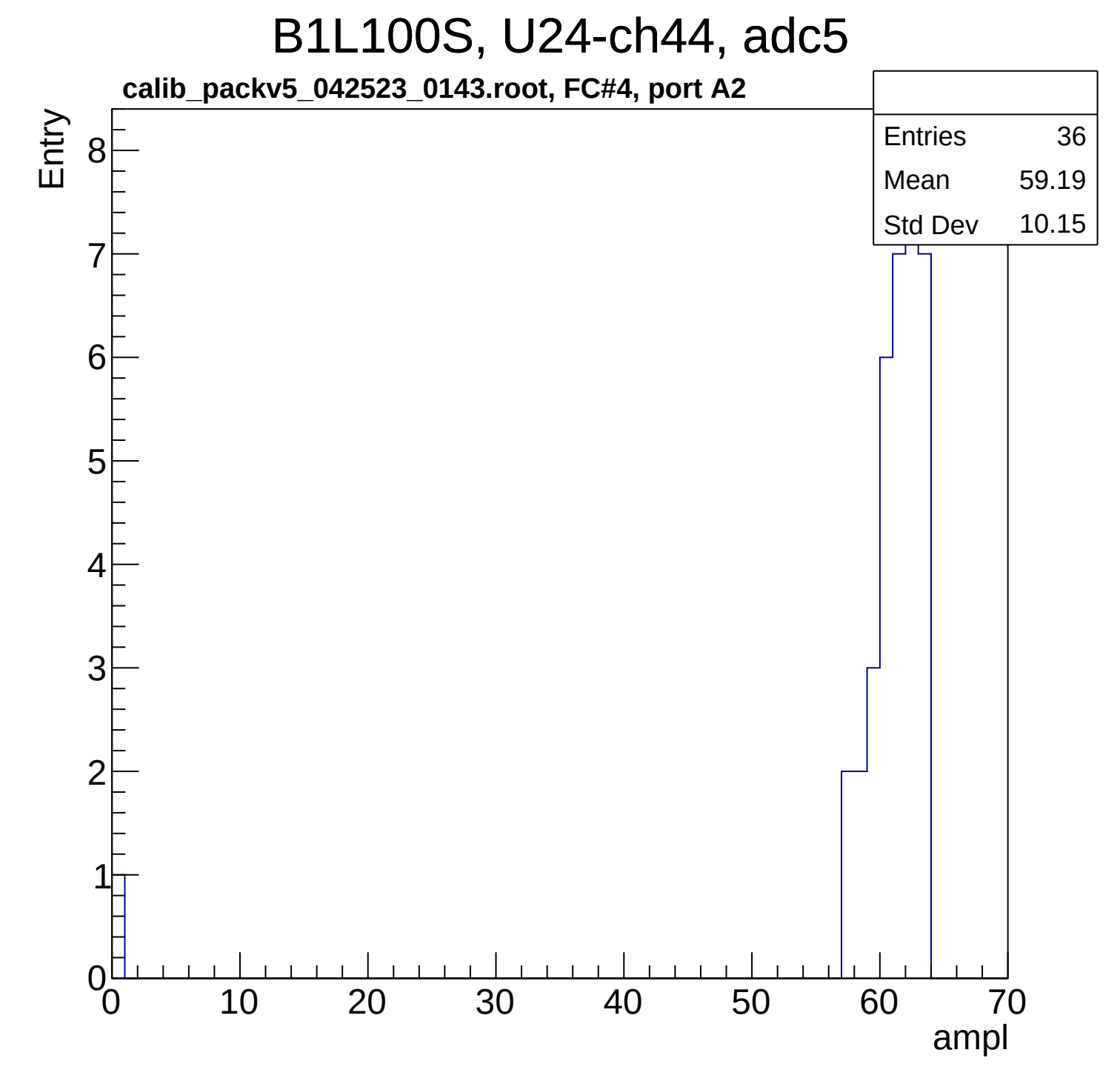

B1L100S, U24-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 28.31
Std Dev 6.098
Gaus mean : 29.8545
Gaus Width: 3.5163
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

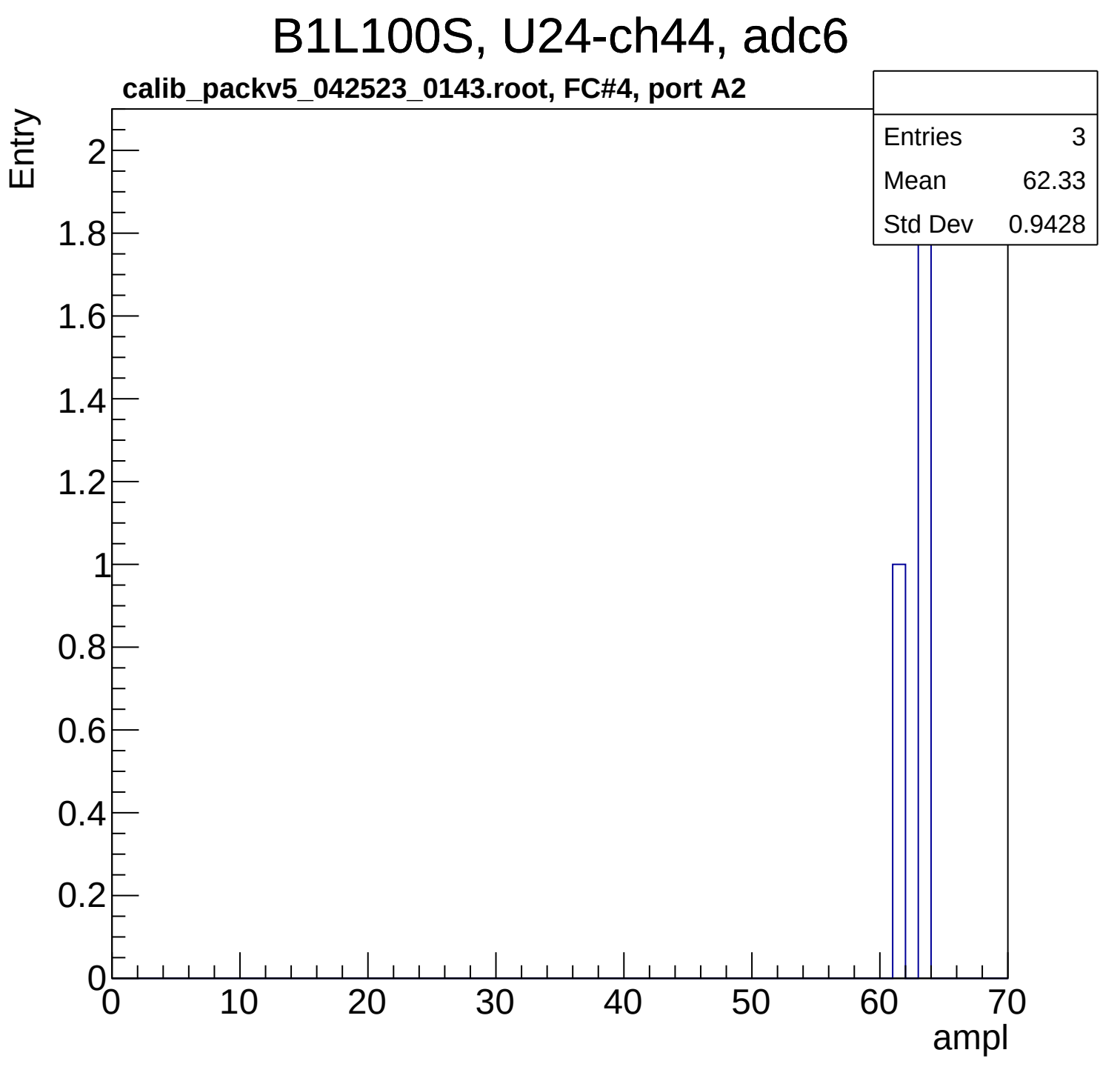

B1L100S, U24-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 36.95
Std Dev 3.967
Gaus mean : 37.5004
Gaus Width: 4.1665
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.79
Std Dev 3.742
Gaus mean : 44.0657
Gaus Width: 4.0717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch44, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 51.49
Std Dev 3.788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 56.83
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.19
Std Dev 10.15
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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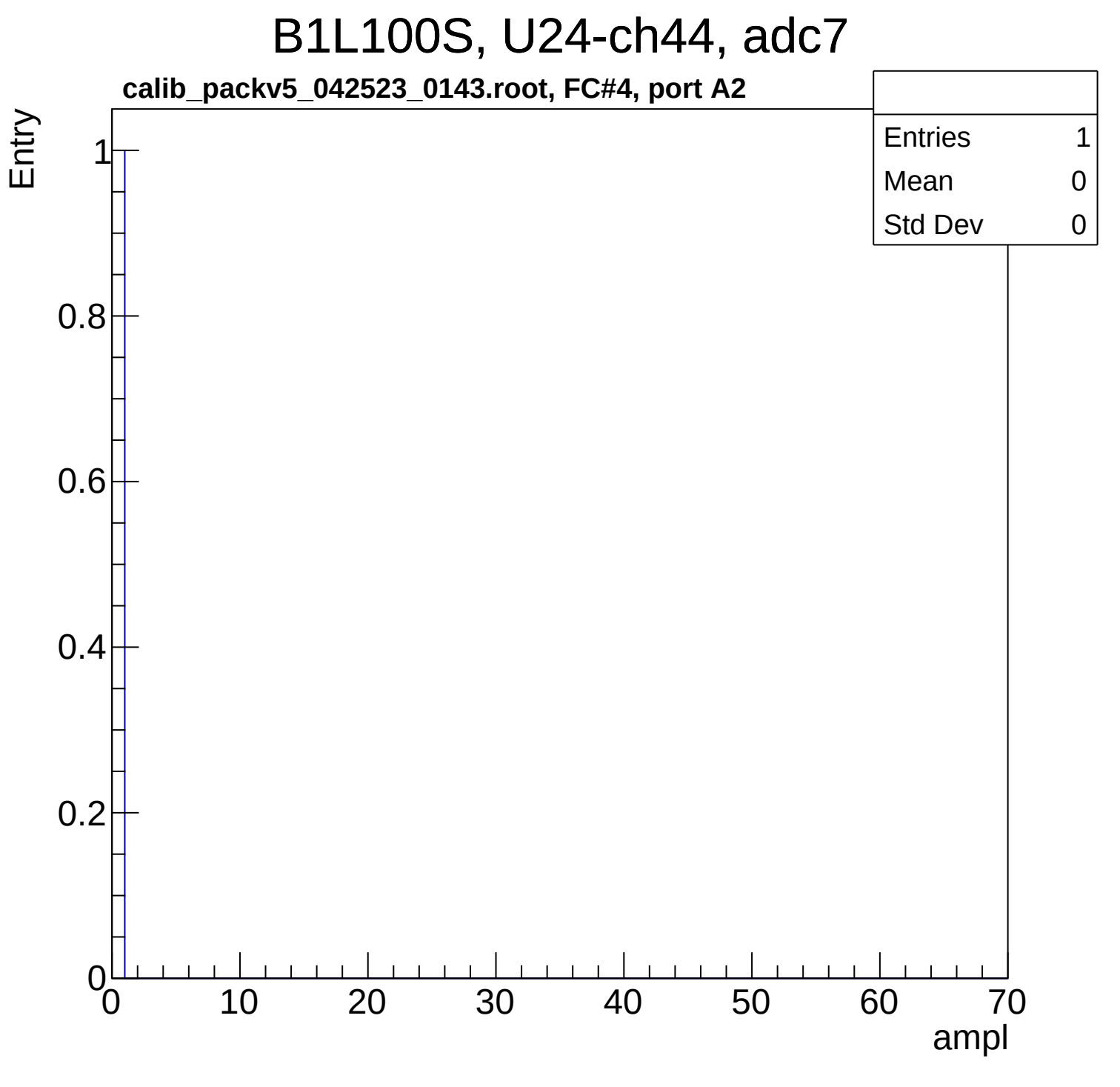
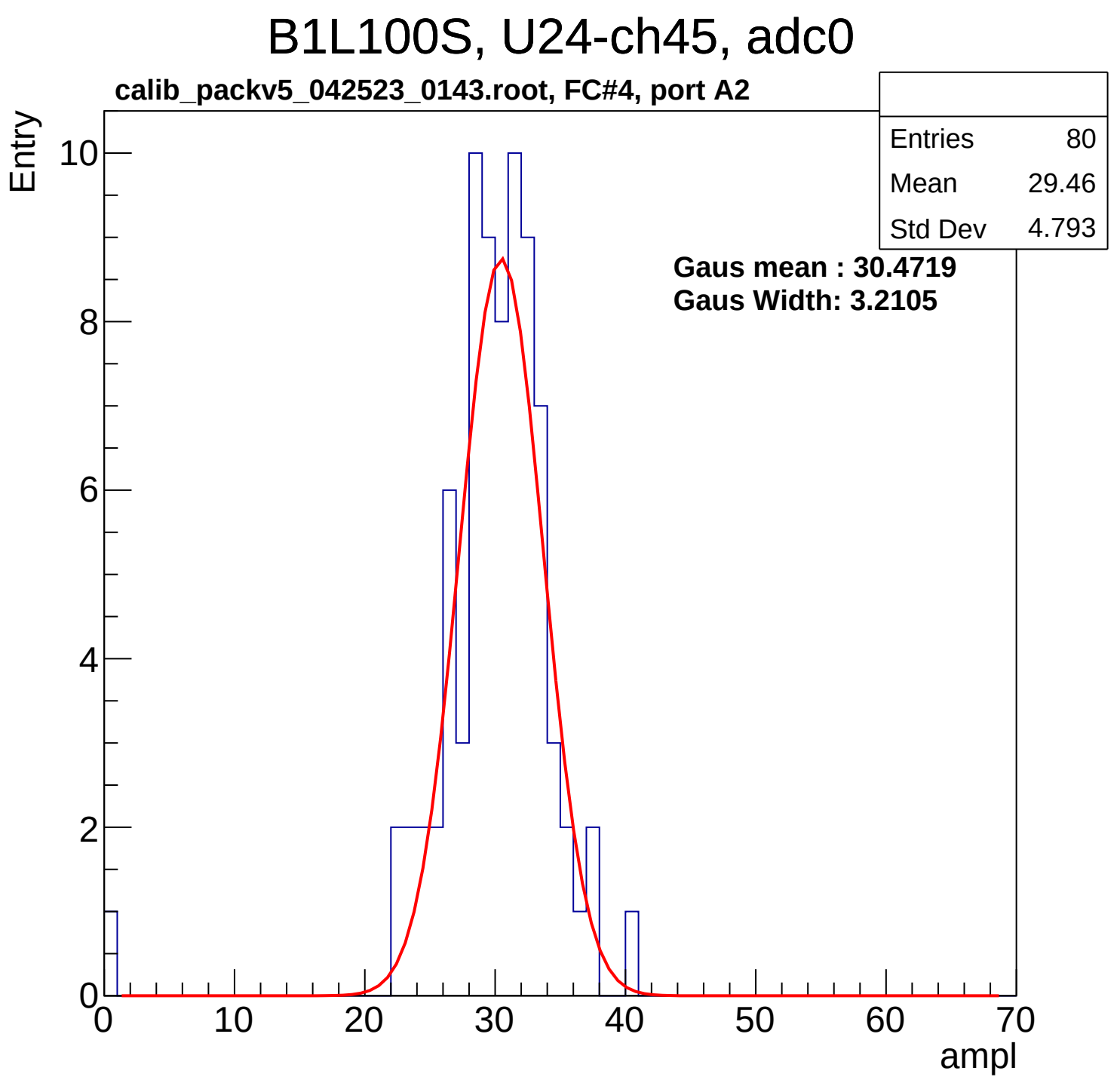
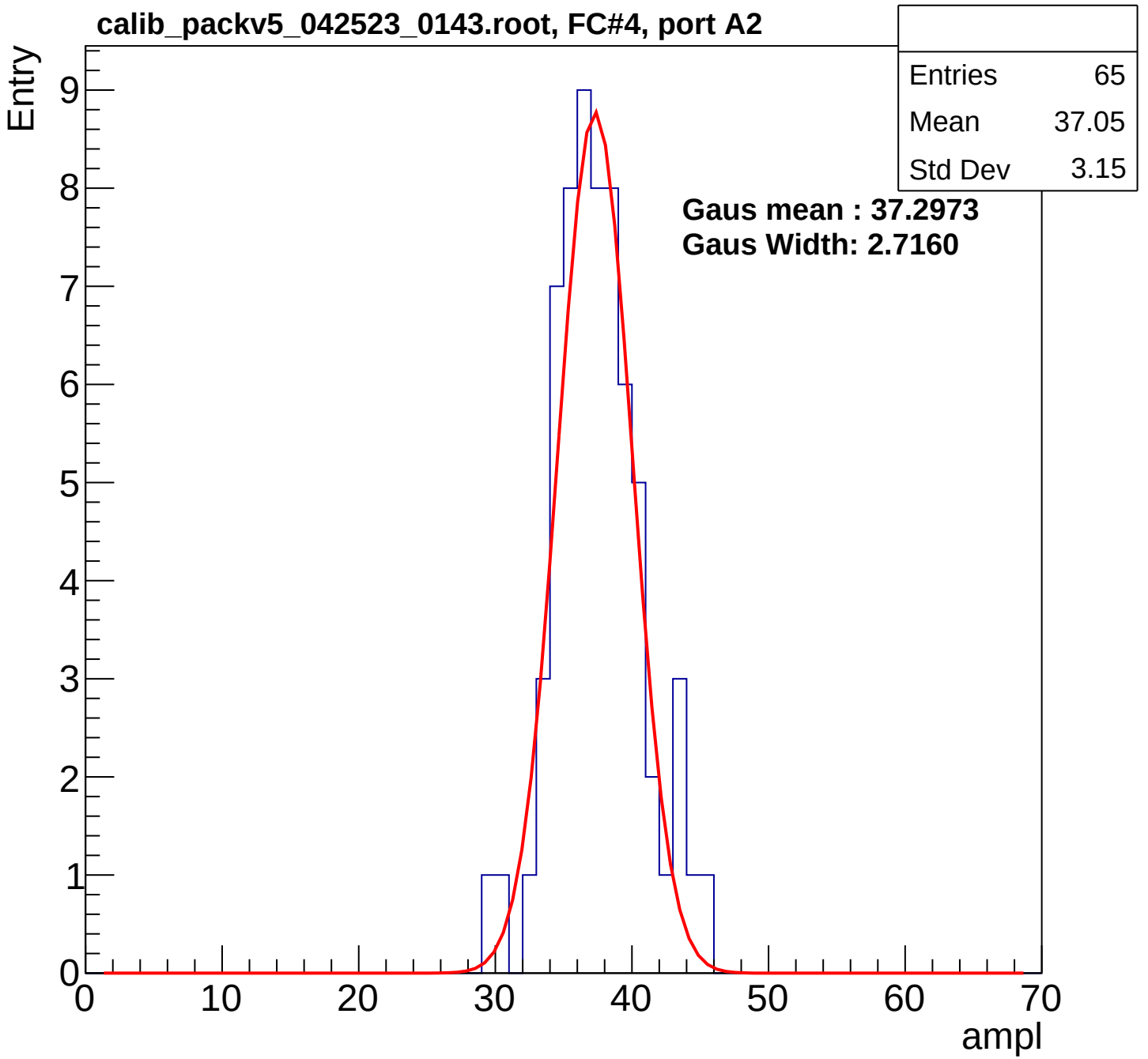
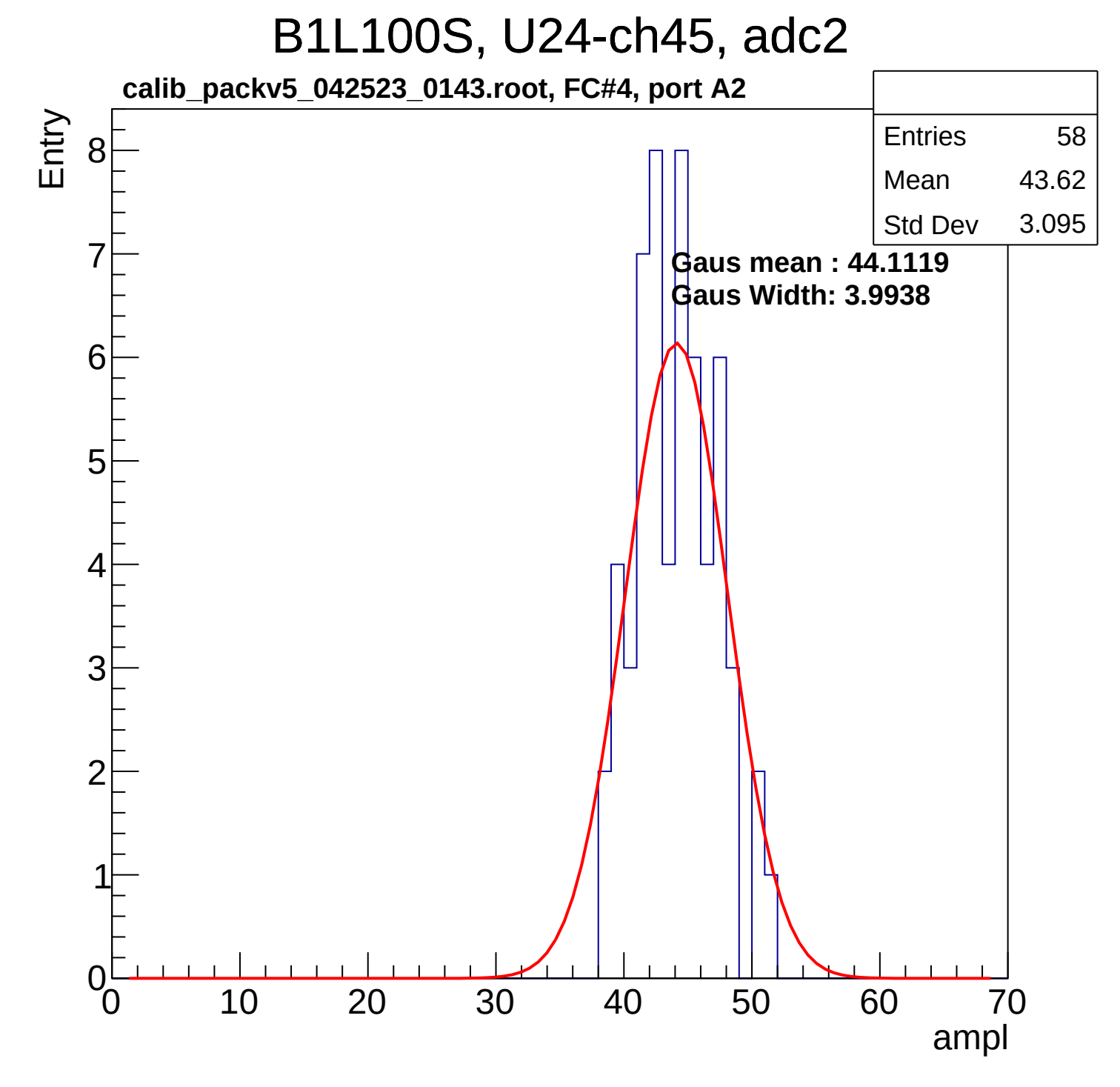
20
30
40
50
60
70


B1L100S, U24-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.46
Std Dev 4.793
Gaus mean : 30.4719
Gaus Width: 3.2105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch45, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 37.05
Std Dev 3.15
Gaus mean : 37.2973
Gaus Width: 2.7160
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch45, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.62
Std Dev 3.095
Gaus mean : 44.1119
Gaus Width: 3.9938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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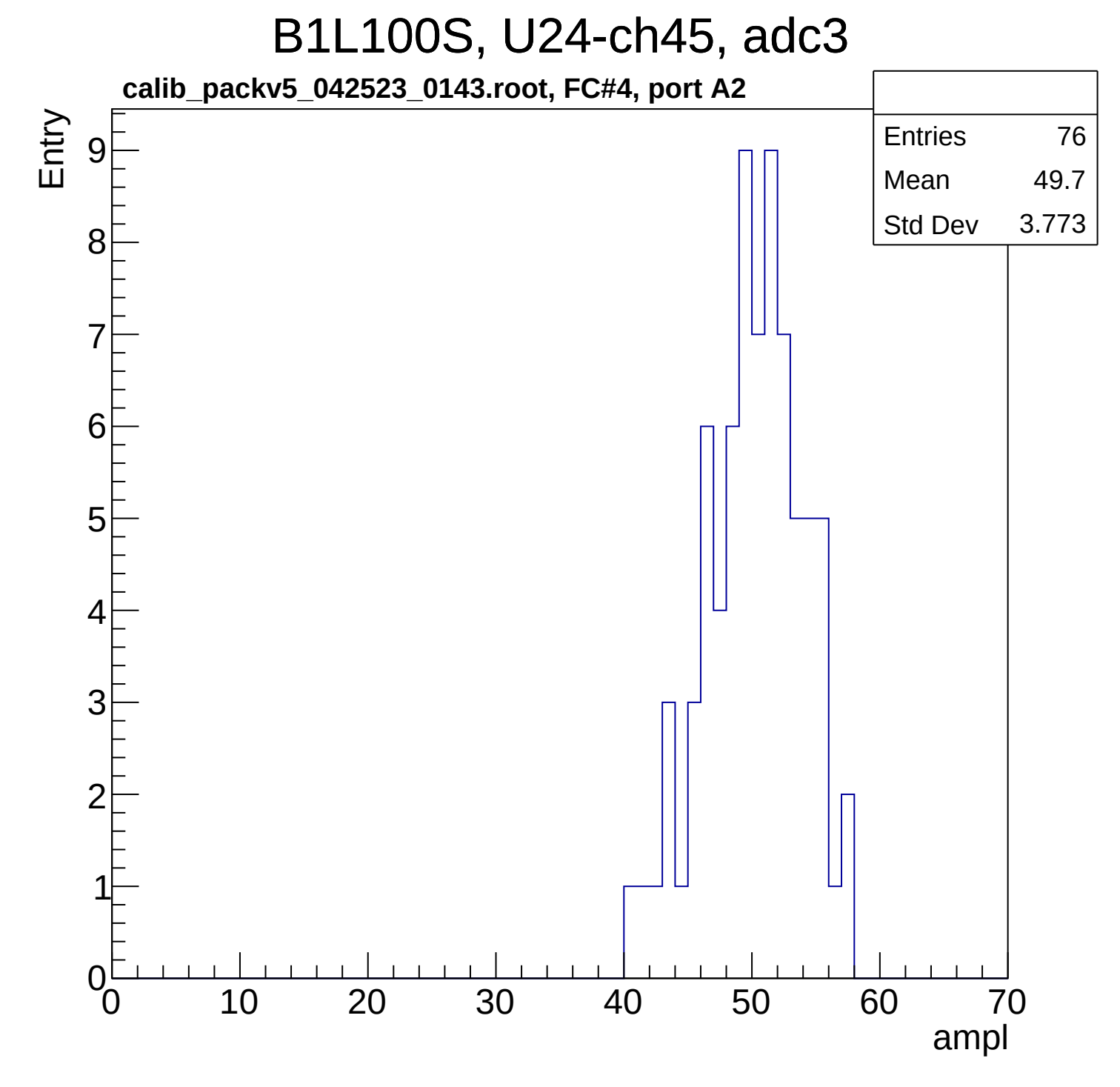
B1L100S, U24-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.7
Std Dev 3.773
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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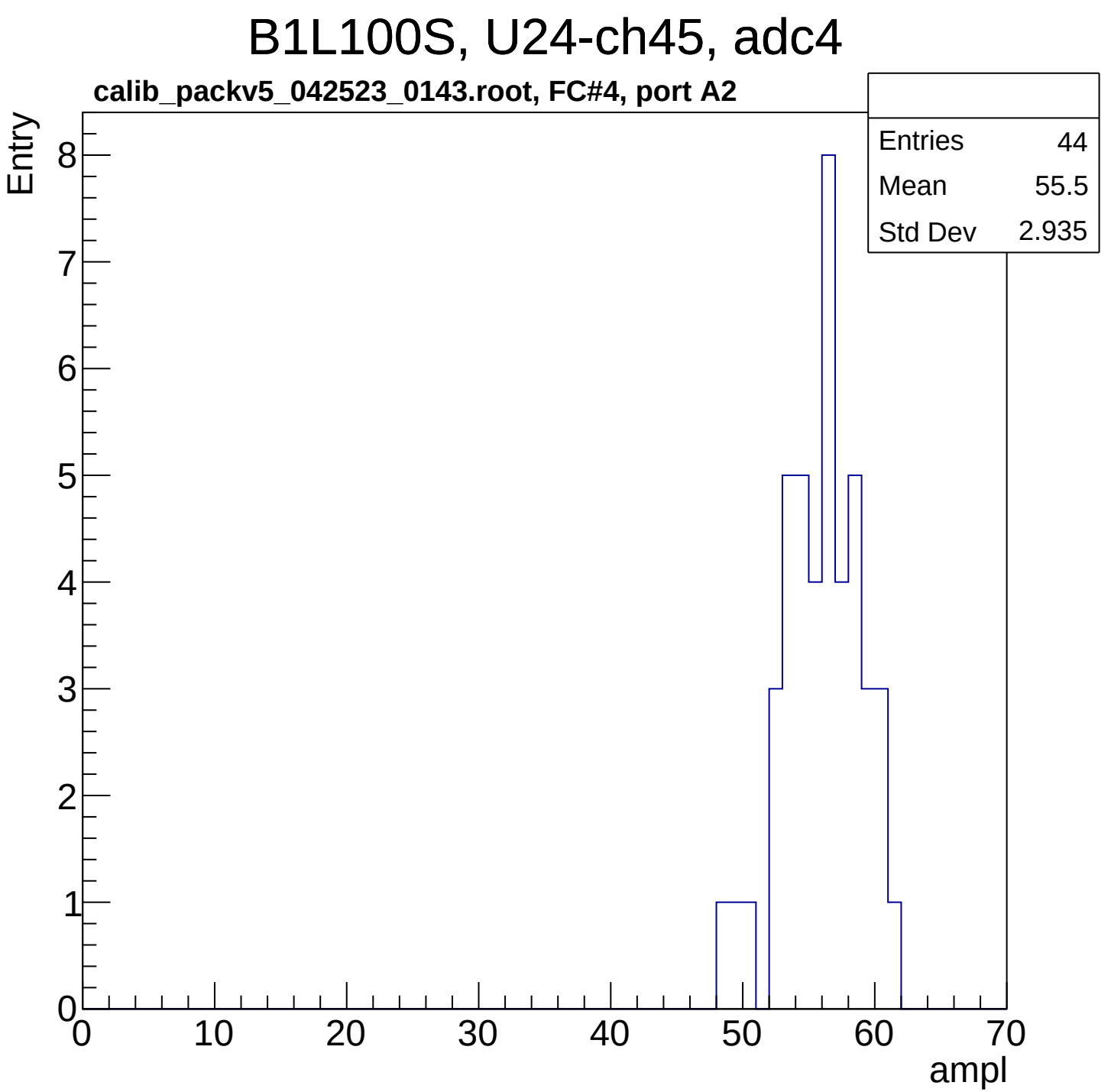
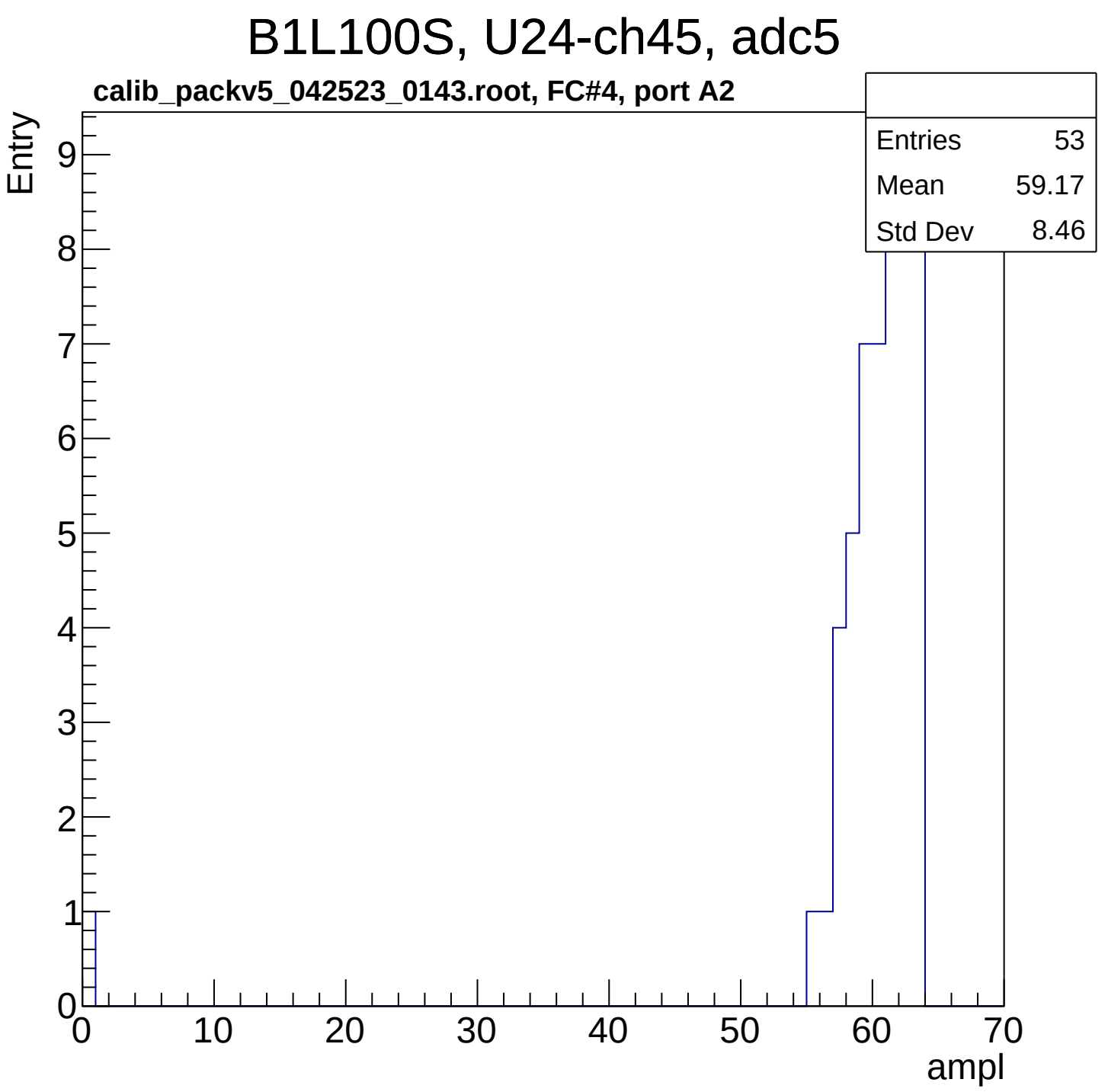
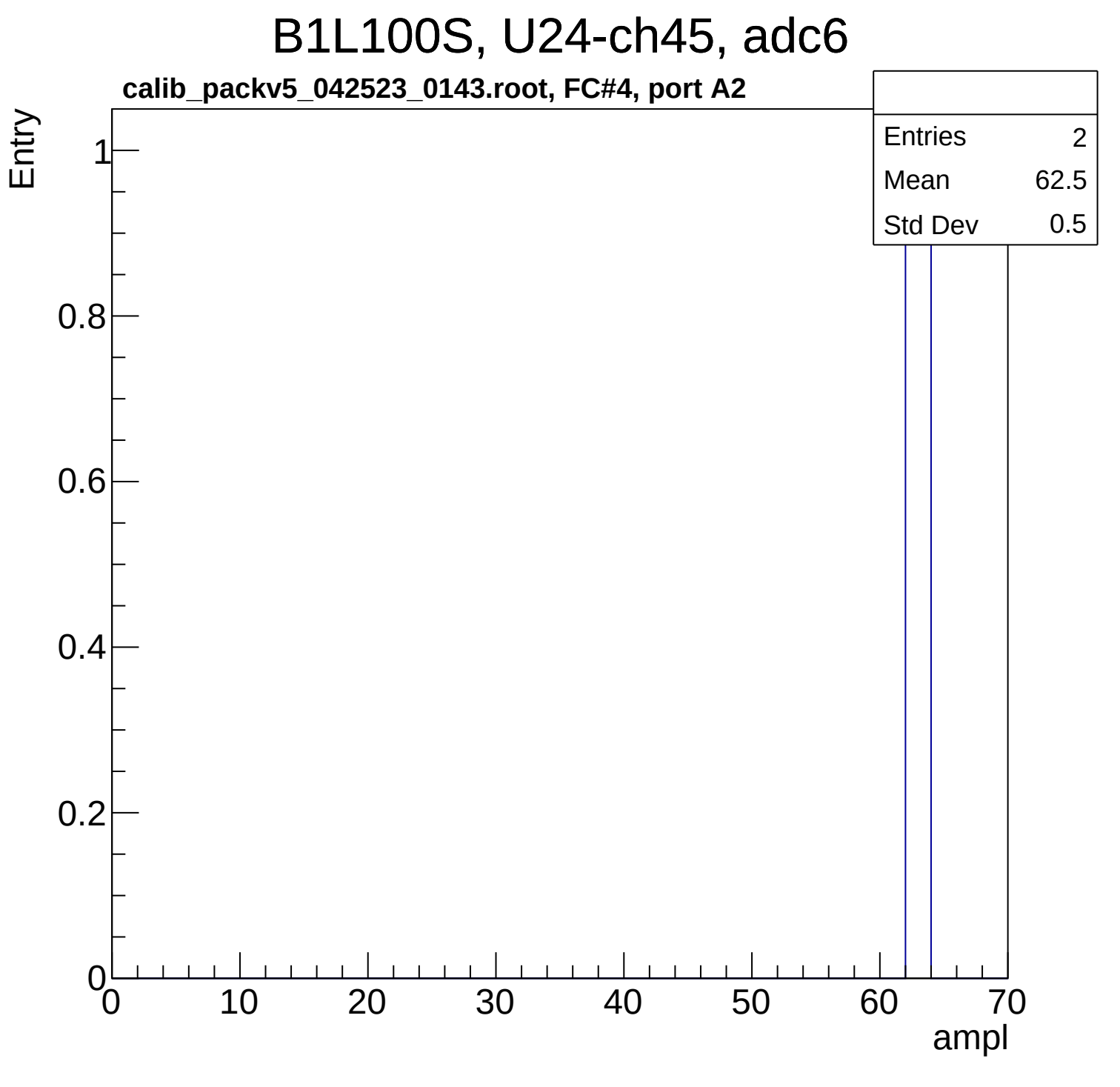
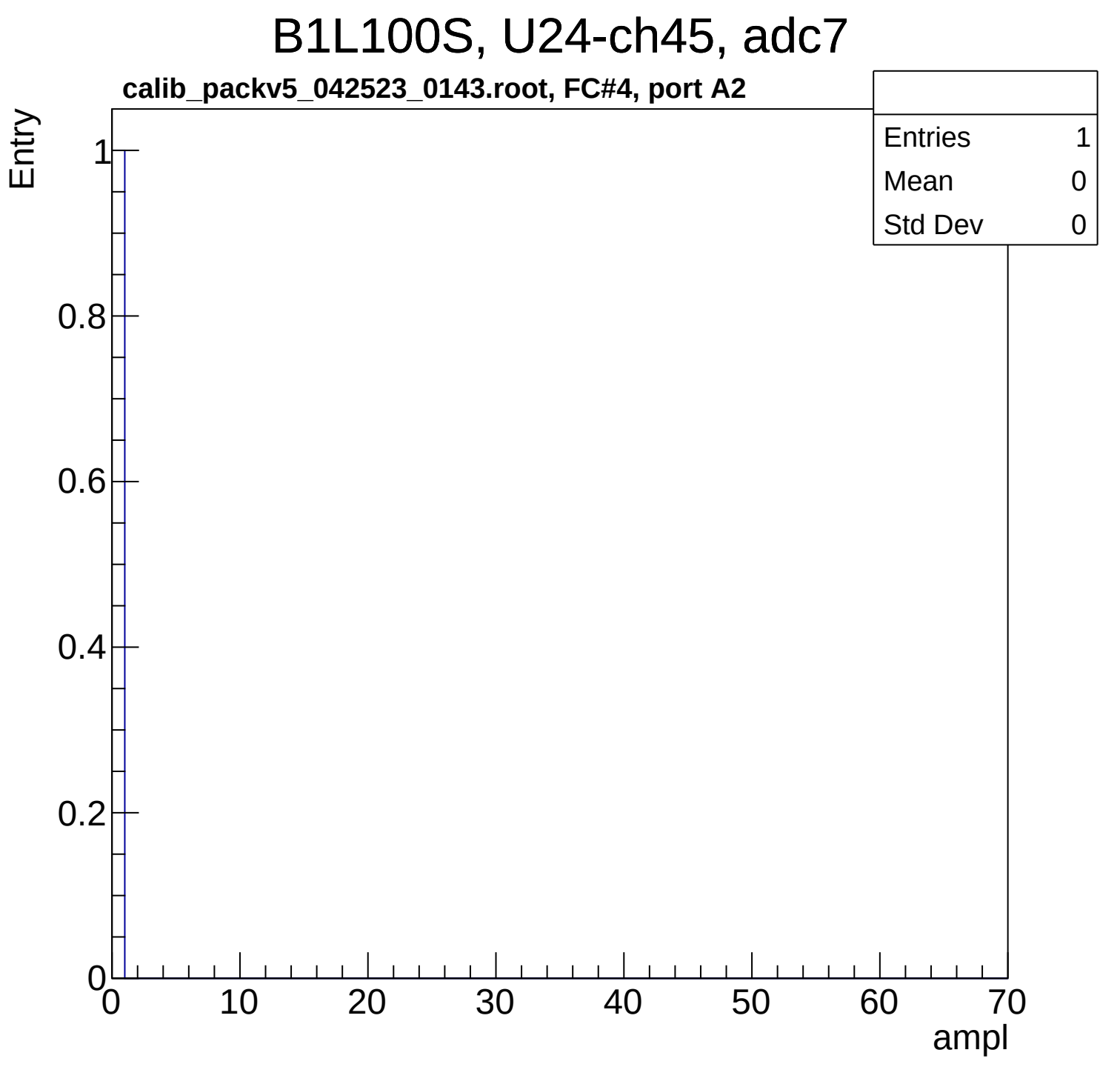
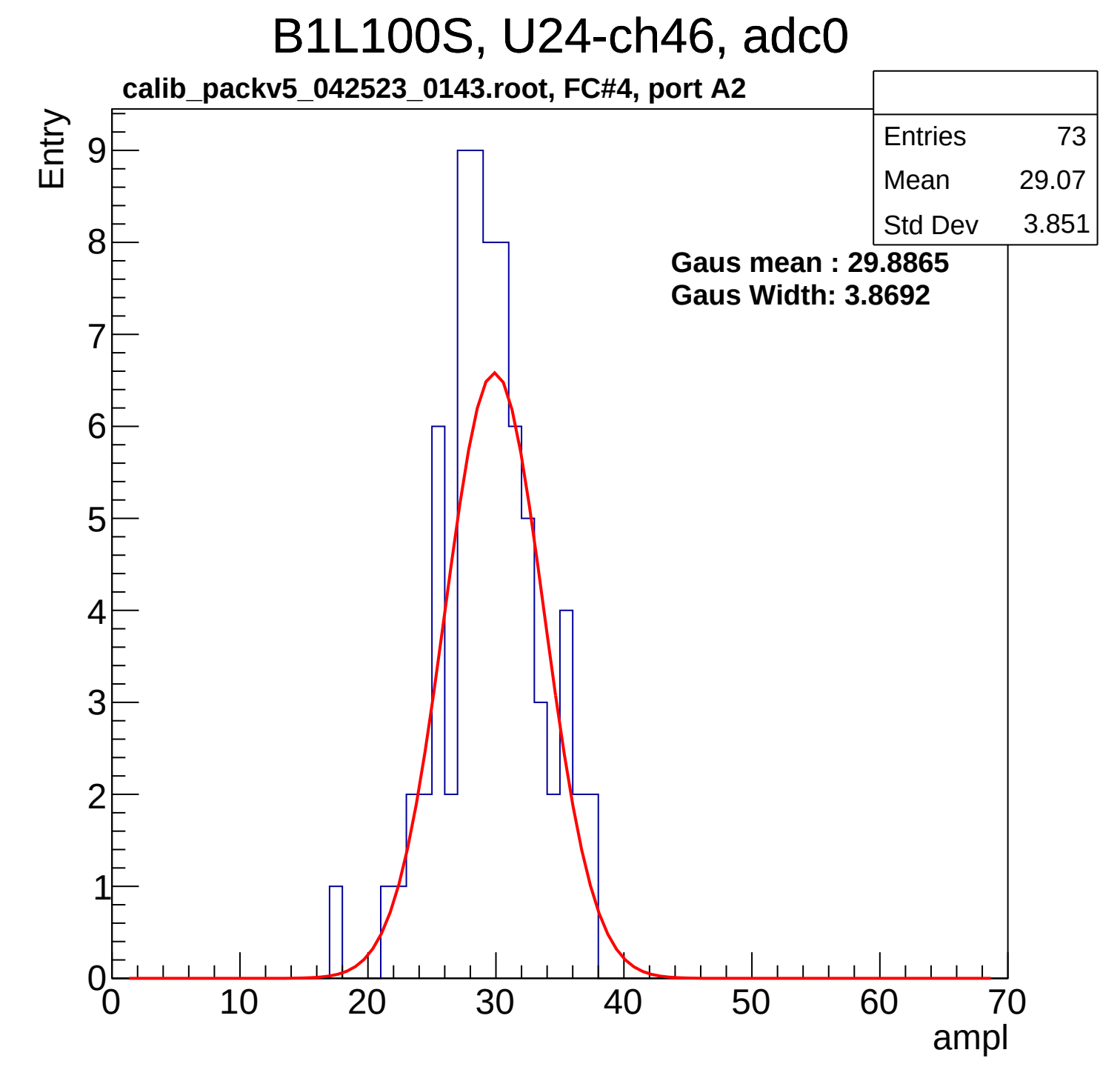

B1L100S, U24-ch45, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 55.5
Std Dev 2.935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 59.17
Std Dev 8.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch45, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch45, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 29.07
Std Dev 3.851
Gaus mean : 29.8865
Gaus Width: 3.8692
Entry
ampl
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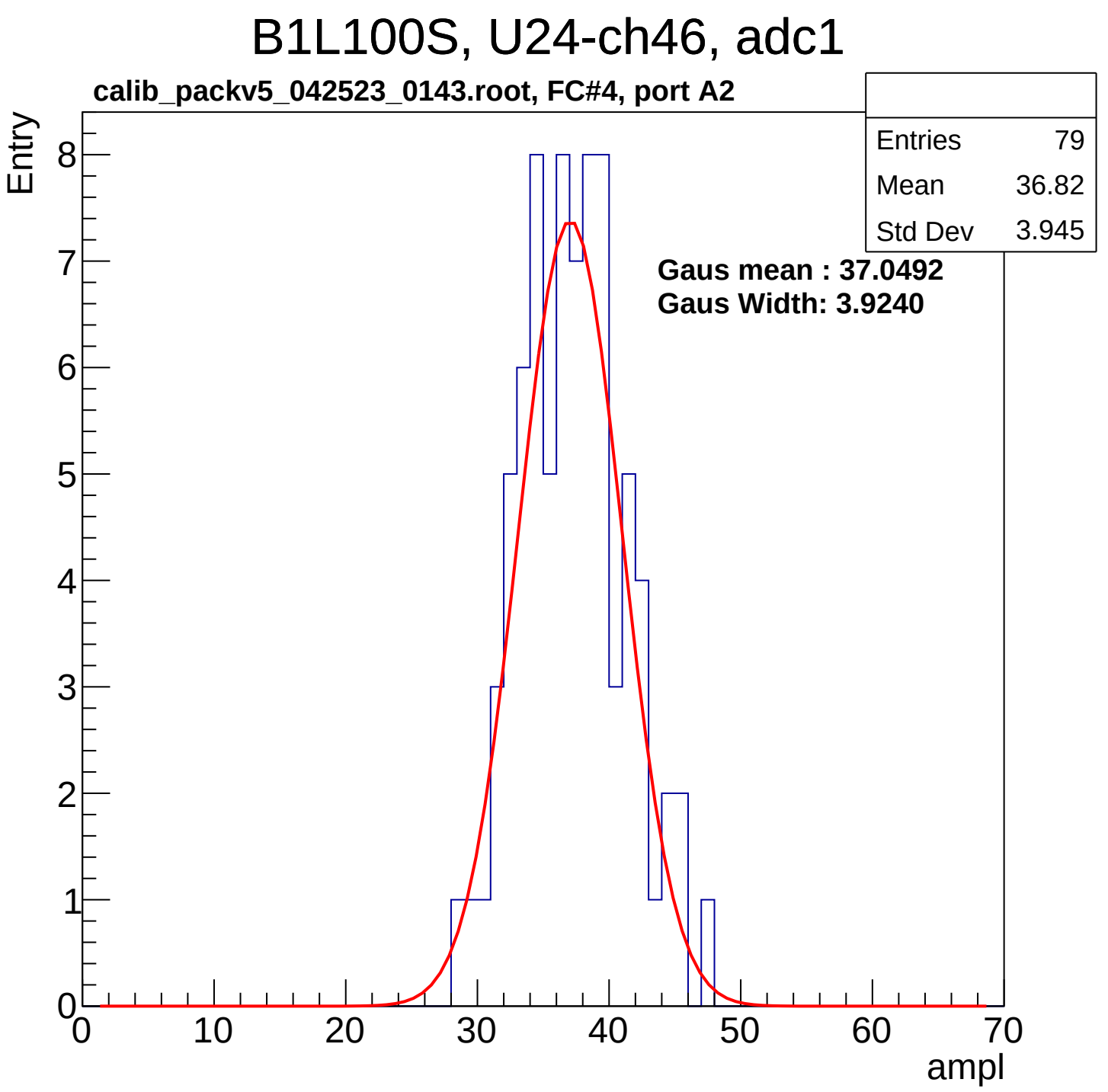
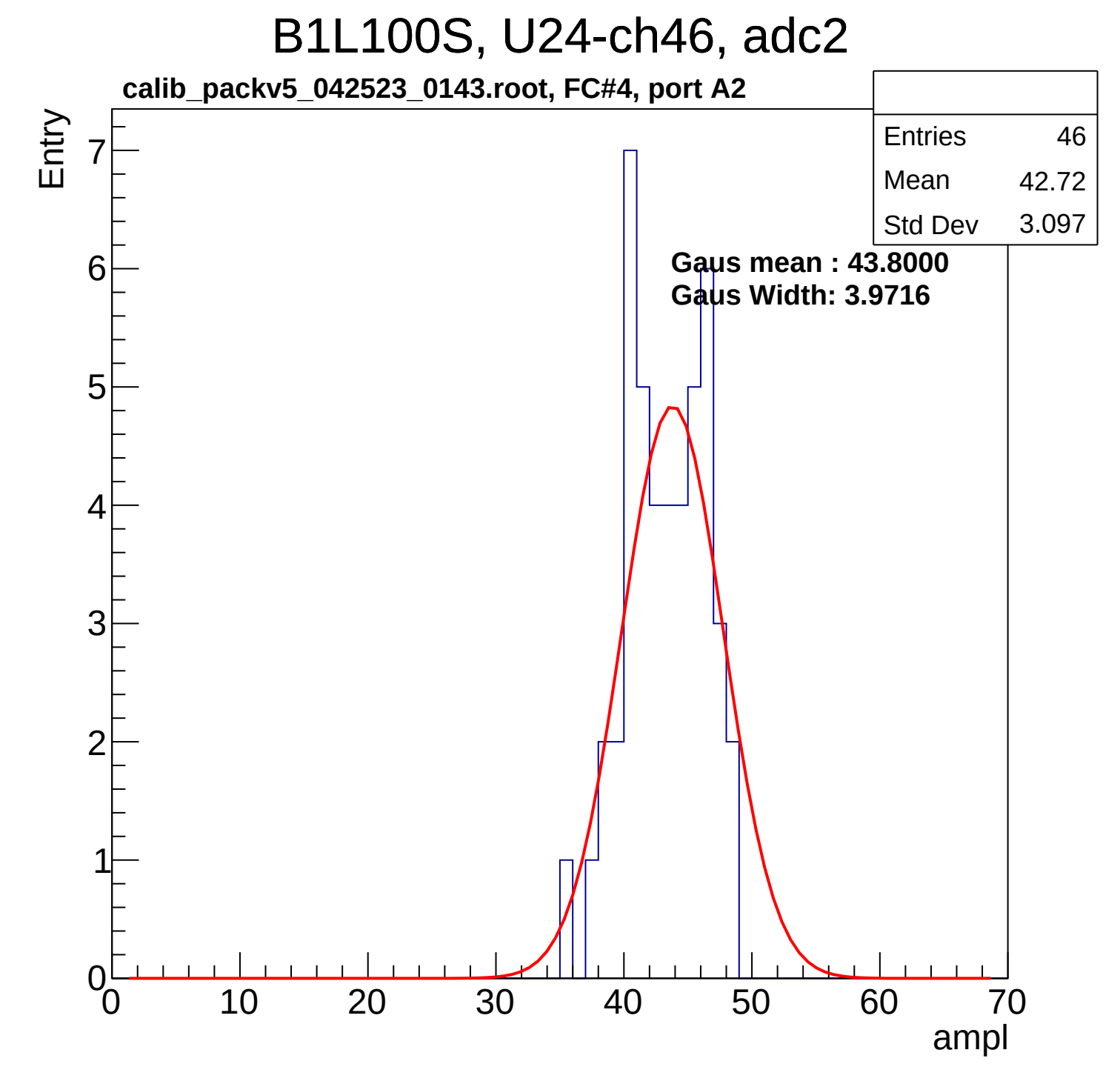
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 36.82
Std Dev 3.945
Gaus mean : 37.0492
Gaus Width: 3.9240
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 42.72
Std Dev 3.097
Gaus mean : 43.8000
Gaus Width: 3.9716
Entry
ampl
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1
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3
4
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0
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40
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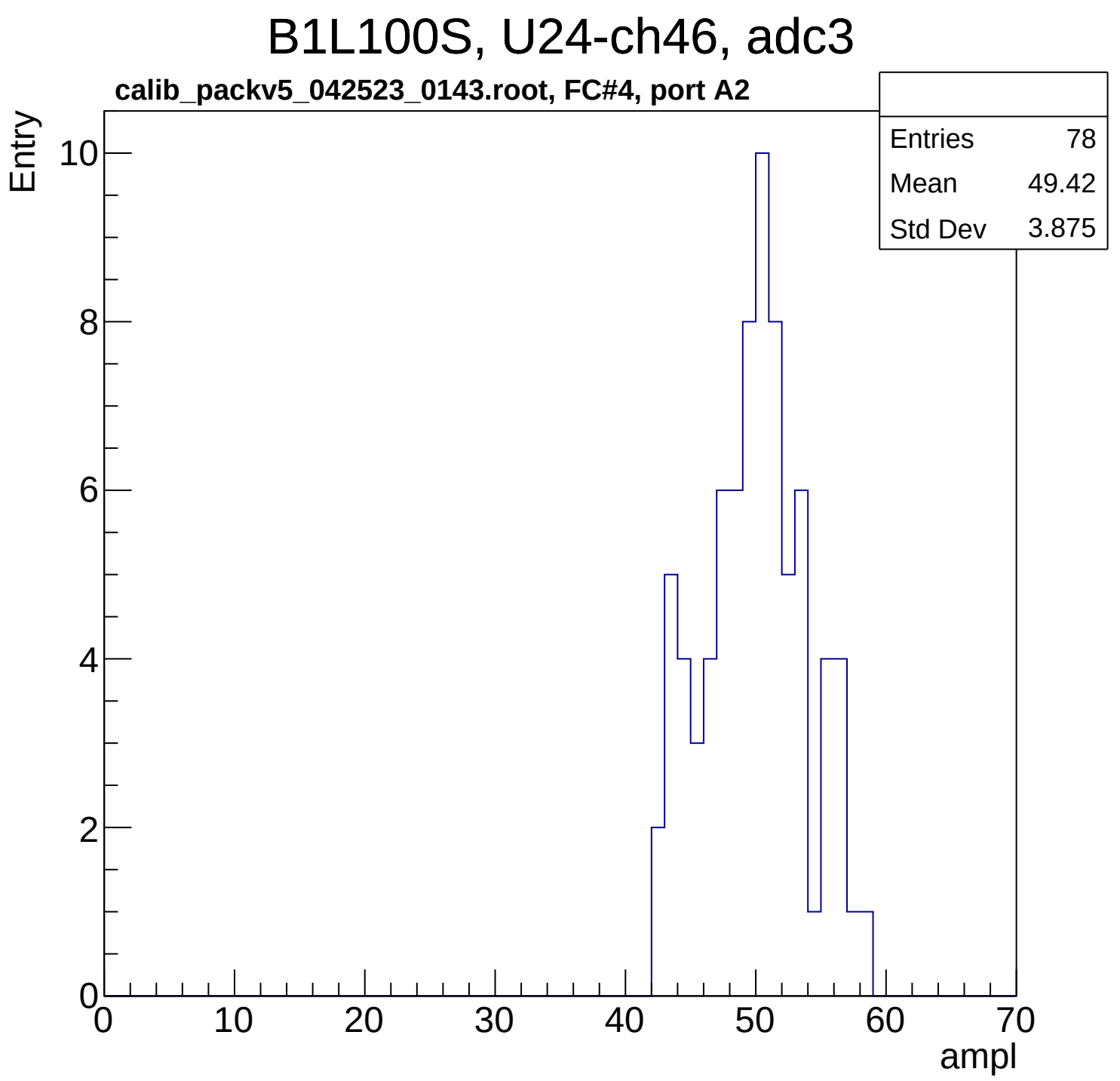
60
70


B1L100S, U24-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 49.42
Std Dev 3.875
Entry
0
2
4
6
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10
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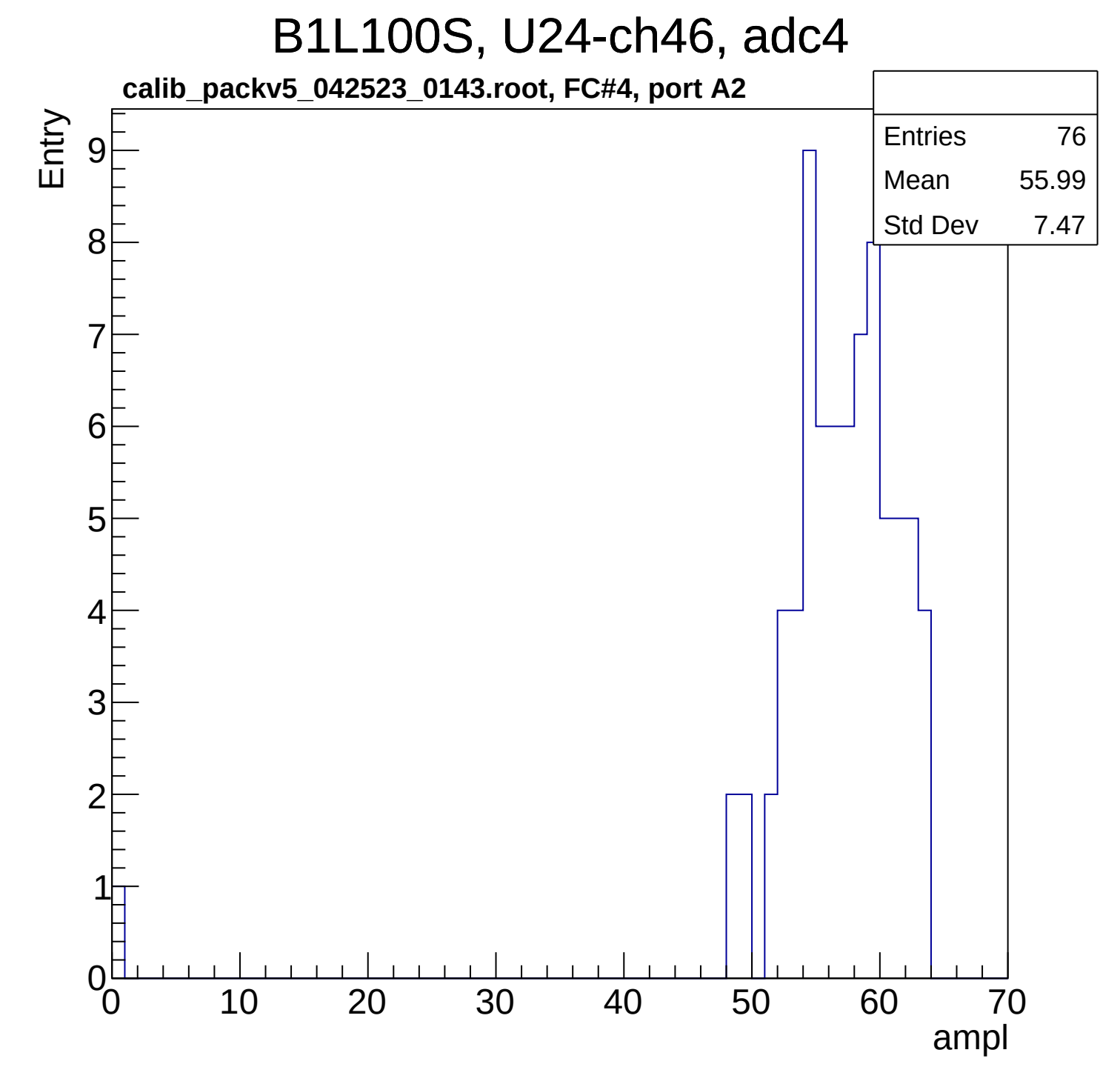
70
ampl


B1L100S, U24-ch46, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 55.99
Std Dev 7.47
Entry
ampl
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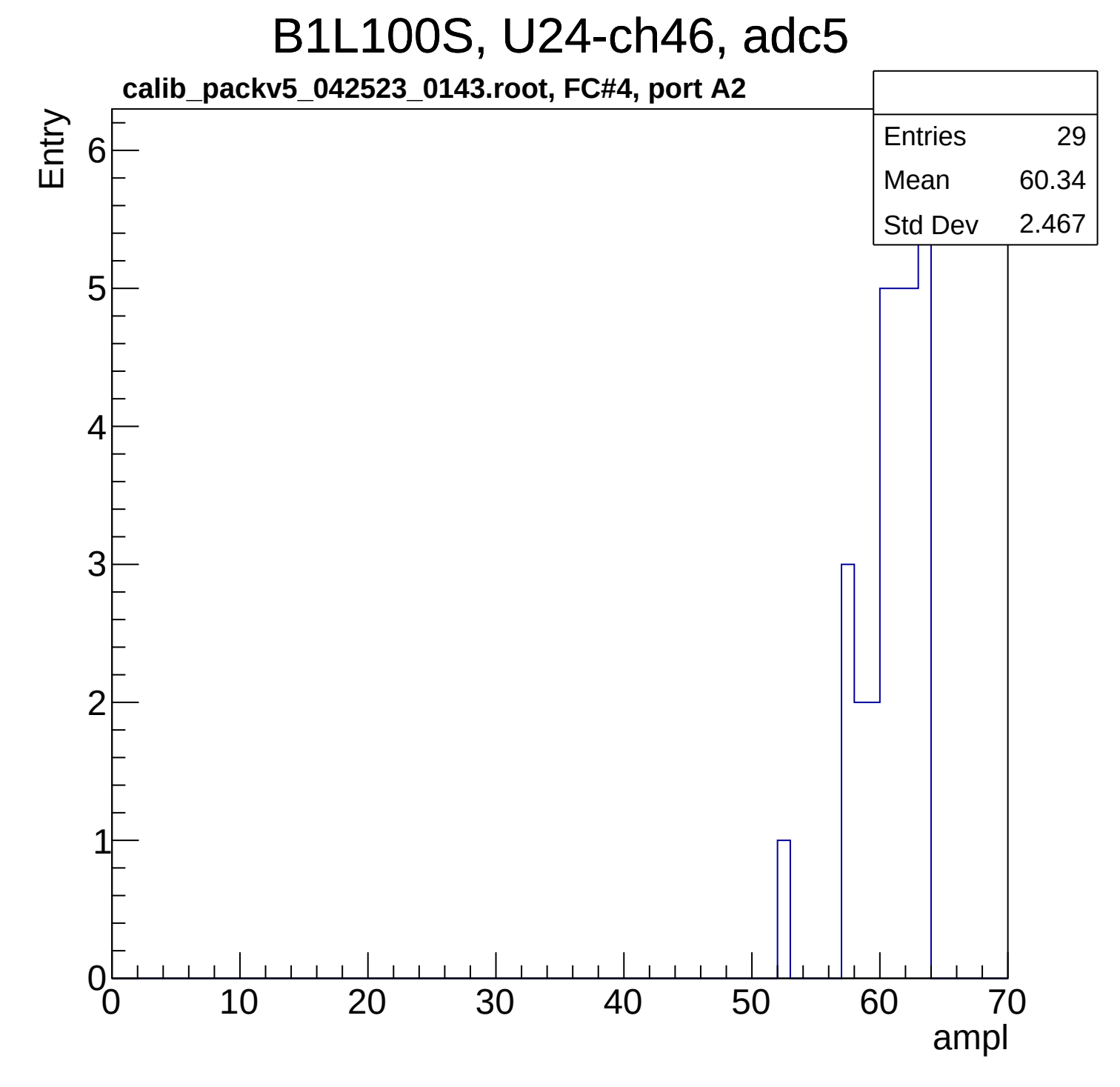
B1L100S, U24-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 60.34
Std Dev 2.467
Entry
ampl
0
1
2
3
4
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0
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60
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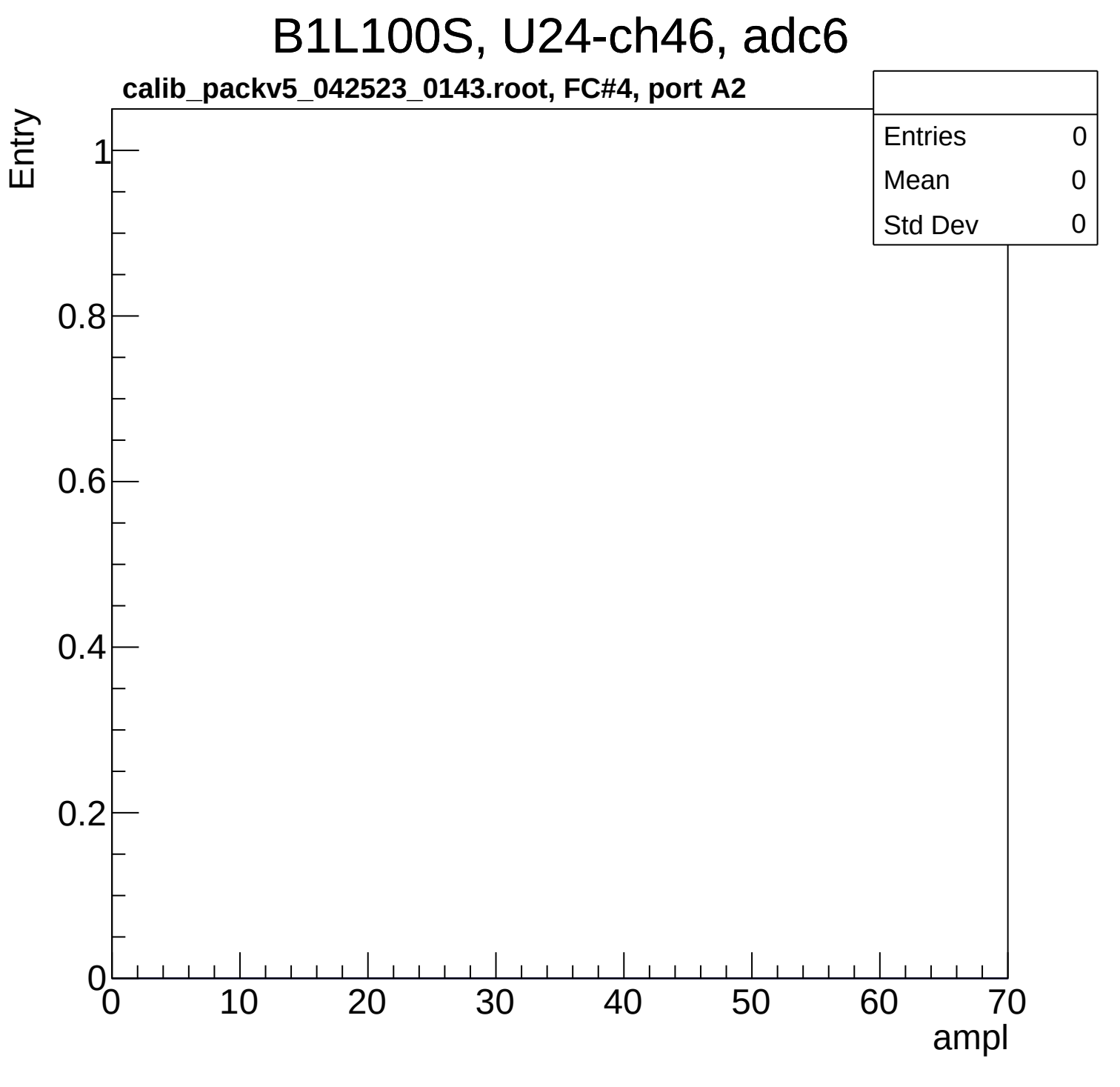
B1L100S, U24-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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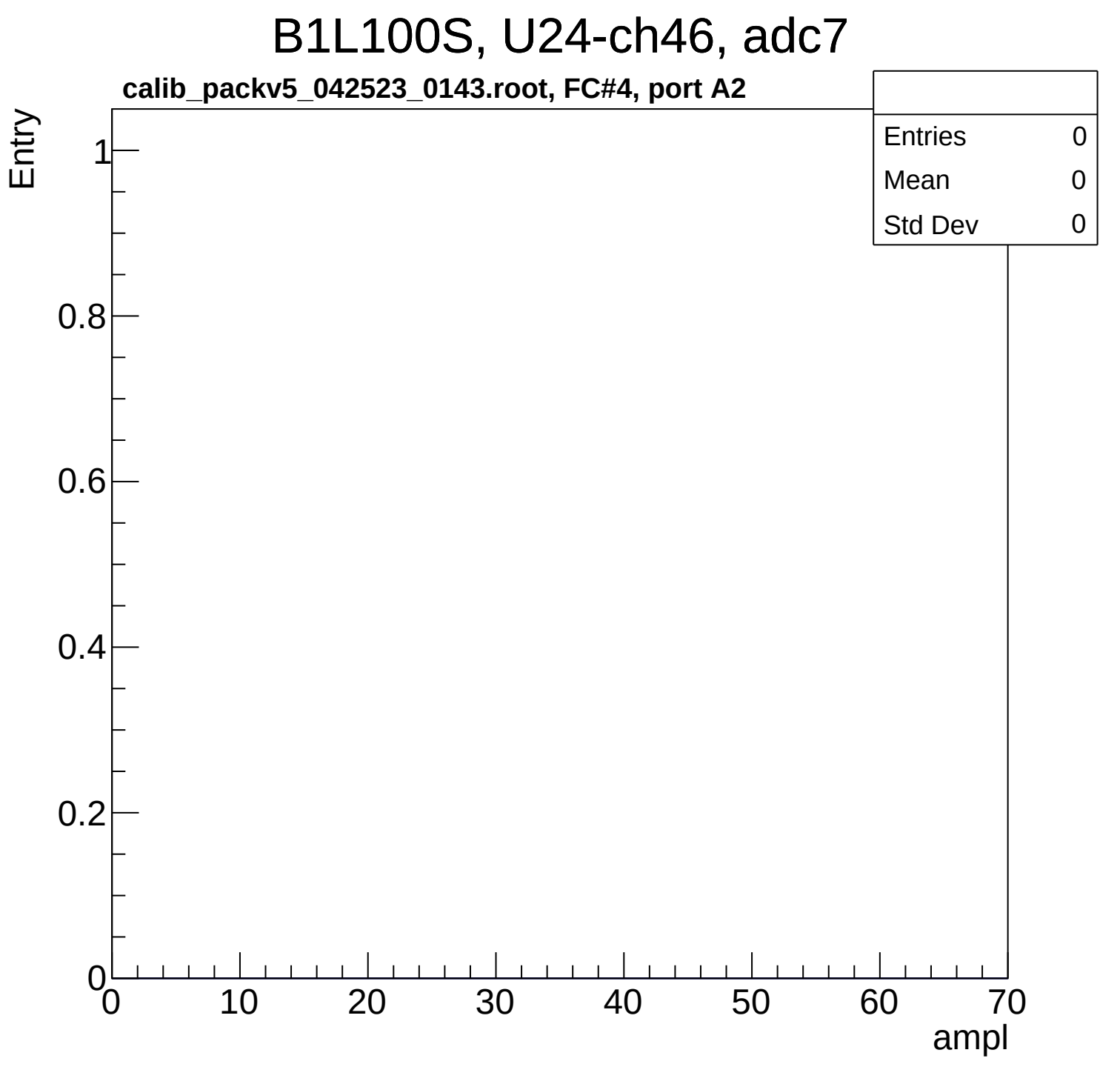
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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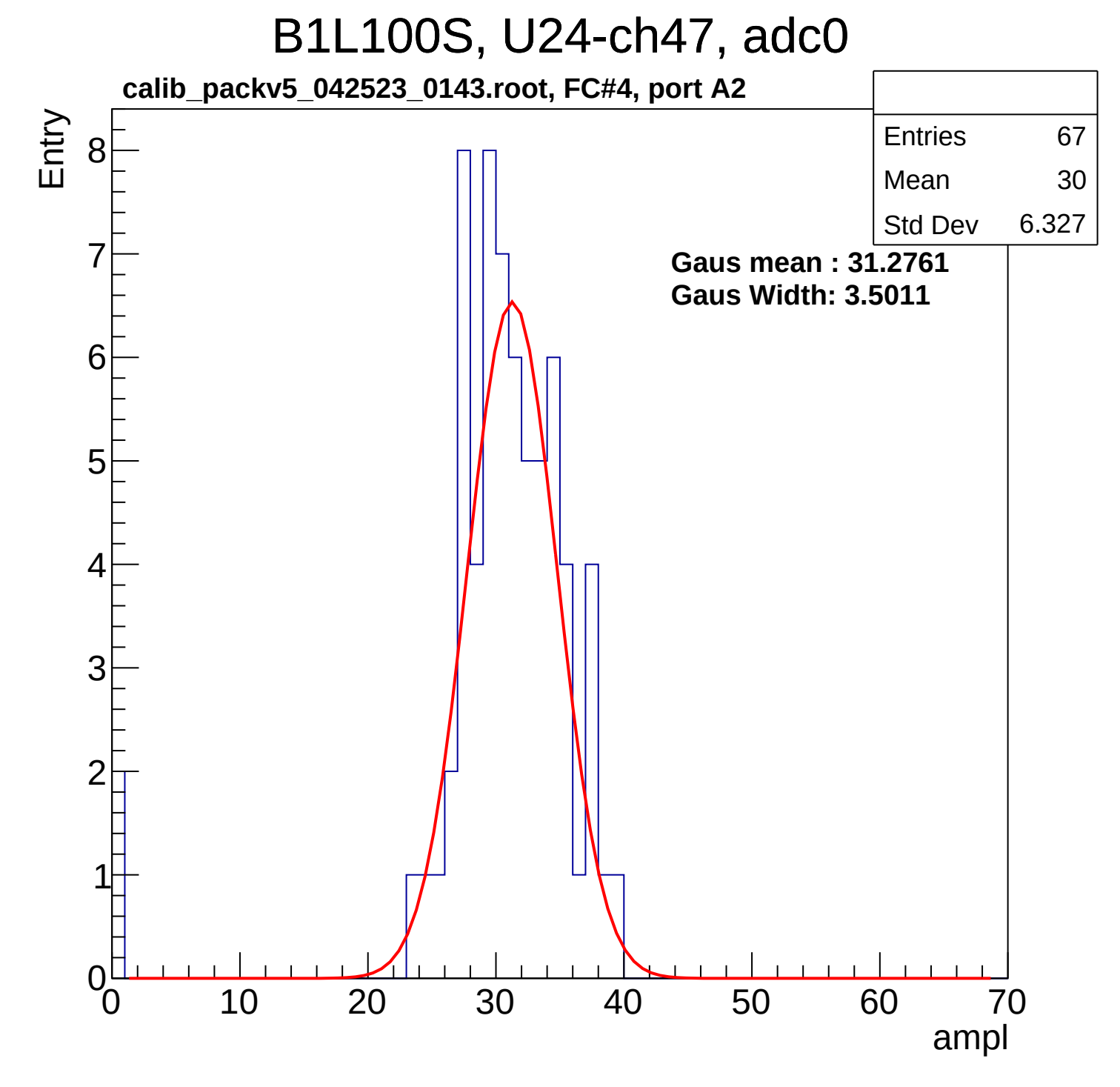
50
60
70
ampl


B1L100S, U24-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30
Std Dev 6.327
Gaus mean : 31.2761
Gaus Width: 3.5011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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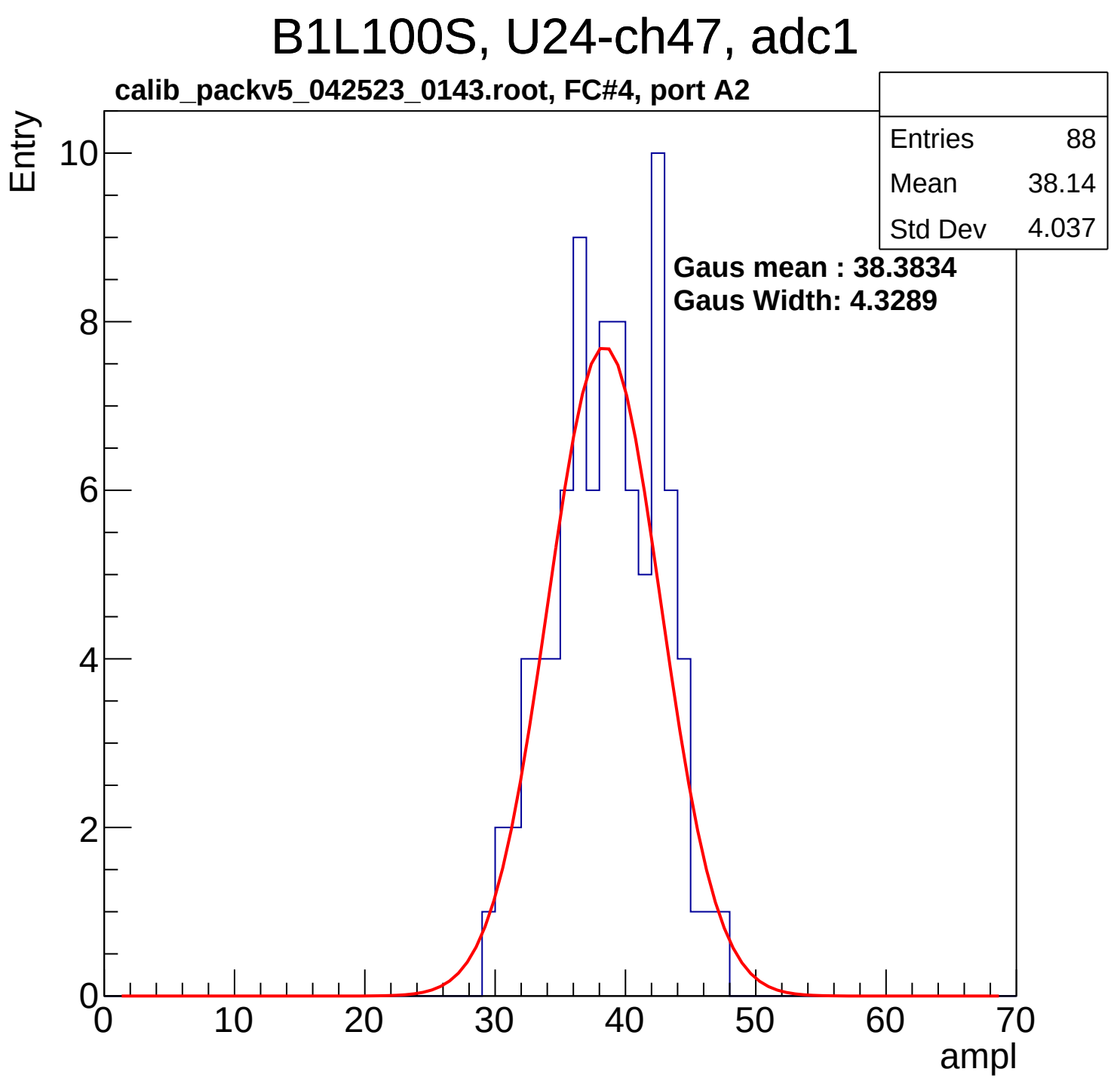
B1L100S, U24-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 38.14
Std Dev 4.037
Gaus mean : 38.3834
Gaus Width: 4.3289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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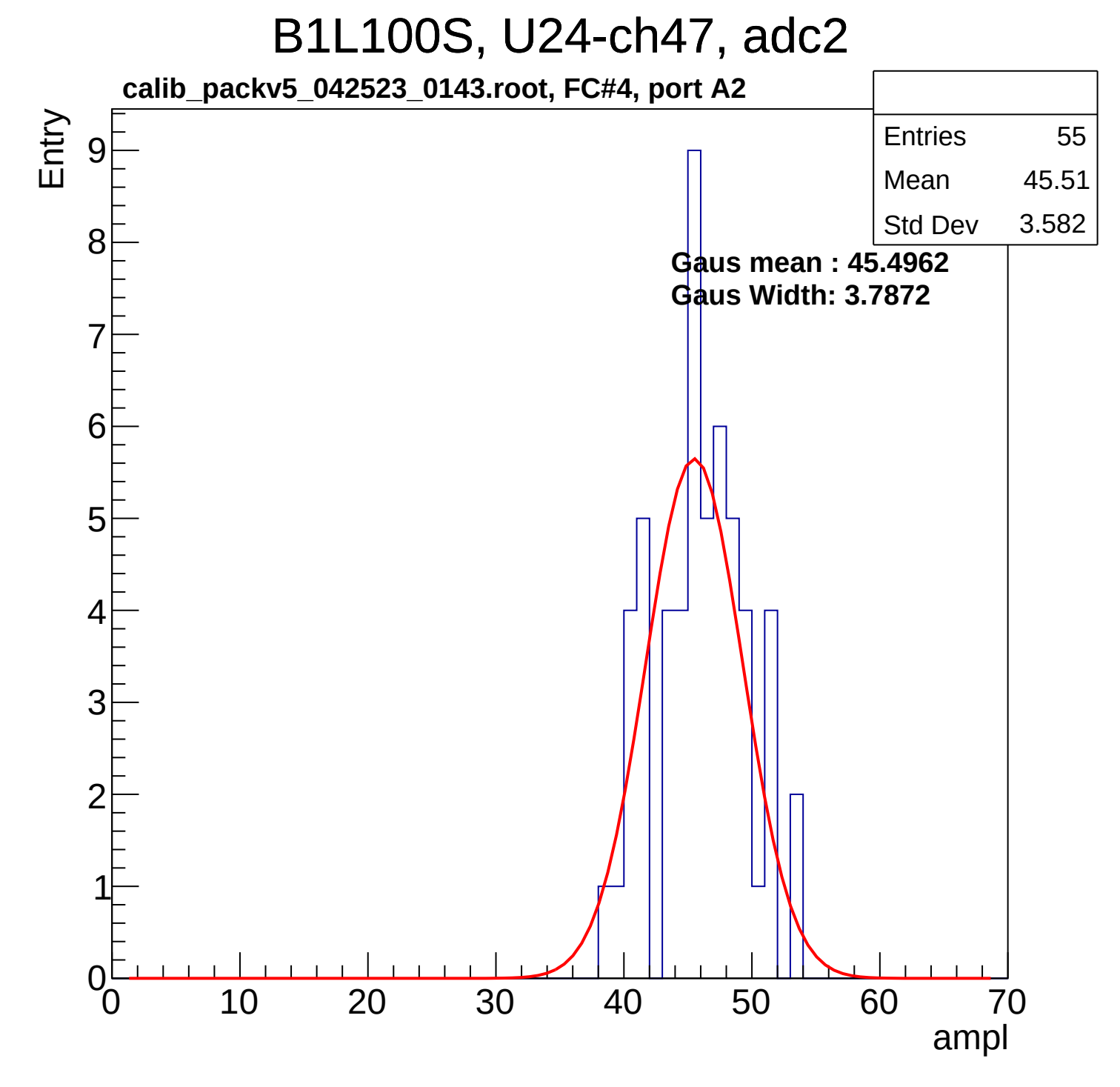
B1L100S, U24-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 45.51
Std Dev 3.582
Gaus mean : 45.4962
Gaus Width: 3.7872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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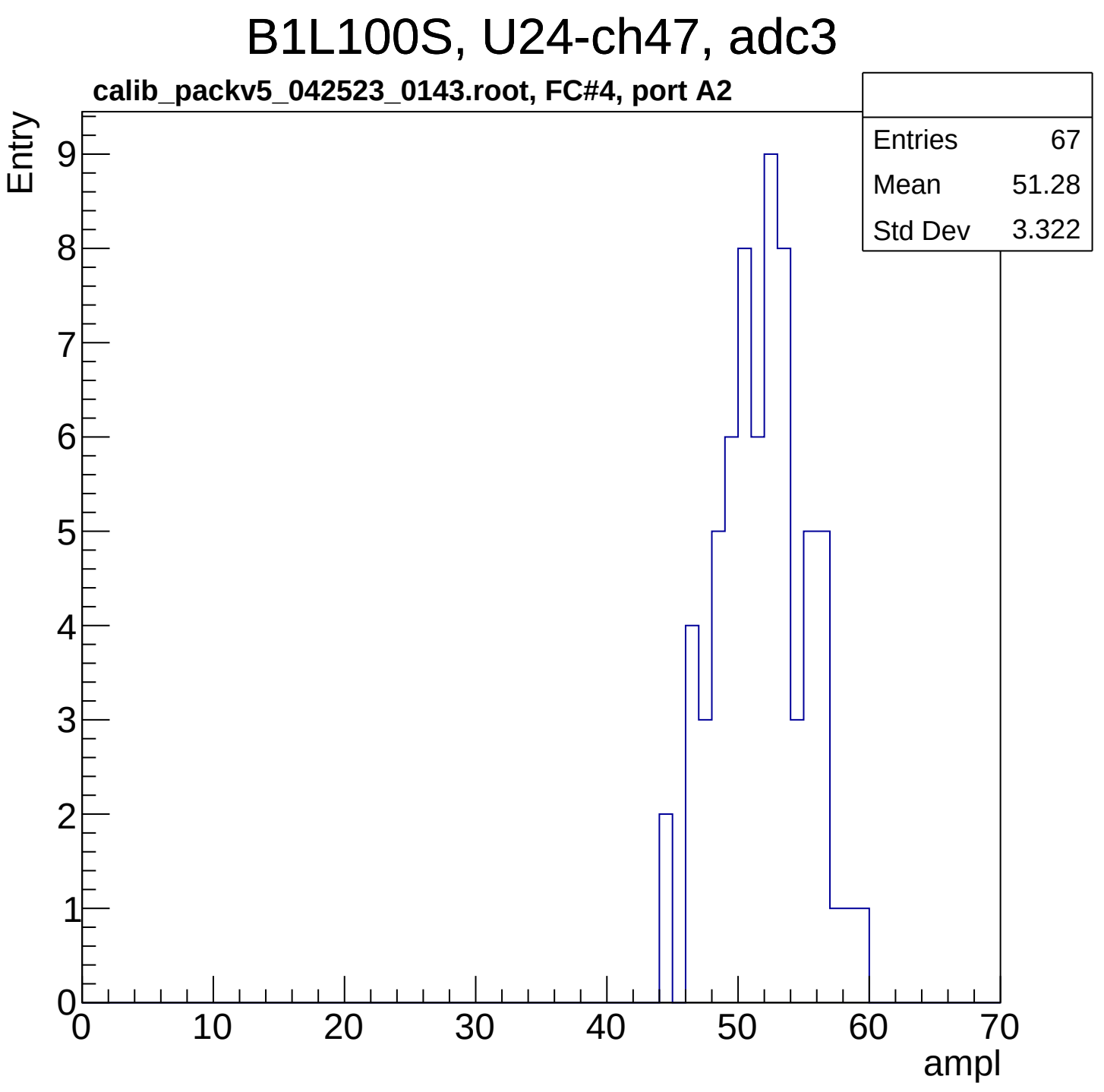
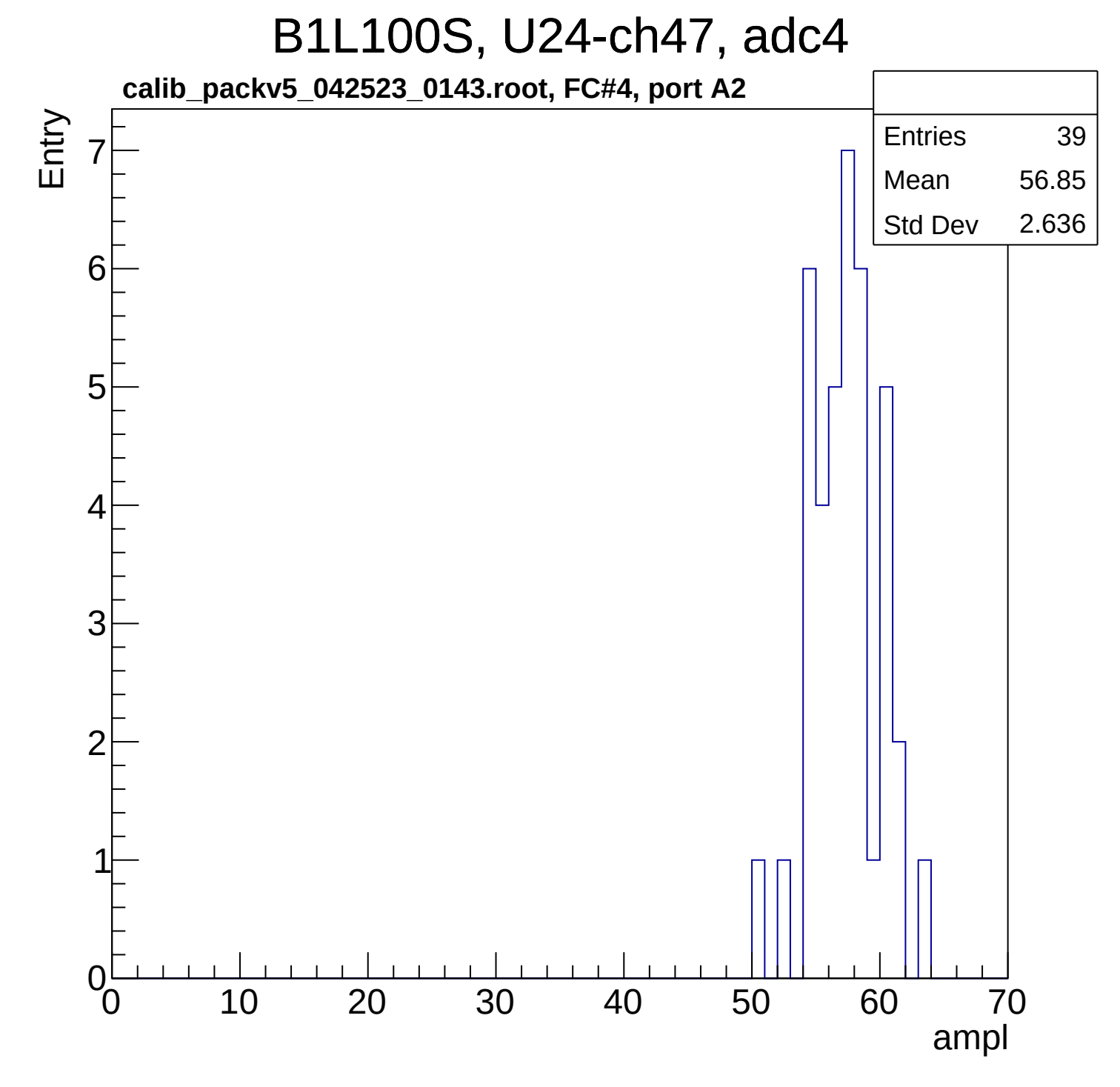

B1L100S, U24-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 51.28
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 56.85
Std Dev 2.636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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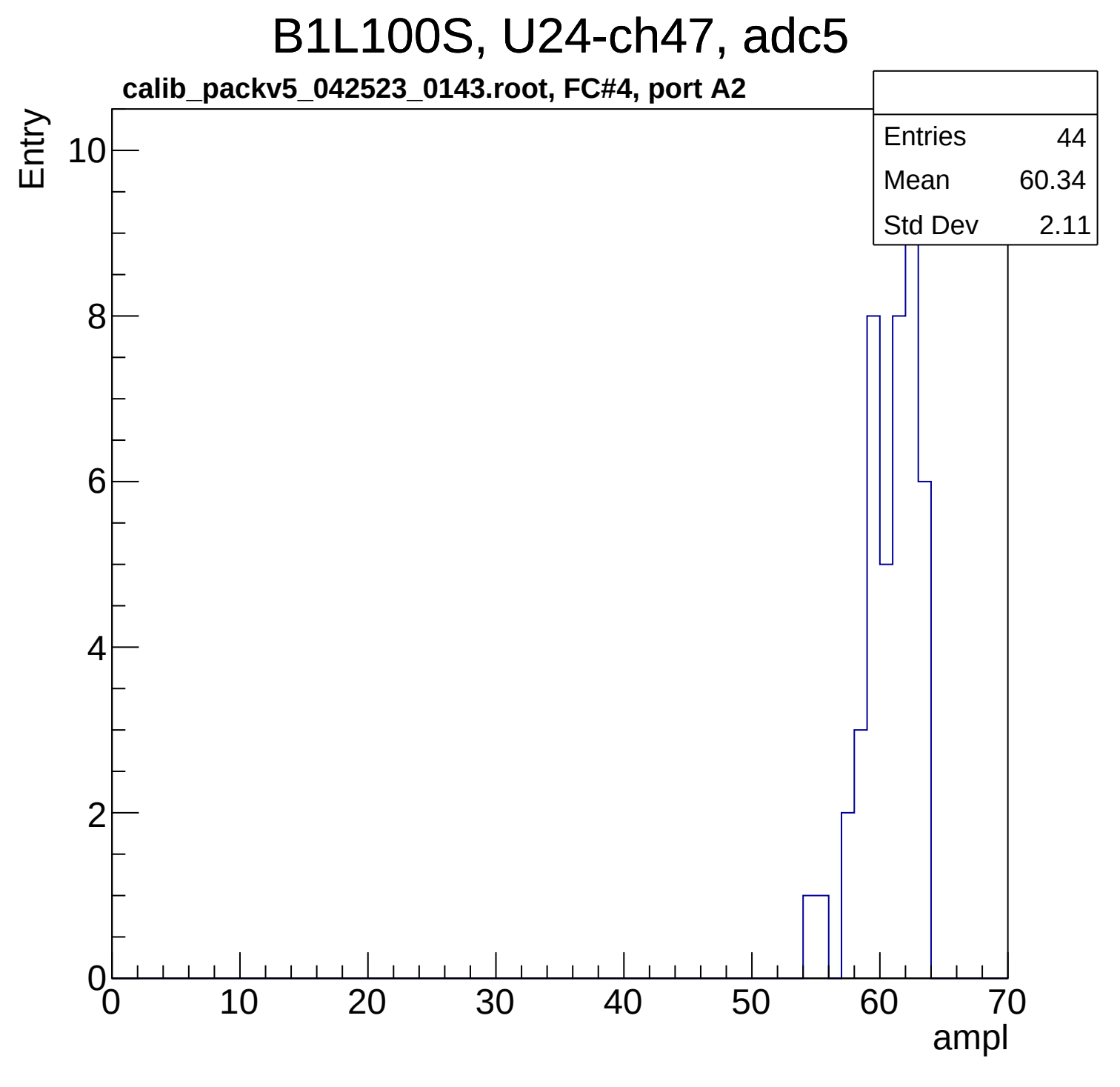
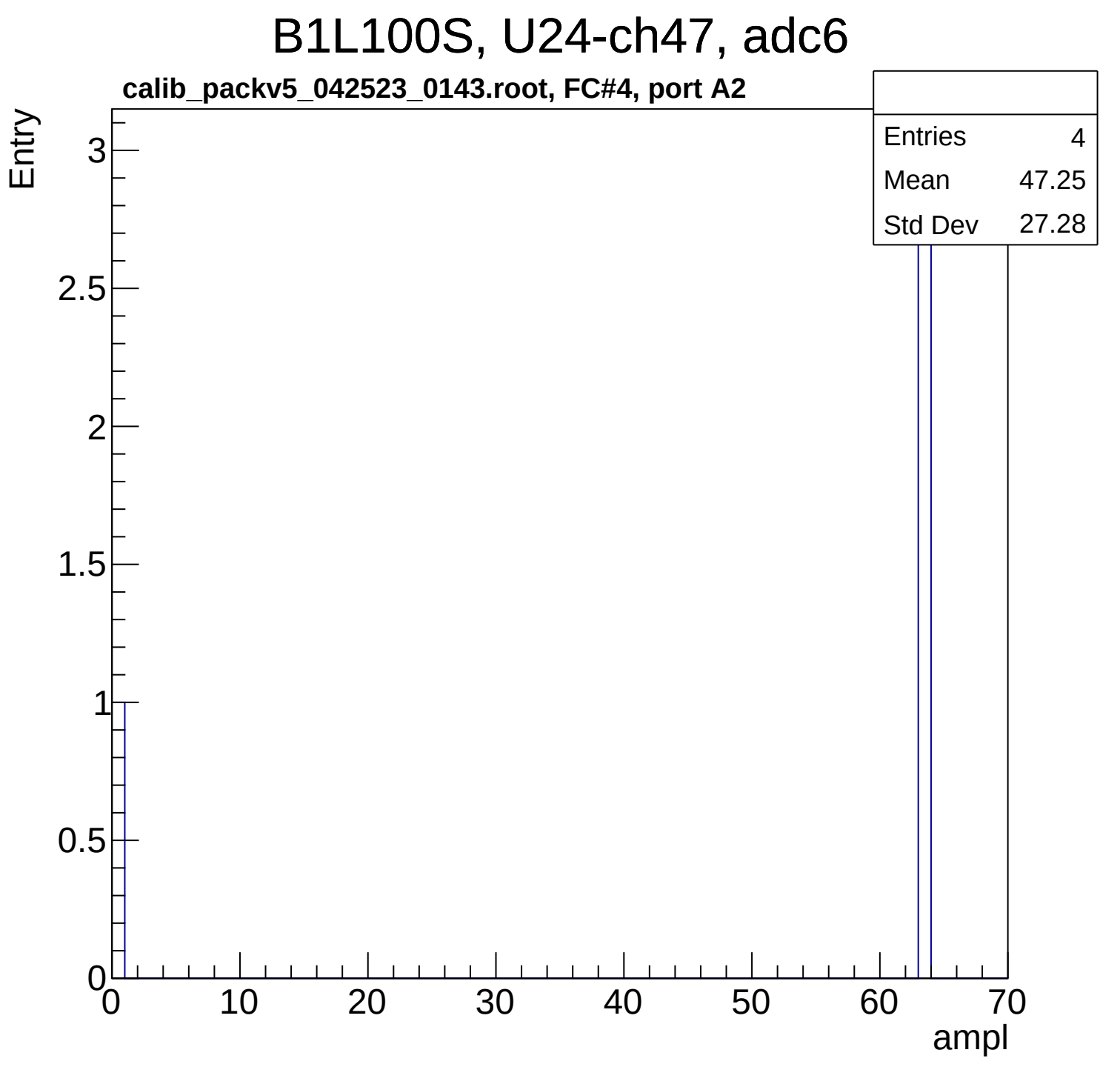
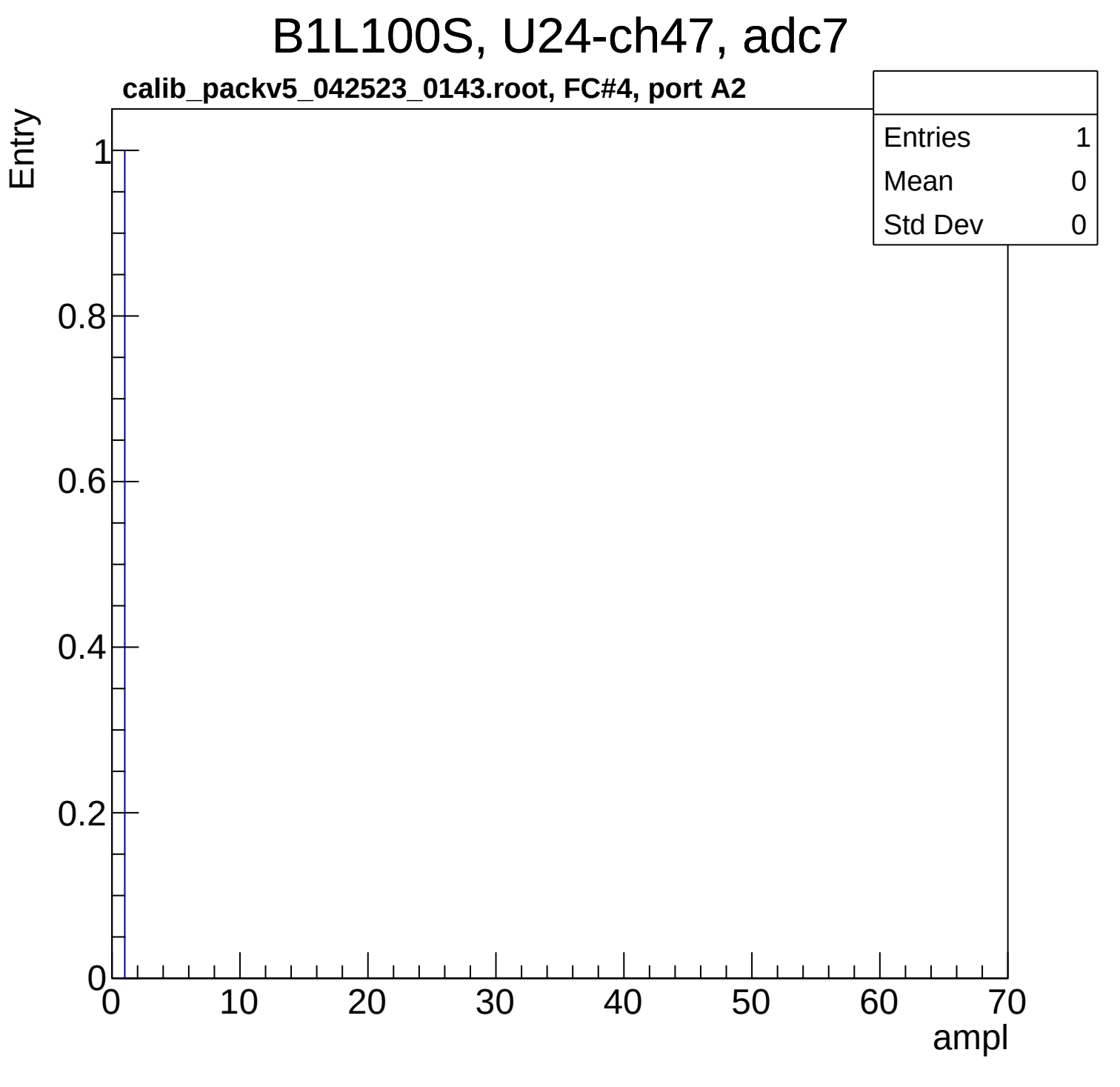
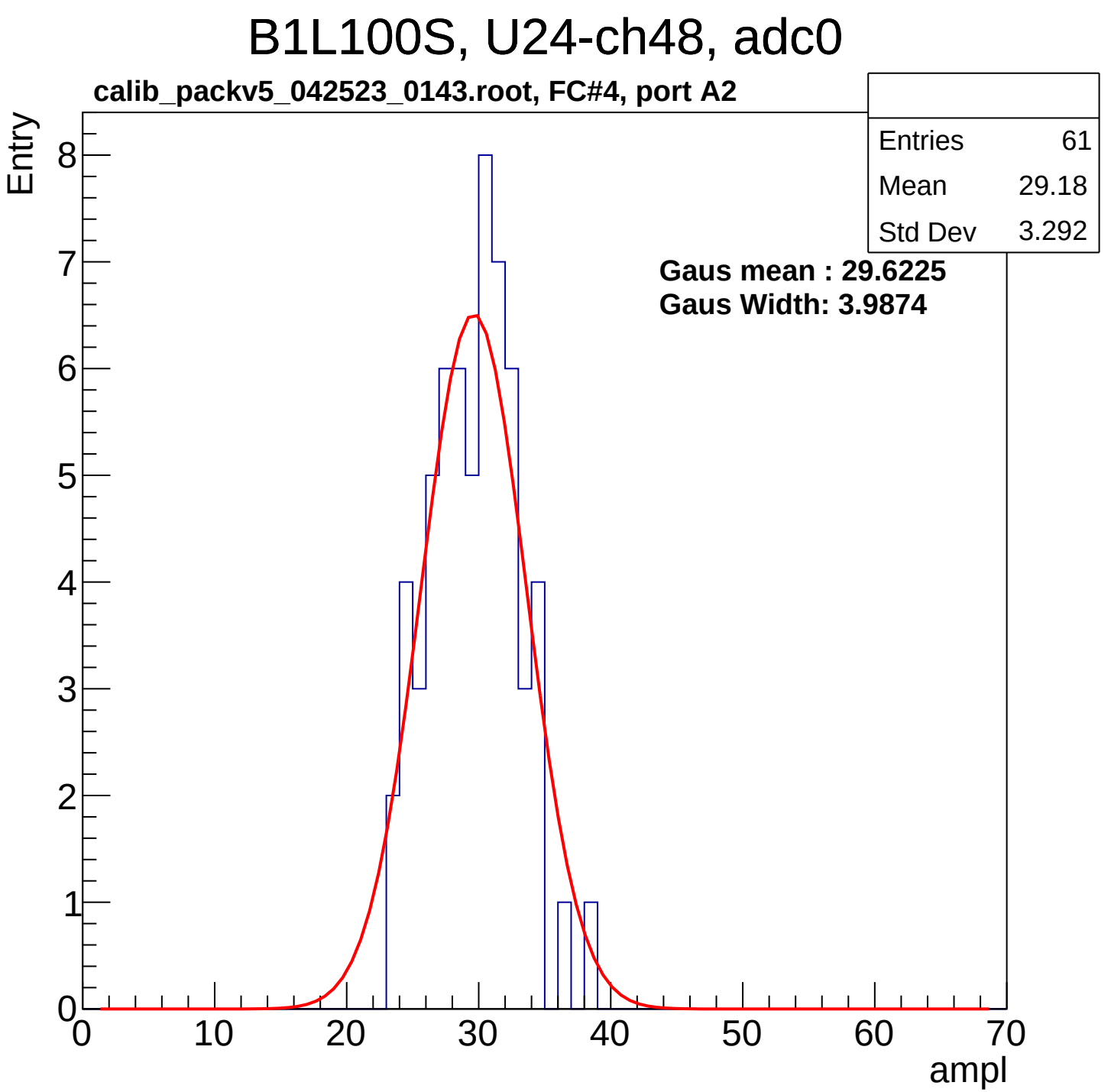
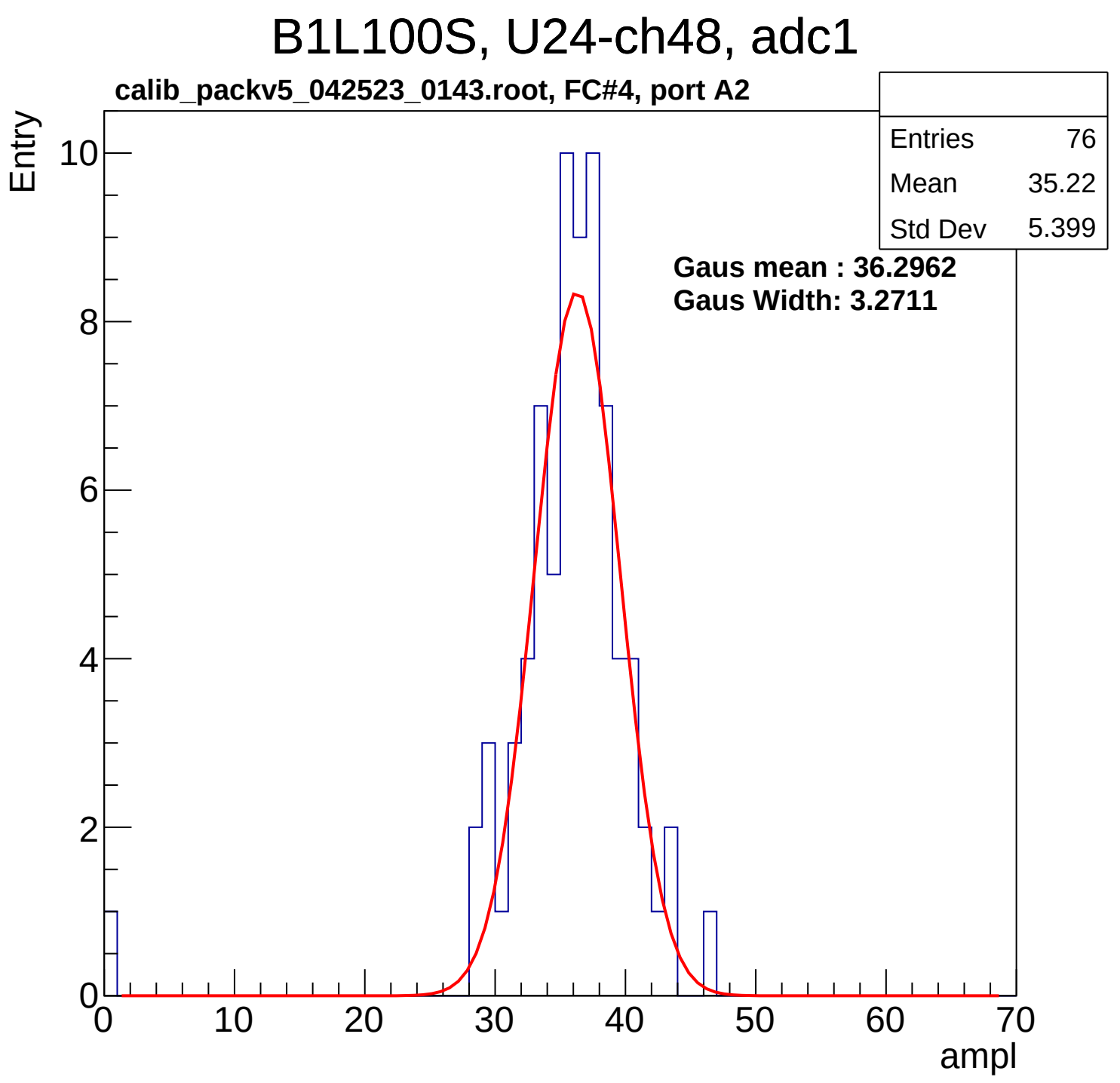
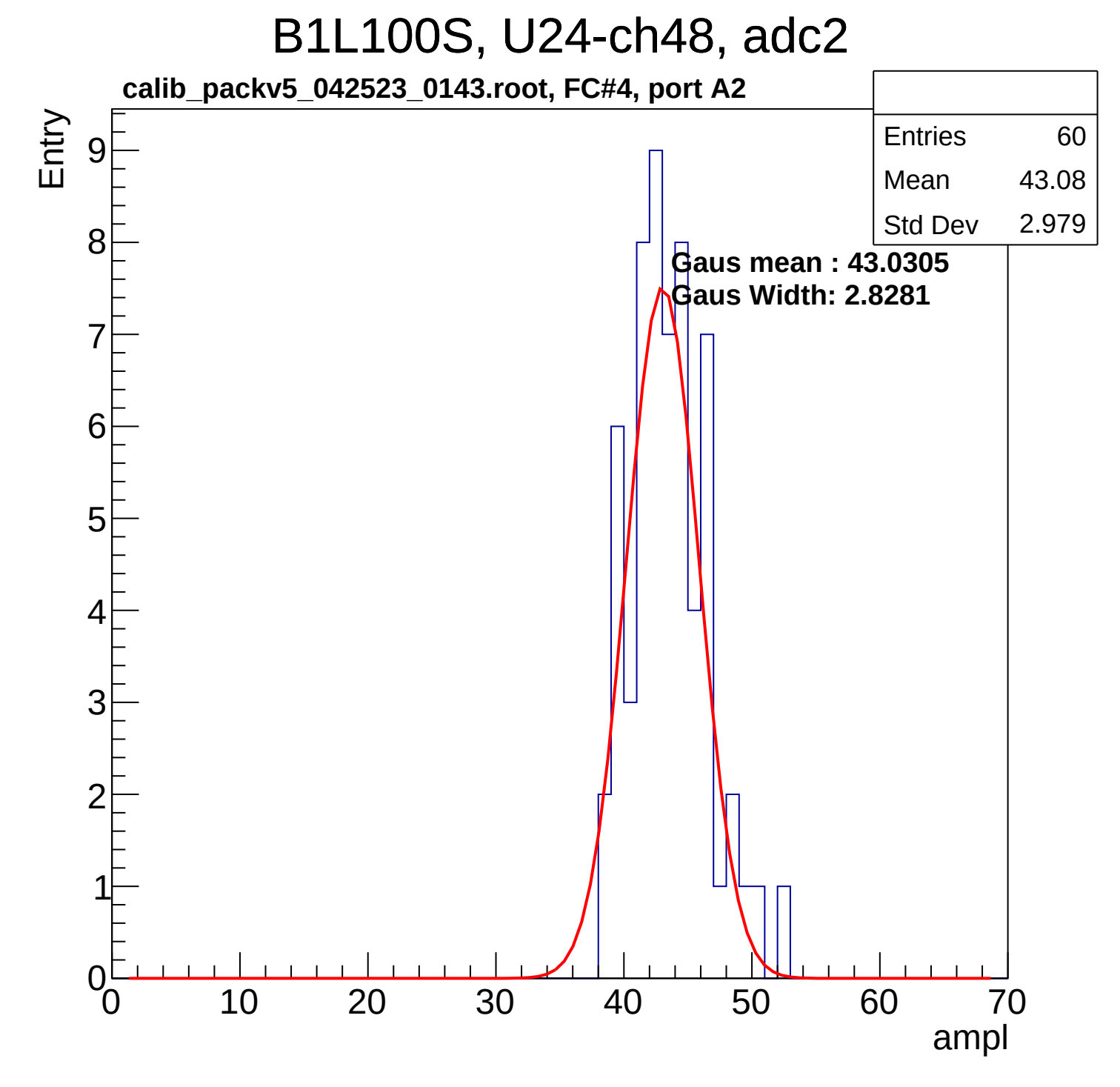

B1L100S, U24-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 60.34
Std Dev 2.11
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

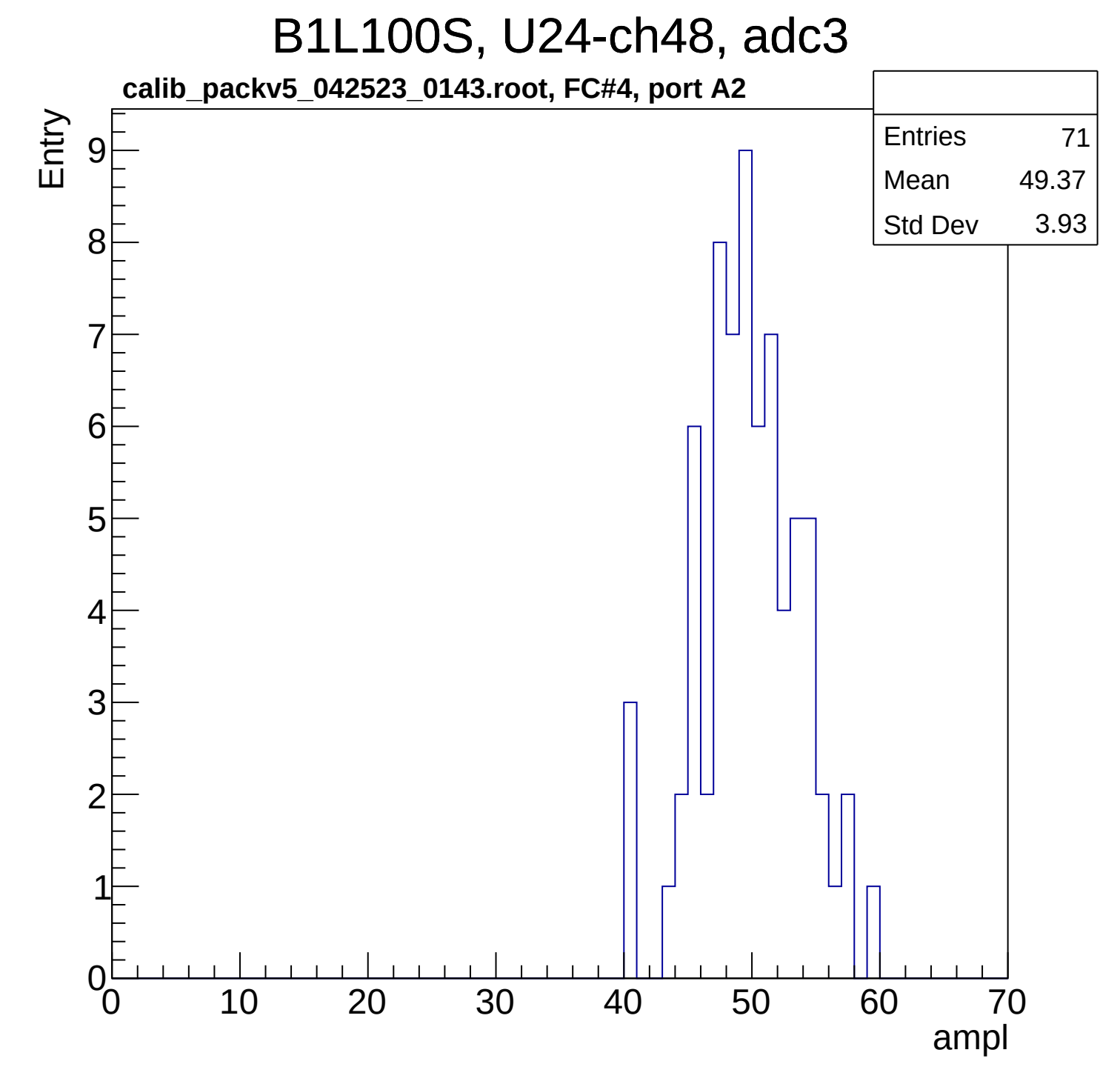

B1L100S, U24-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.18
Std Dev 3.292
Gaus mean : 29.6225
Gaus Width: 3.9874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.22
Std Dev 5.399
Gaus mean : 36.2962
Gaus Width: 3.2711
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch48, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.08
Std Dev 2.979
Gaus mean : 43.0305
Gaus Width: 2.8281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.37
Std Dev 3.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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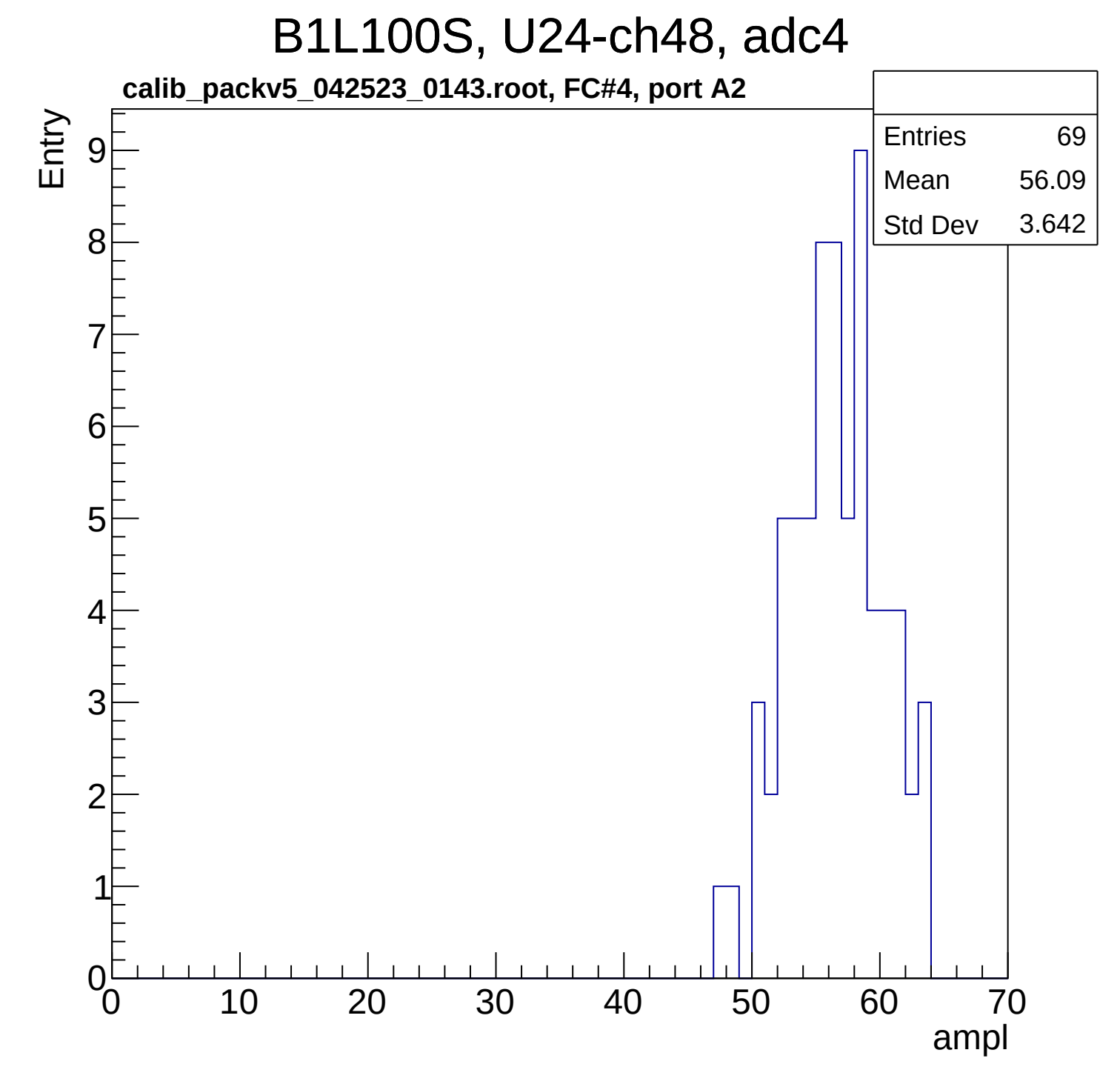
B1L100S, U24-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.09
Std Dev 3.642
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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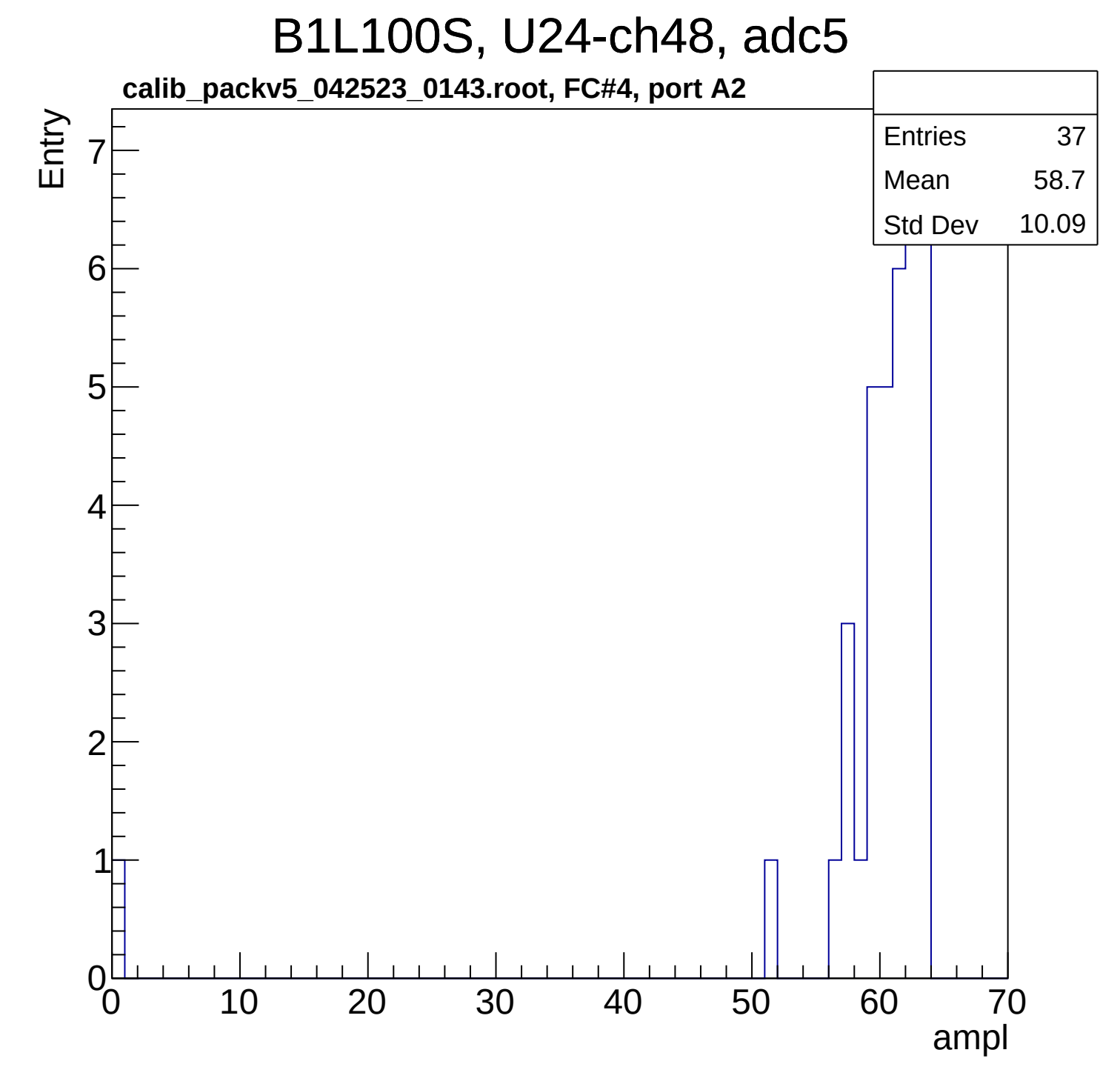
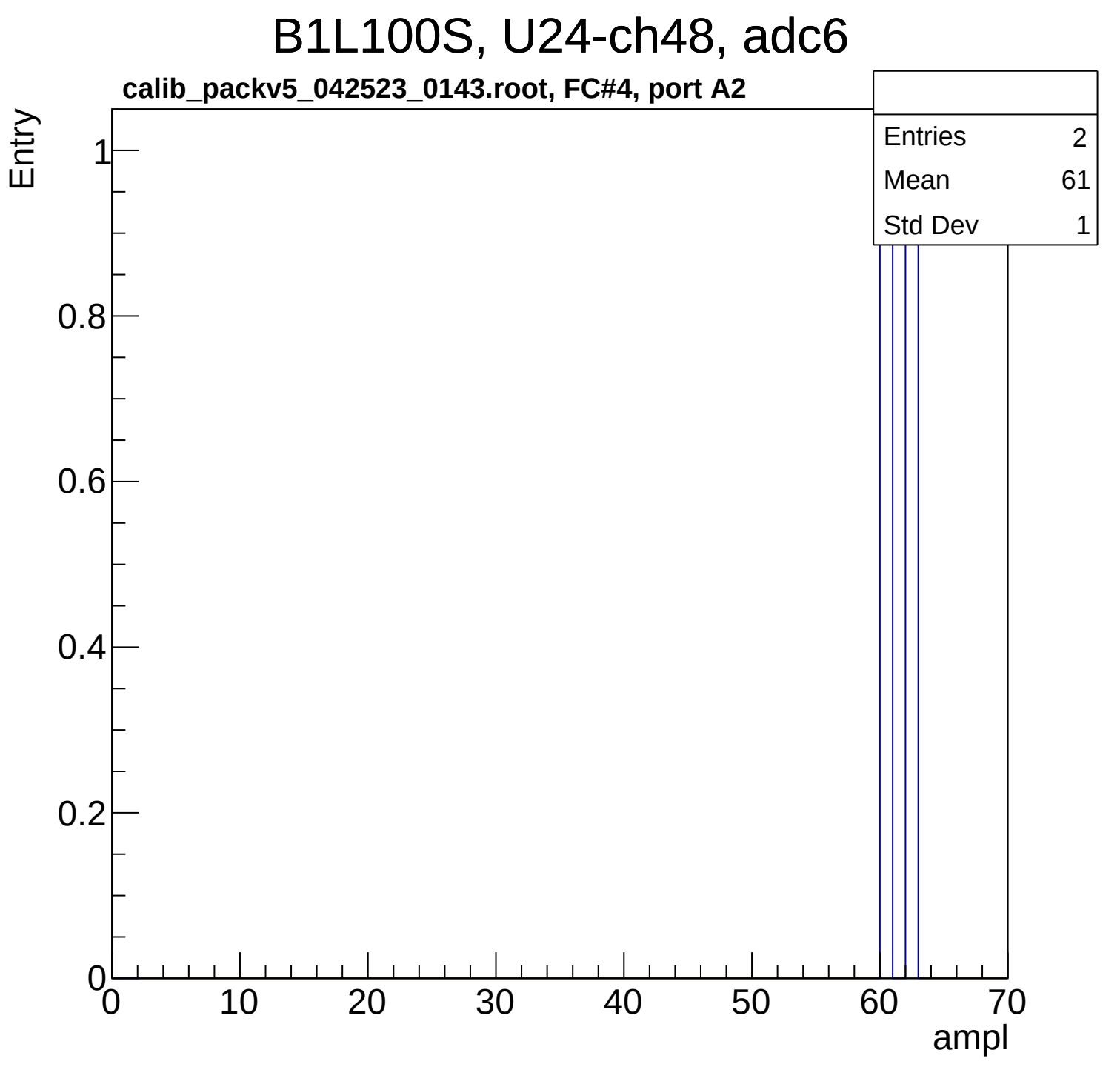

B1L100S, U24-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.7
Std Dev 10.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch48, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
30
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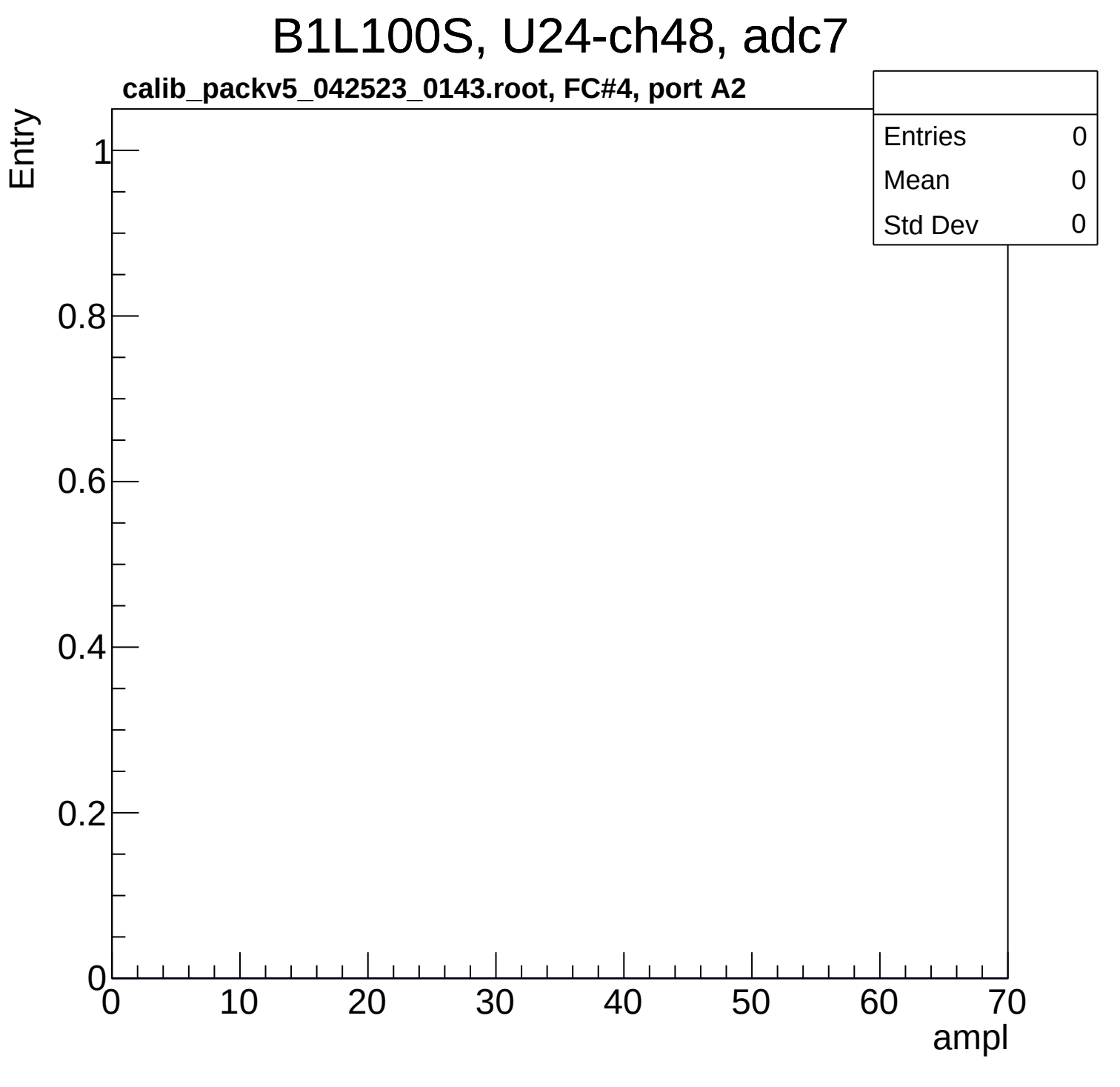
50
60
70
ampl


B1L100S, U24-ch48, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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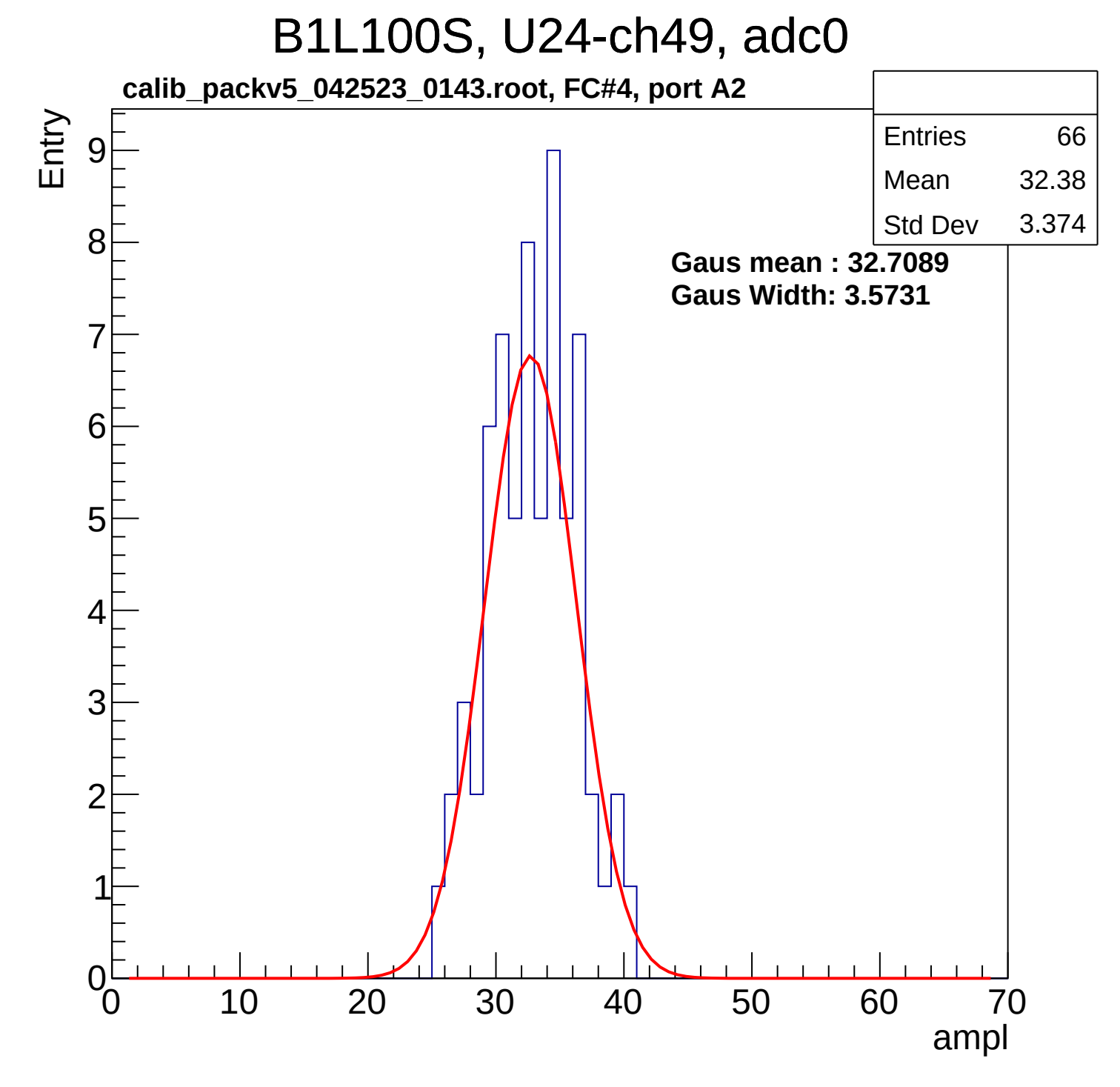
50
60
70
ampl


B1L100S, U24-ch49, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 32.38
Std Dev 3.374
Gaus mean : 32.7089
Gaus Width: 3.5731
Entry
ampl
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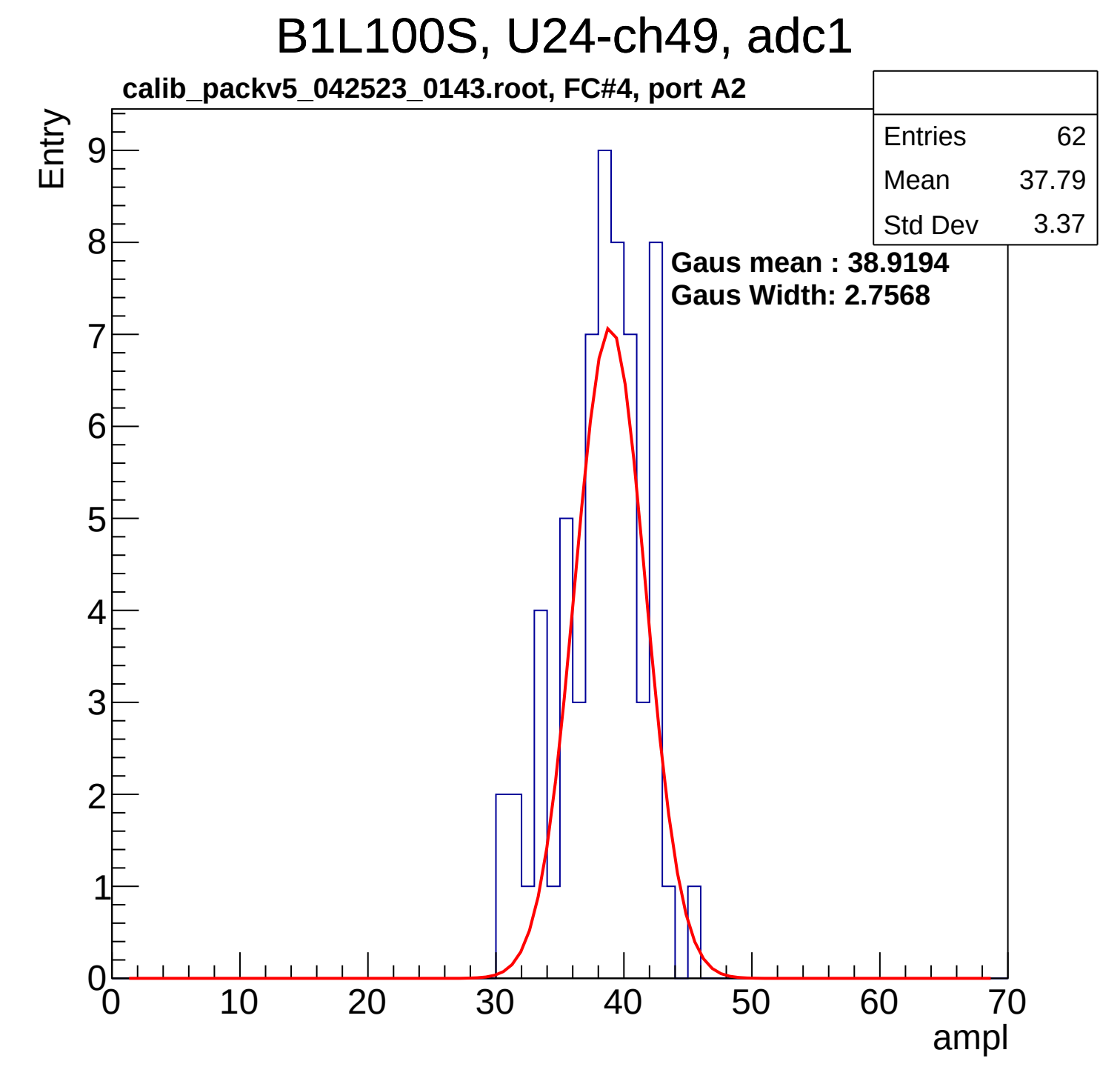
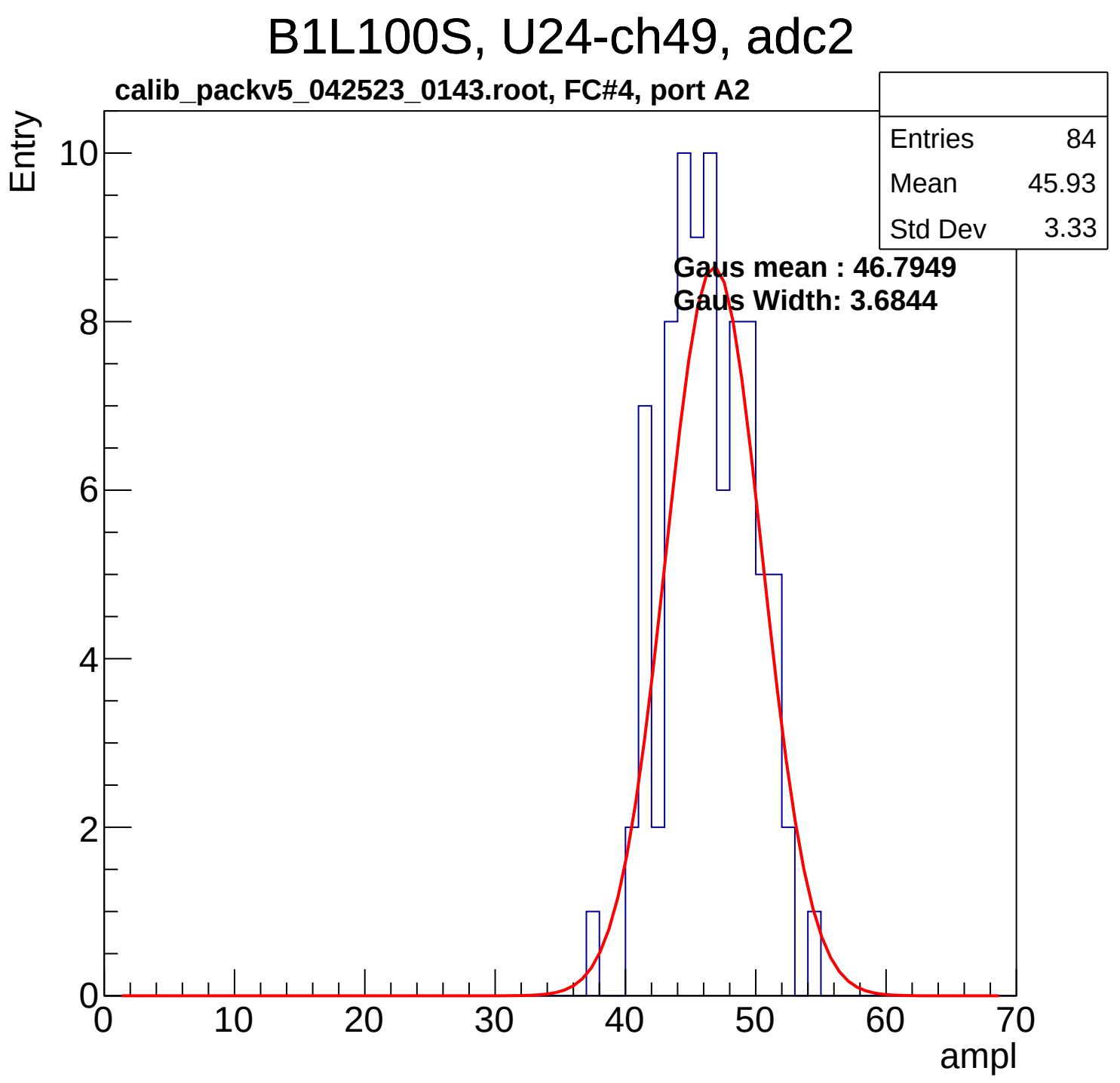
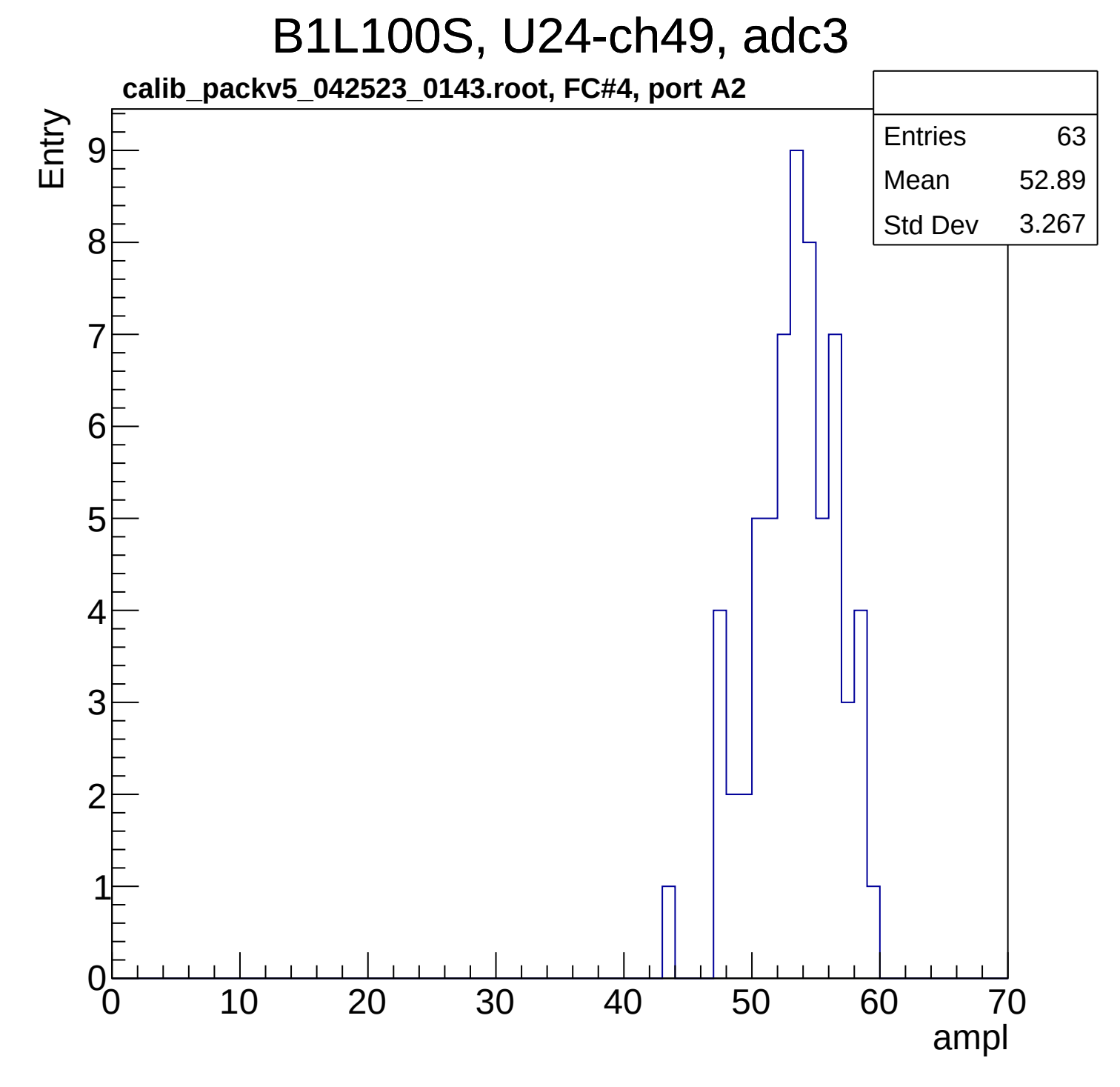
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 37.79
Std Dev 3.37
Gaus mean : 38.9194
Gaus Width: 2.7568
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 45.93
Std Dev 3.33
Gaus mean : 46.7949
Gaus Width: 3.6844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 52.89
Std Dev 3.267
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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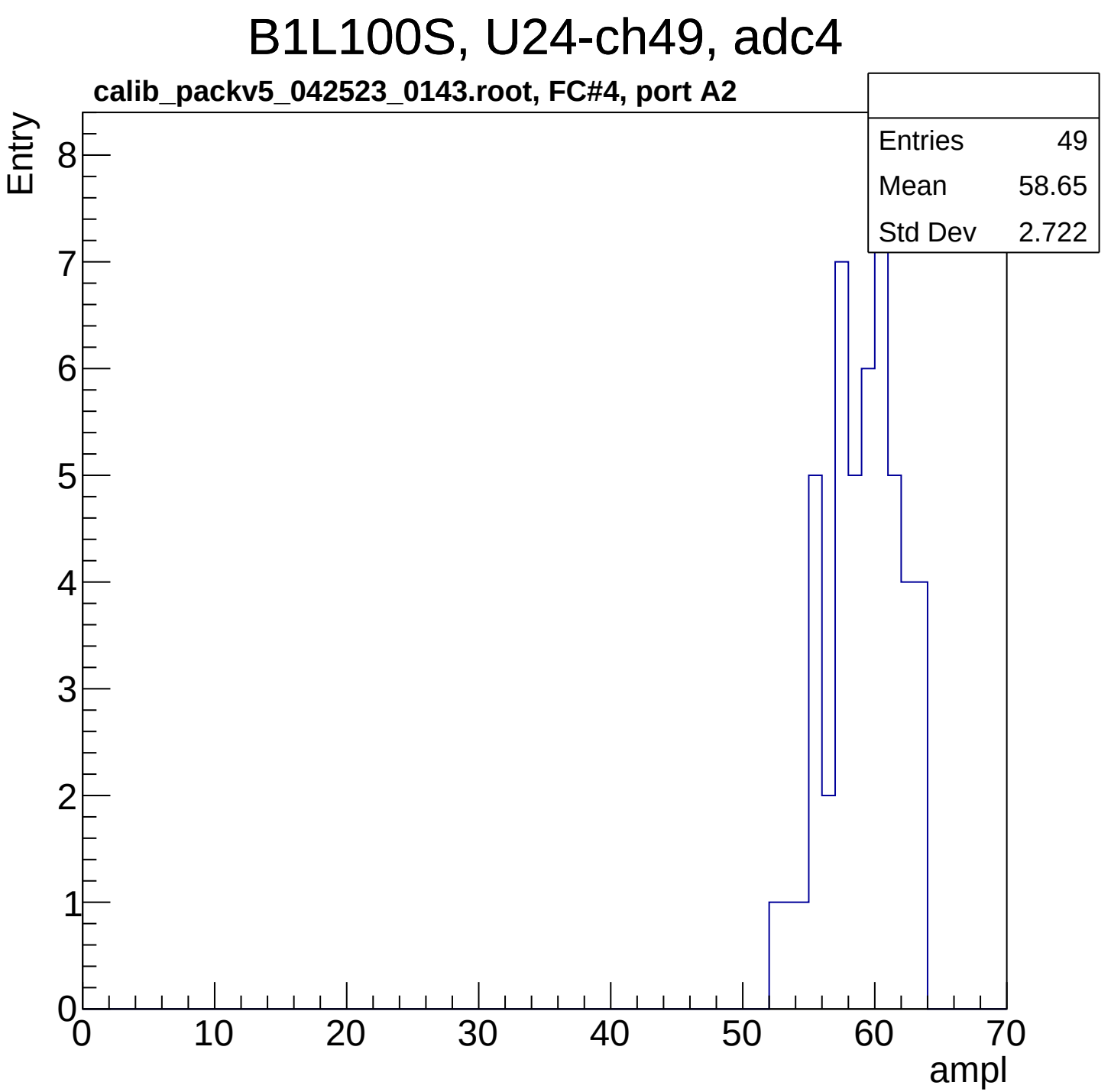
40
50
60
70


B1L100S, U24-ch49, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.65
Std Dev 2.722
Entry
ampl
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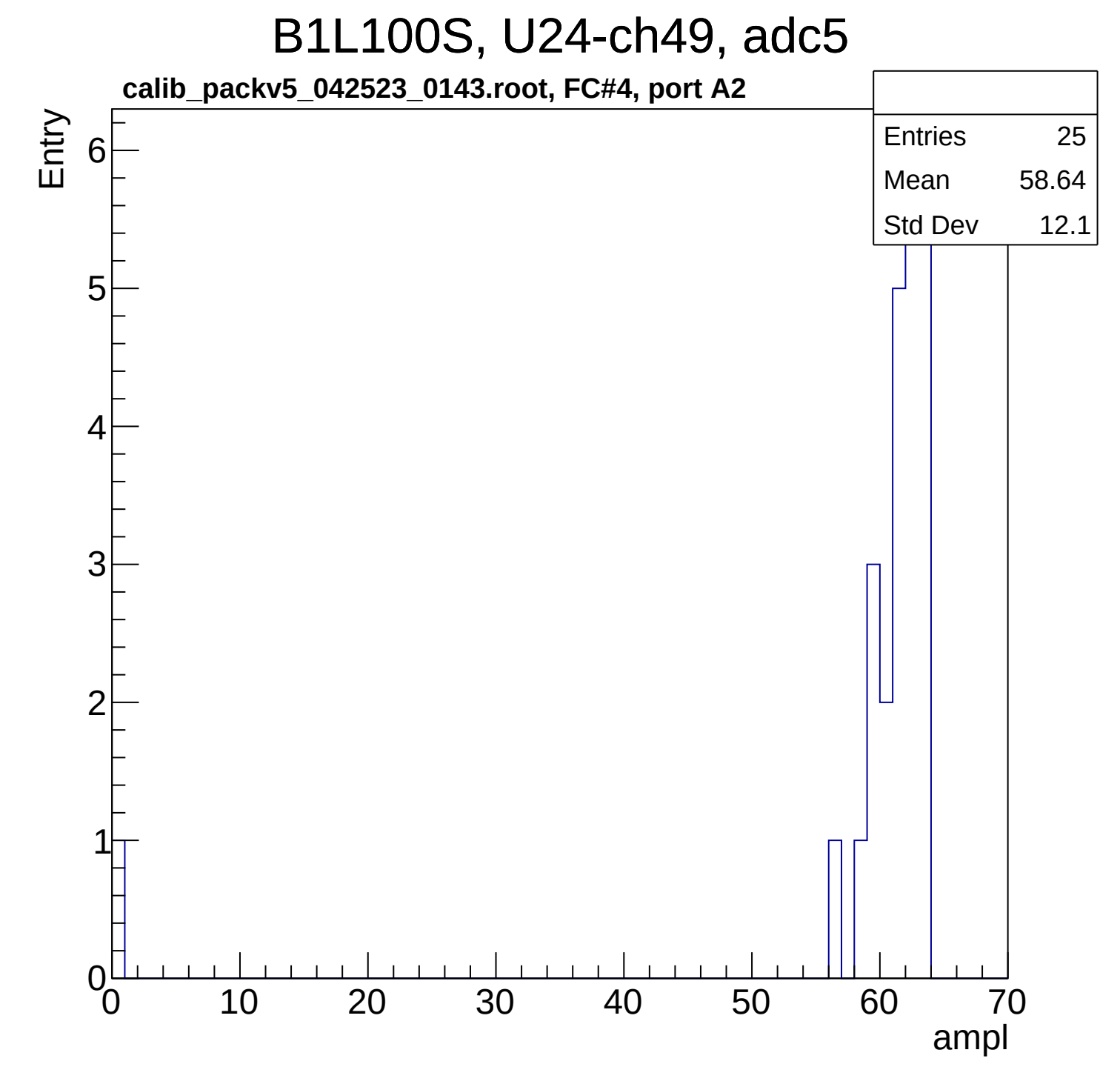
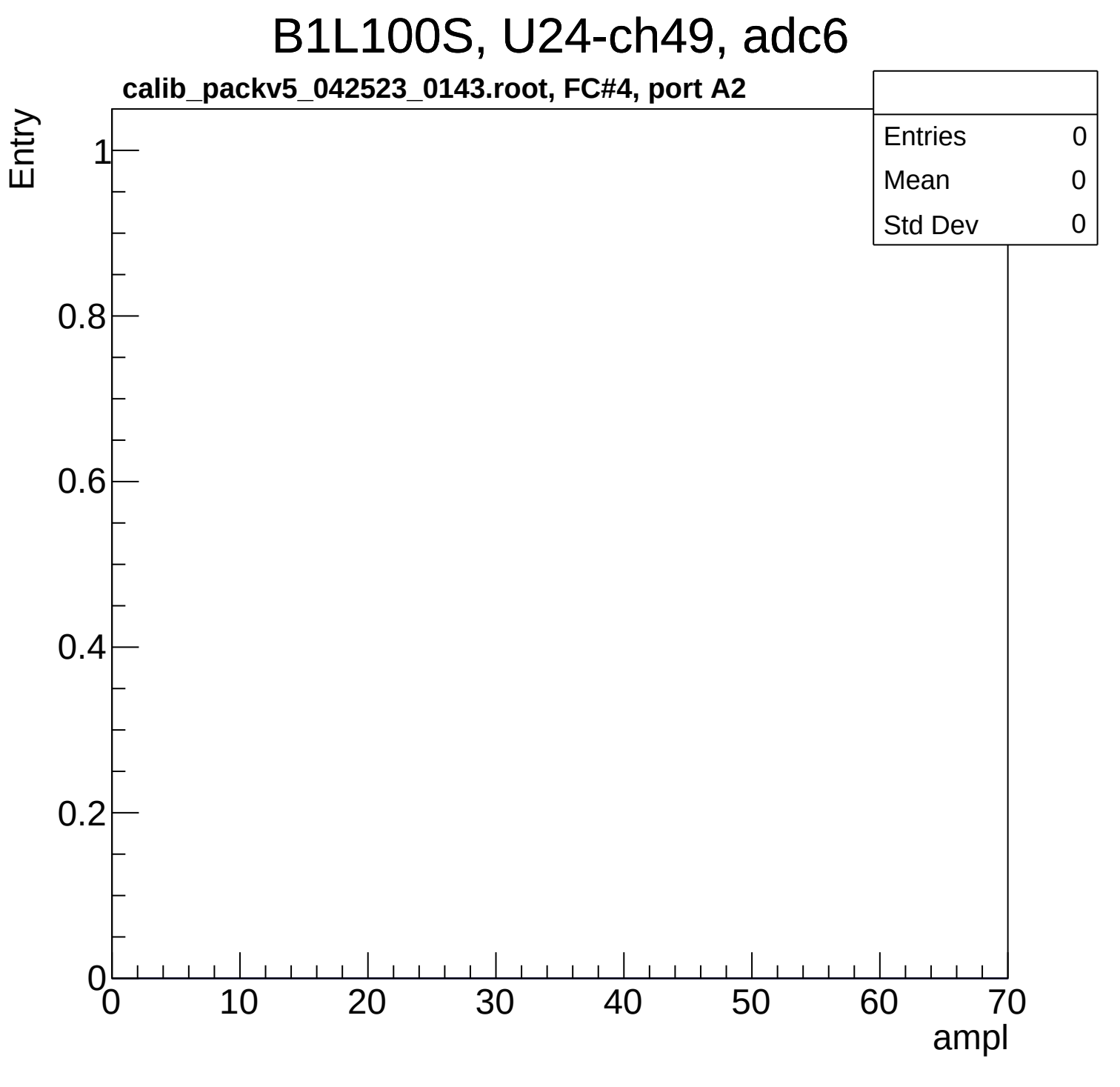
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch49, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.64
Std Dev 12.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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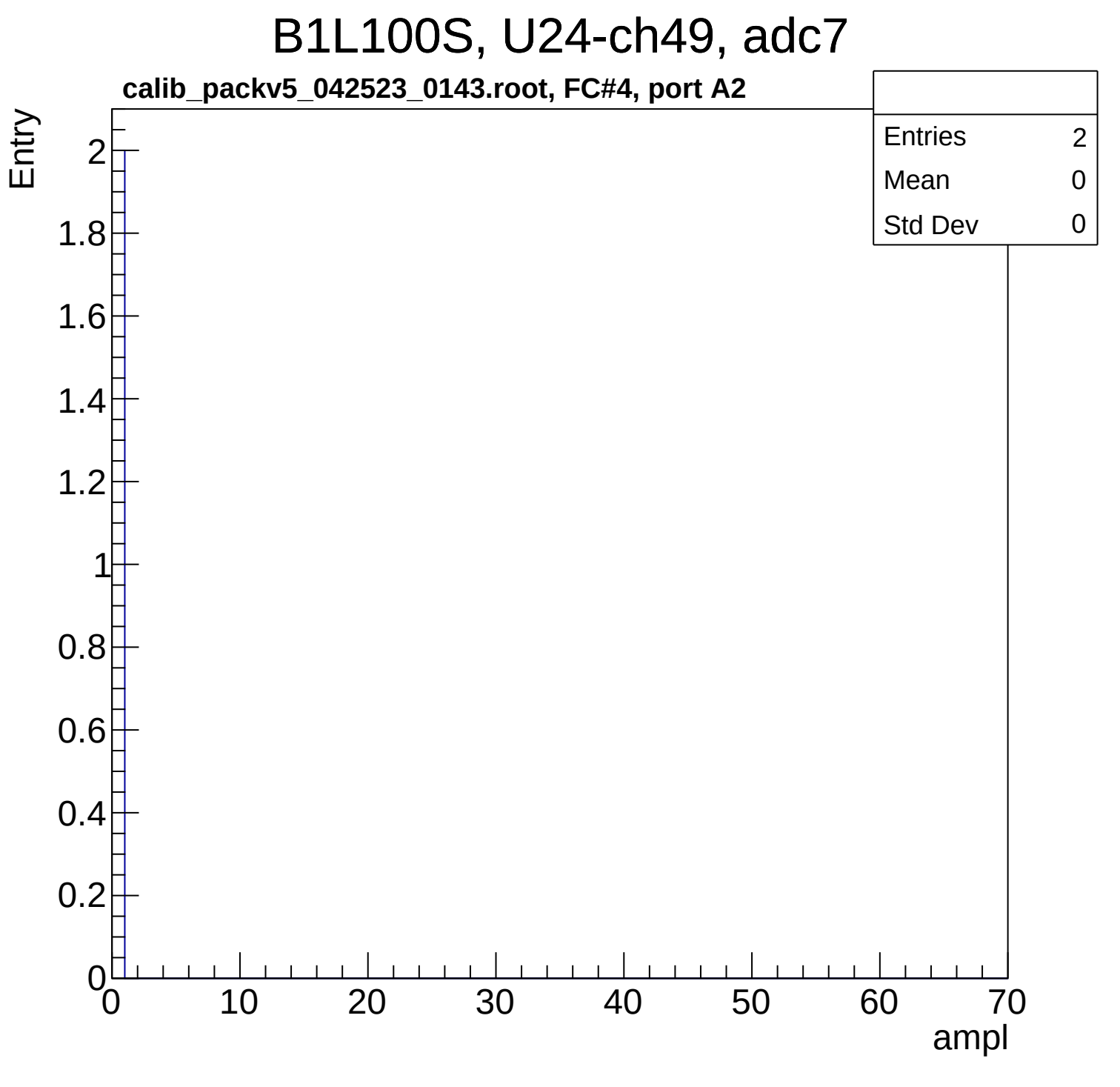
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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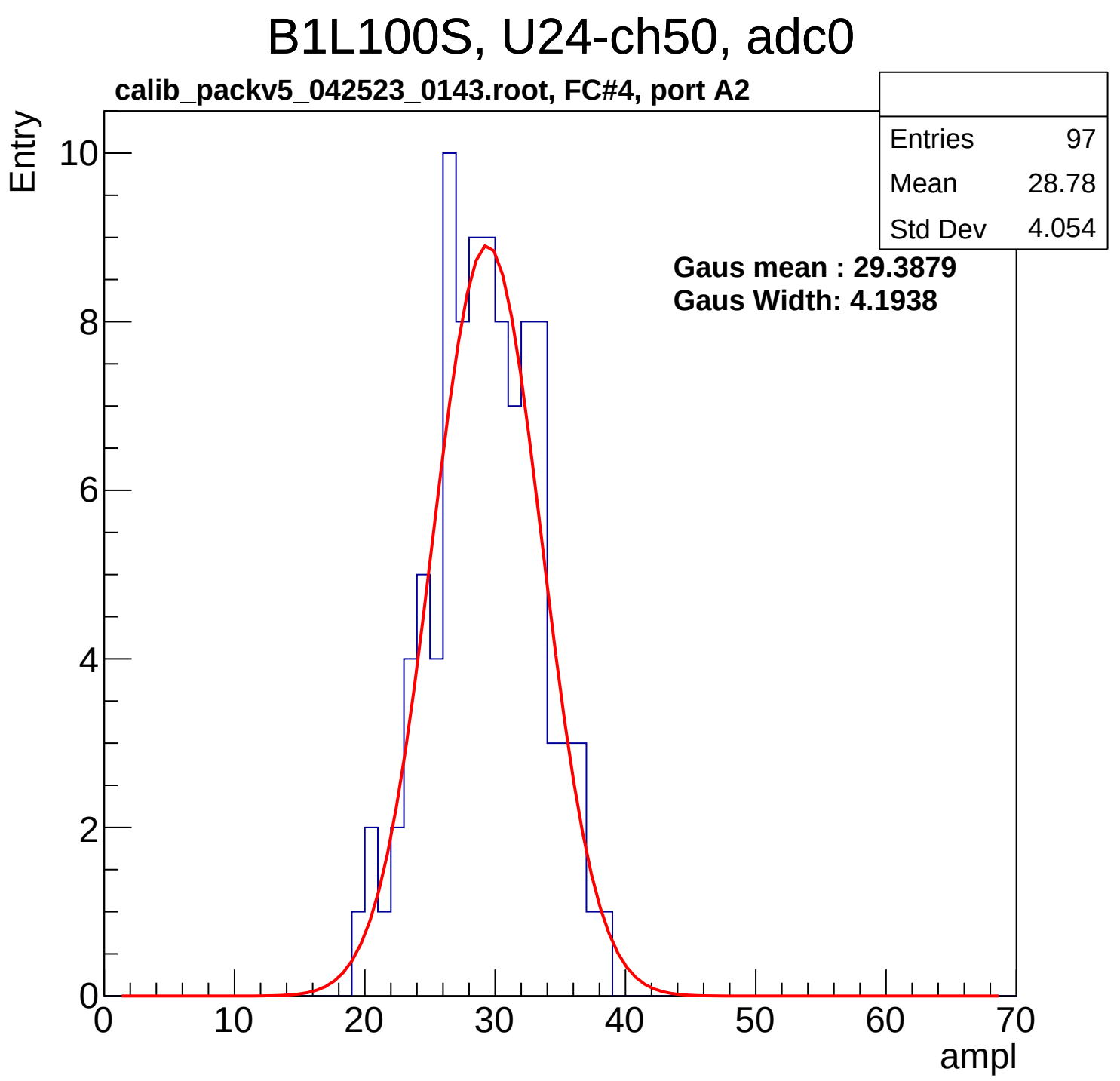
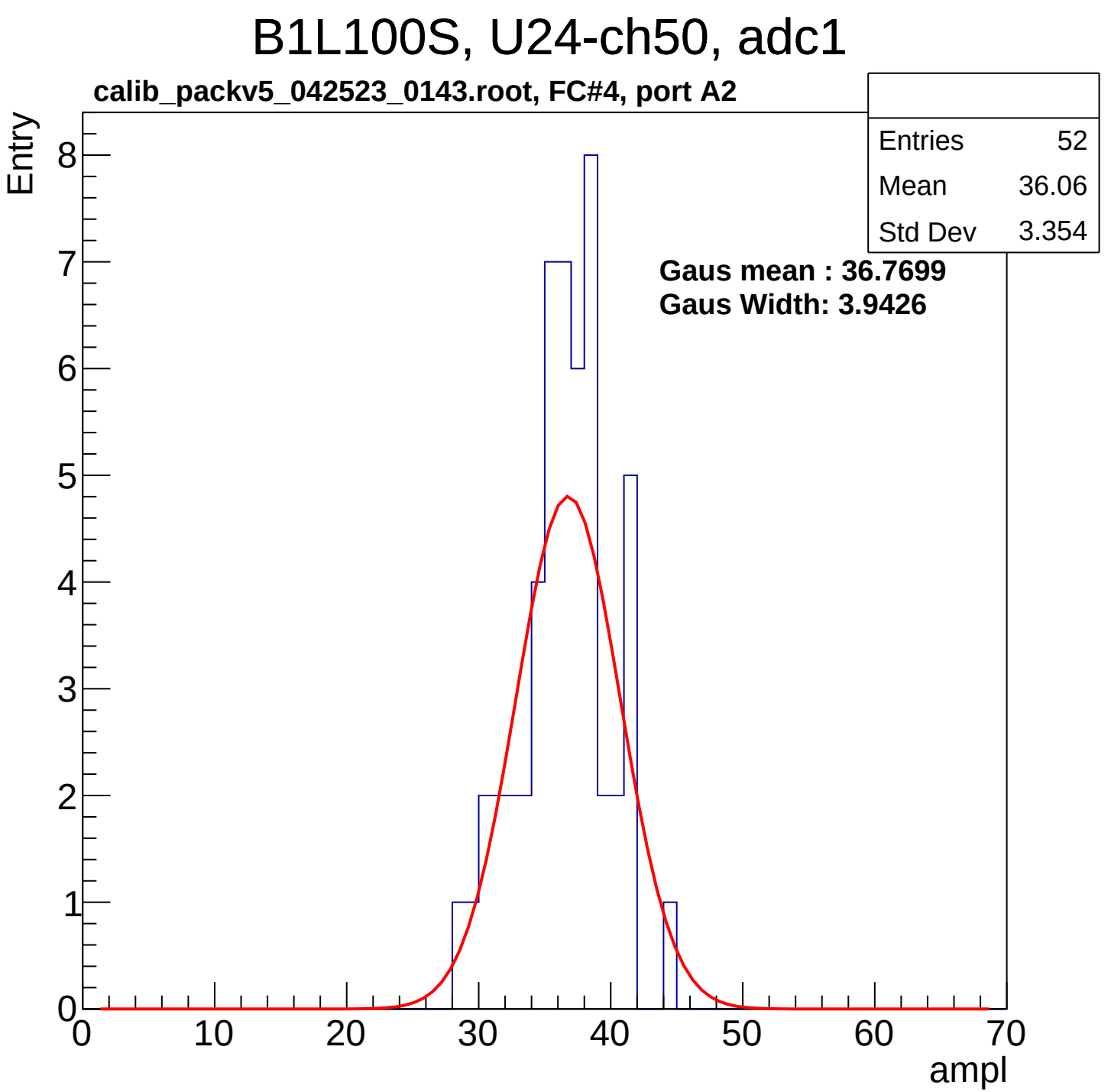
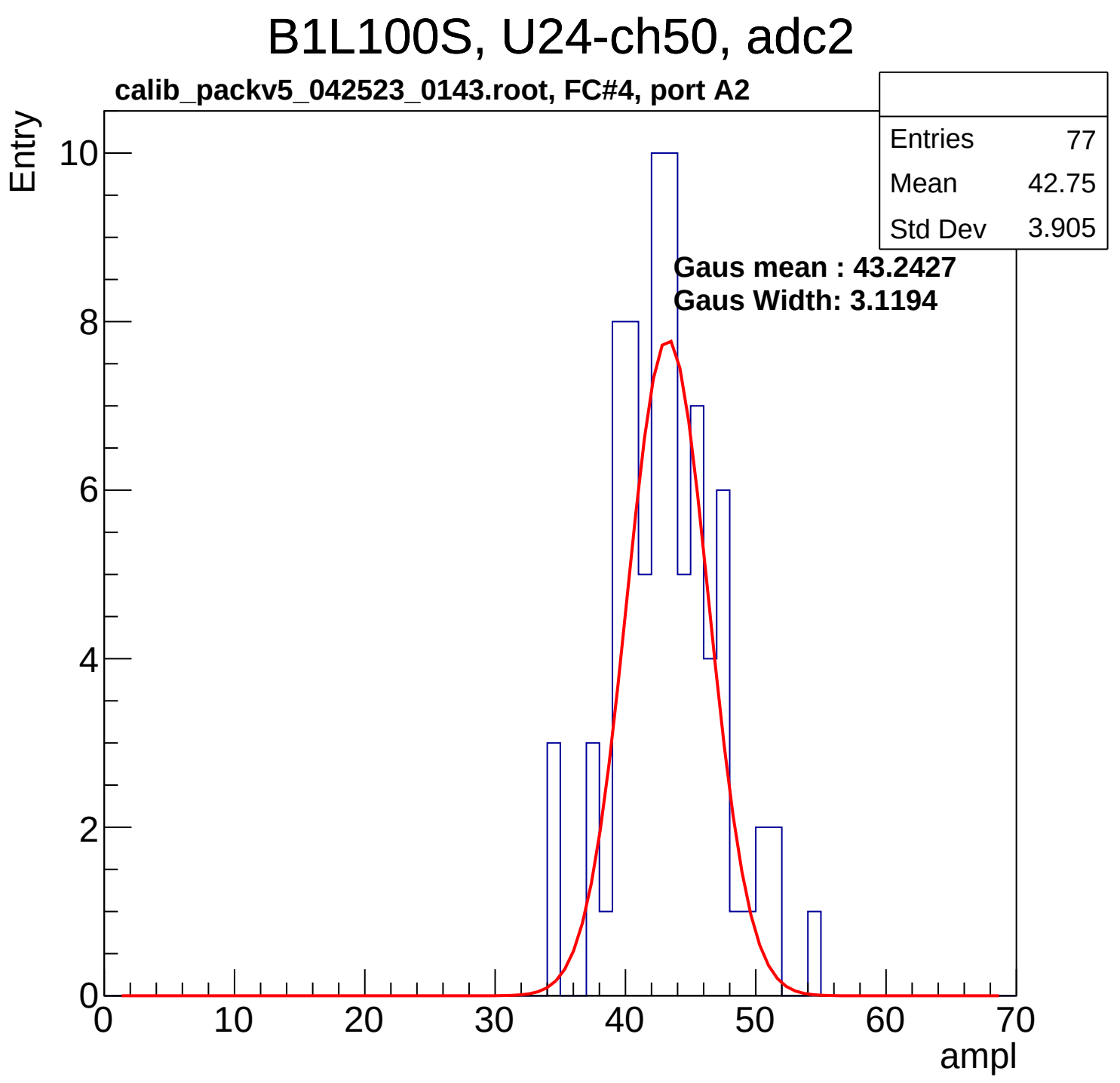
50
60
70
ampl


B1L100S, U24-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 97
Mean 28.78
Std Dev 4.054
Gaus mean : 29.3879
Gaus Width: 4.1938
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 36.06
Std Dev 3.354
Gaus mean : 36.7699
Gaus Width: 3.9426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 42.75
Std Dev 3.905
Gaus mean : 43.2427
Gaus Width: 3.1194
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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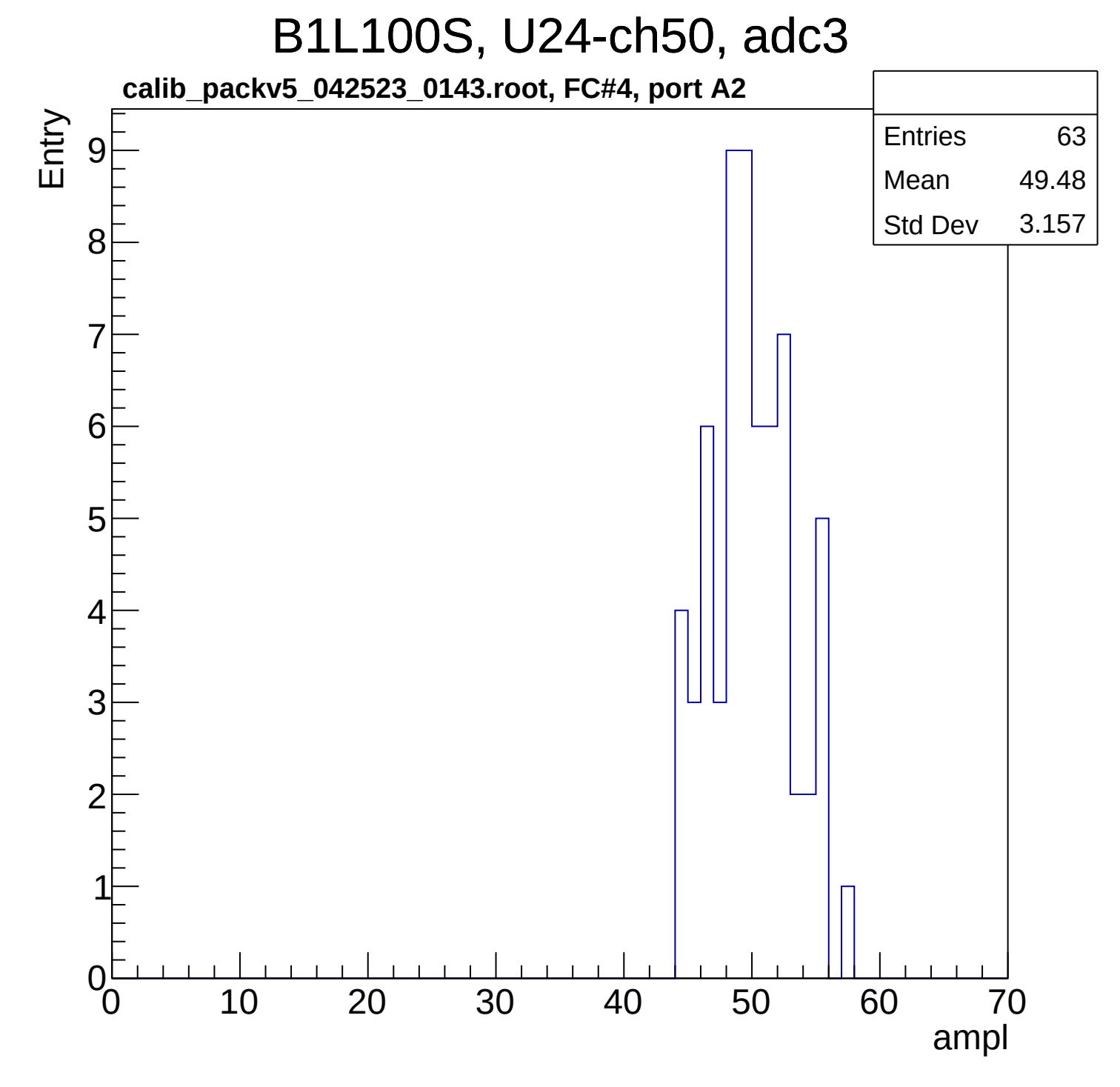
B1L100S, U24-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.48
Std Dev 3.157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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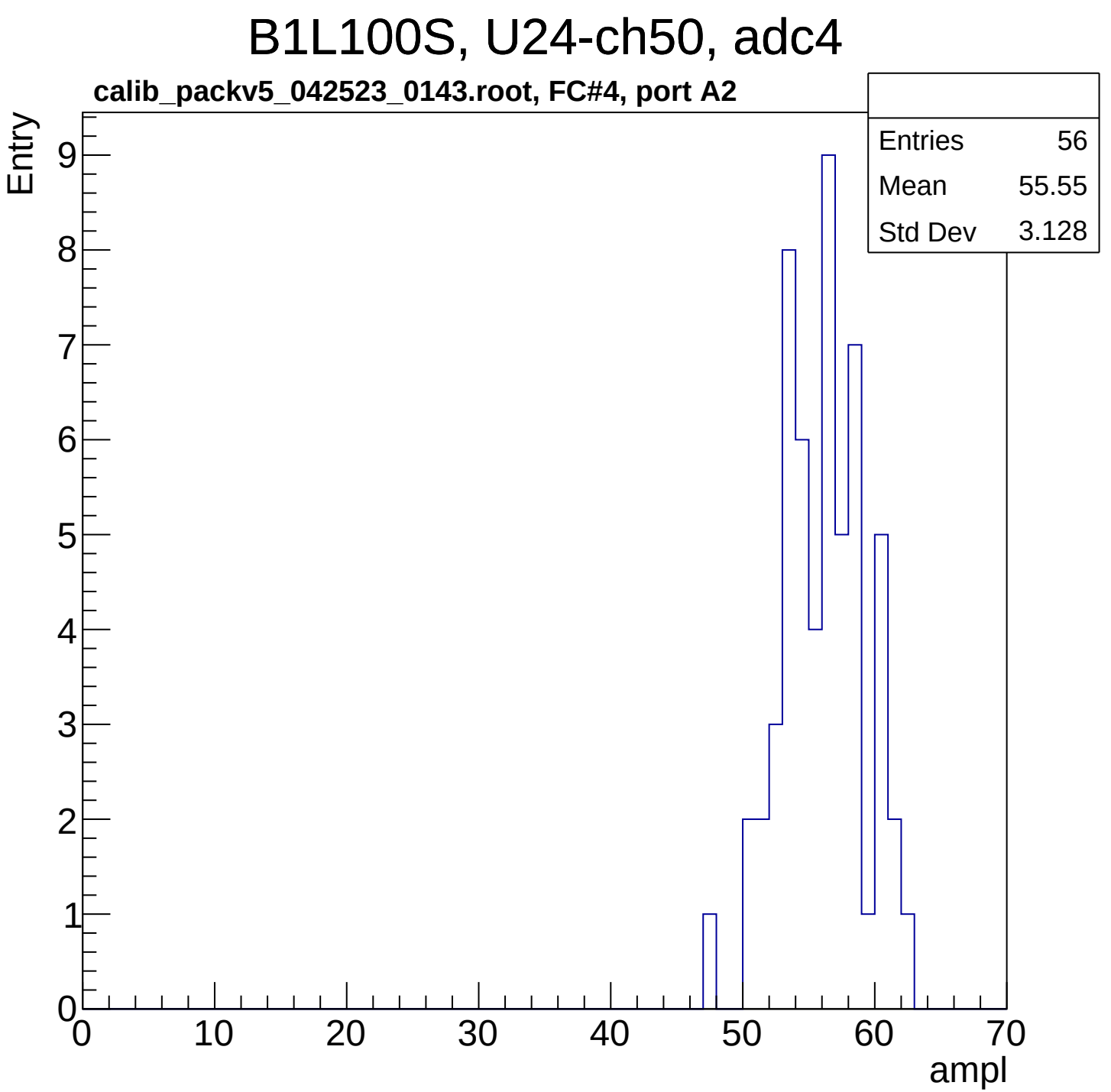
B1L100S, U24-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.55
Std Dev 3.128
Entry
ampl
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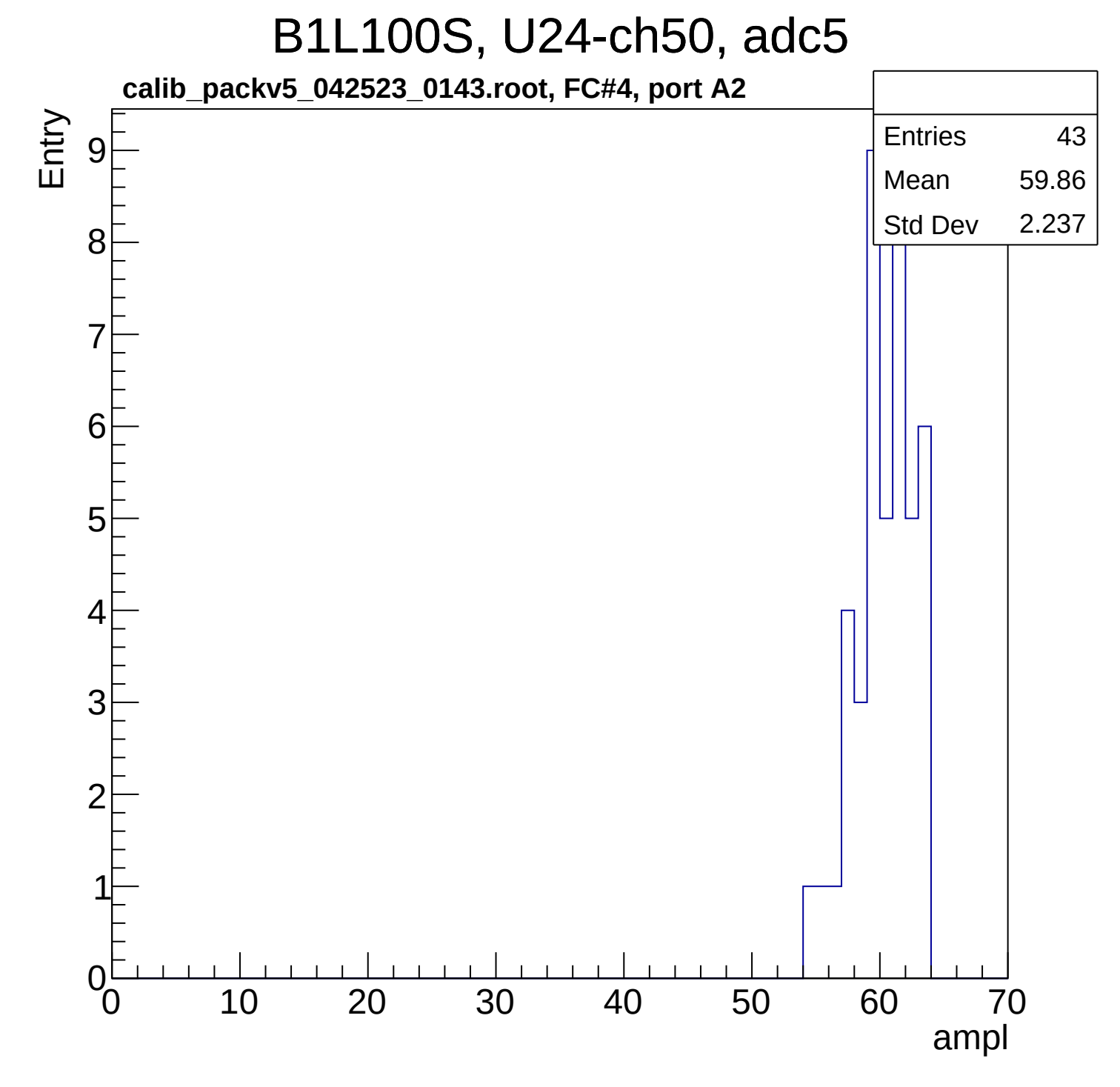
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59.86
Std Dev 2.237
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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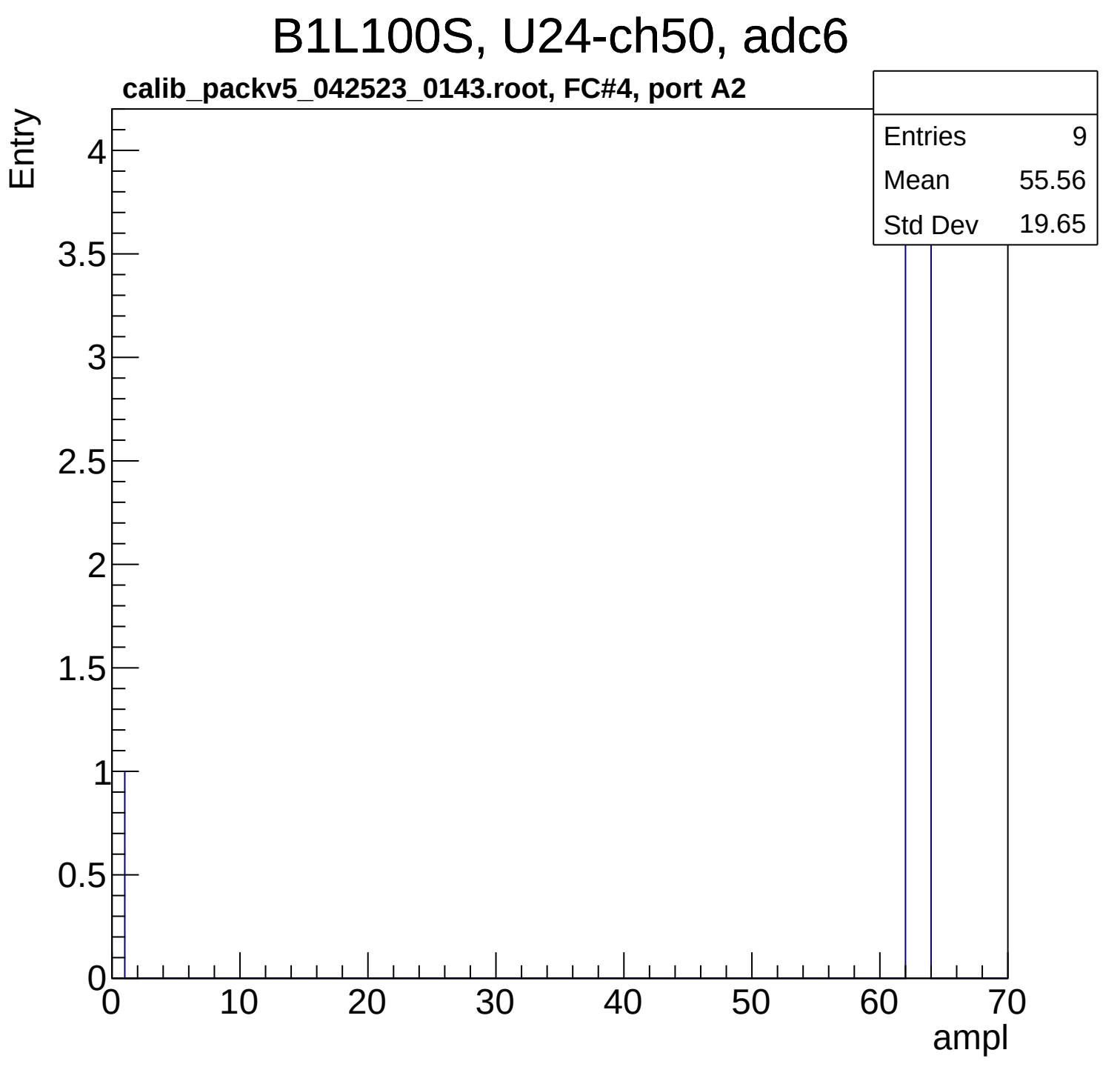
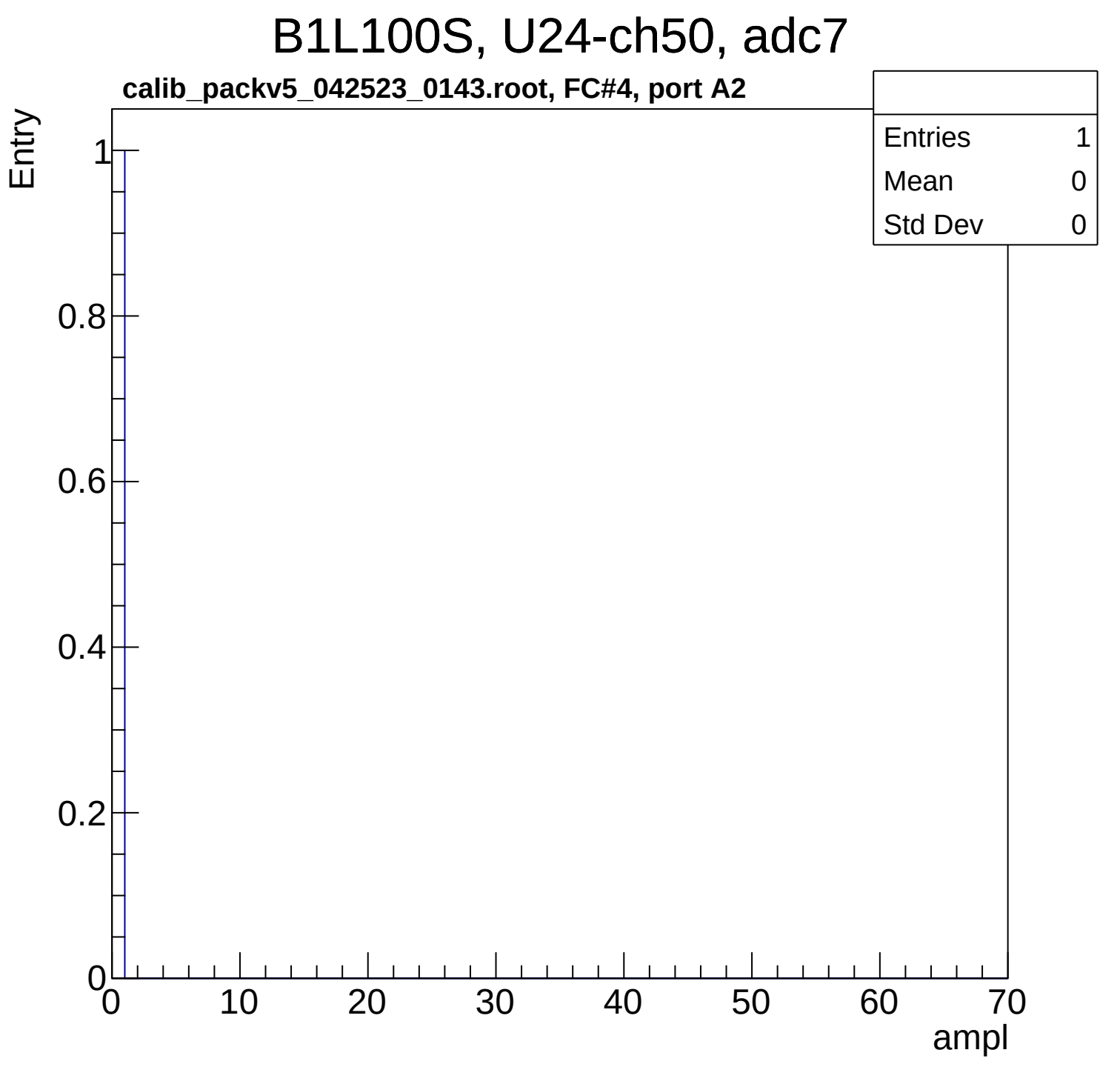
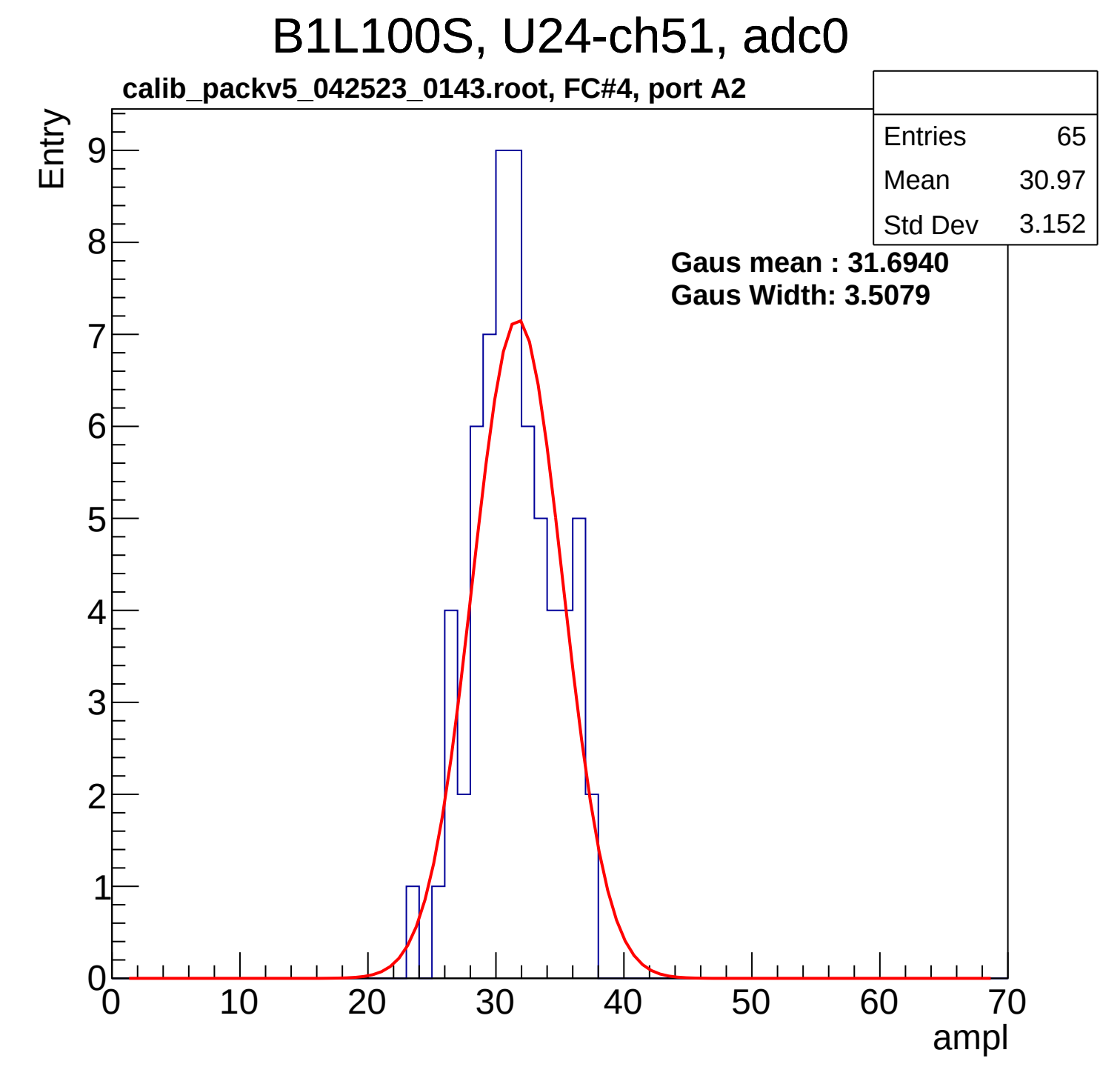
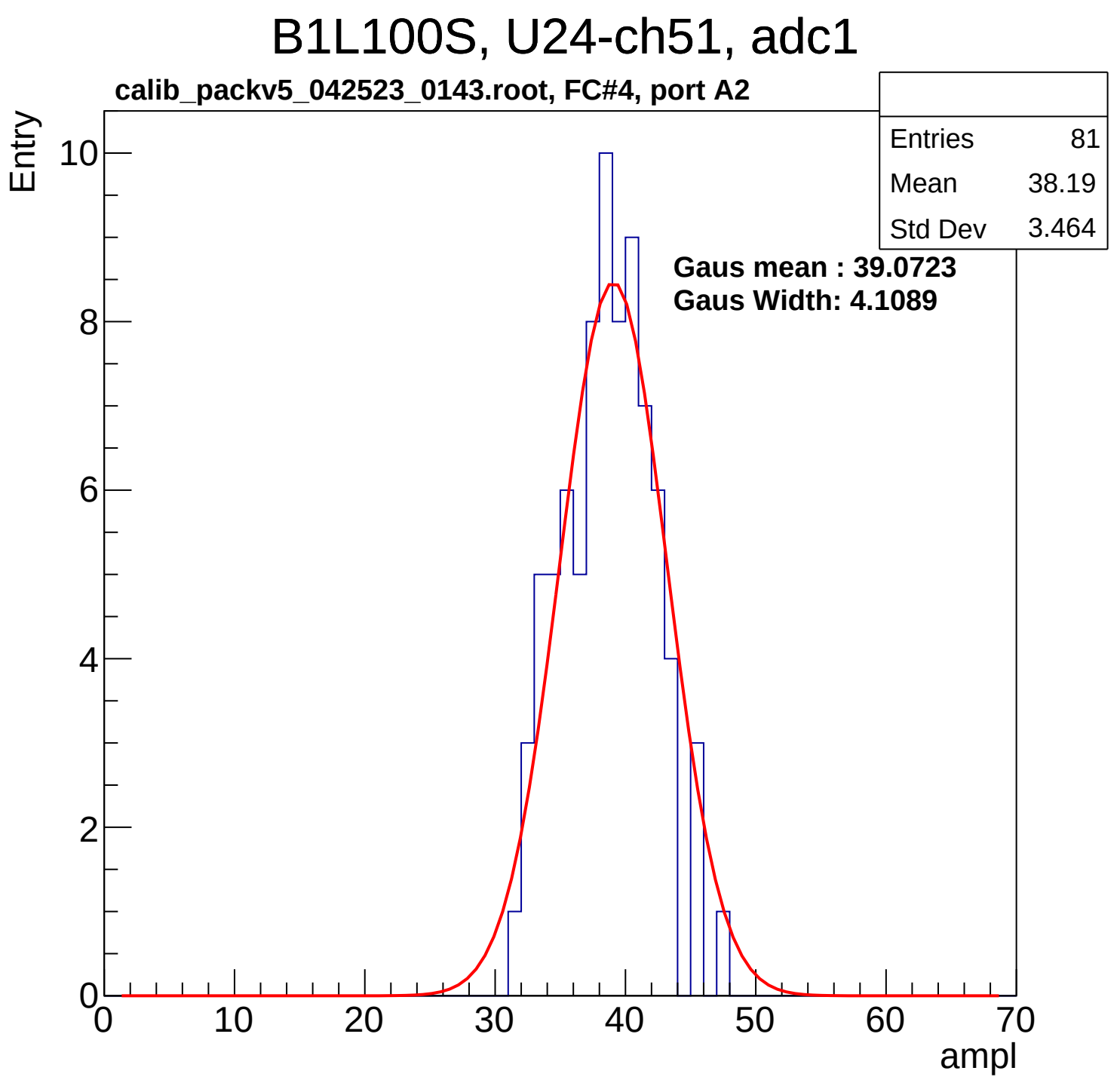
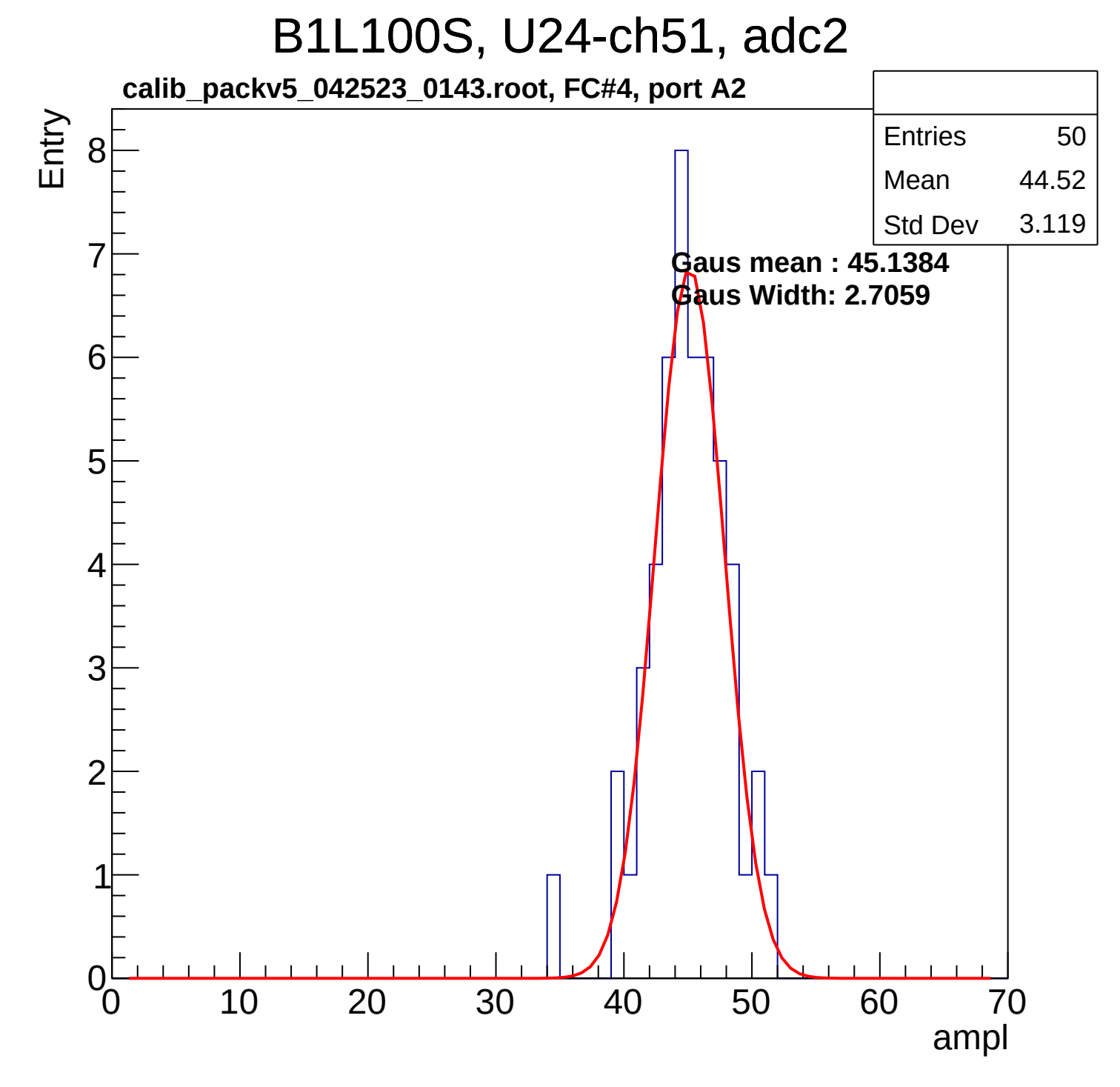
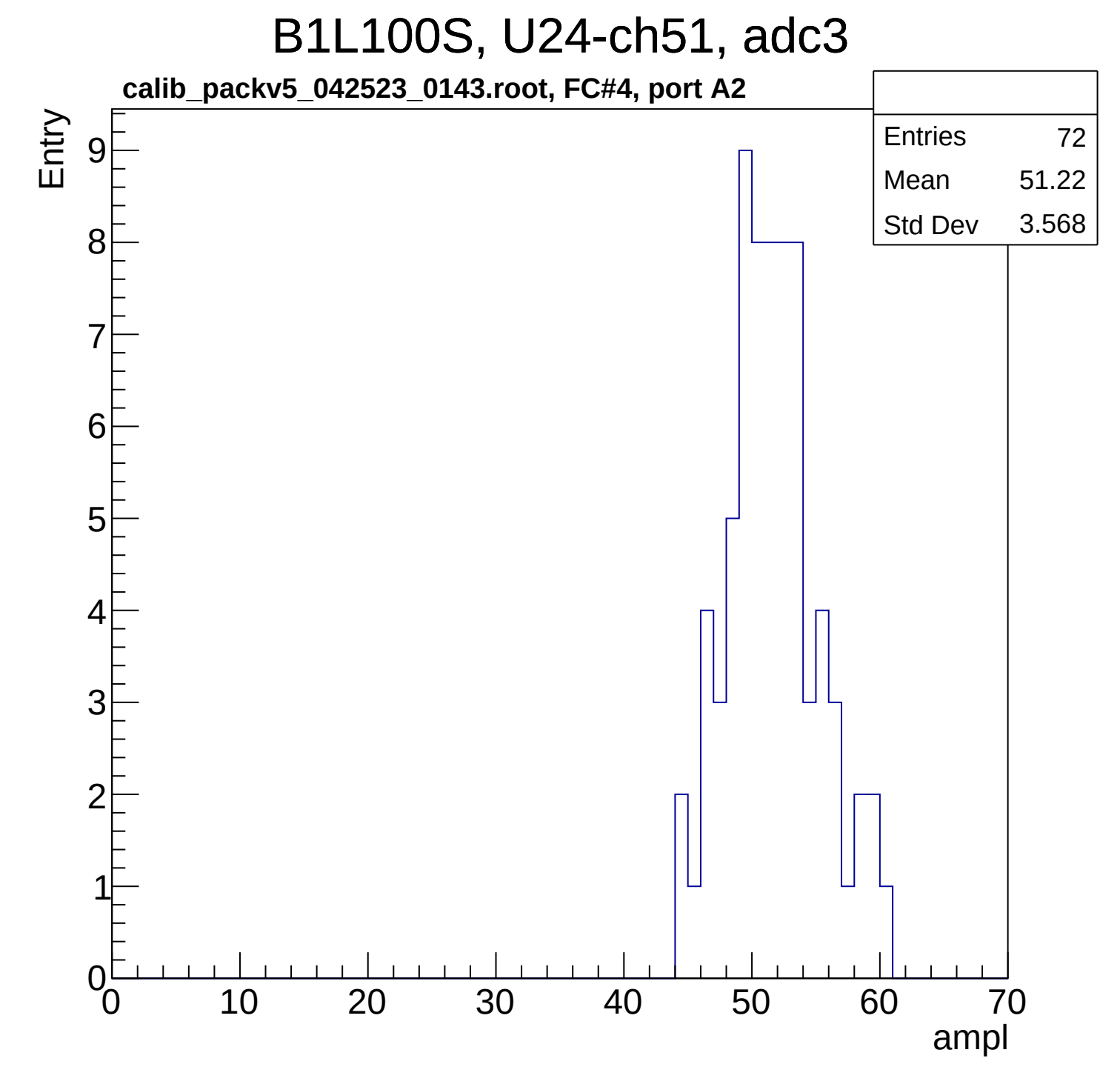

B1L100S, U24-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 55.56
Std Dev 19.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

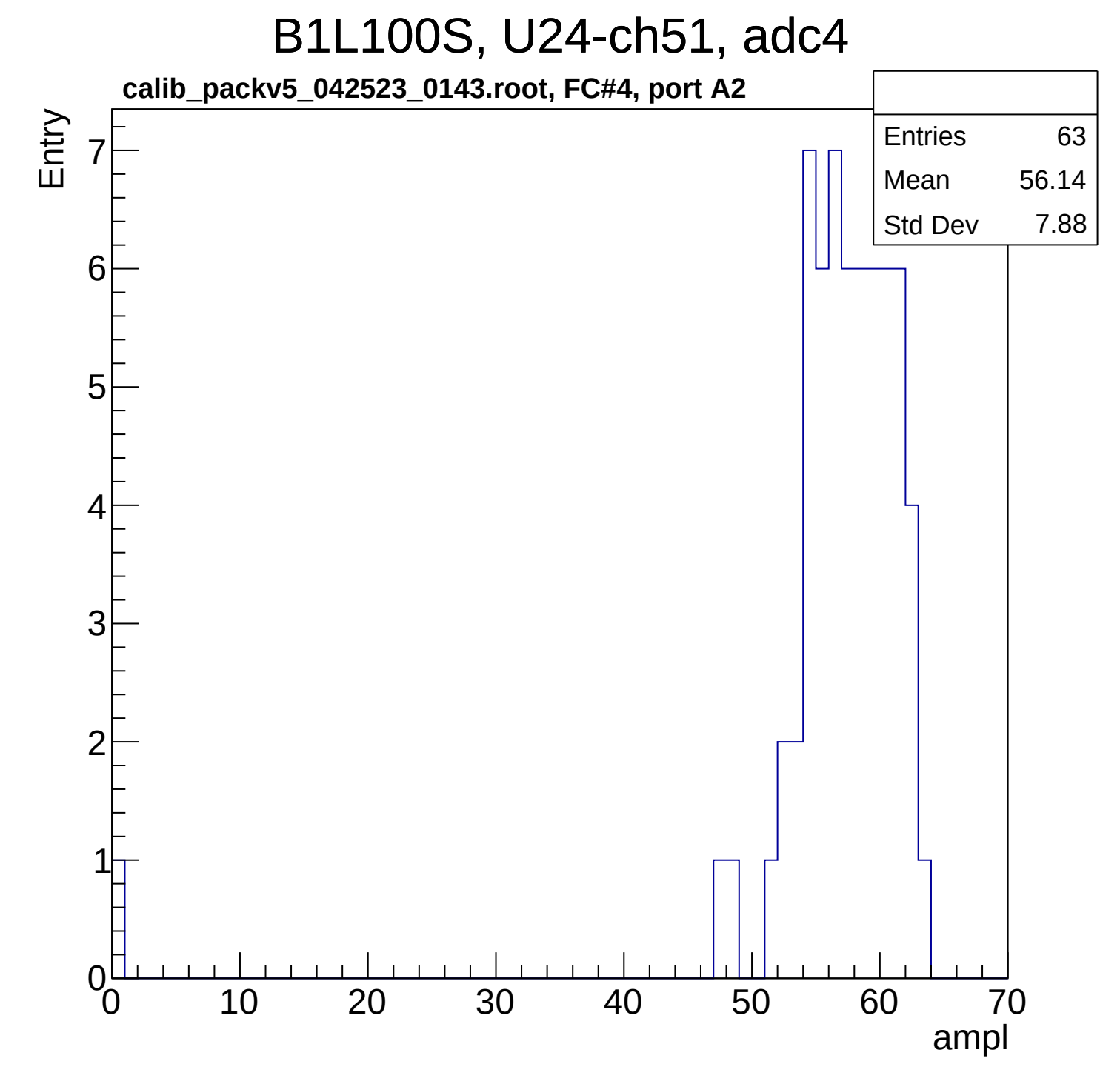

B1L100S, U24-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.97
Std Dev 3.152
Gaus mean : 31.6940
Gaus Width: 3.5079
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 38.19
Std Dev 3.464
Gaus mean : 39.0723
Gaus Width: 4.1089
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 44.52
Std Dev 3.119
Gaus mean : 45.1384
Gaus Width: 2.7059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch51, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 51.22
Std Dev 3.568
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch51, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.14
Std Dev 7.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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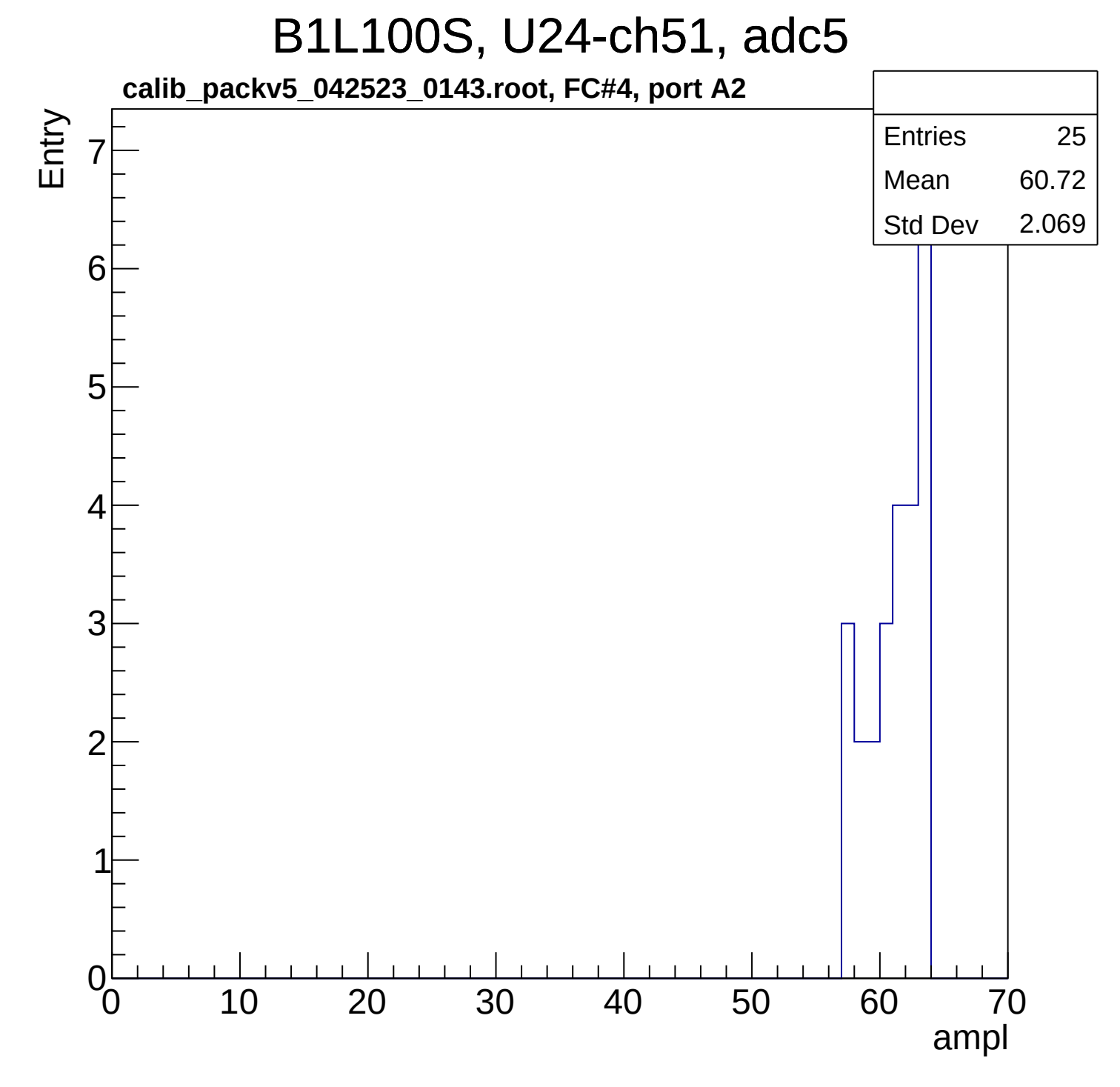
B1L100S, U24-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 60.72
Std Dev 2.069
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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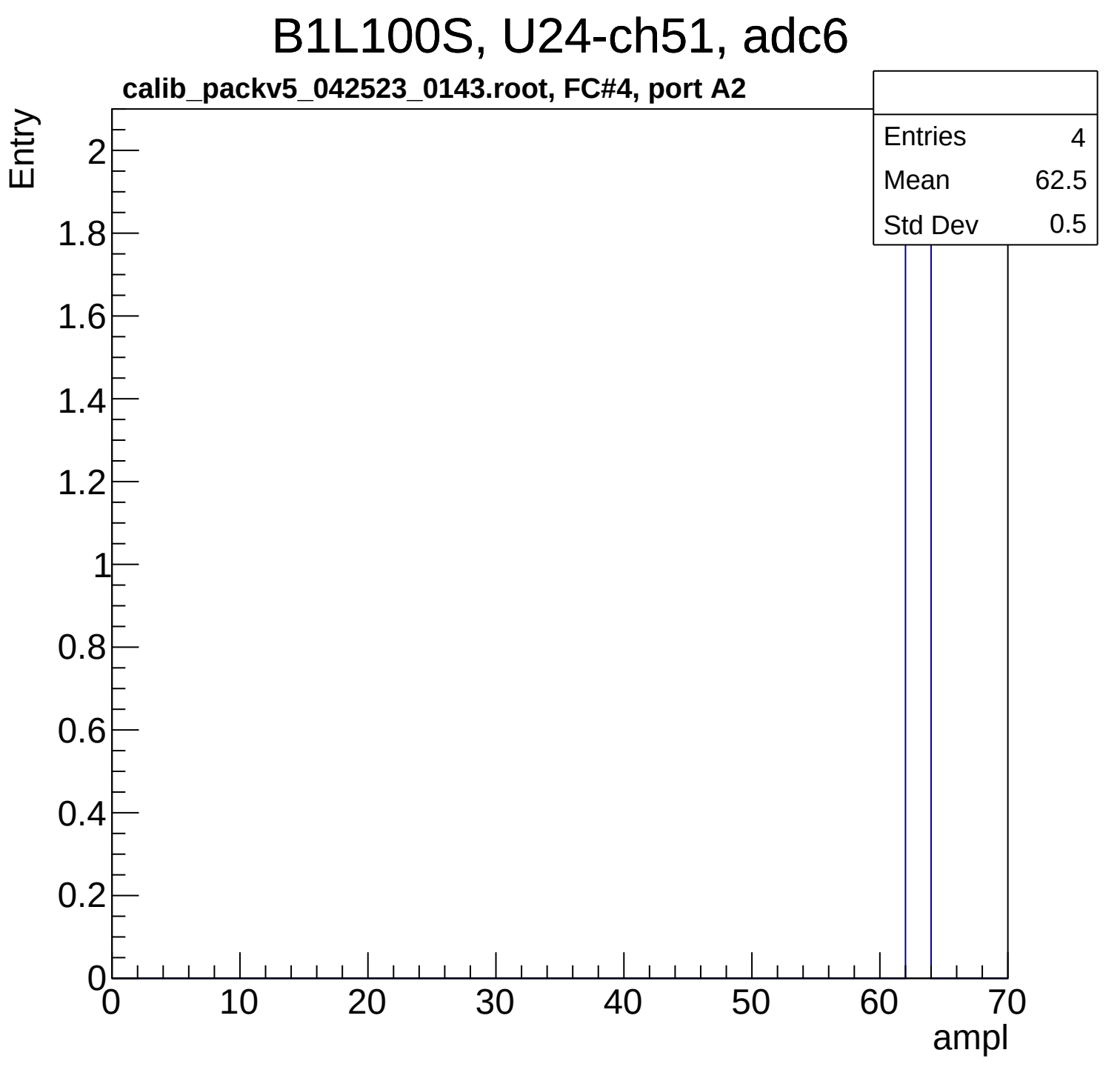
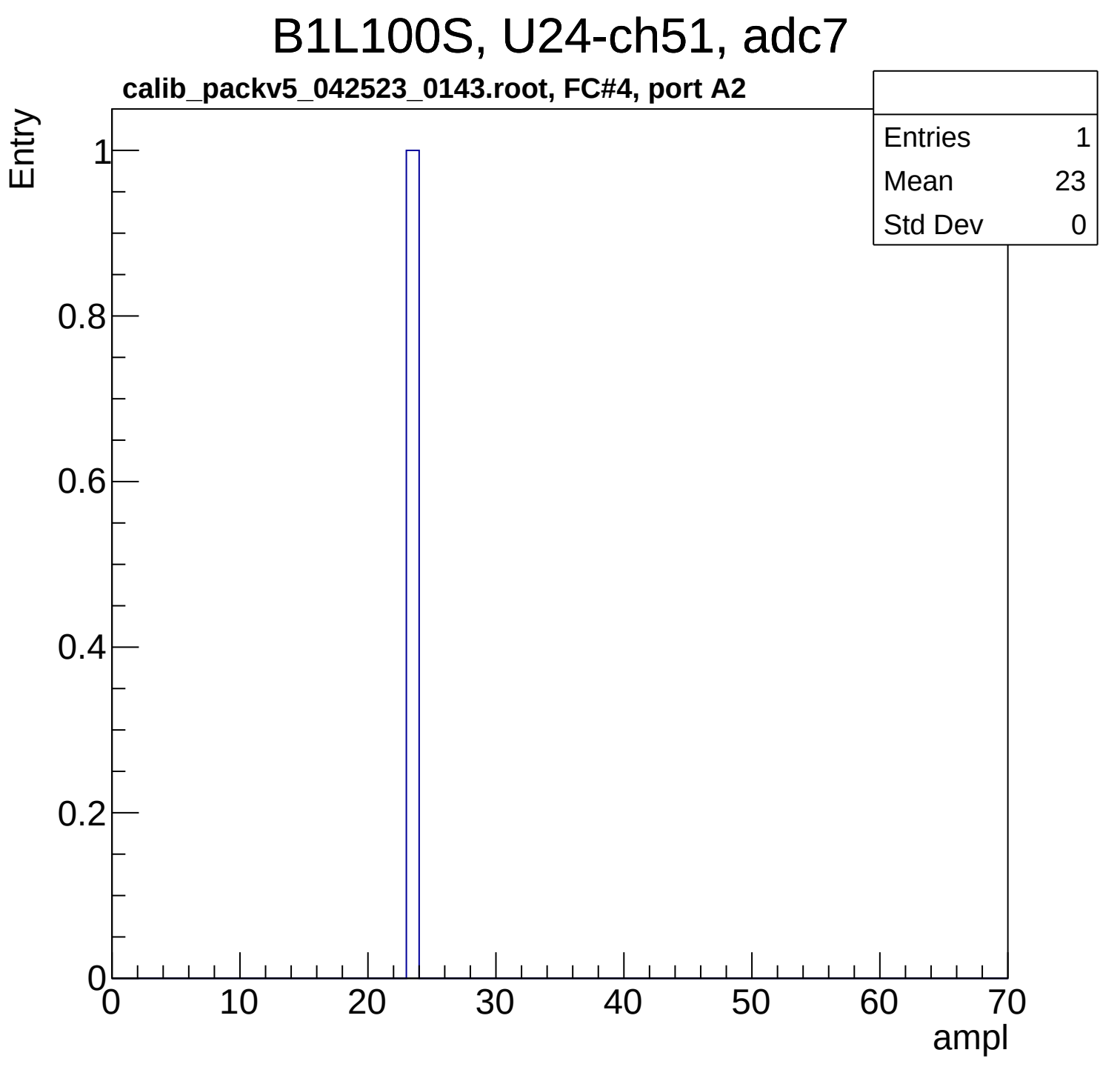
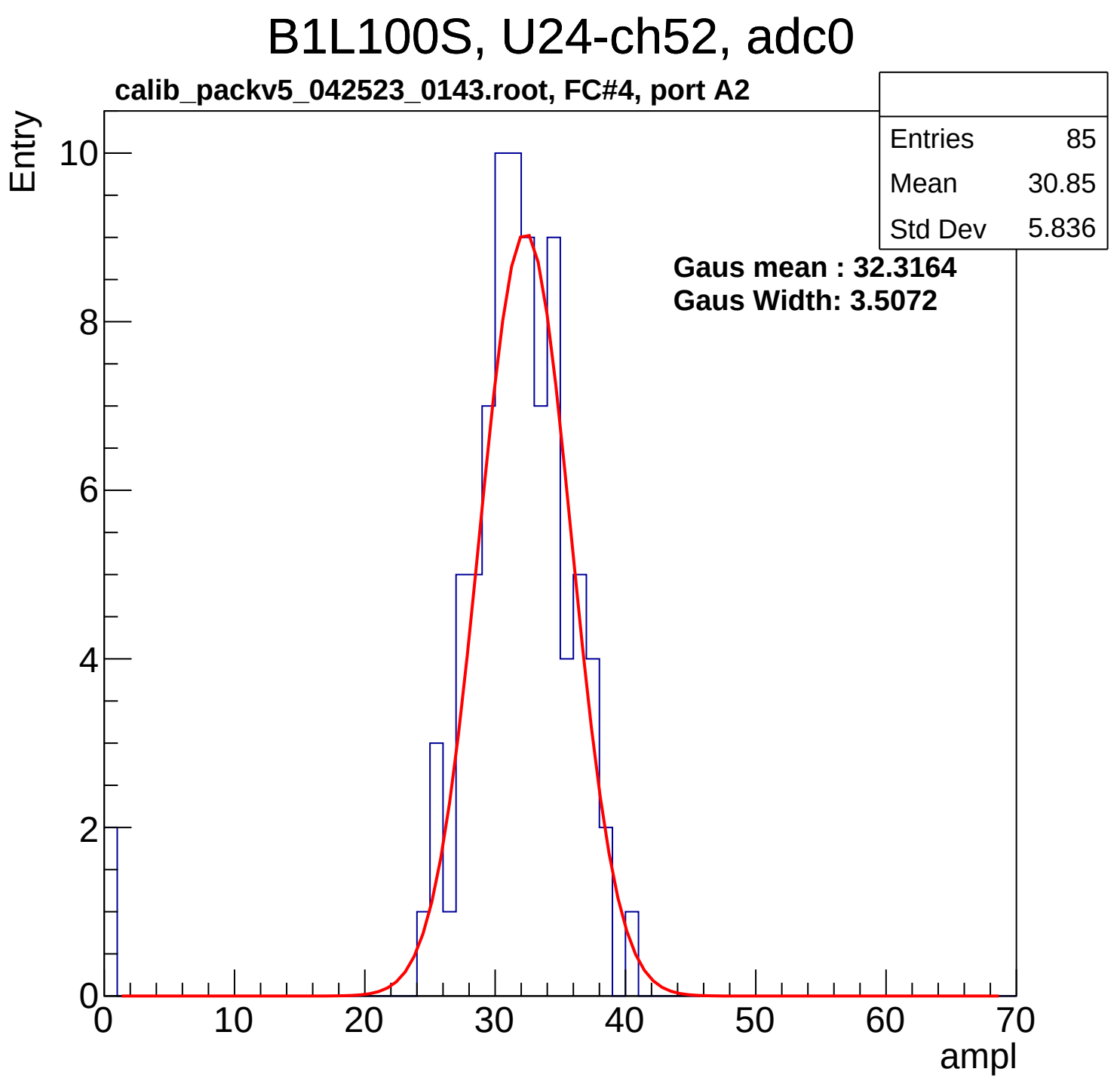
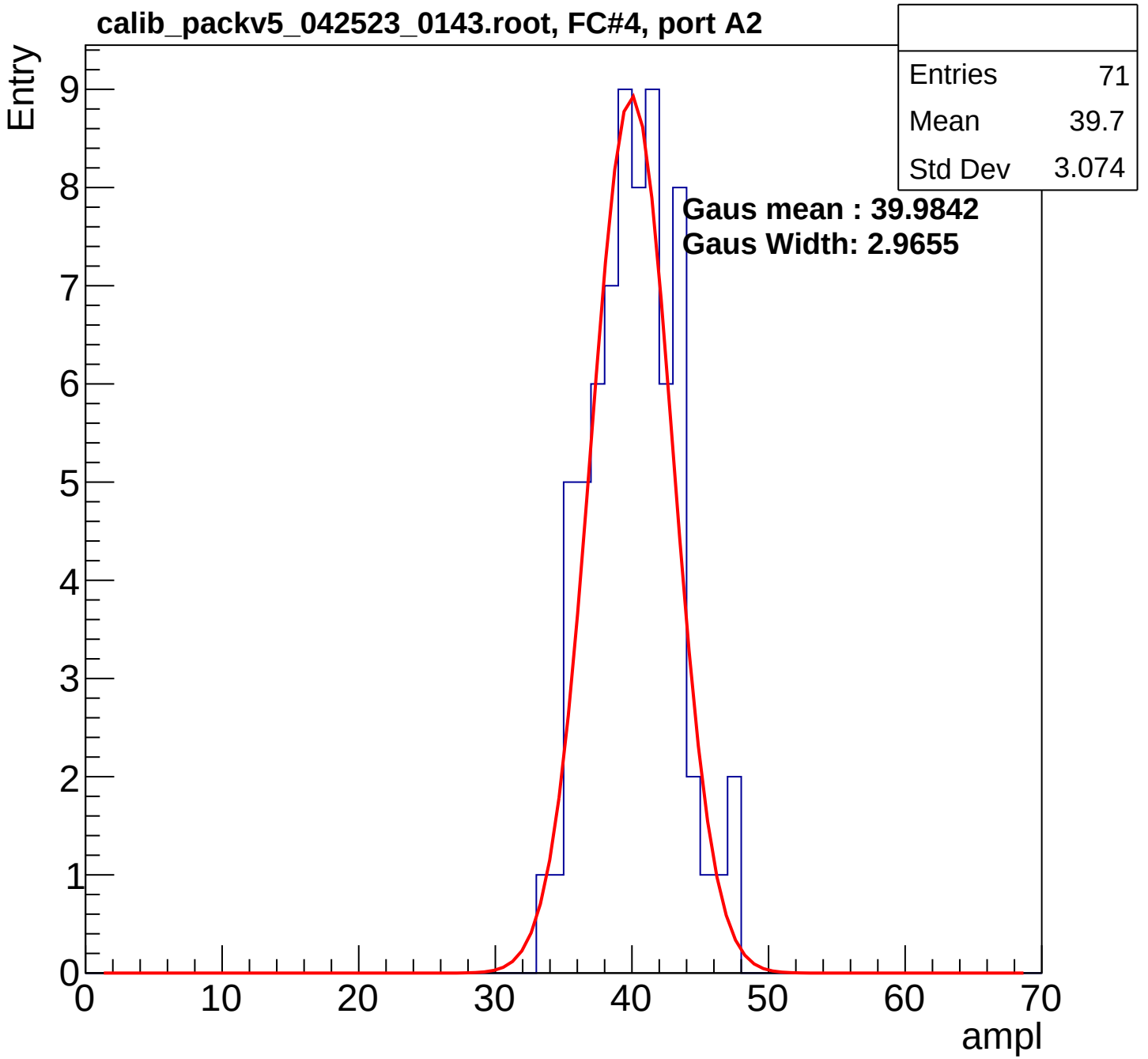
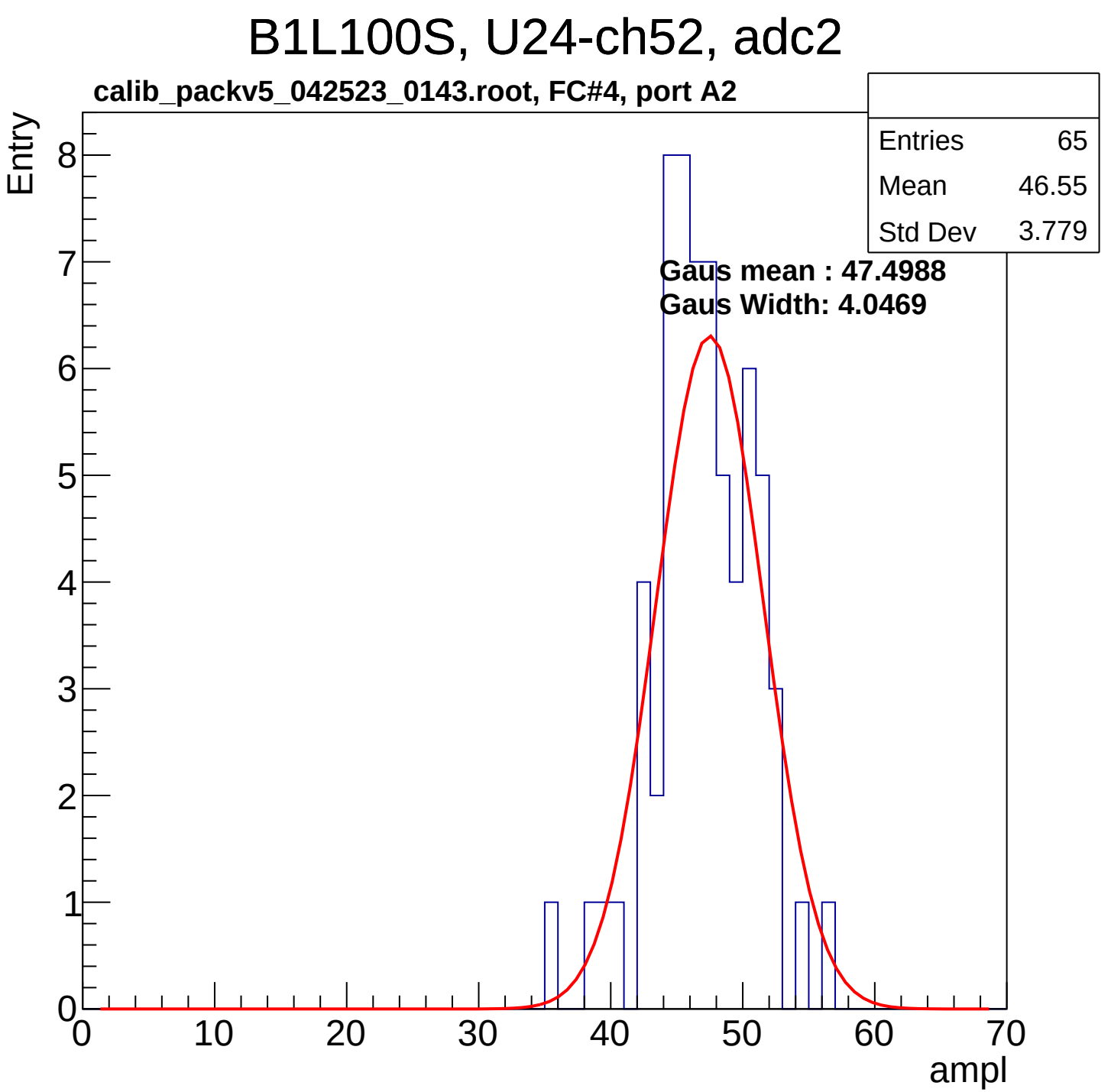
40
50
60
70

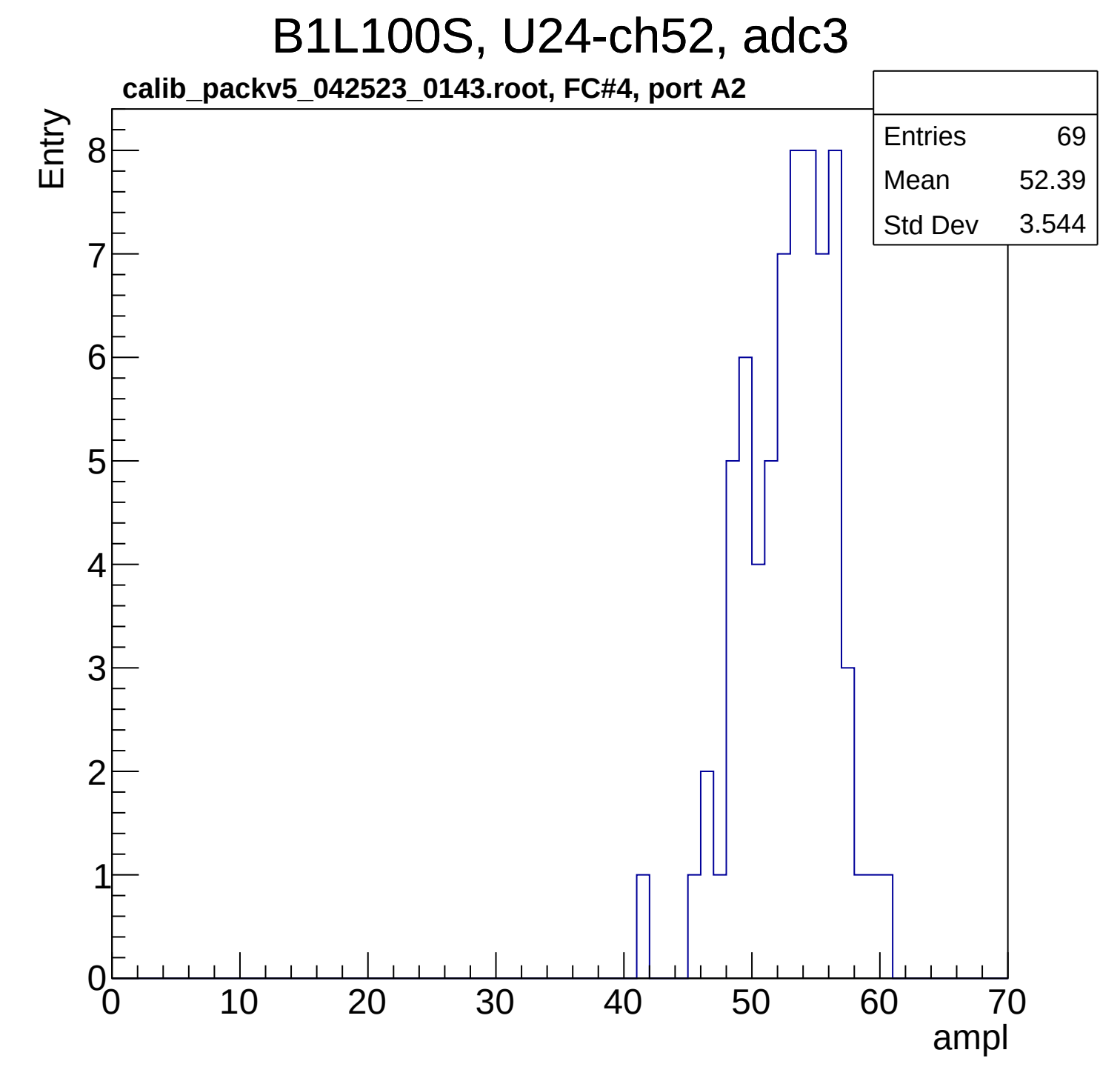

B1L100S, U24-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 30.85
Std Dev 5.836
Gaus mean : 32.3164
Gaus Width: 3.5072
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 39.7
Std Dev 3.074
Gaus mean : 39.9842
Gaus Width: 2.9655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 46.55
Std Dev 3.779
Gaus mean : 47.4988
Gaus Width: 4.0469
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch52, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 52.39
Std Dev 3.544
Entry
ampl
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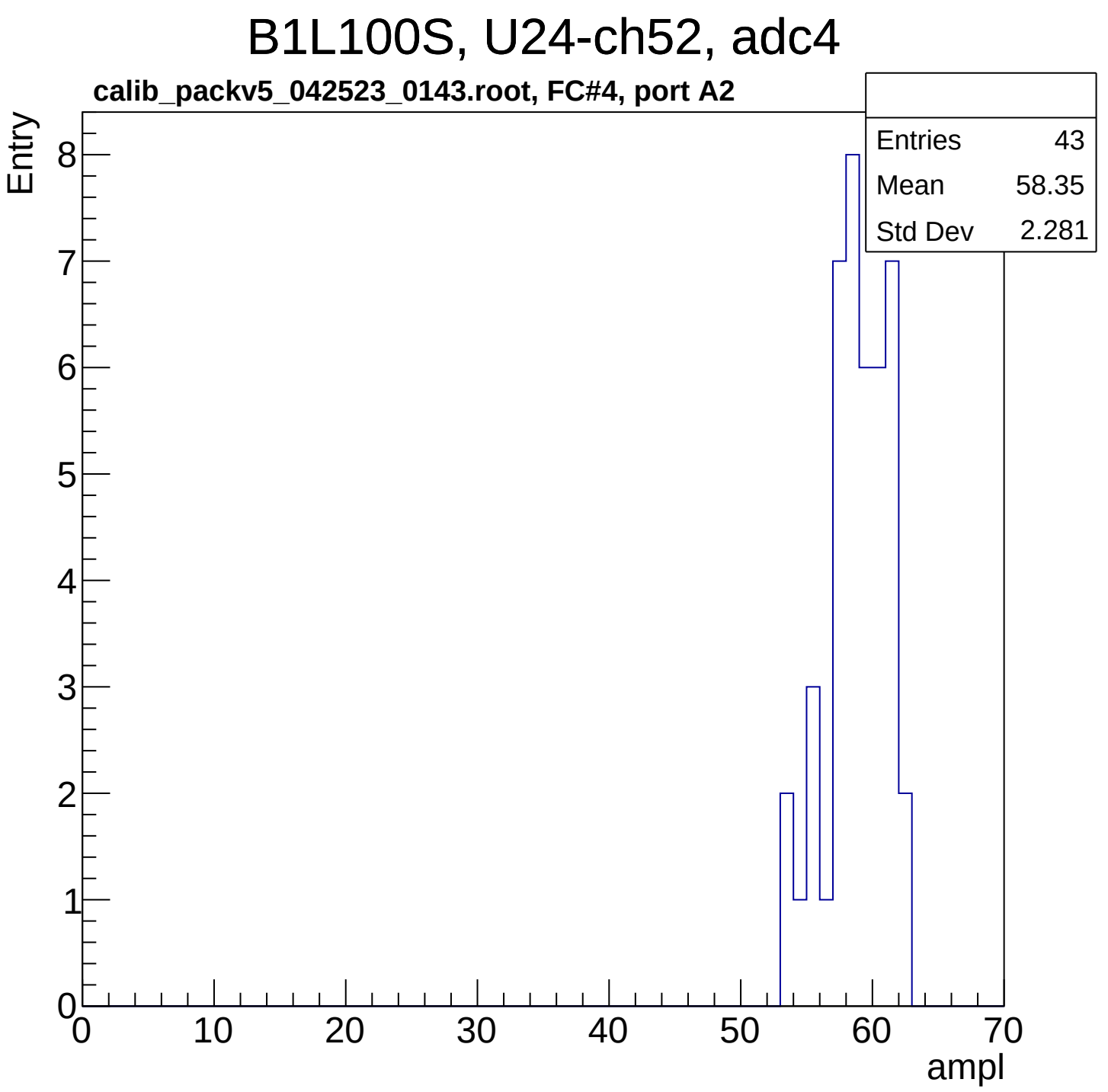
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.35
Std Dev 2.281
Entry
ampl
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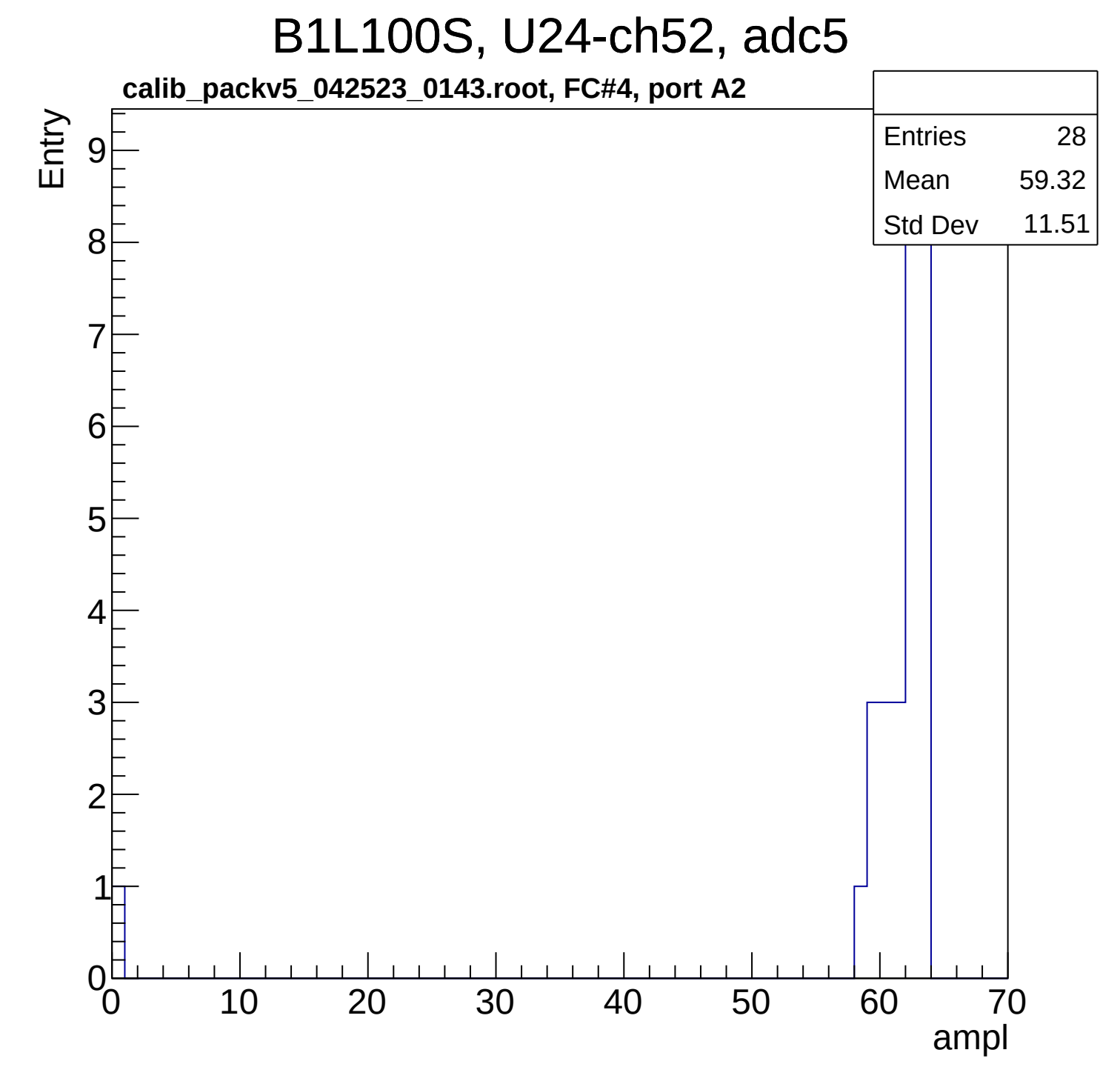
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 59.32
Std Dev 11.51
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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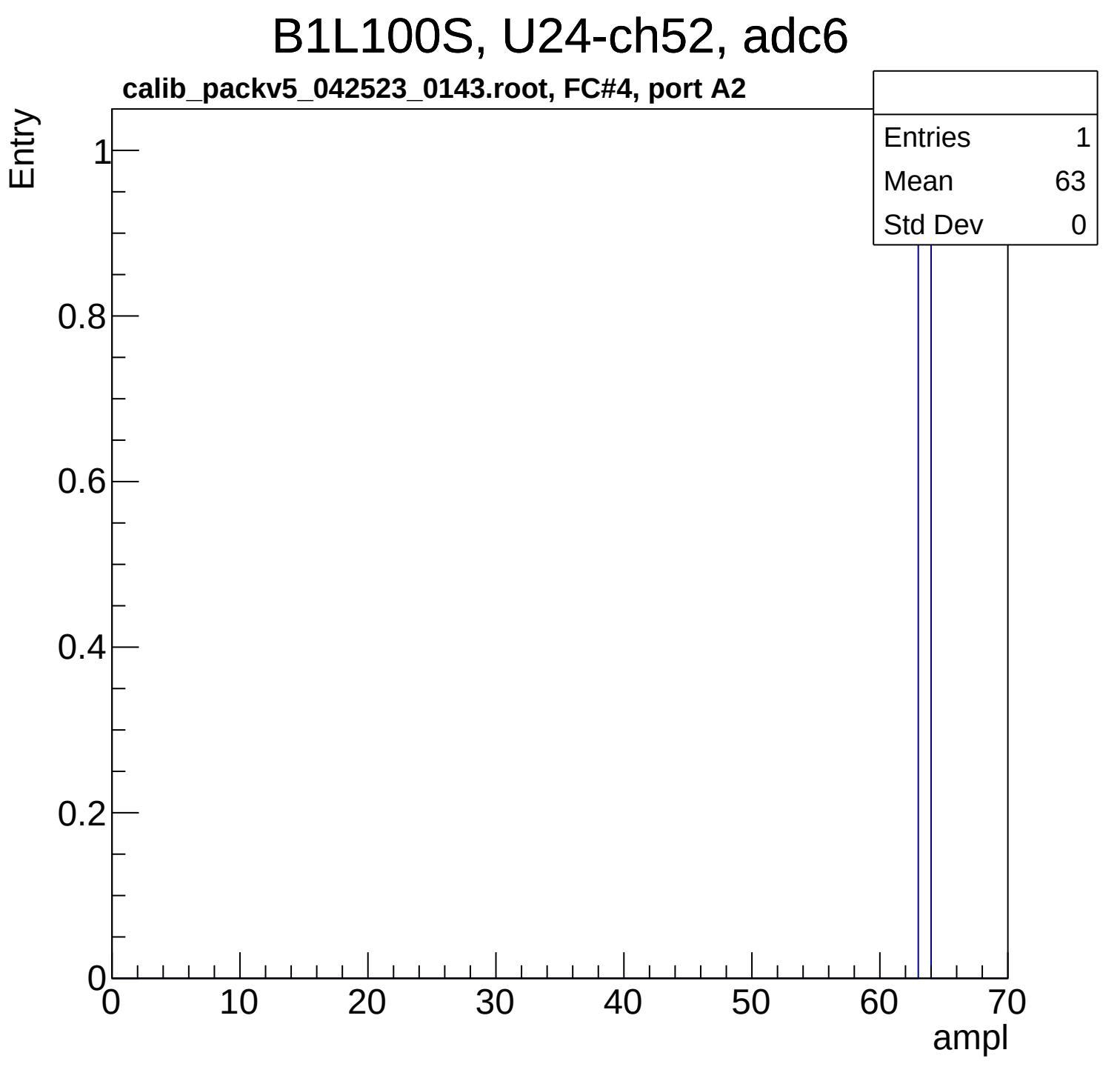
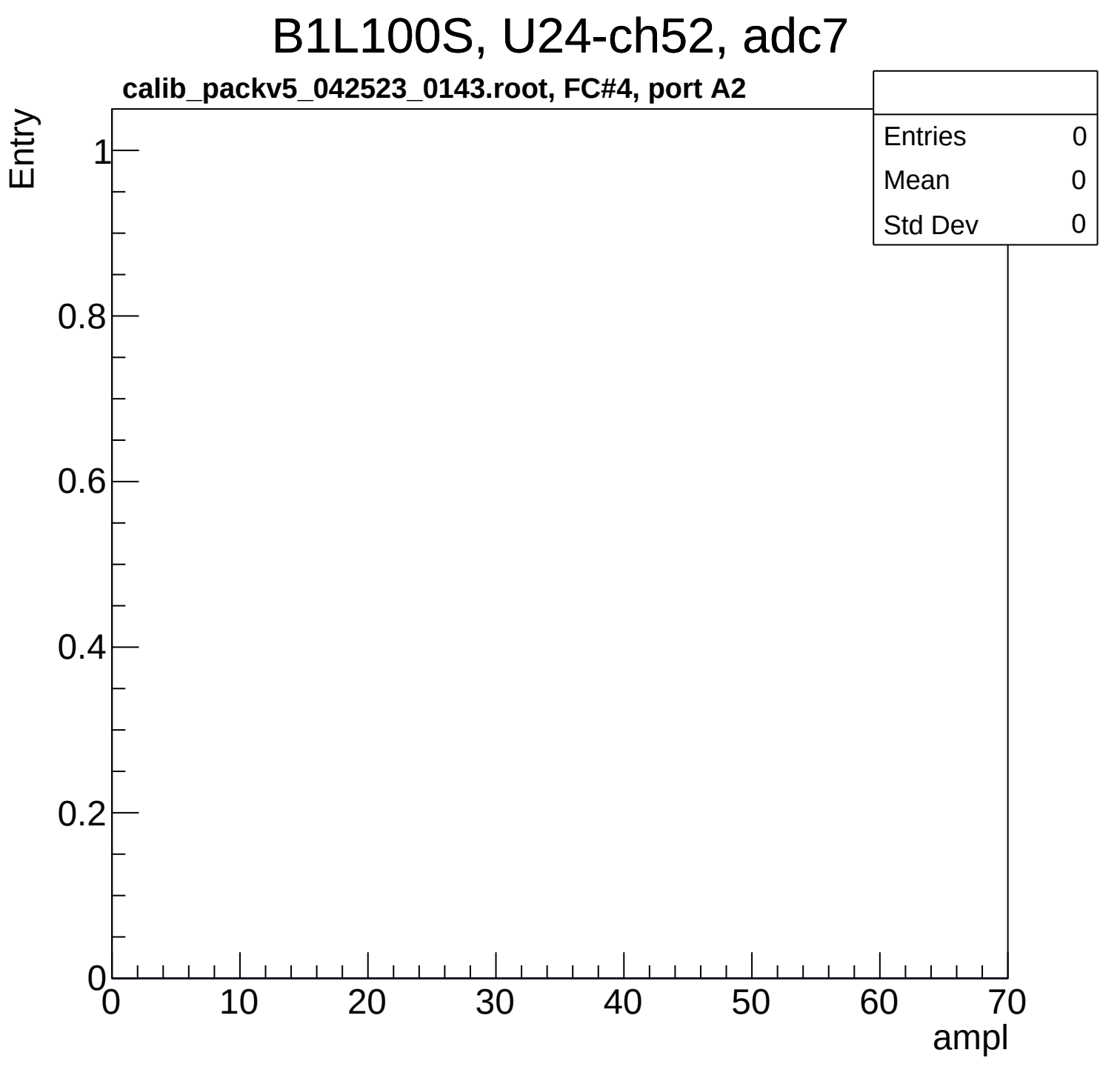
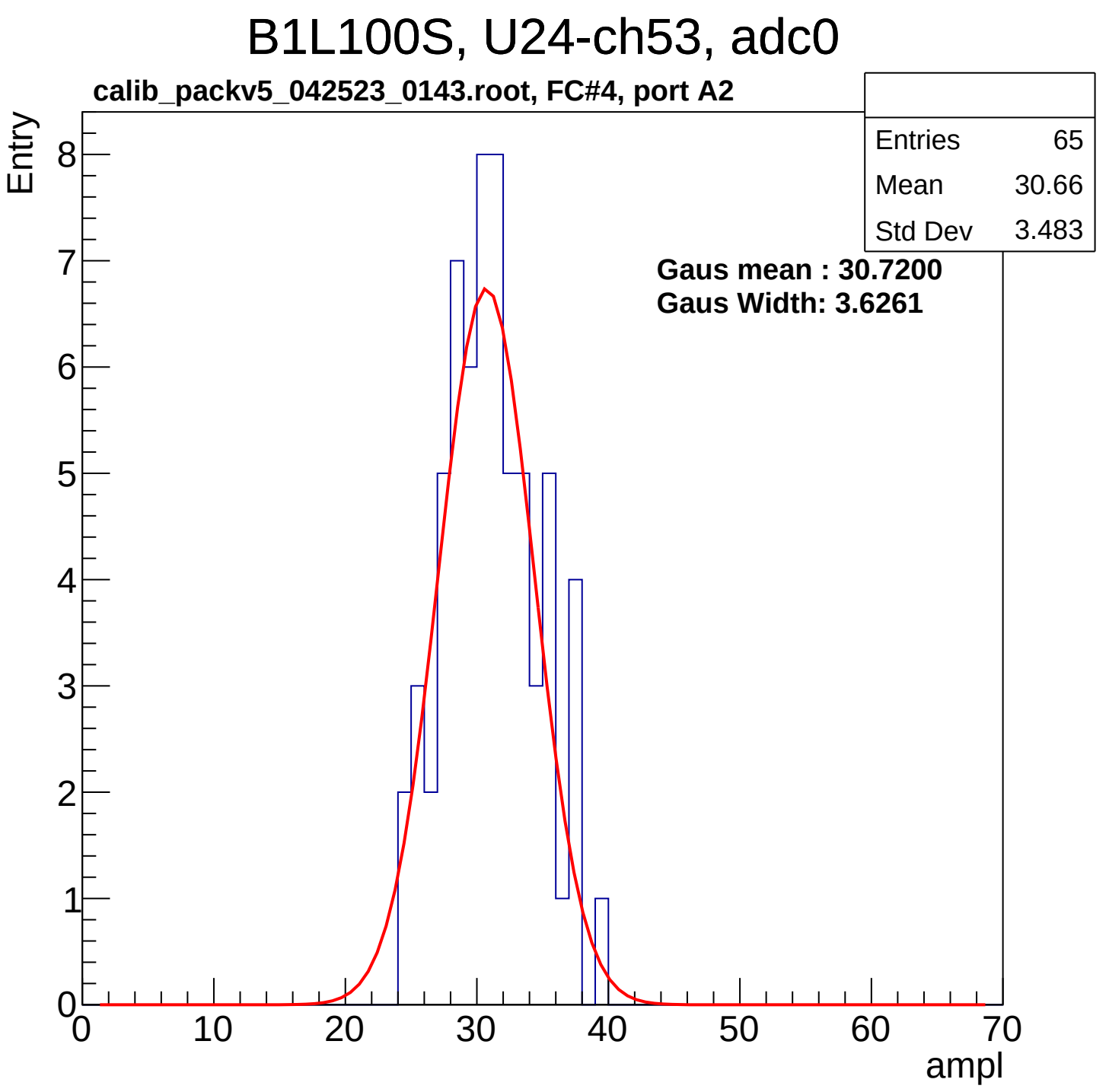
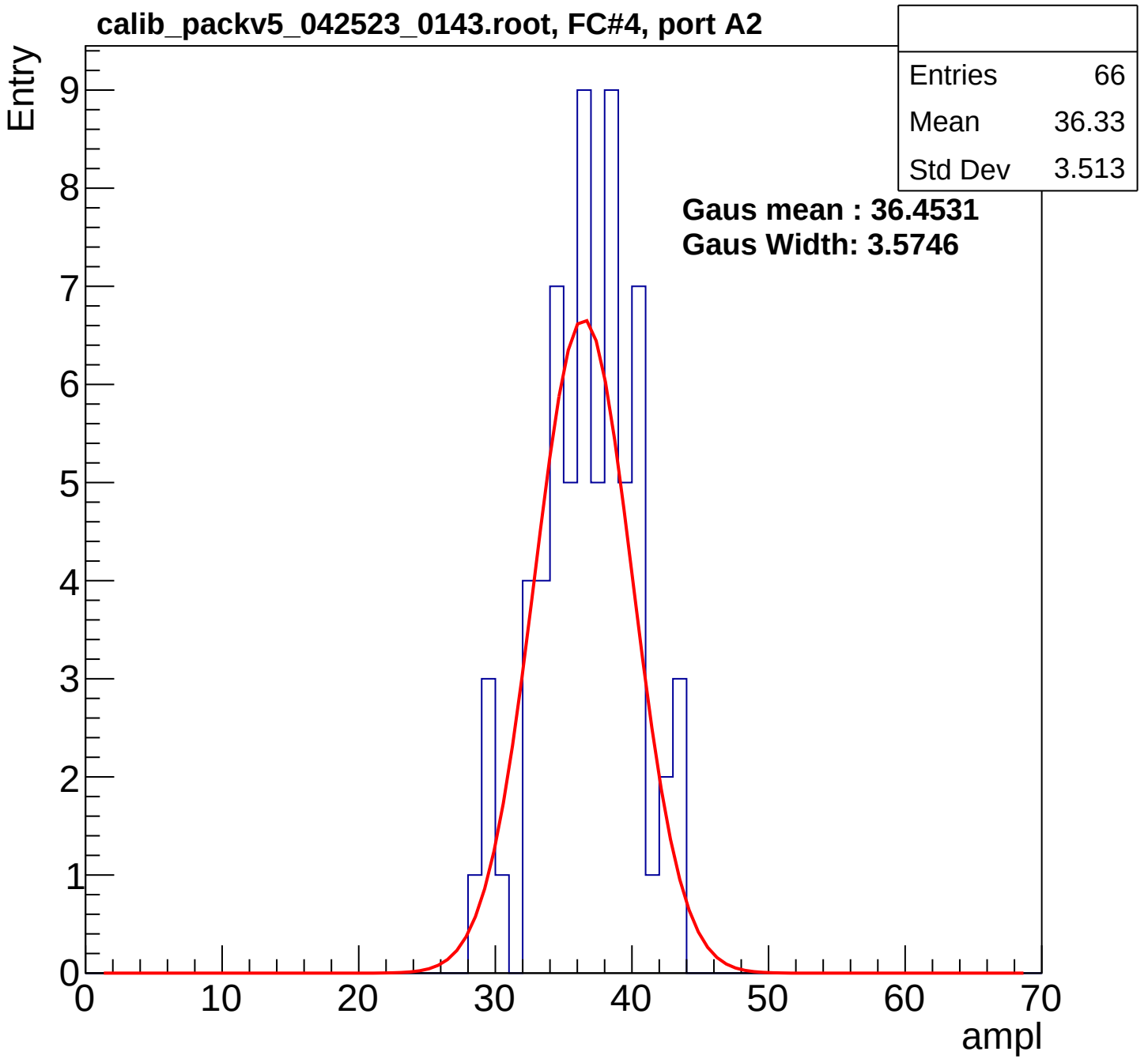
40
50
60
70


B1L100S, U24-ch52, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.66
Std Dev 3.483
Gaus mean : 30.7200
Gaus Width: 3.6261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.33
Std Dev 3.513
Gaus mean : 36.4531
Gaus Width: 3.5746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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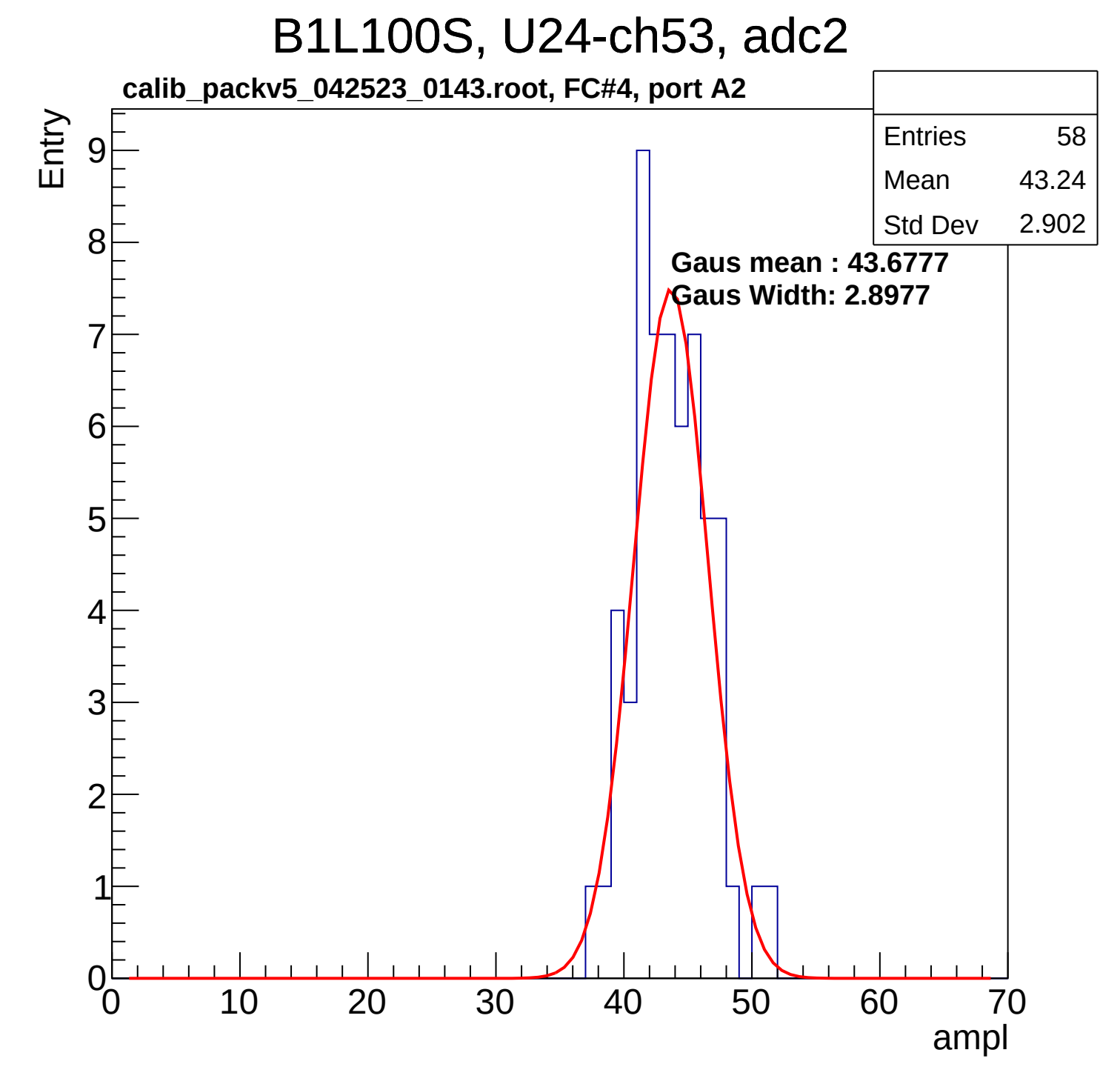
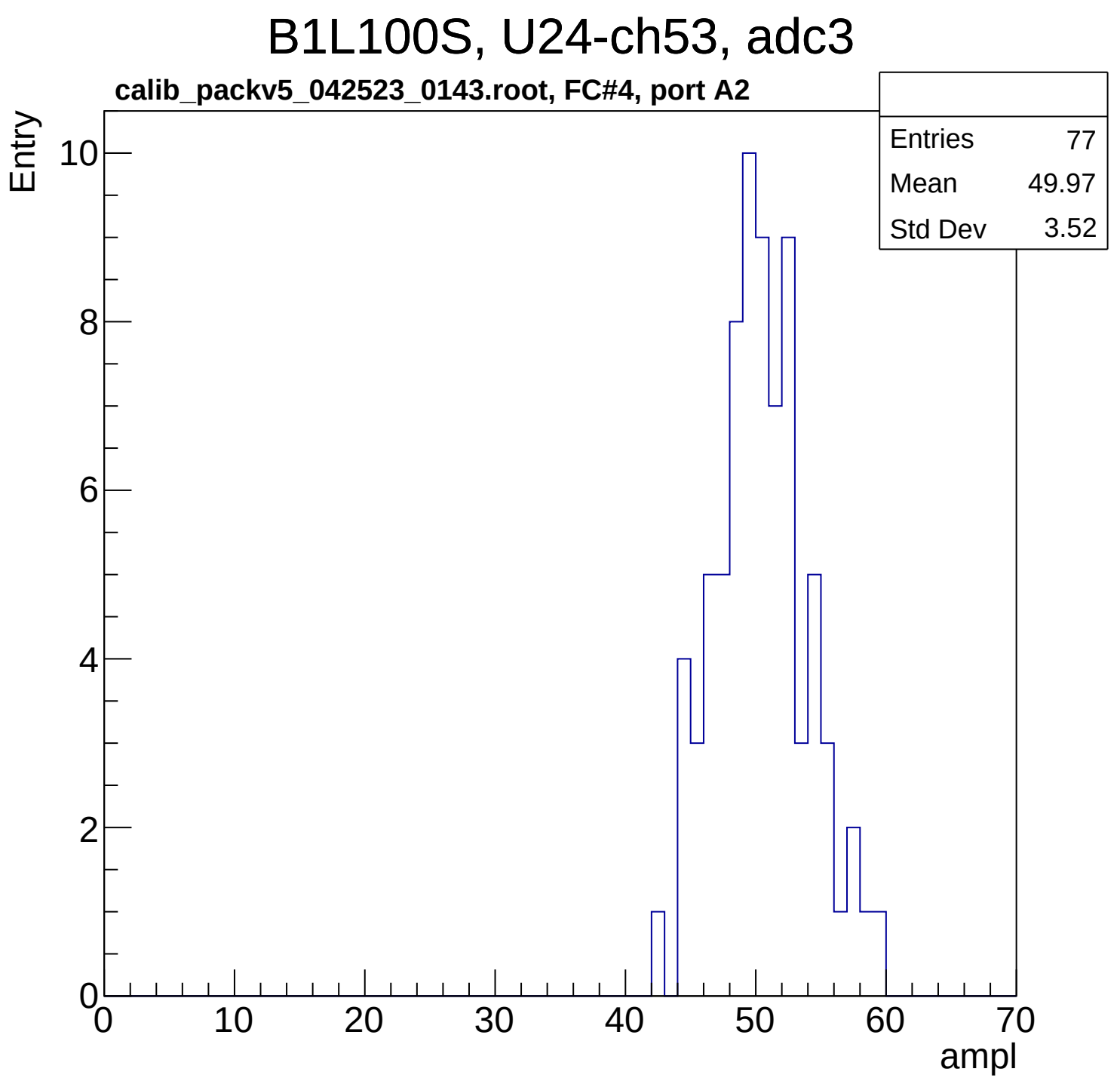
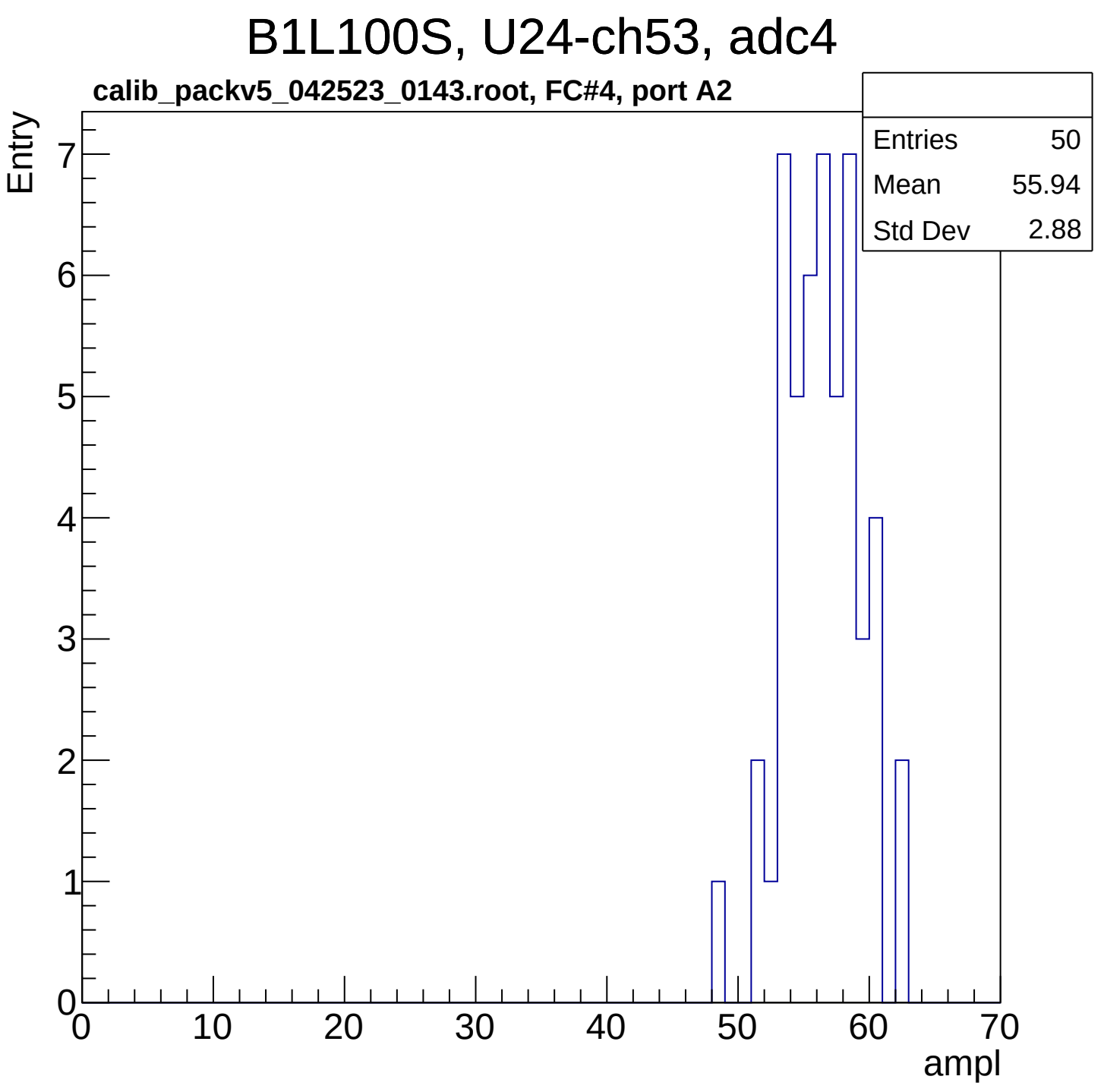
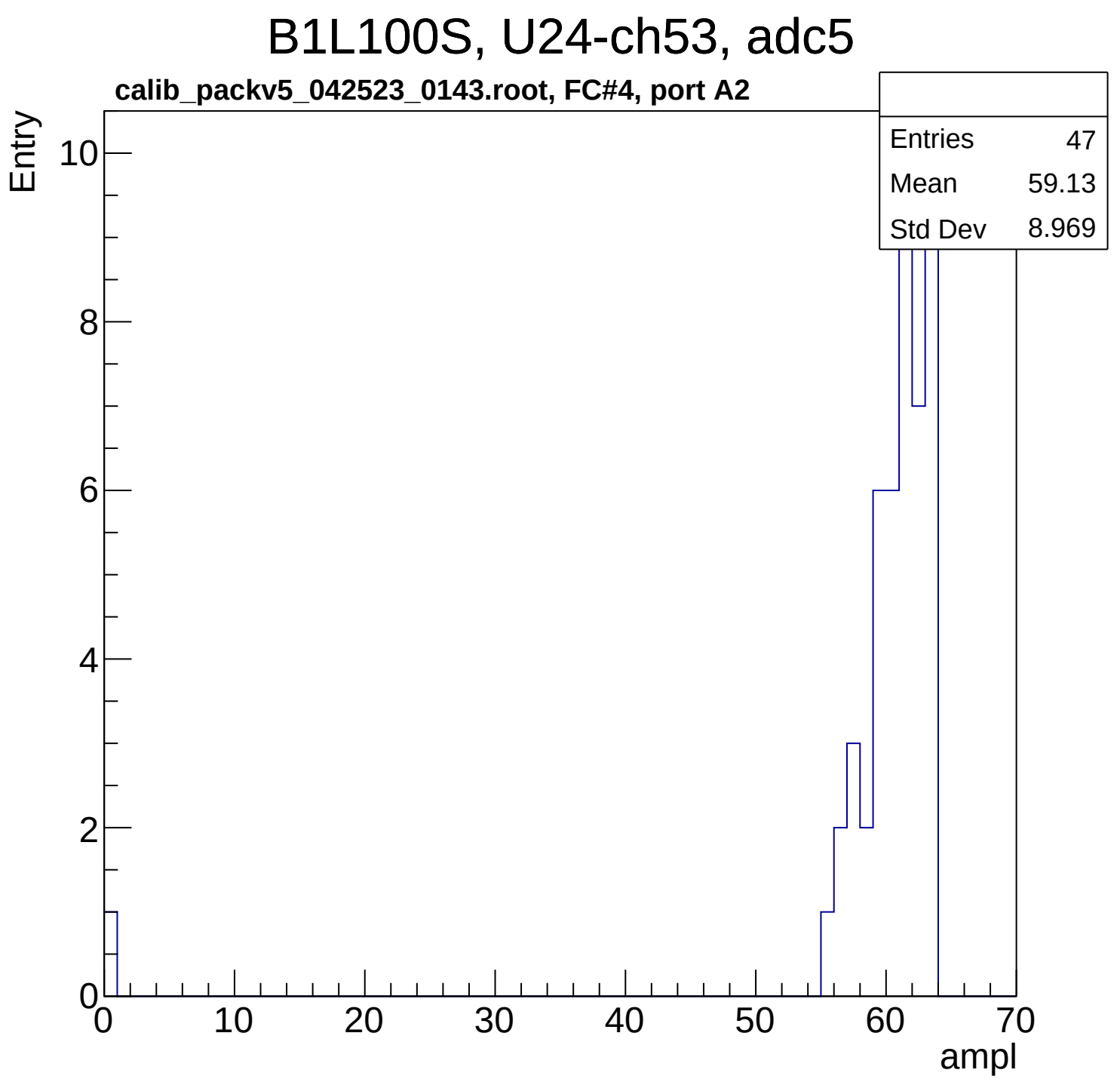
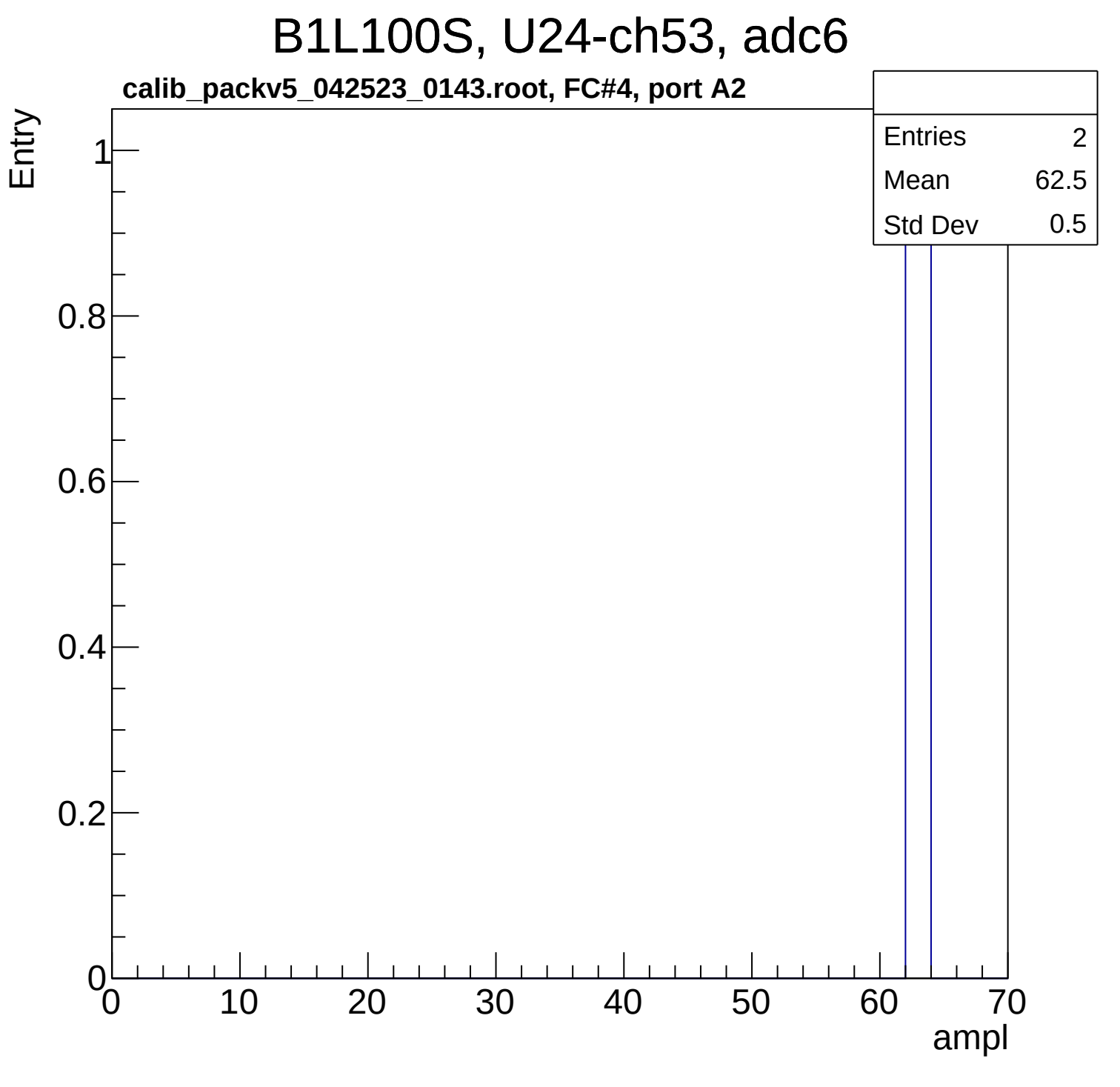
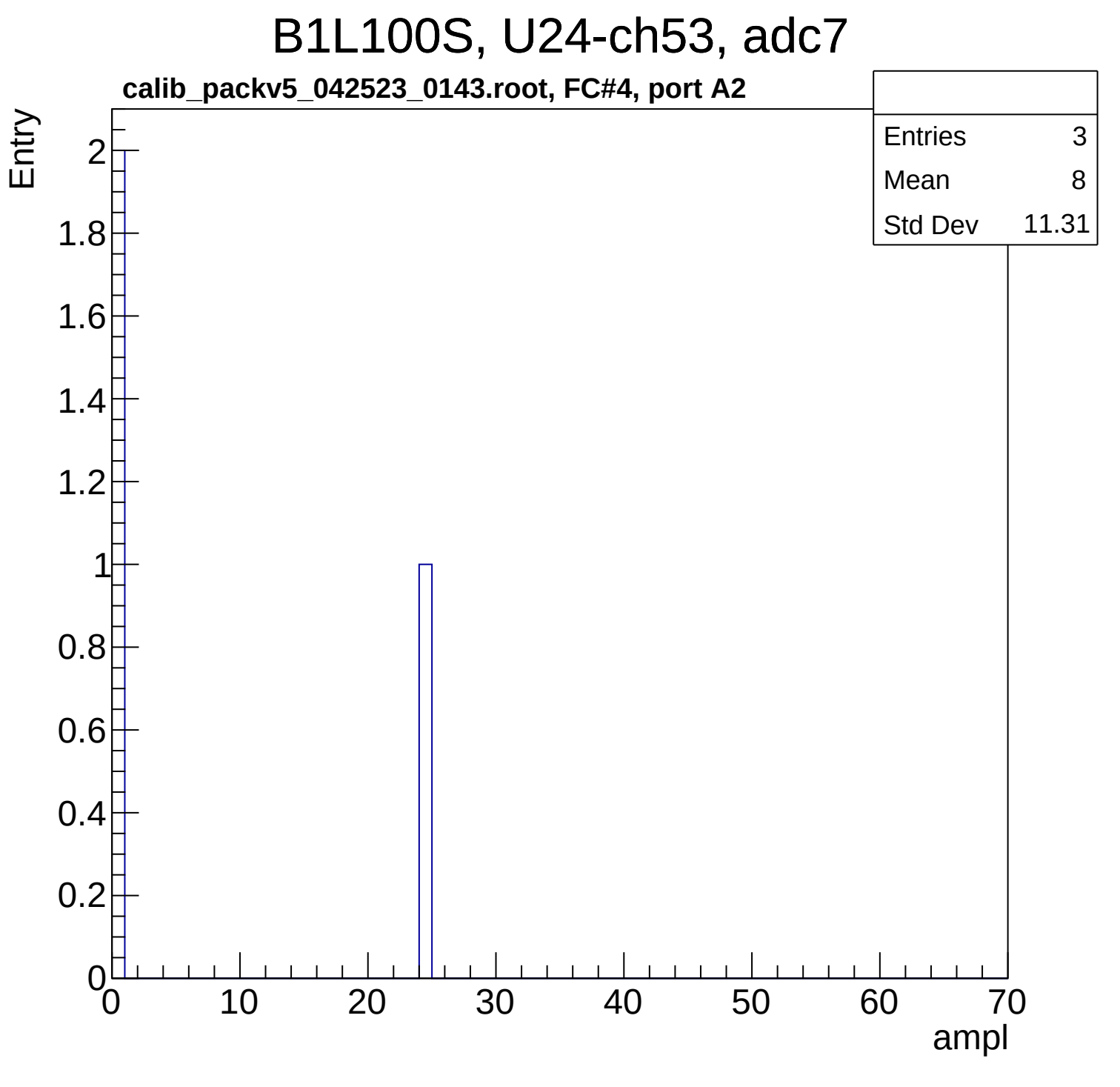

B1L100S, U24-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.24
Std Dev 2.902
Gaus mean : 43.6777
Gaus Width: 2.8977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 49.97
Std Dev 3.52
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.94
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.13
Std Dev 8.969
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch53, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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60
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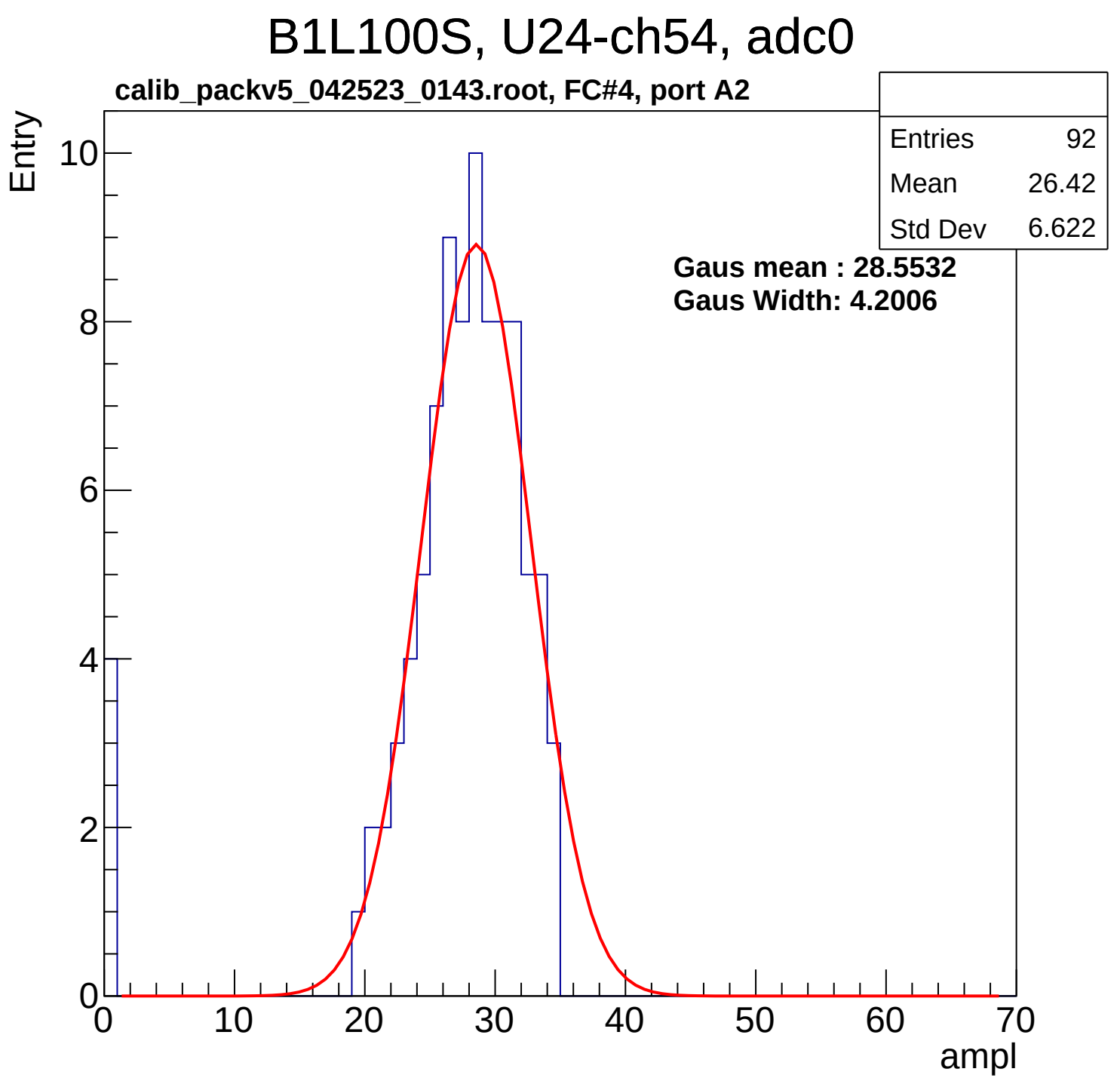
B1L100S, U24-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 92
Mean 26.42
Std Dev 6.622
Gaus mean : 28.5532
Gaus Width: 4.2006
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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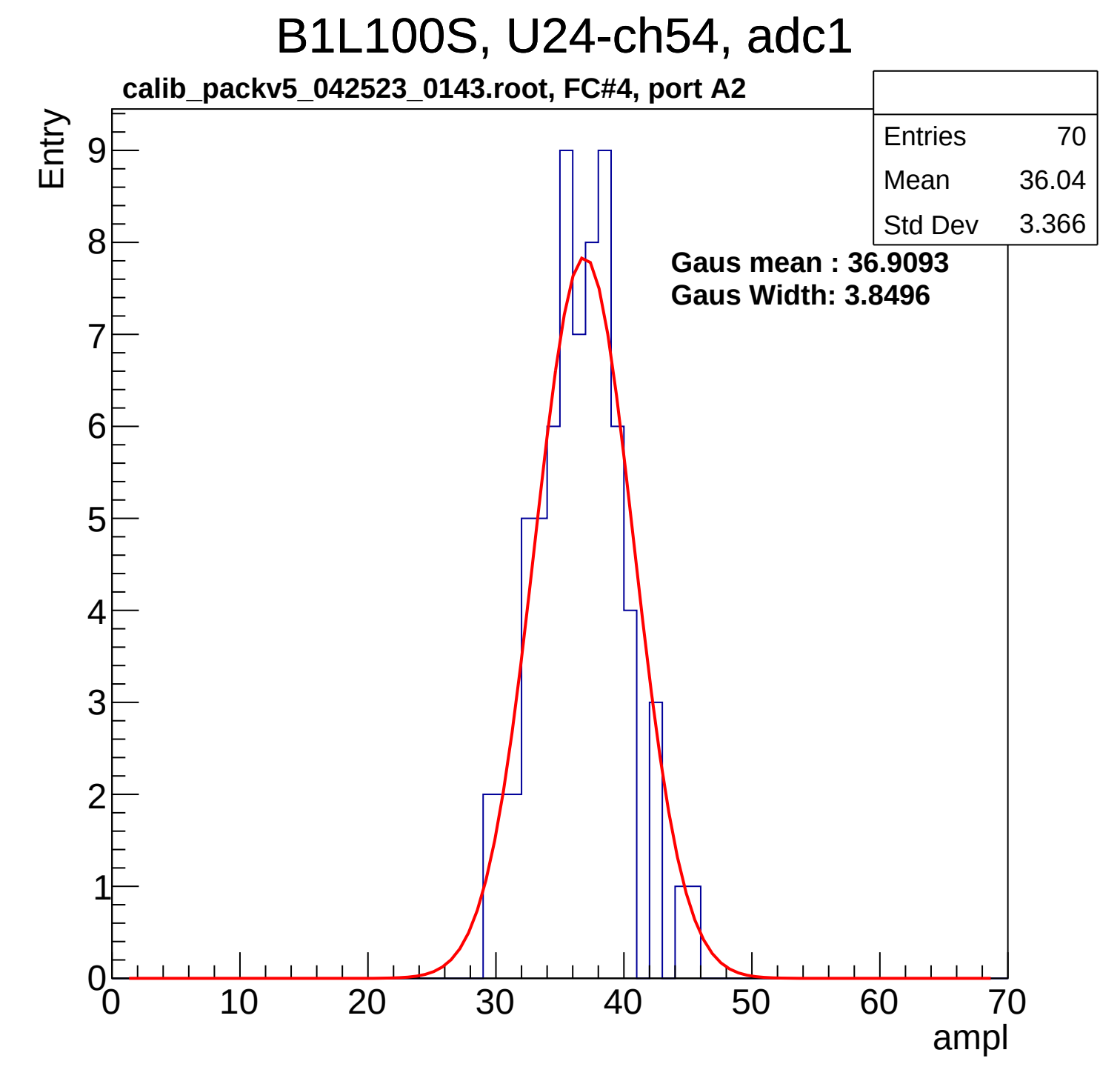
B1L100S, U24-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.04
Std Dev 3.366
Gaus mean : 36.9093
Gaus Width: 3.8496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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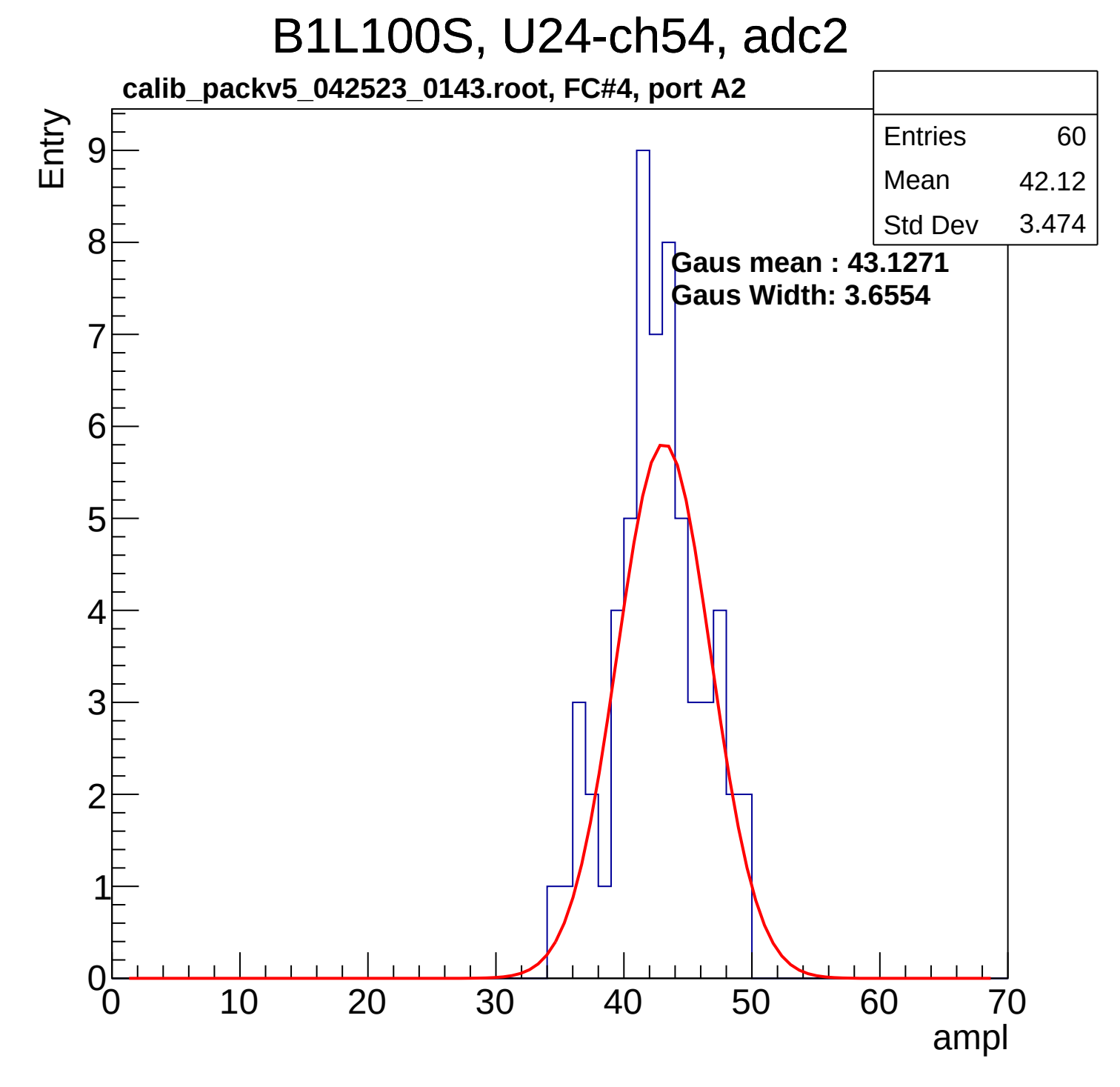
B1L100S, U24-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.12
Std Dev 3.474
Gaus mean : 43.1271
Gaus Width: 3.6554
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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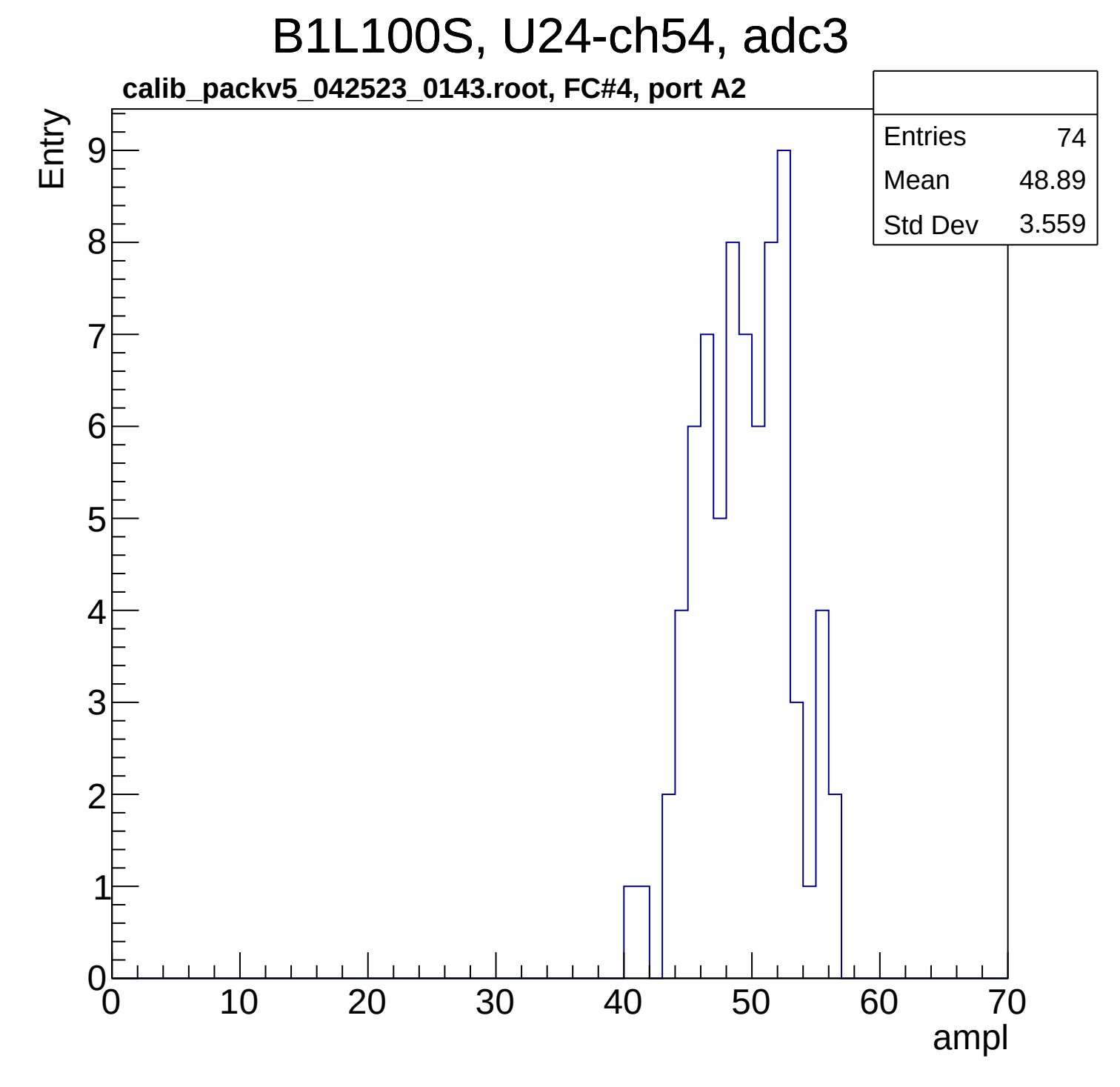
B1L100S, U24-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 48.89
Std Dev 3.559
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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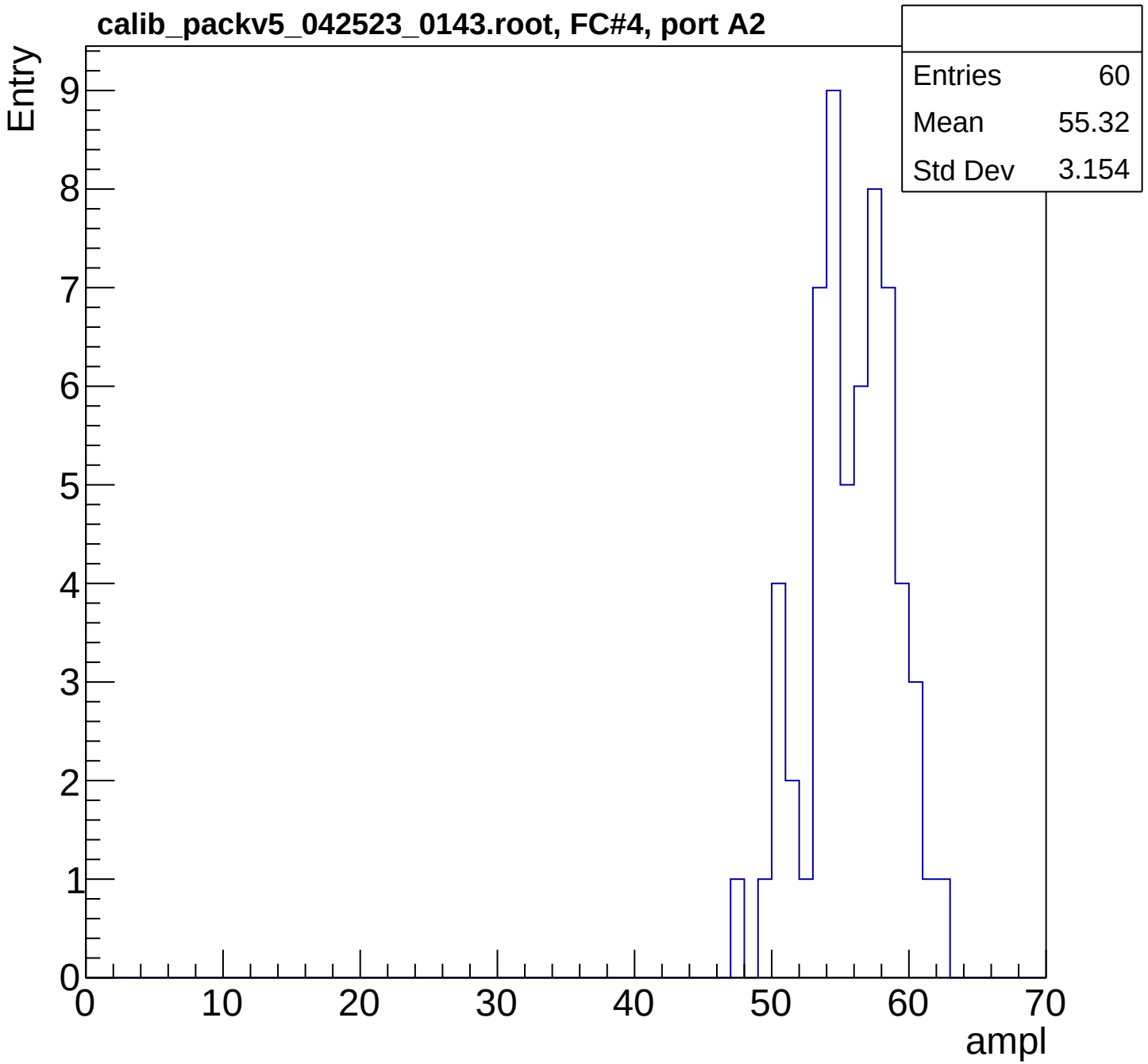
B1L100S, U24-ch54, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.32
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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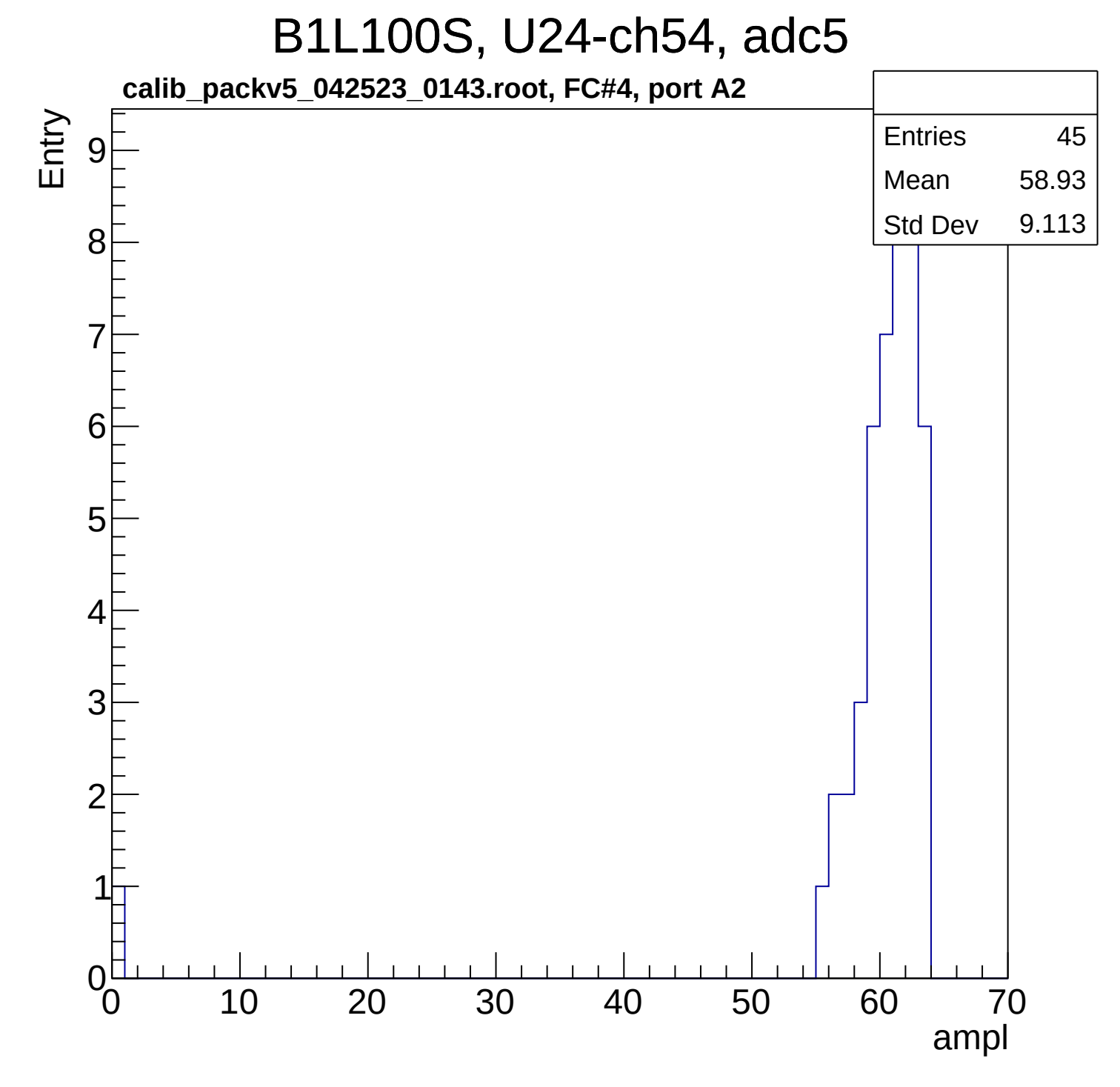
B1L100S, U24-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.93
Std Dev 9.113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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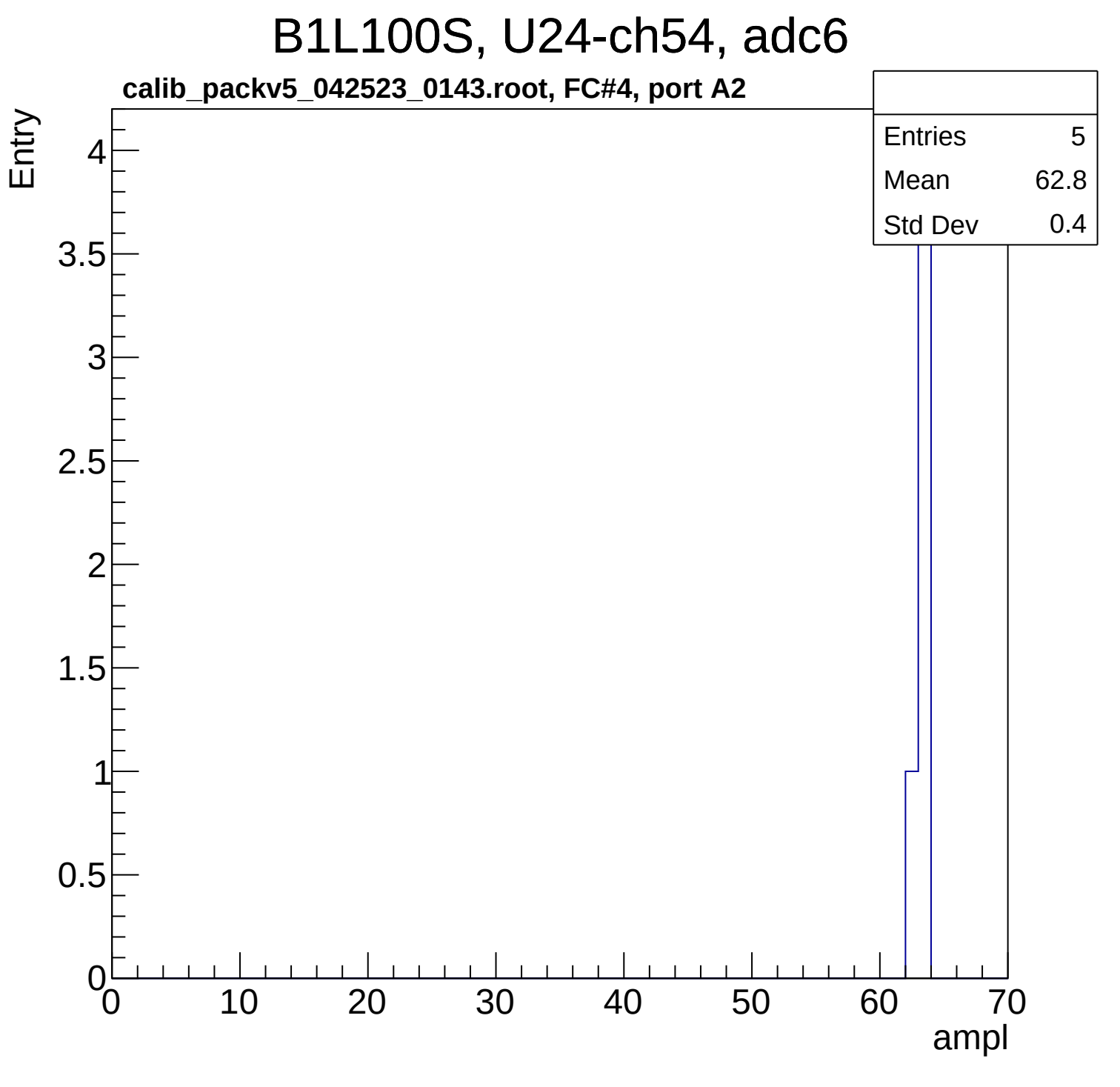
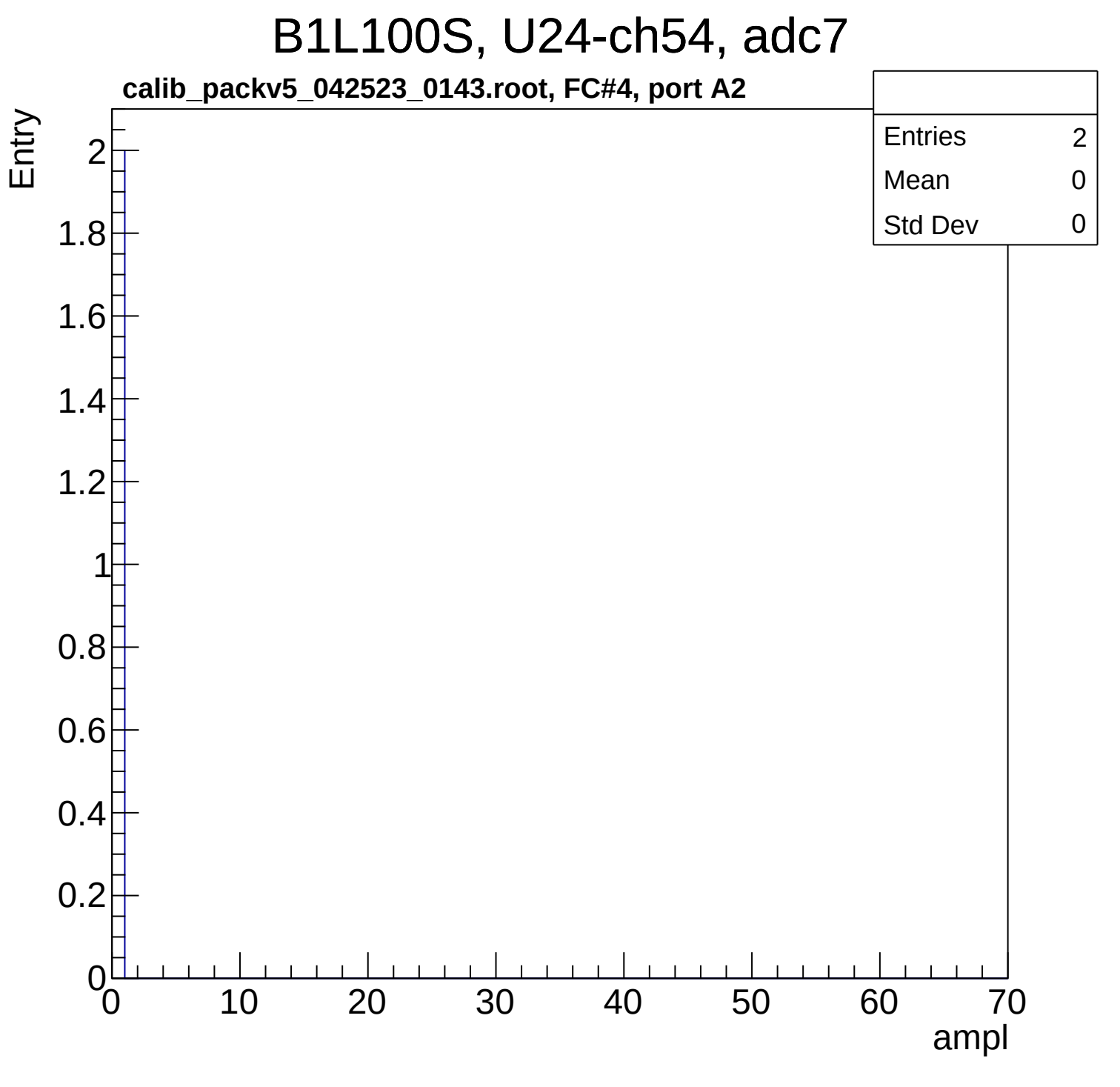
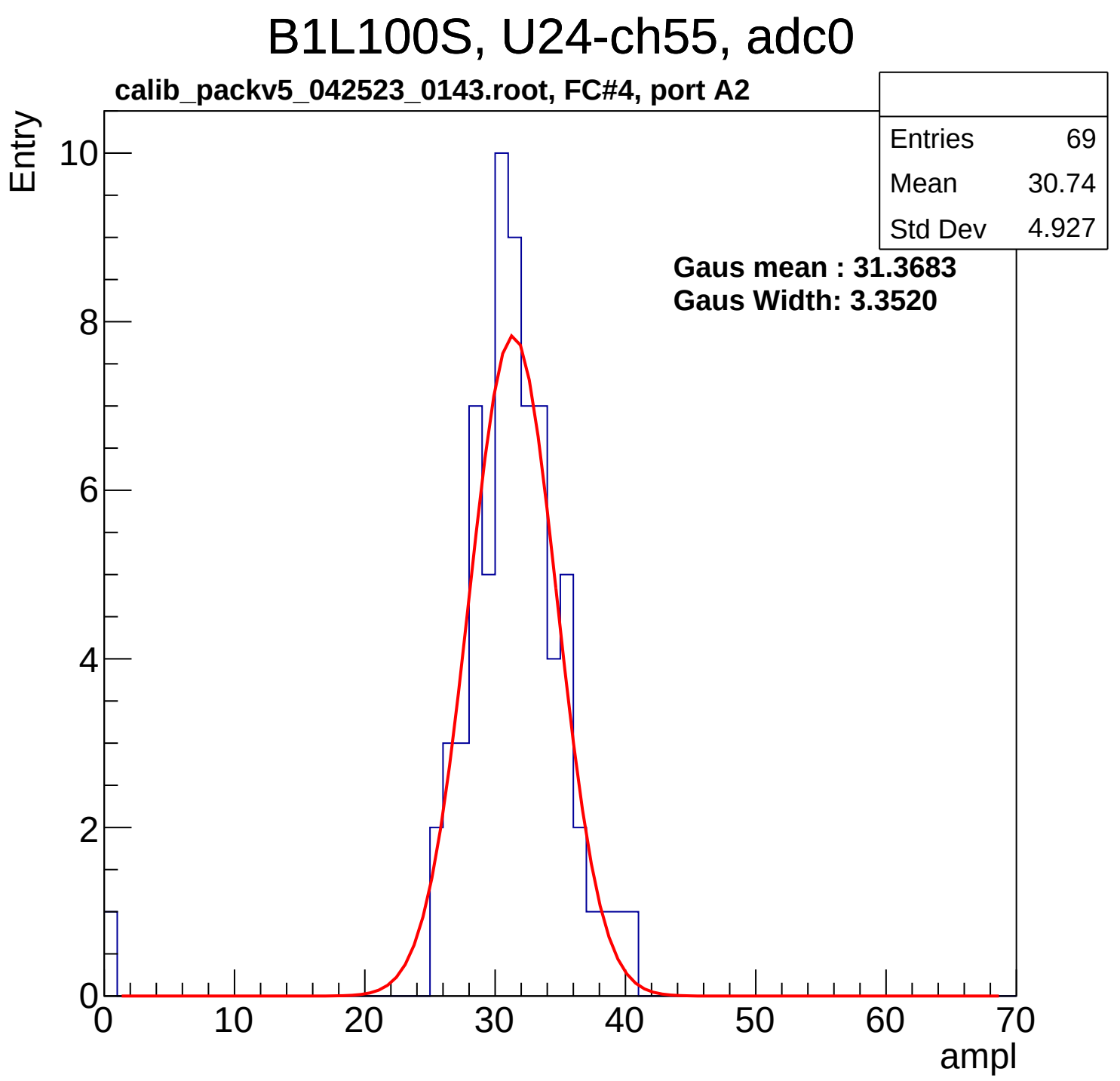
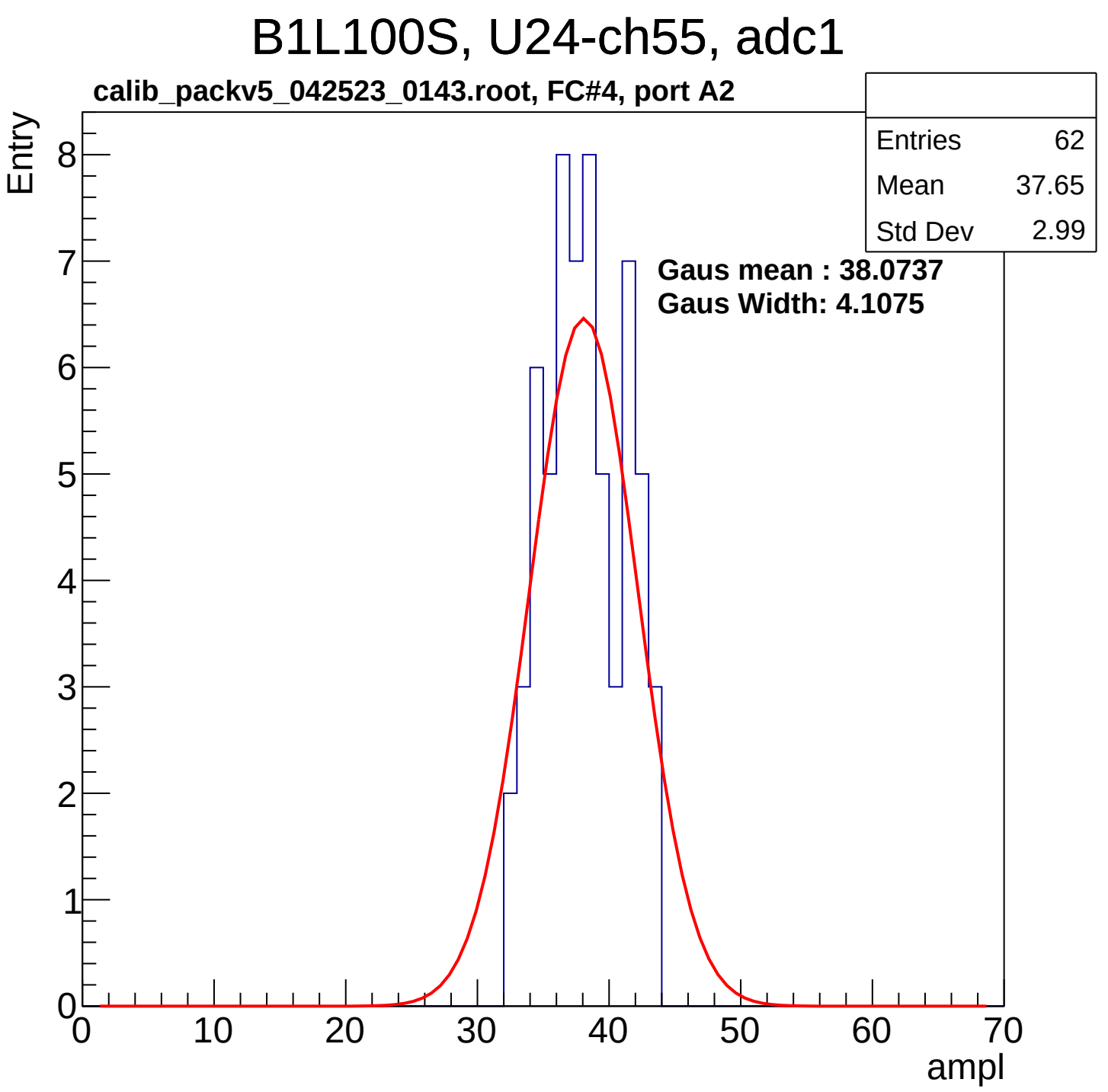
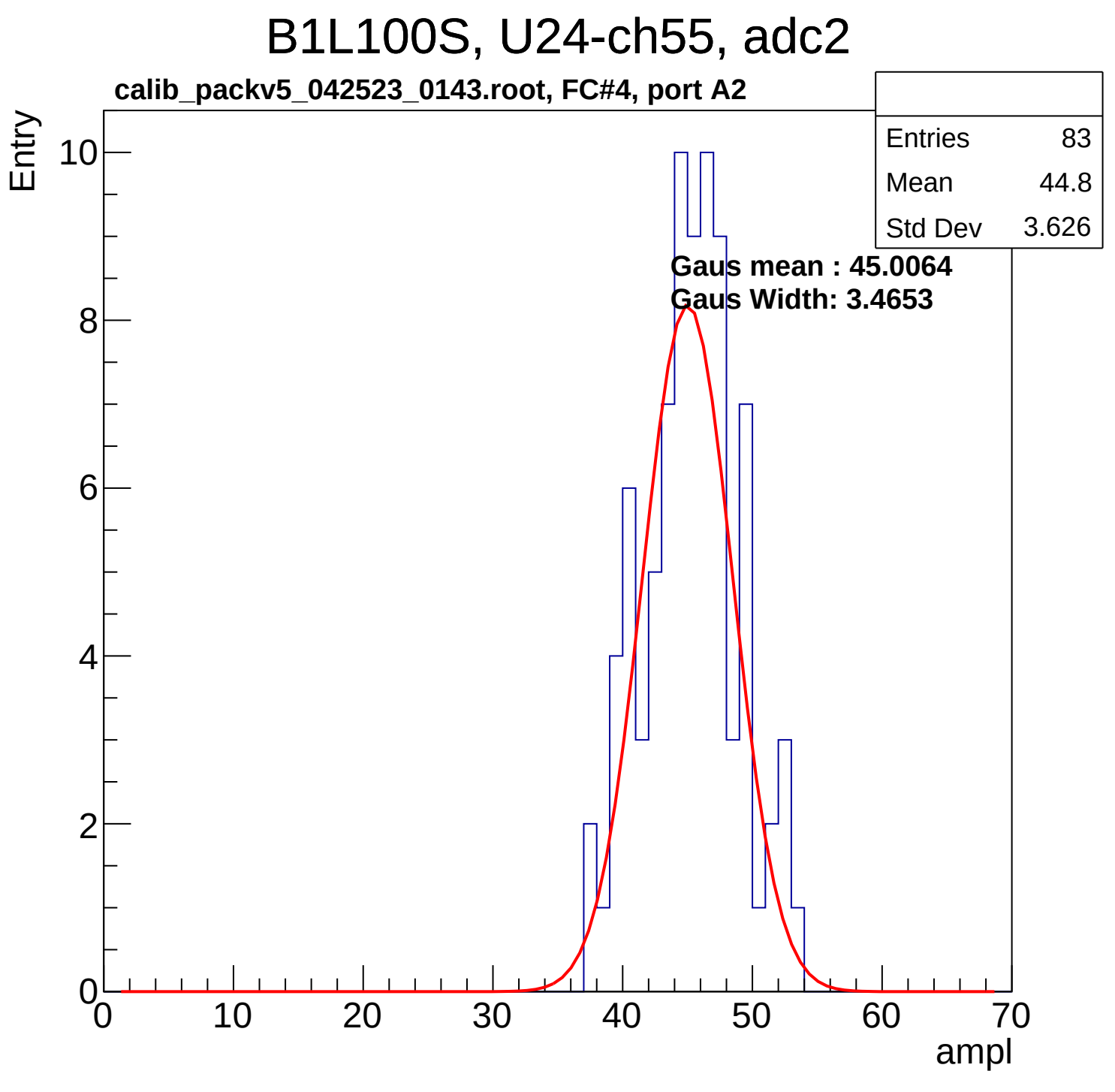

B1L100S, U24-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U24-ch54, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 30.74
Std Dev 4.927
Gaus mean : 31.3683
Gaus Width: 3.3520
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 37.65
Std Dev 2.99
Gaus mean : 38.0737
Gaus Width: 4.1075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch55, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 44.8
Std Dev 3.626
Gaus mean : 45.0064
Gaus Width: 3.4653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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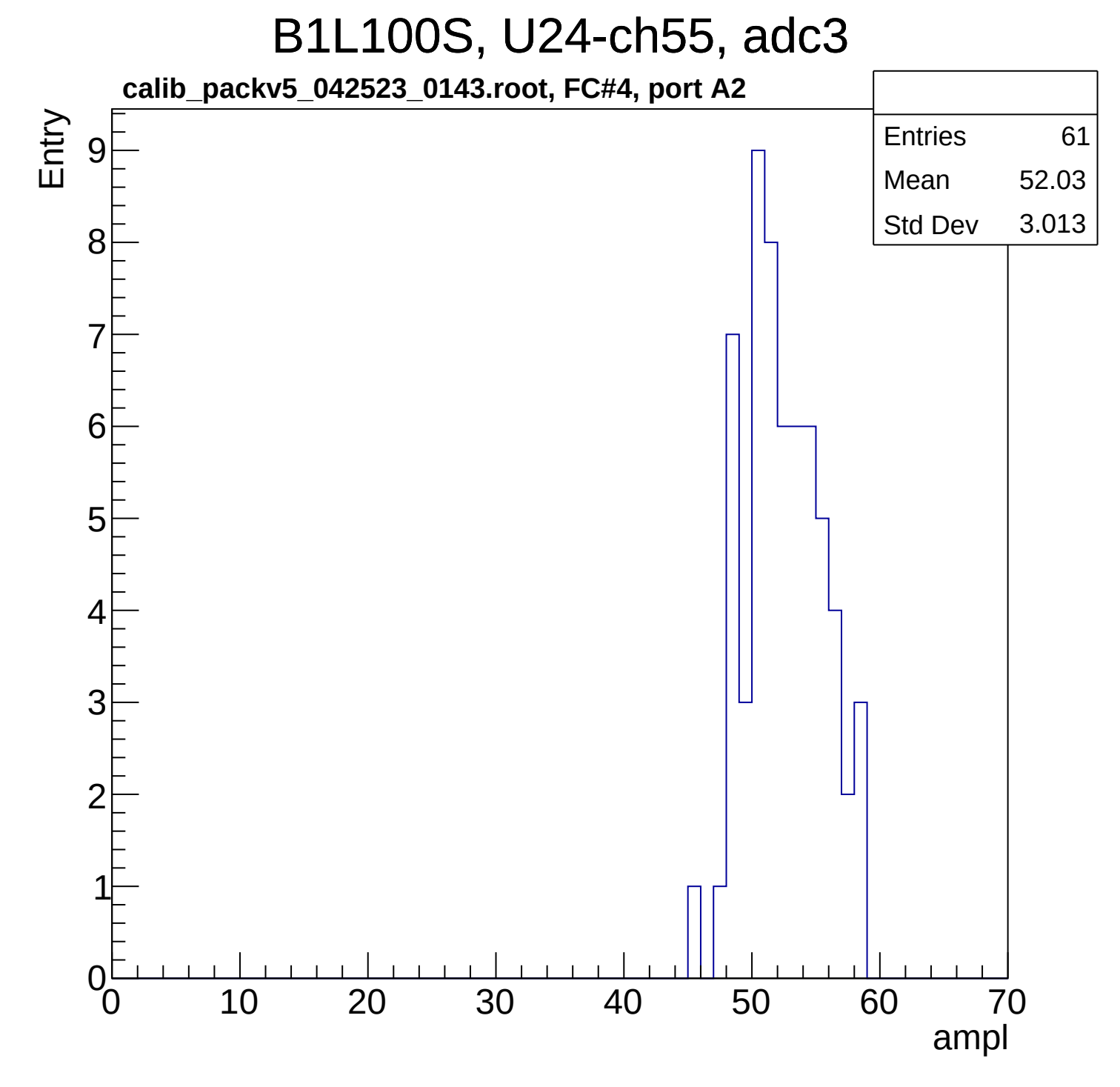
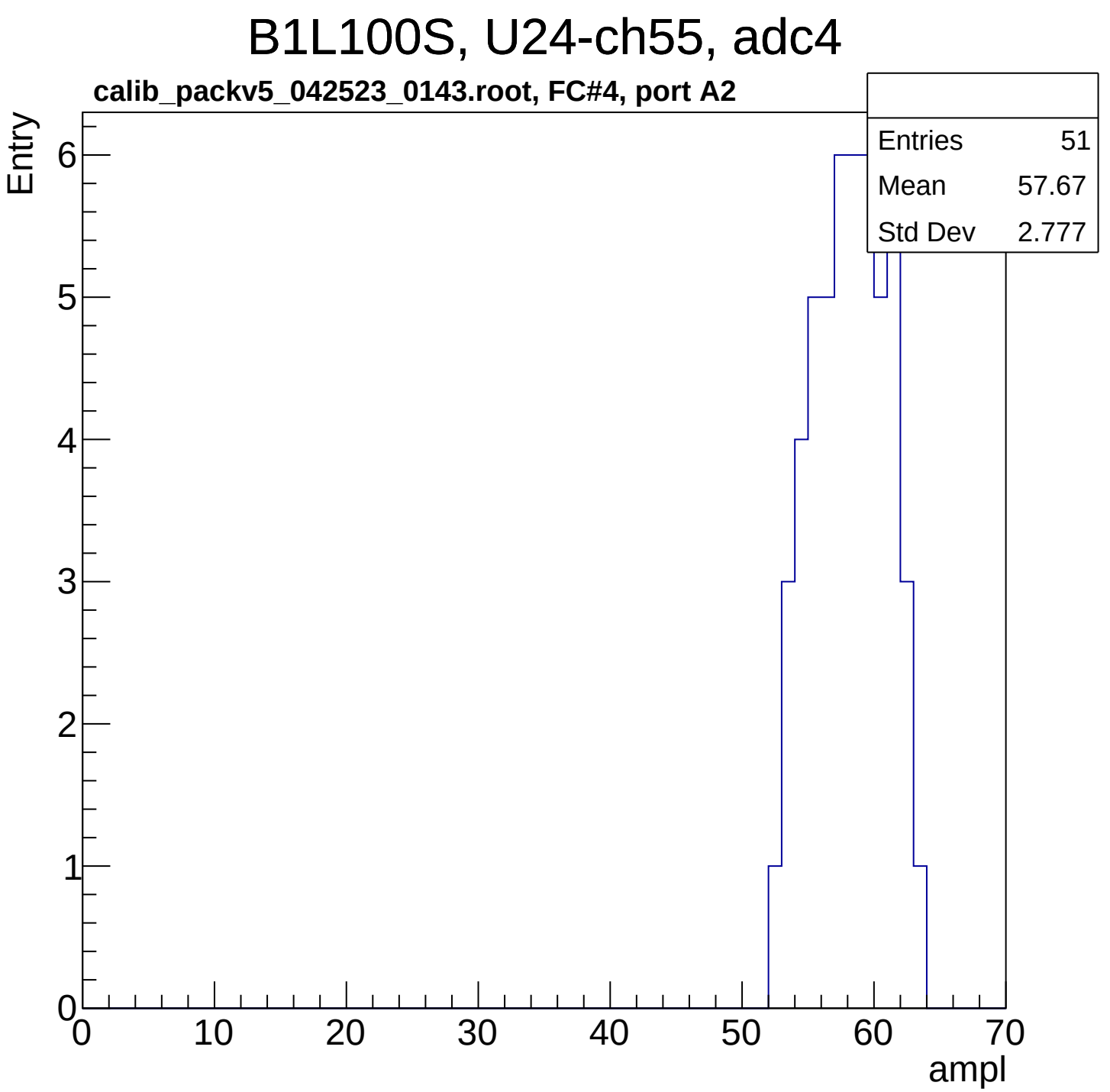
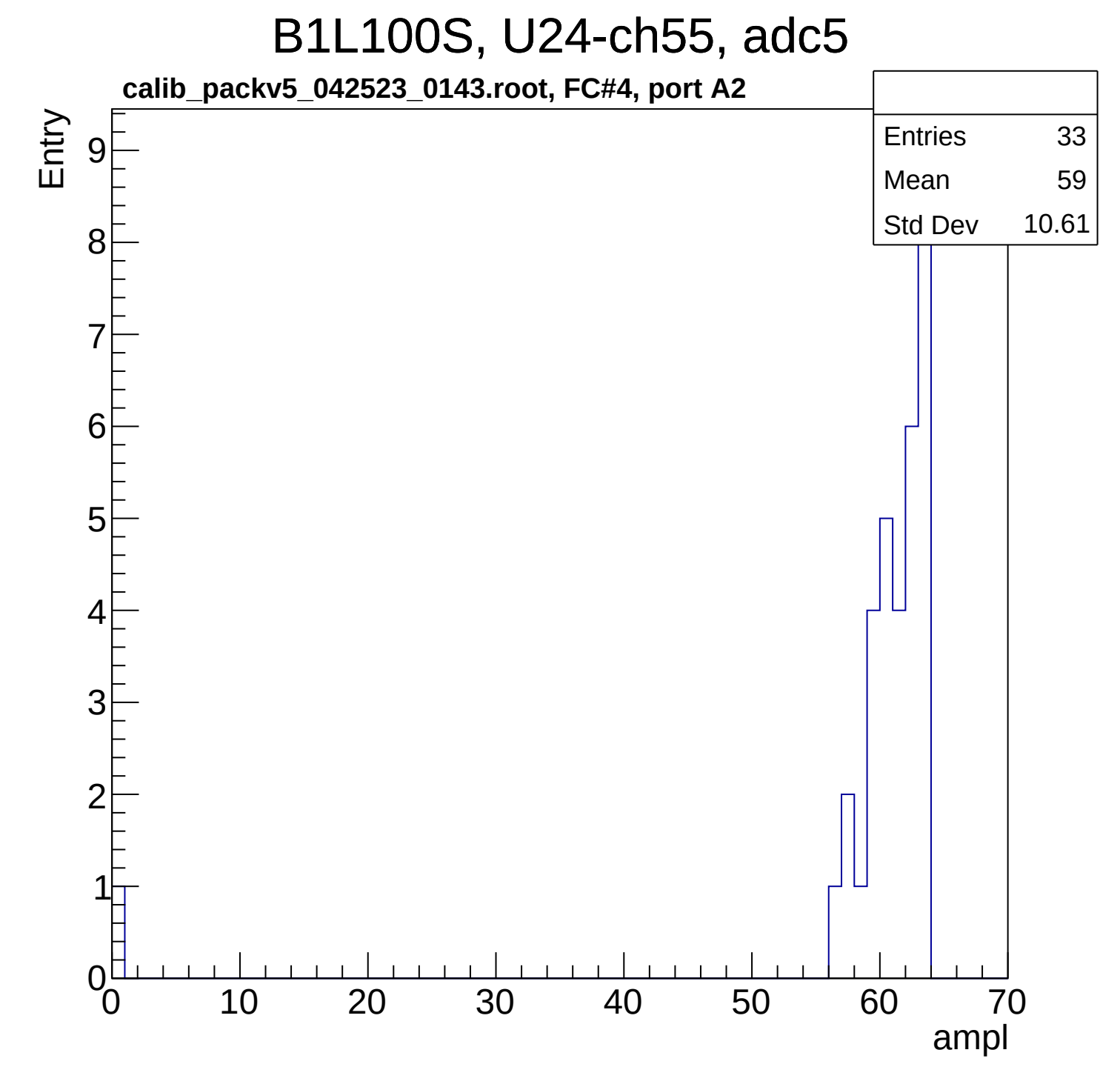

B1L100S, U24-ch55, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 52.03
Std Dev 3.013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.67
Std Dev 2.777
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59
Std Dev 10.61
Entry
ampl
0
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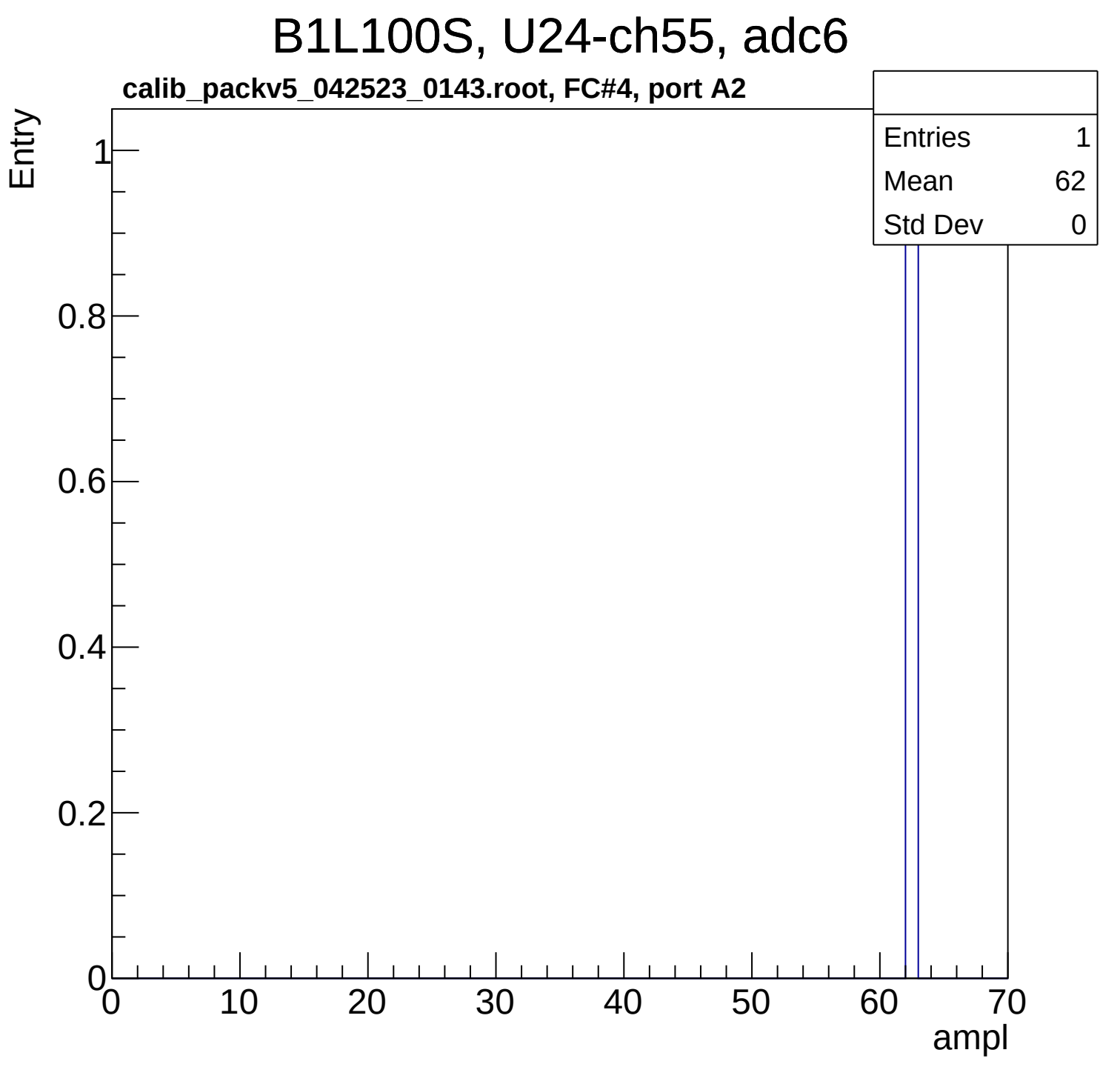
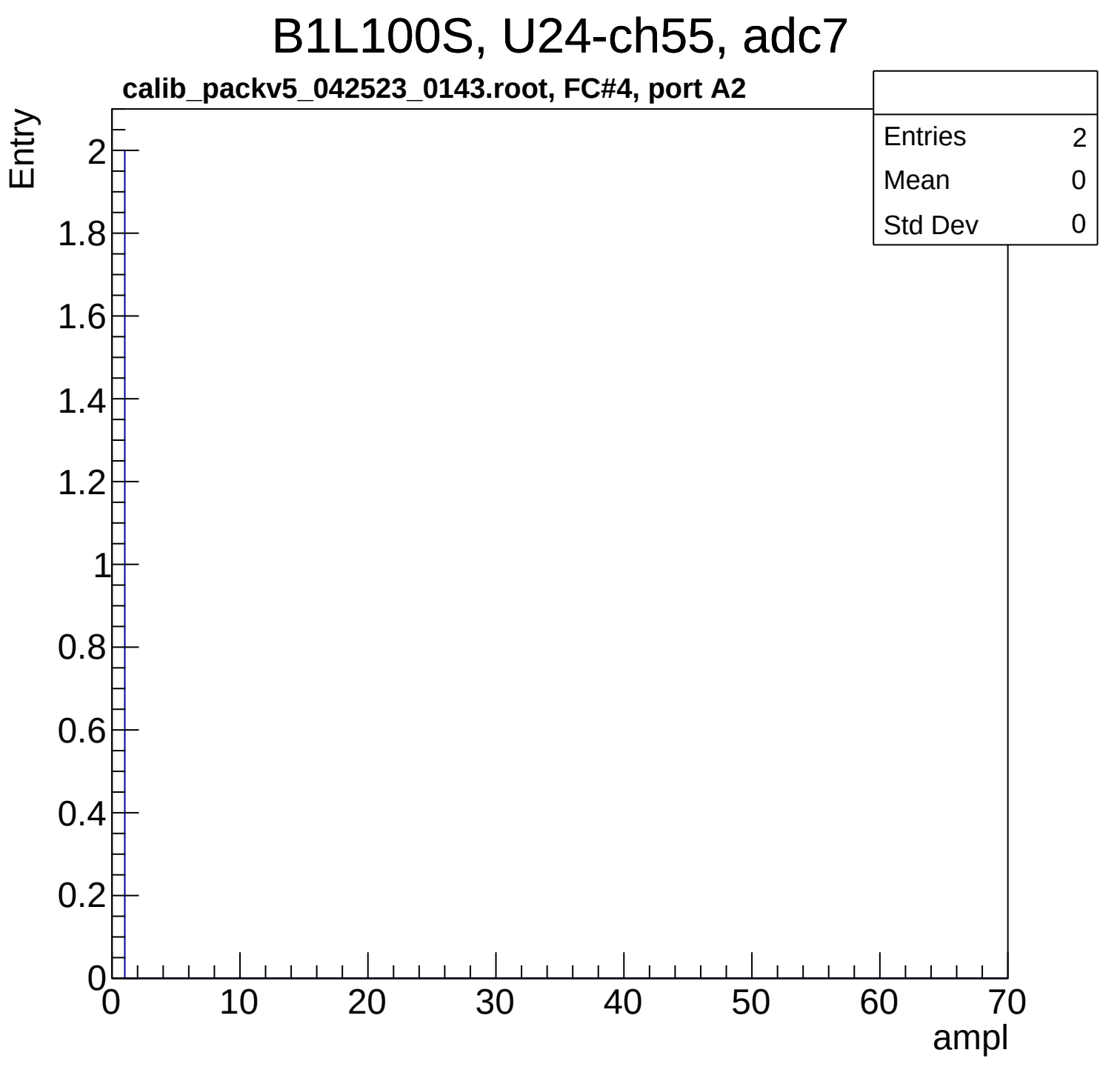
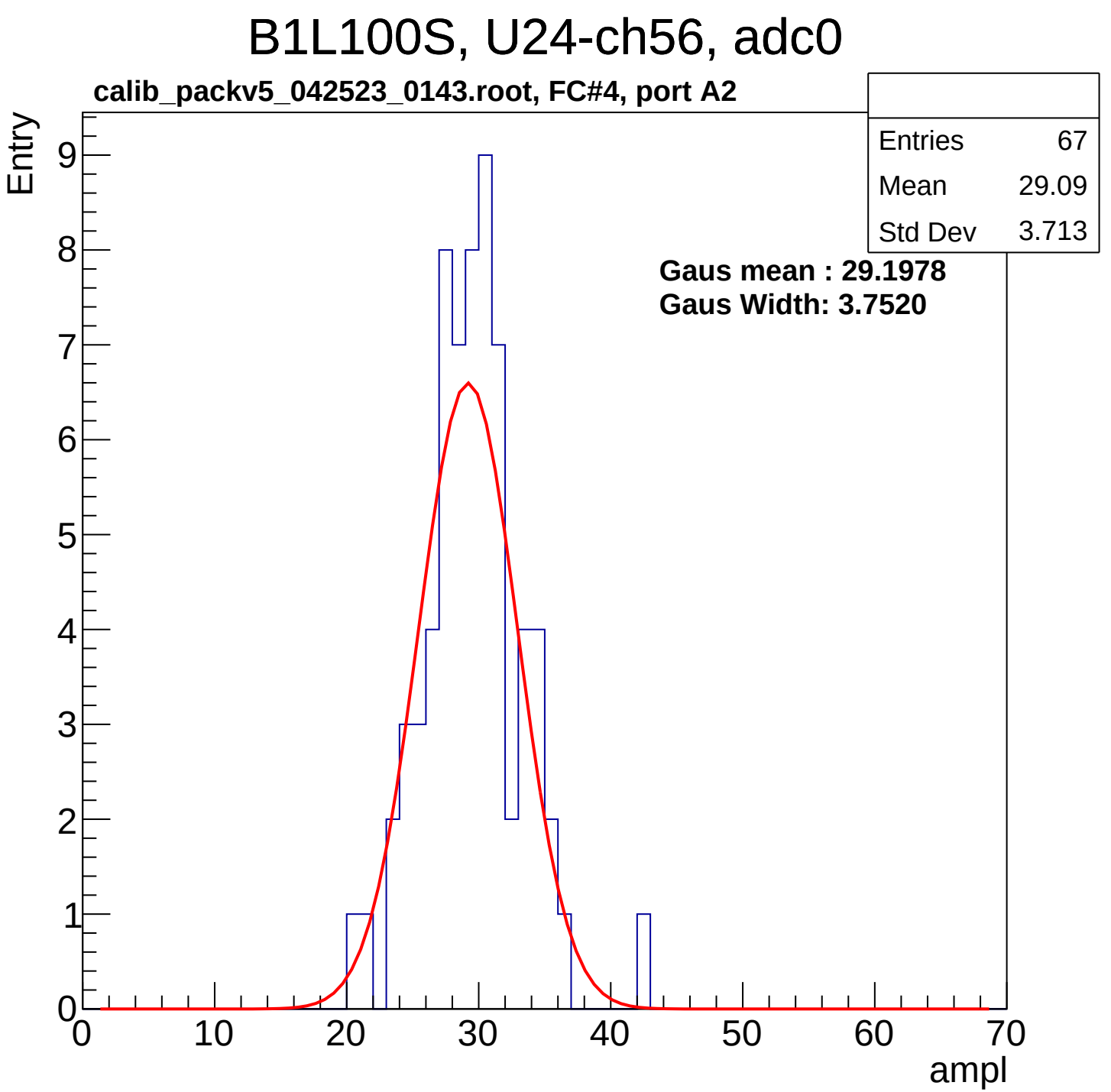
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch55, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.09
Std Dev 3.713
Gaus mean : 29.1978
Gaus Width: 3.7520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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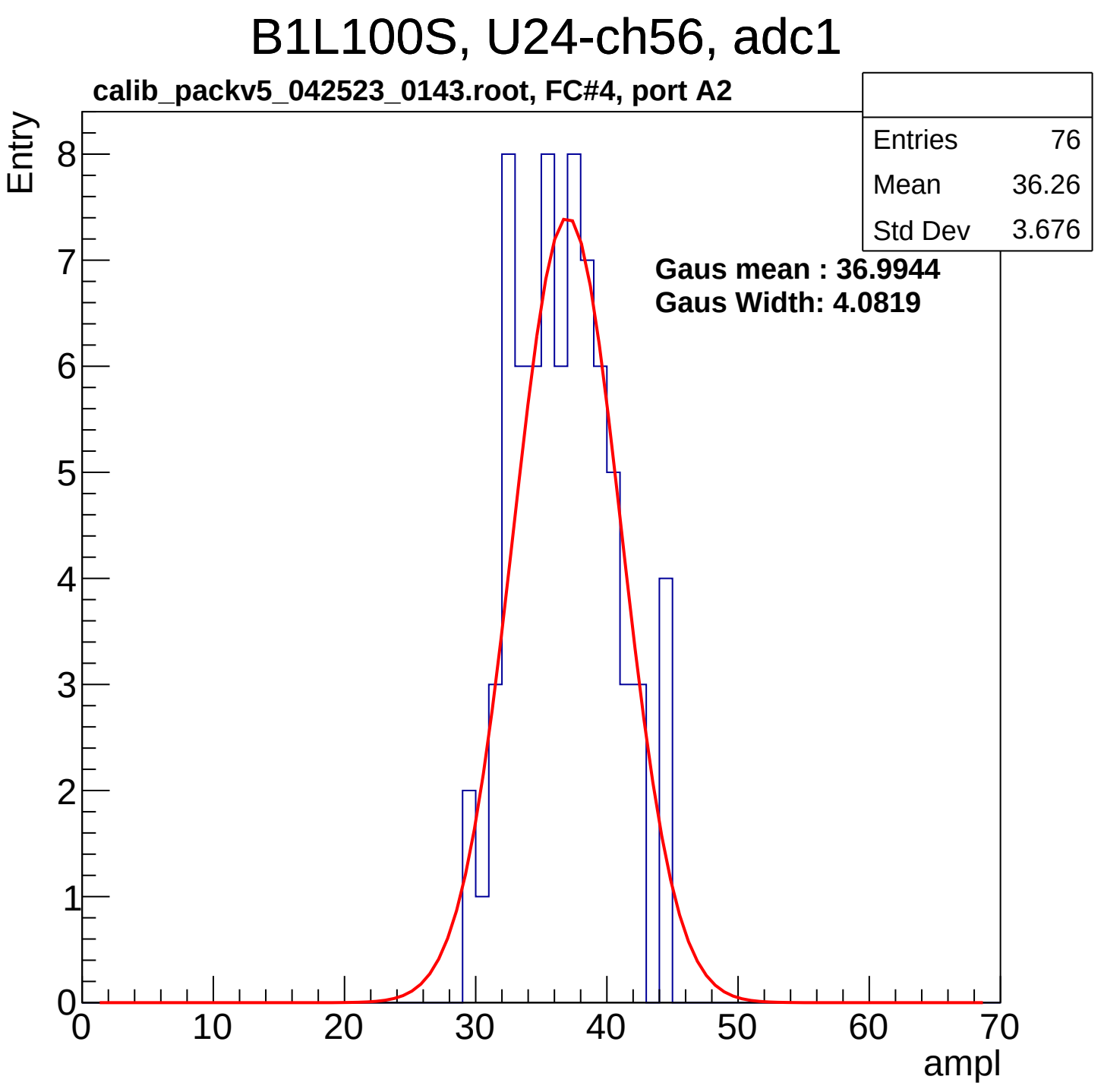
B1L100S, U24-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.26
Std Dev 3.676
Gaus mean : 36.9944
Gaus Width: 4.0819
Entry
ampl
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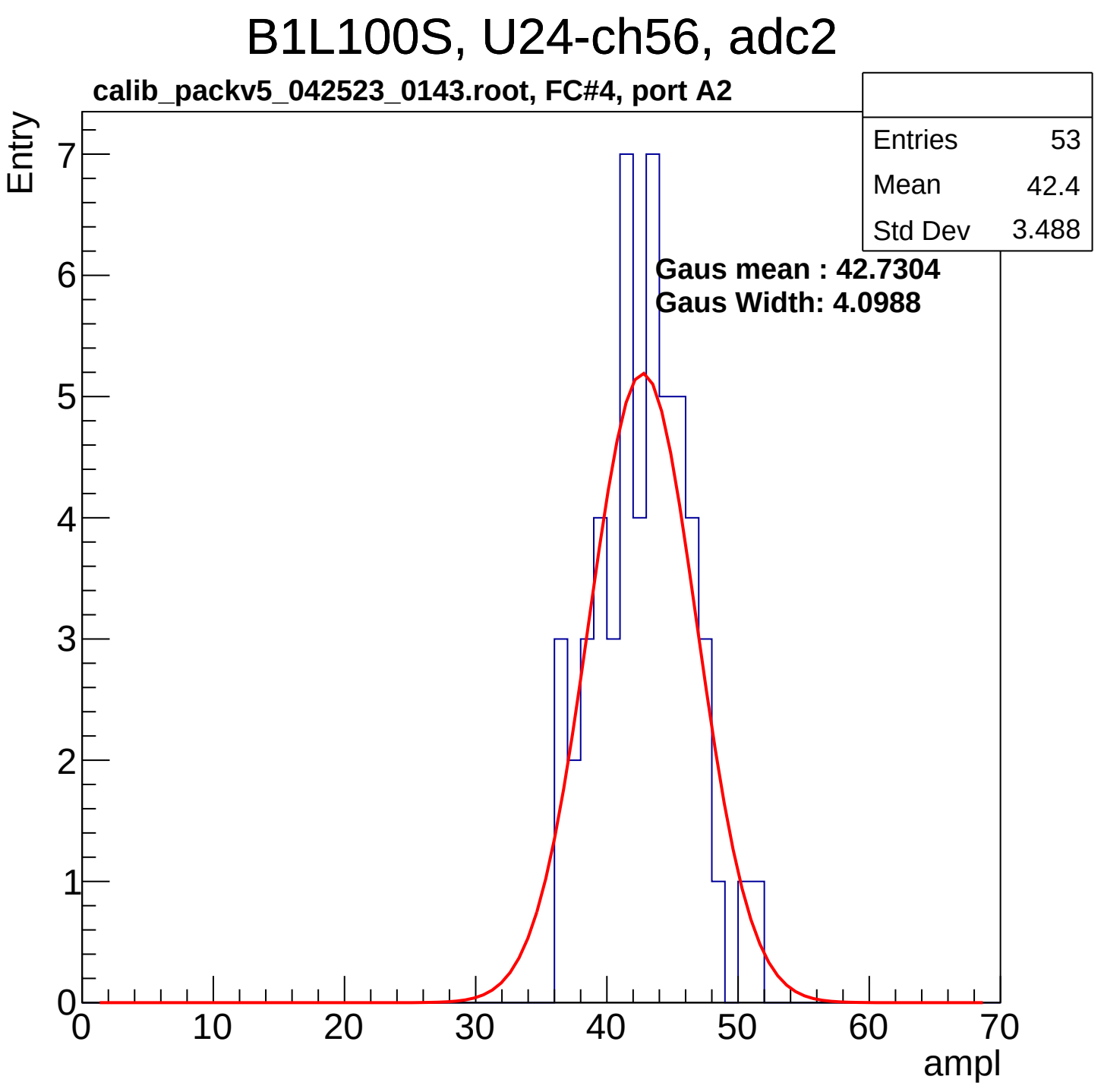
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 42.4
Std Dev 3.488
Gaus mean : 42.7304
Gaus Width: 4.0988
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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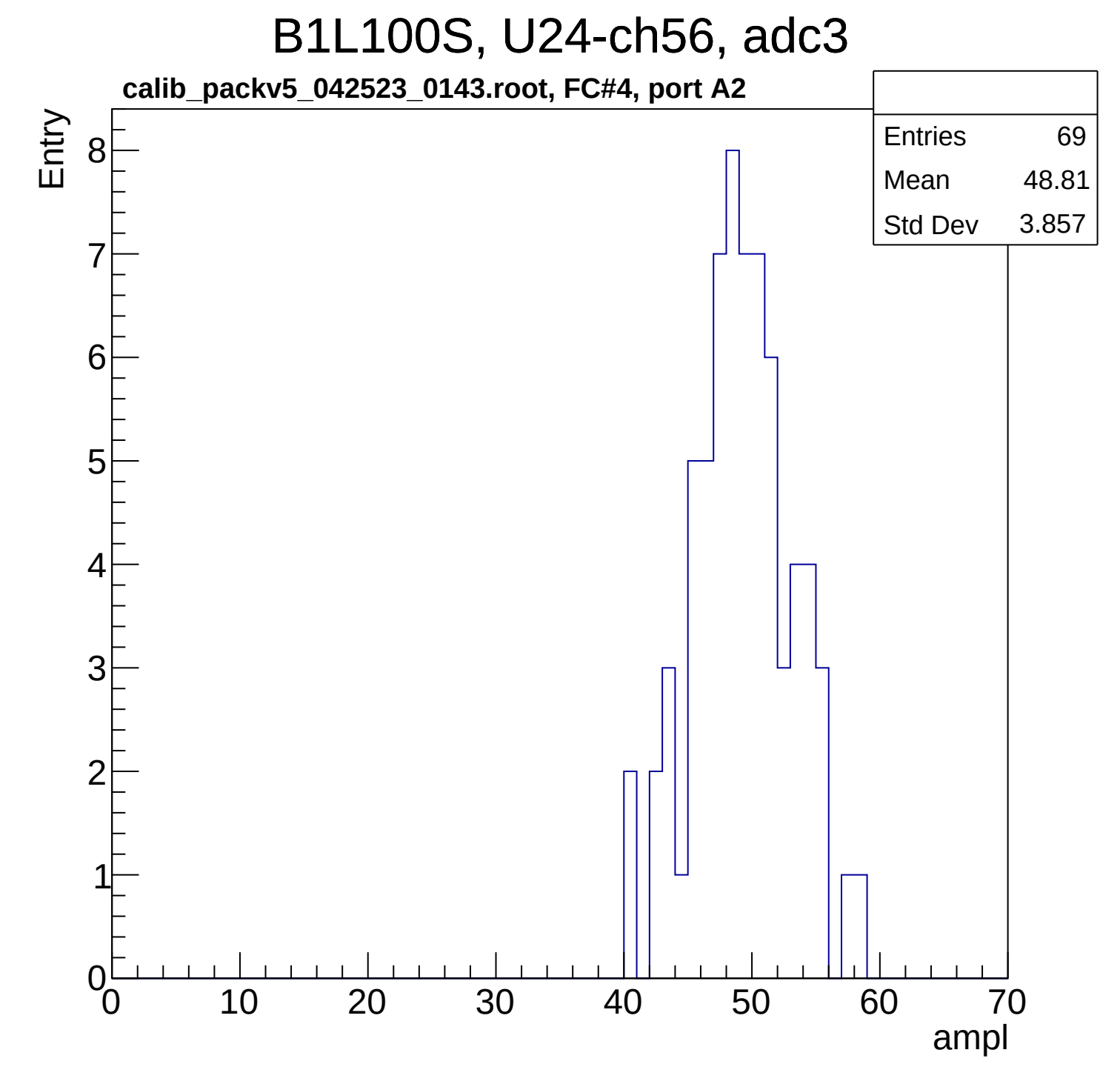
B1L100S, U24-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.81
Std Dev 3.857
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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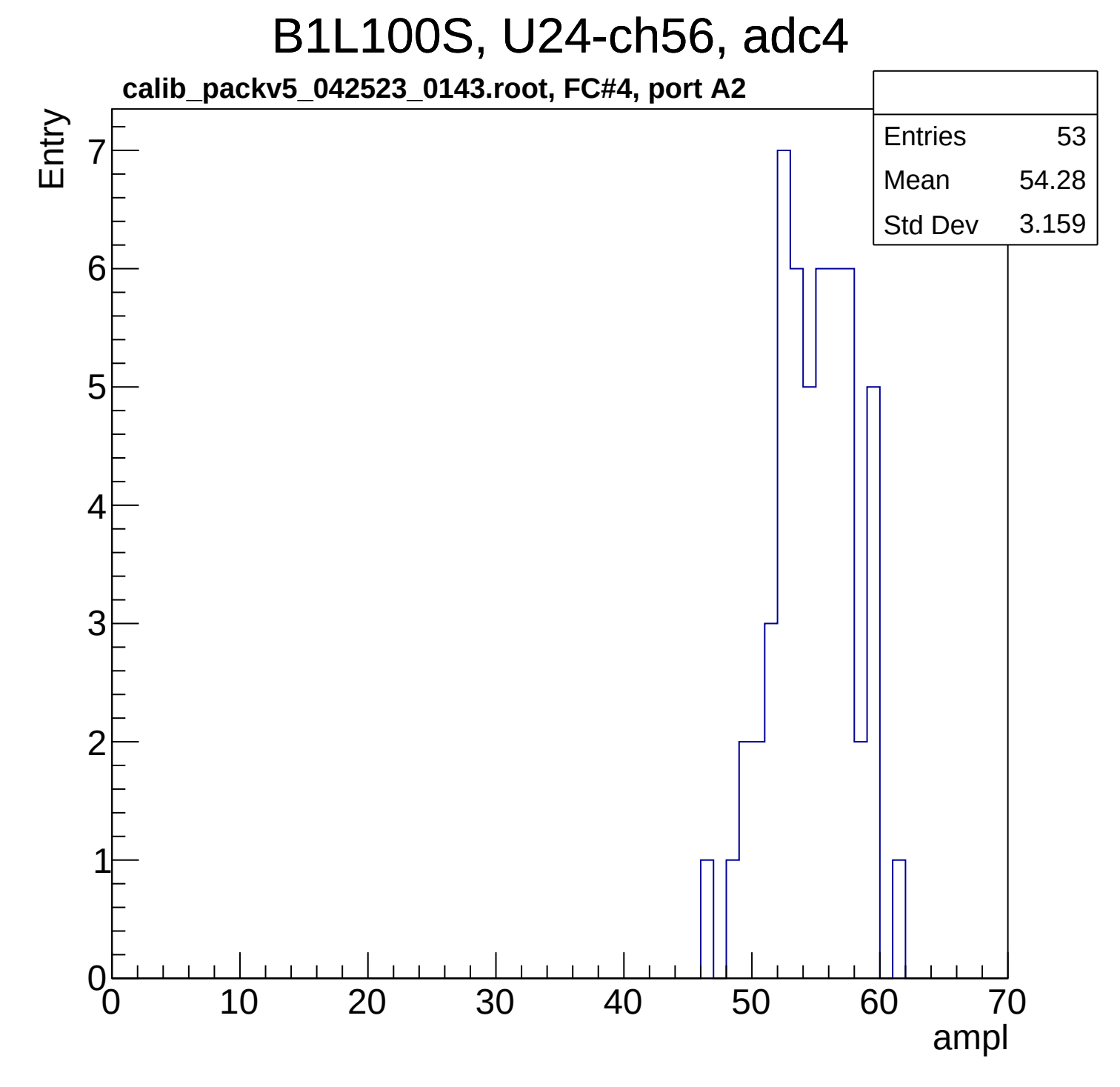
B1L100S, U24-ch56, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 54.28
Std Dev 3.159
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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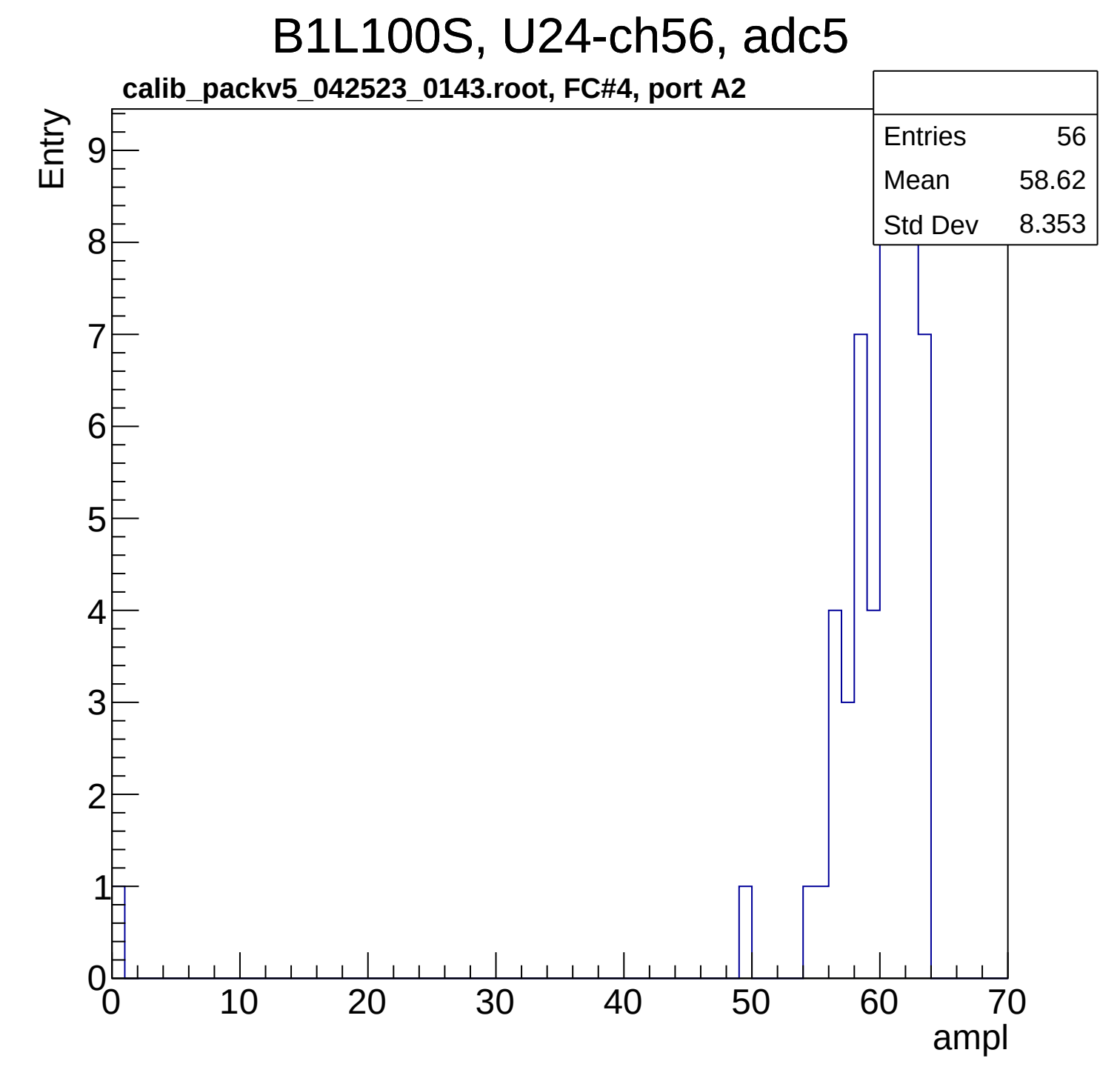
B1L100S, U24-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.62
Std Dev 8.353
Entry
ampl
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3
4
5
6
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0
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60
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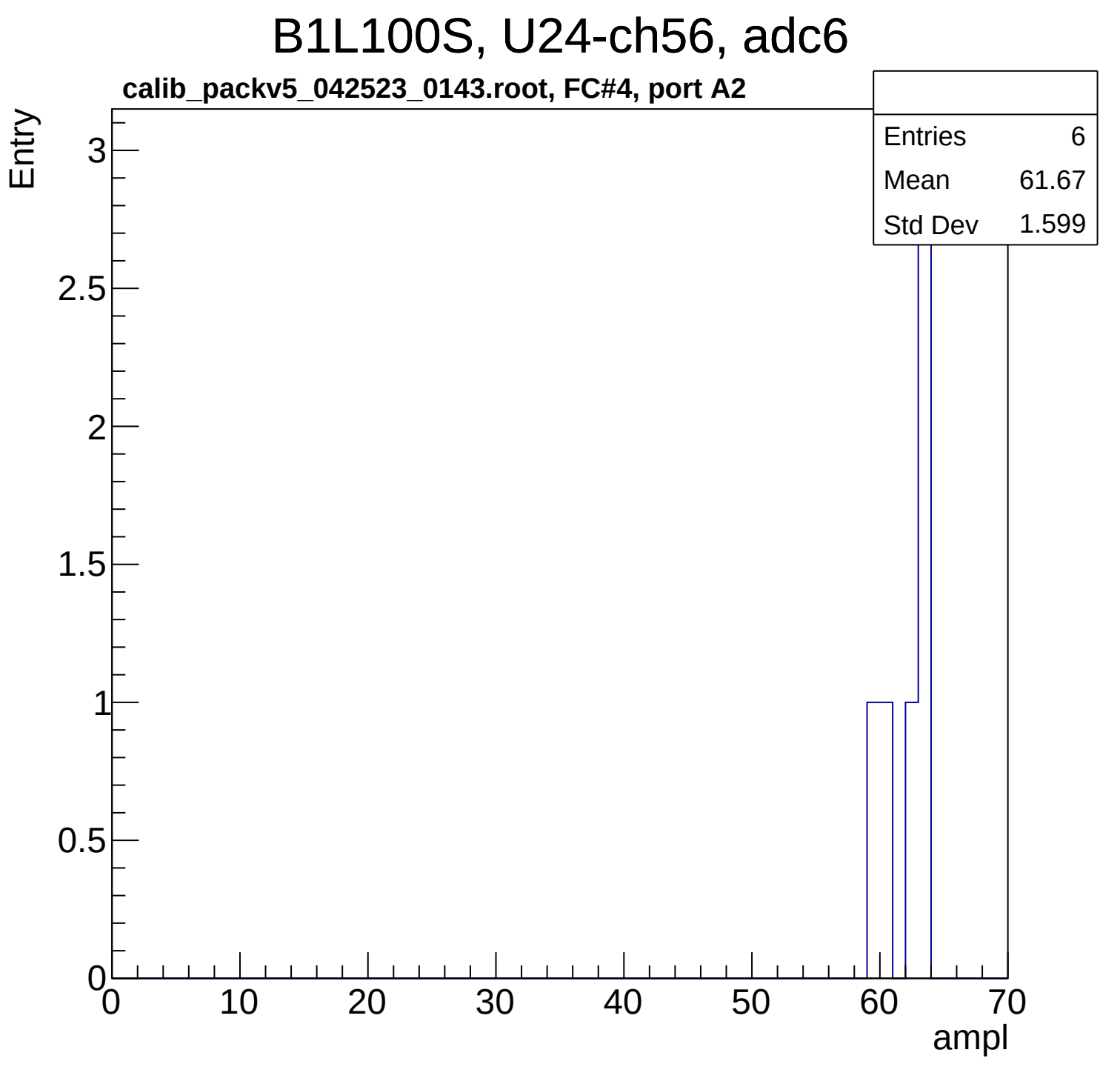
B1L100S, U24-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.67
Std Dev 1.599
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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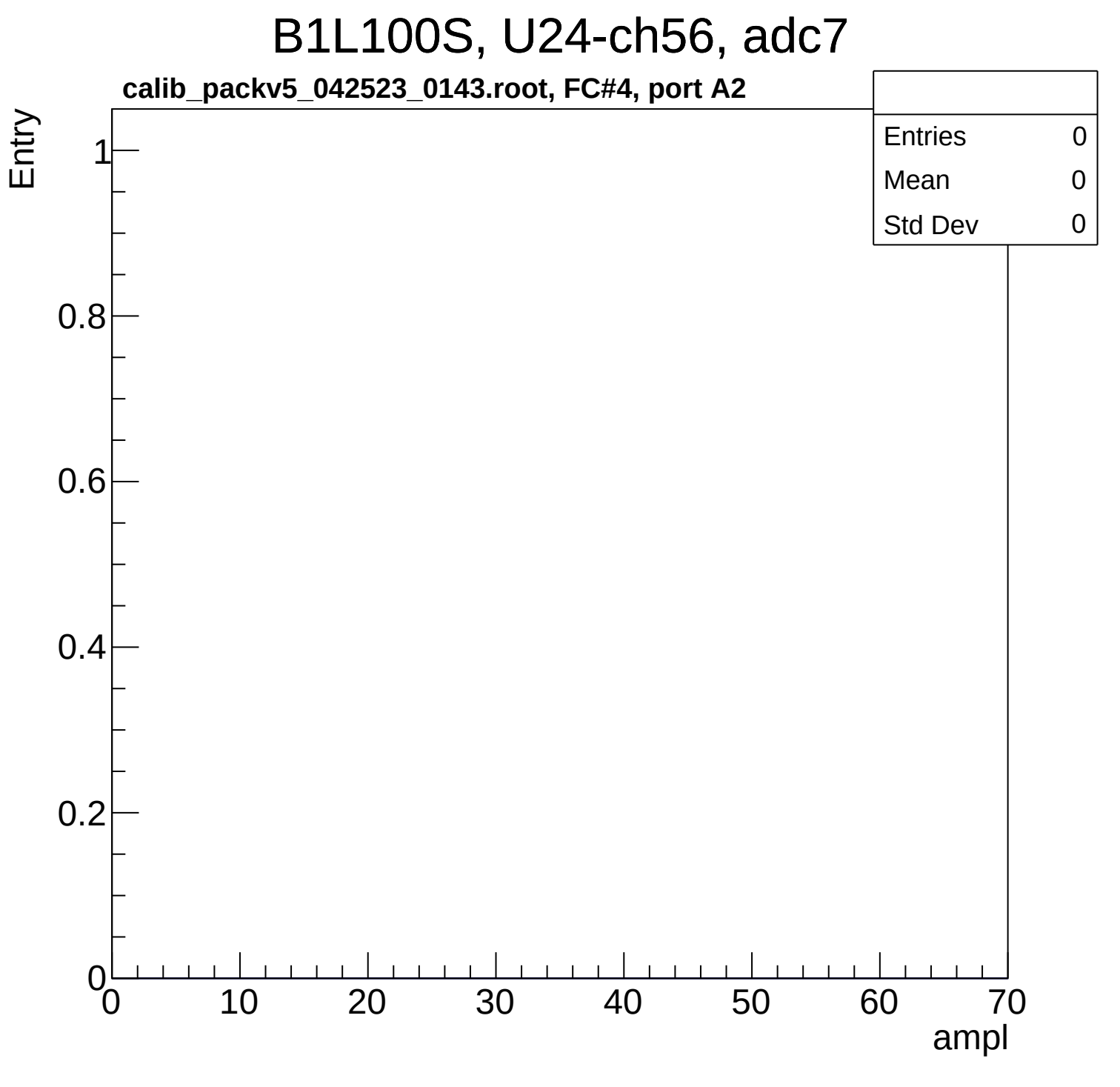
B1L100S, U24-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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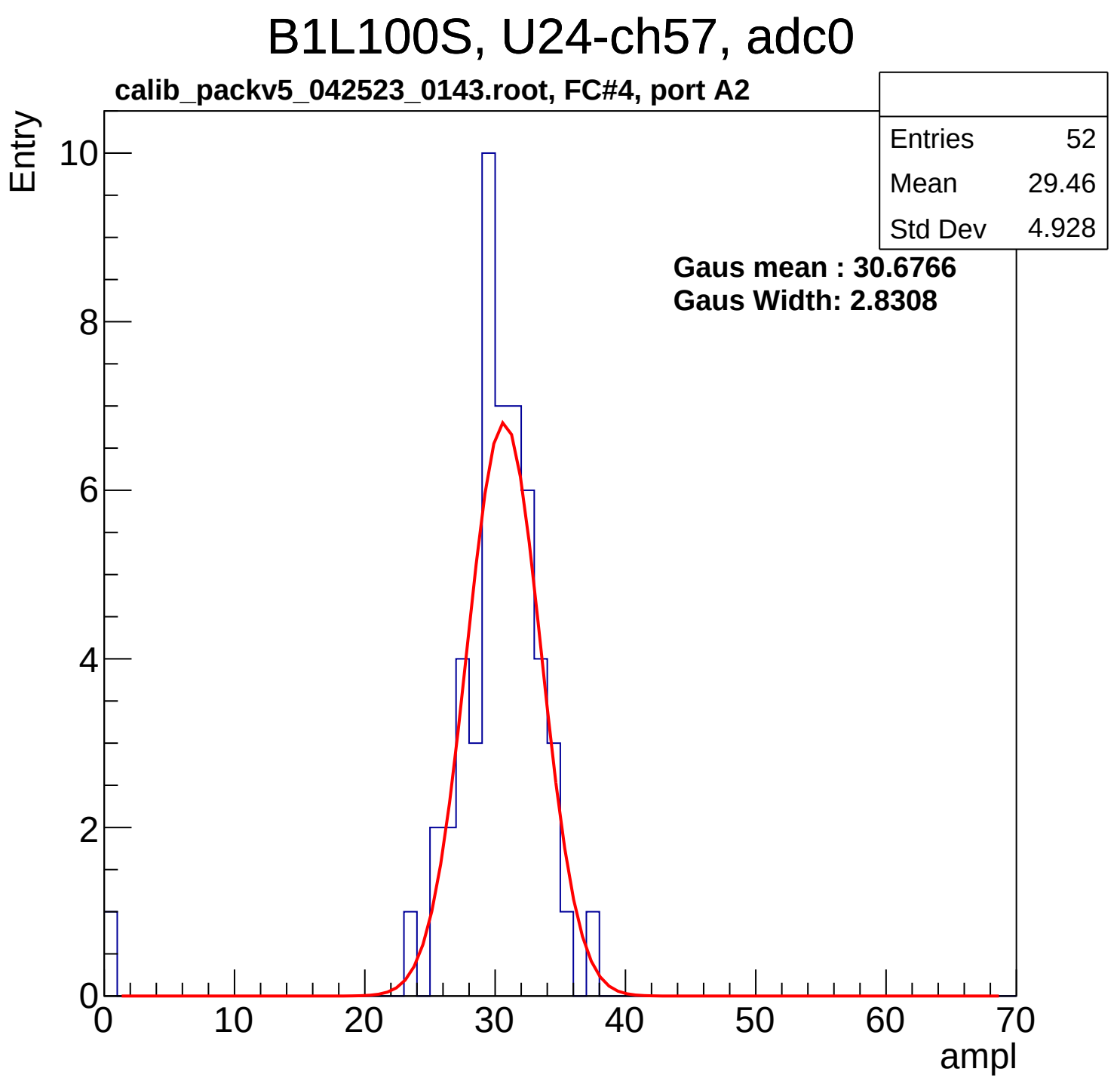
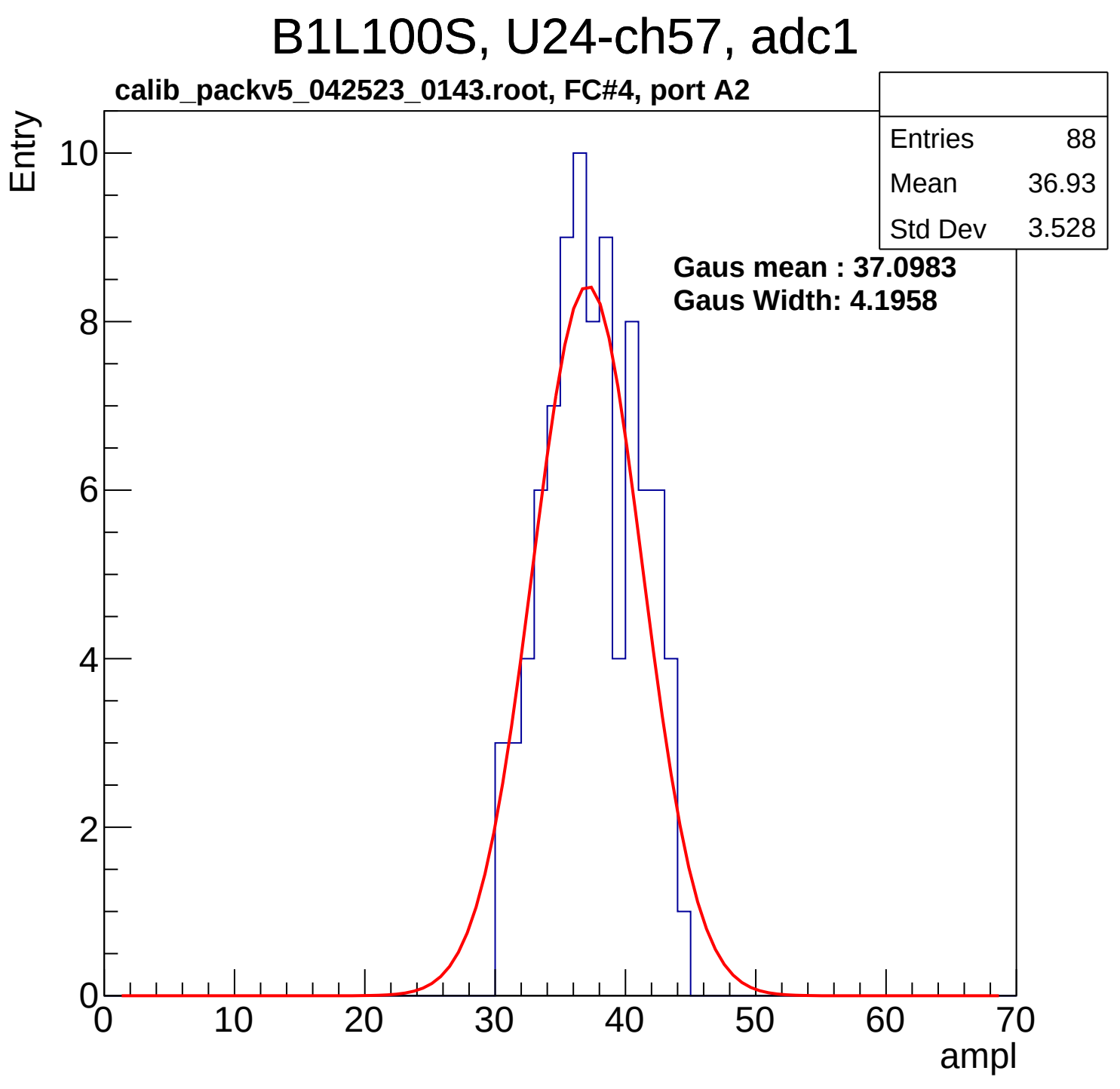
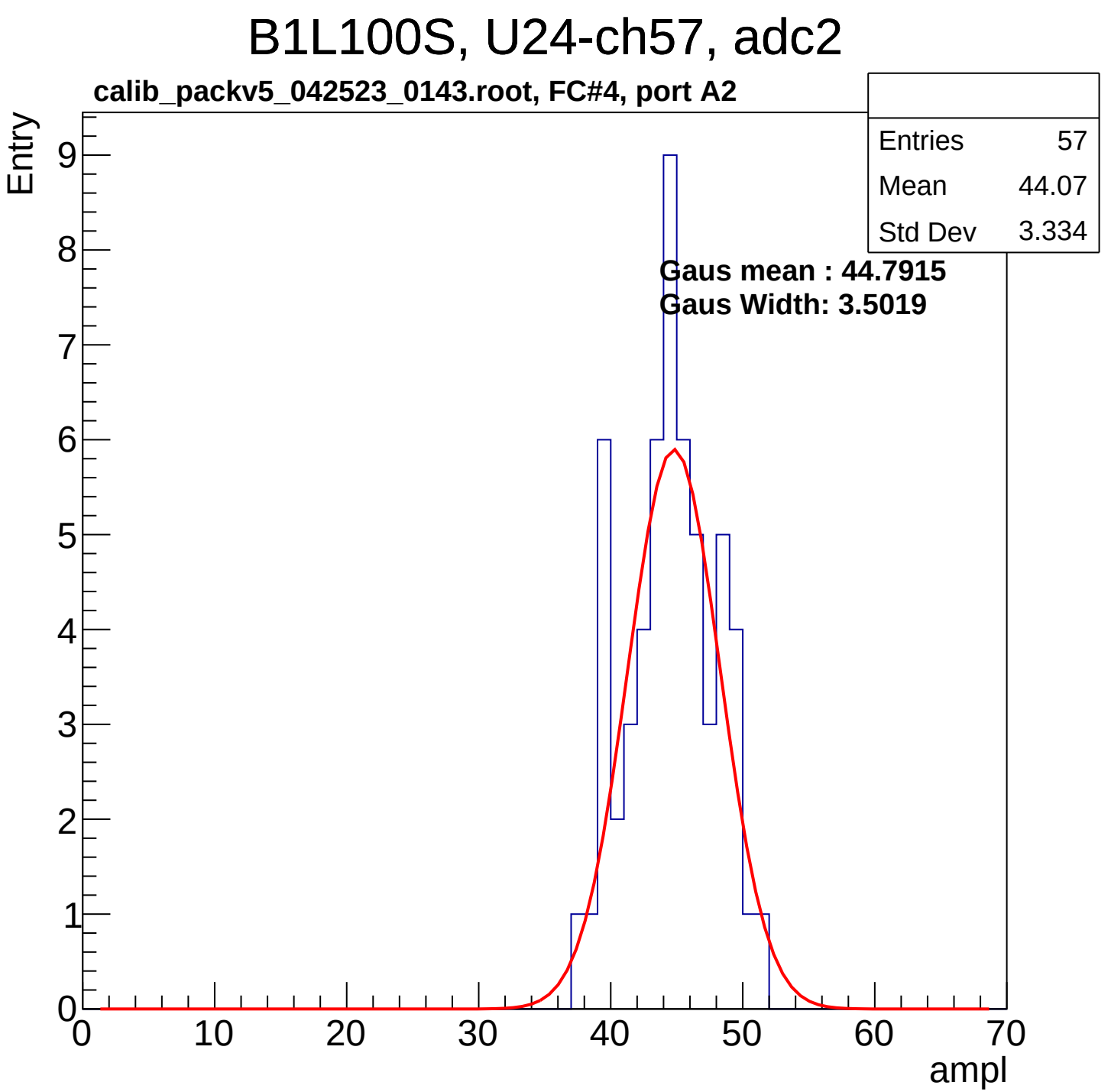
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 29.46
Std Dev 4.928
Gaus mean : 30.6766
Gaus Width: 2.8308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 36.93
Std Dev 3.528
Gaus mean : 37.0983
Gaus Width: 4.1958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.07
Std Dev 3.334
Gaus mean : 44.7915
Gaus Width: 3.5019
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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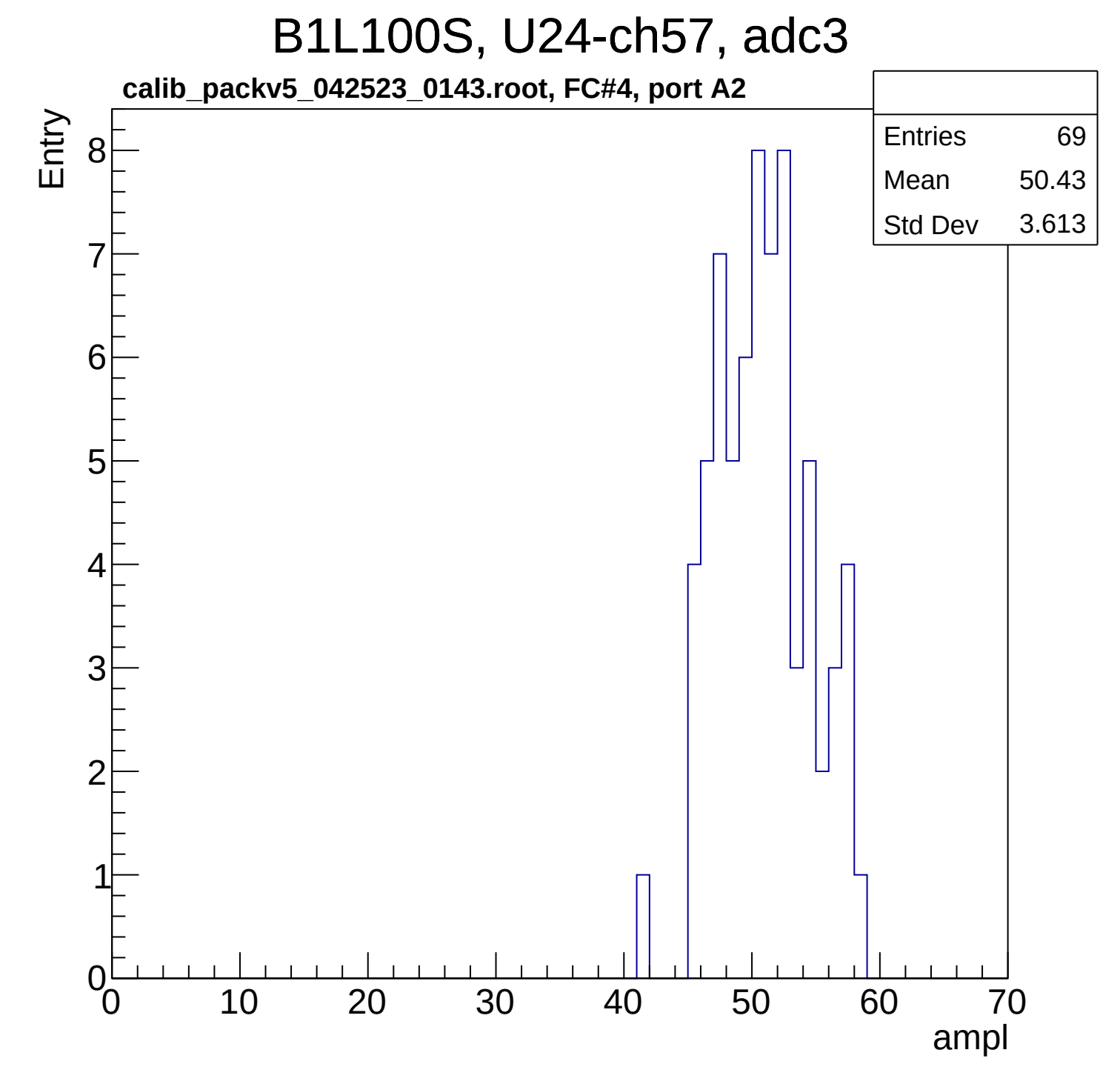
B1L100S, U24-ch57, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 50.43
Std Dev 3.613
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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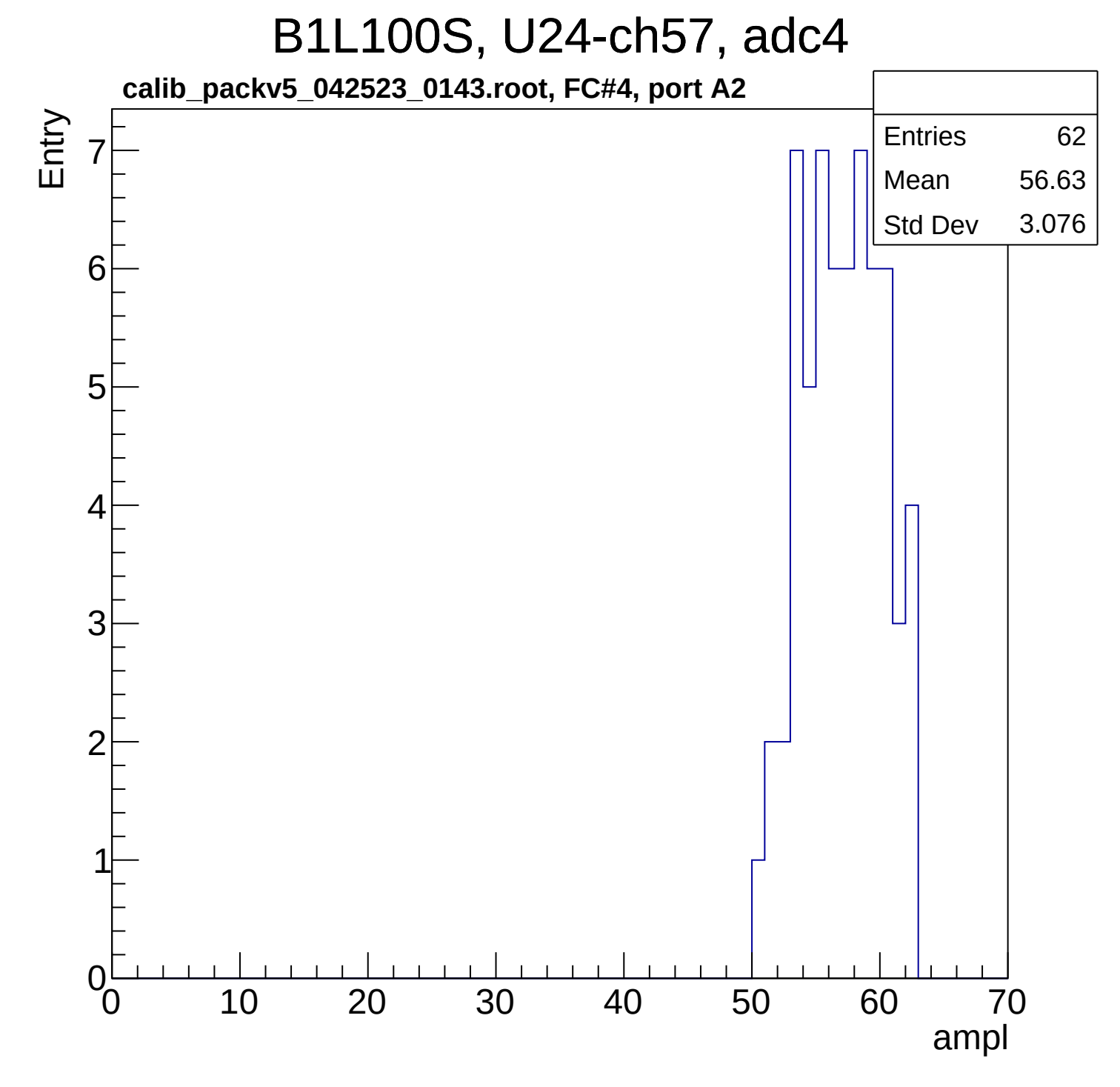
B1L100S, U24-ch57, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 56.63
Std Dev 3.076
Entry
ampl
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2
3
4
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6
7
0
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20
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60
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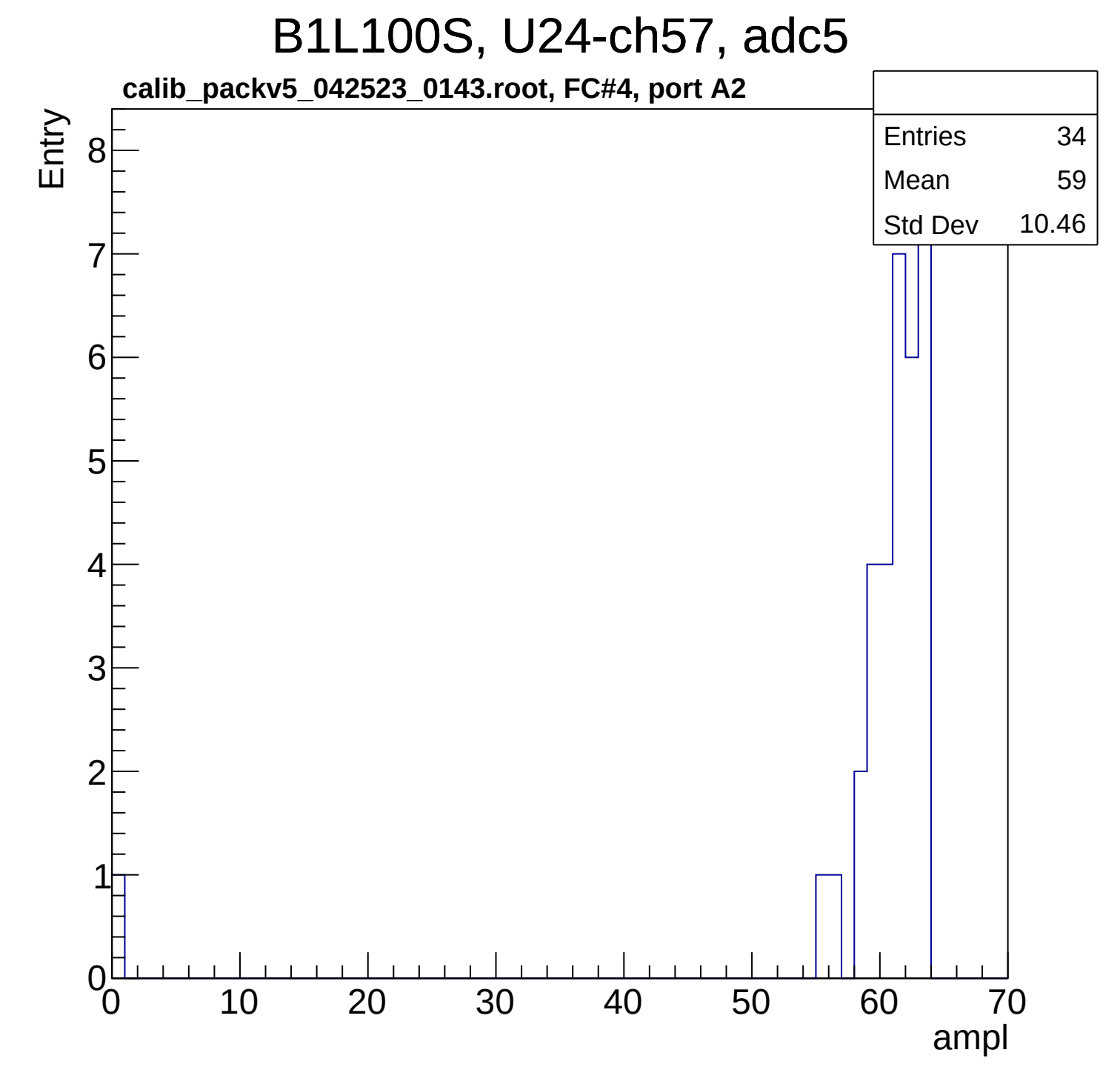
B1L100S, U24-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59
Std Dev 10.46
Entry
ampl
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1
2
3
4
5
6
7
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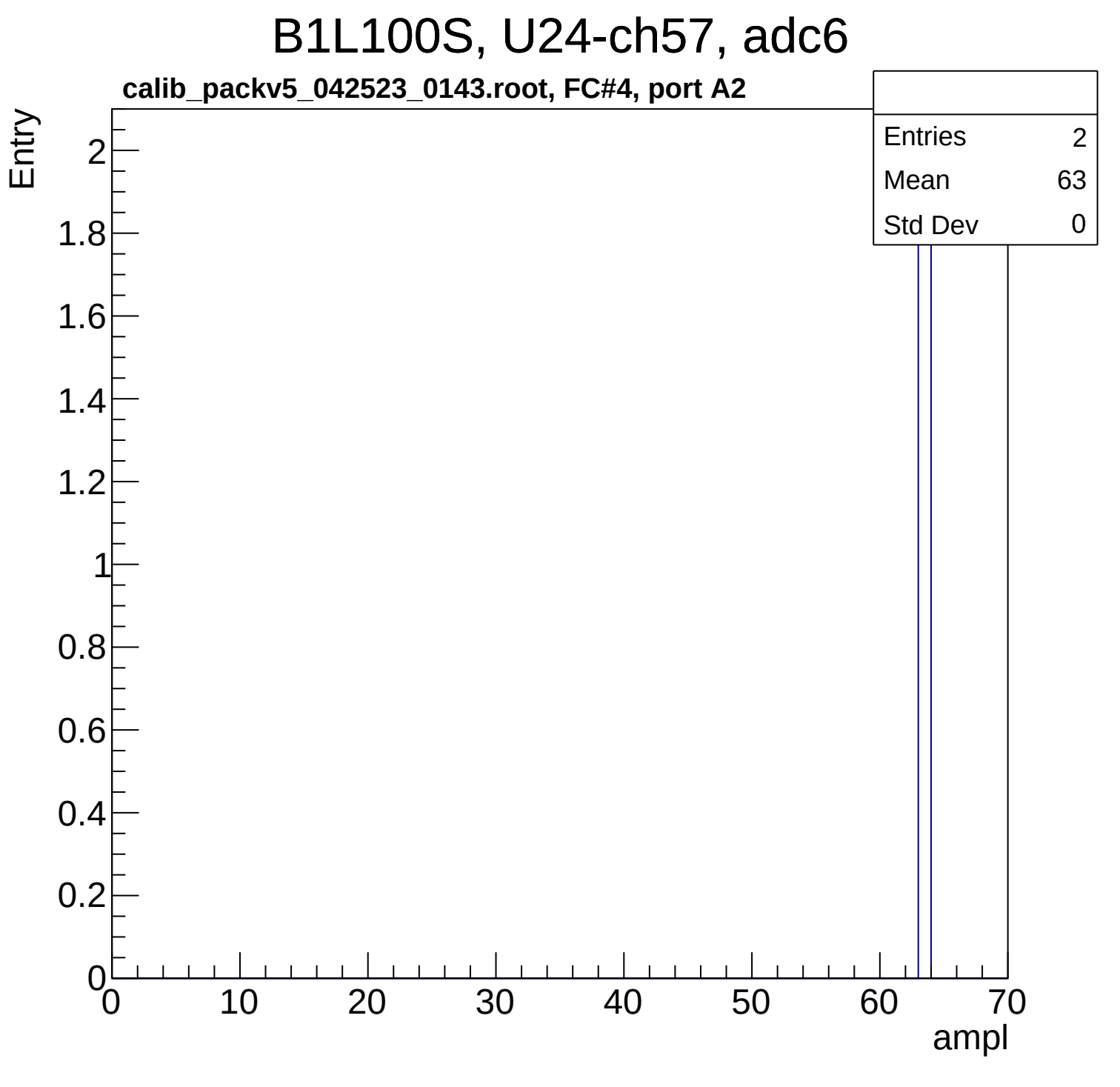
B1L100S, U24-ch57, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
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60
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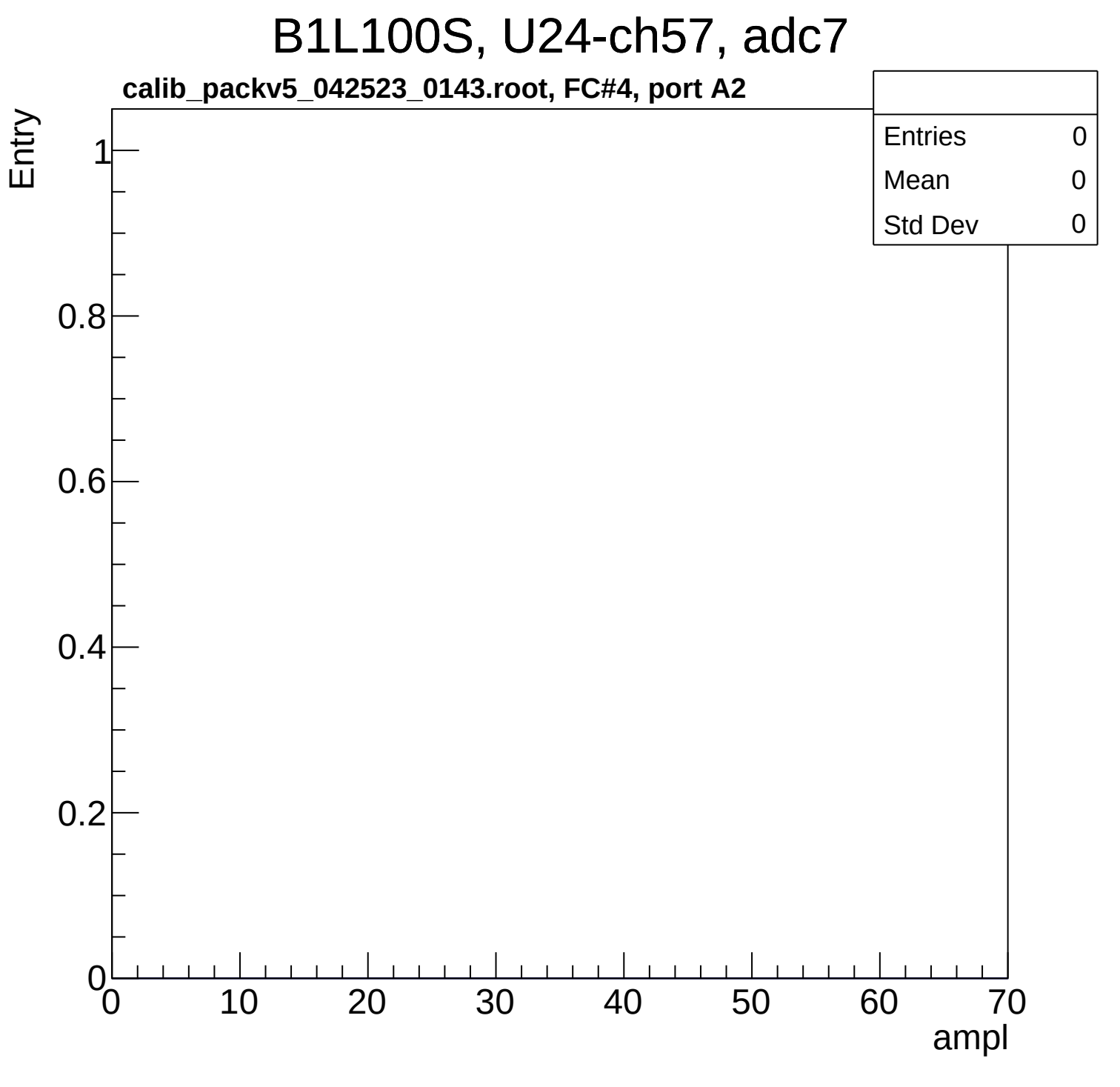
B1L100S, U24-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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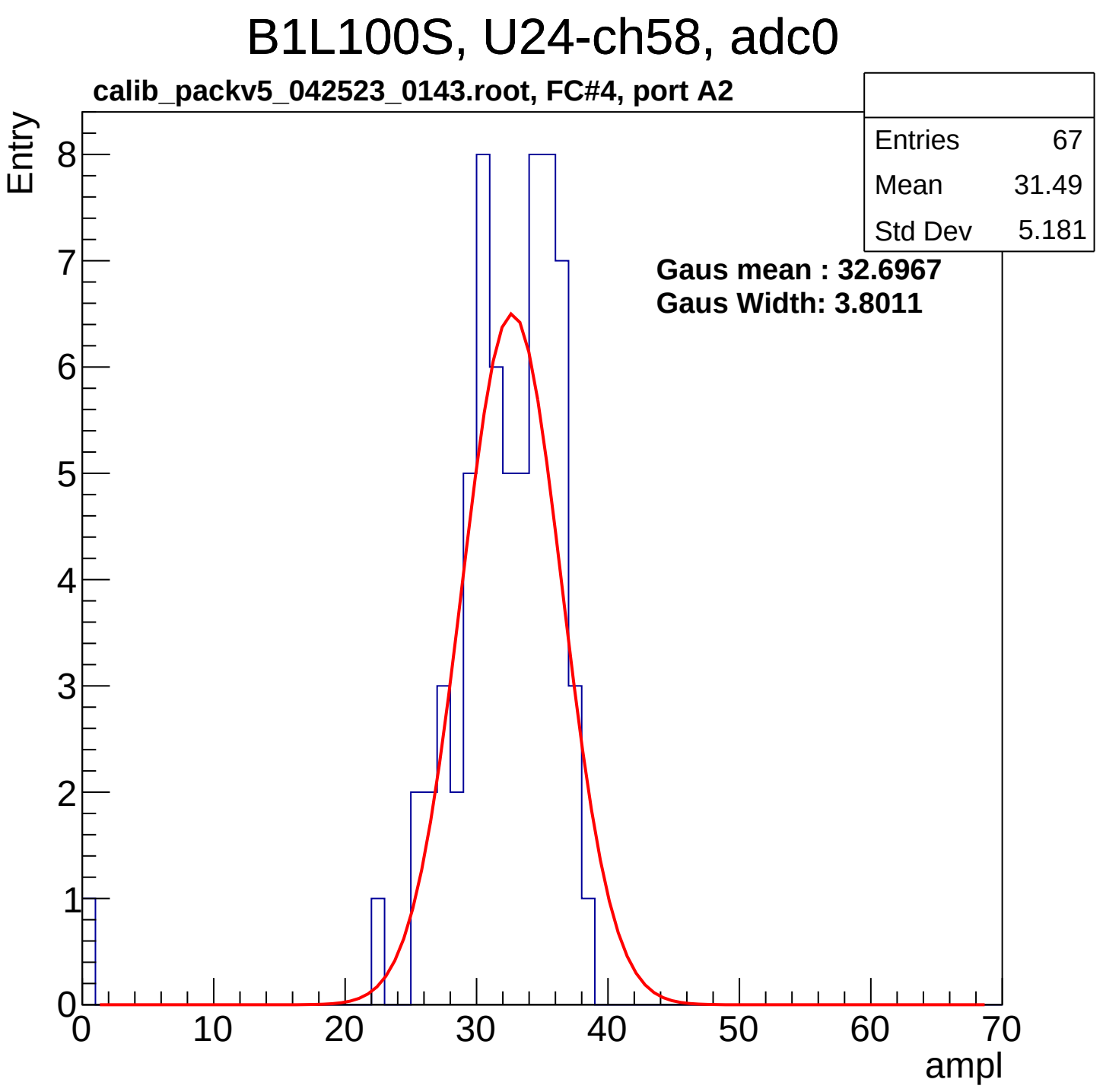
30
40
50
60
70
ampl


B1L100S, U24-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 31.49
Std Dev 5.181
Gaus mean : 32.6967
Gaus Width: 3.8011
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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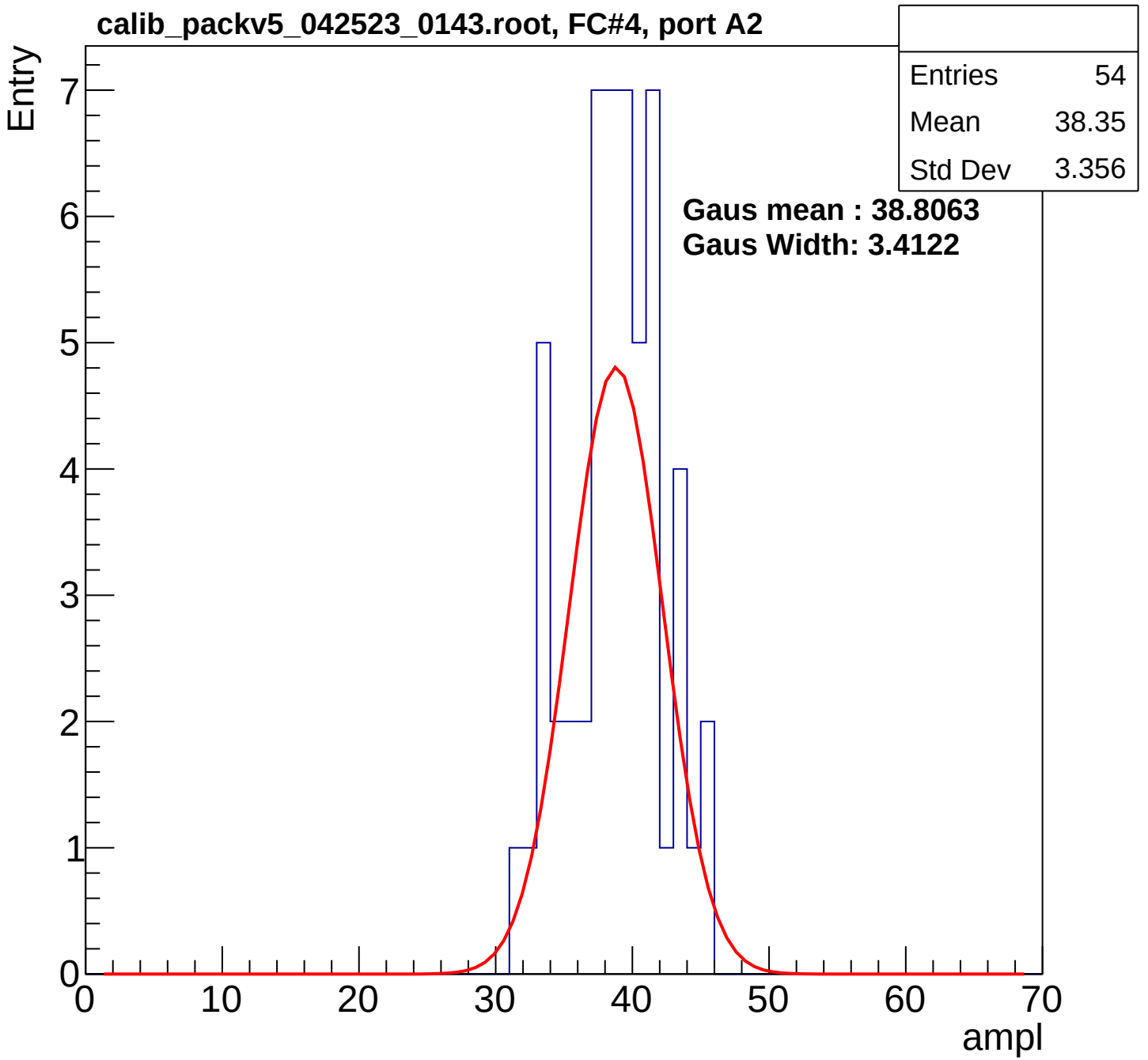
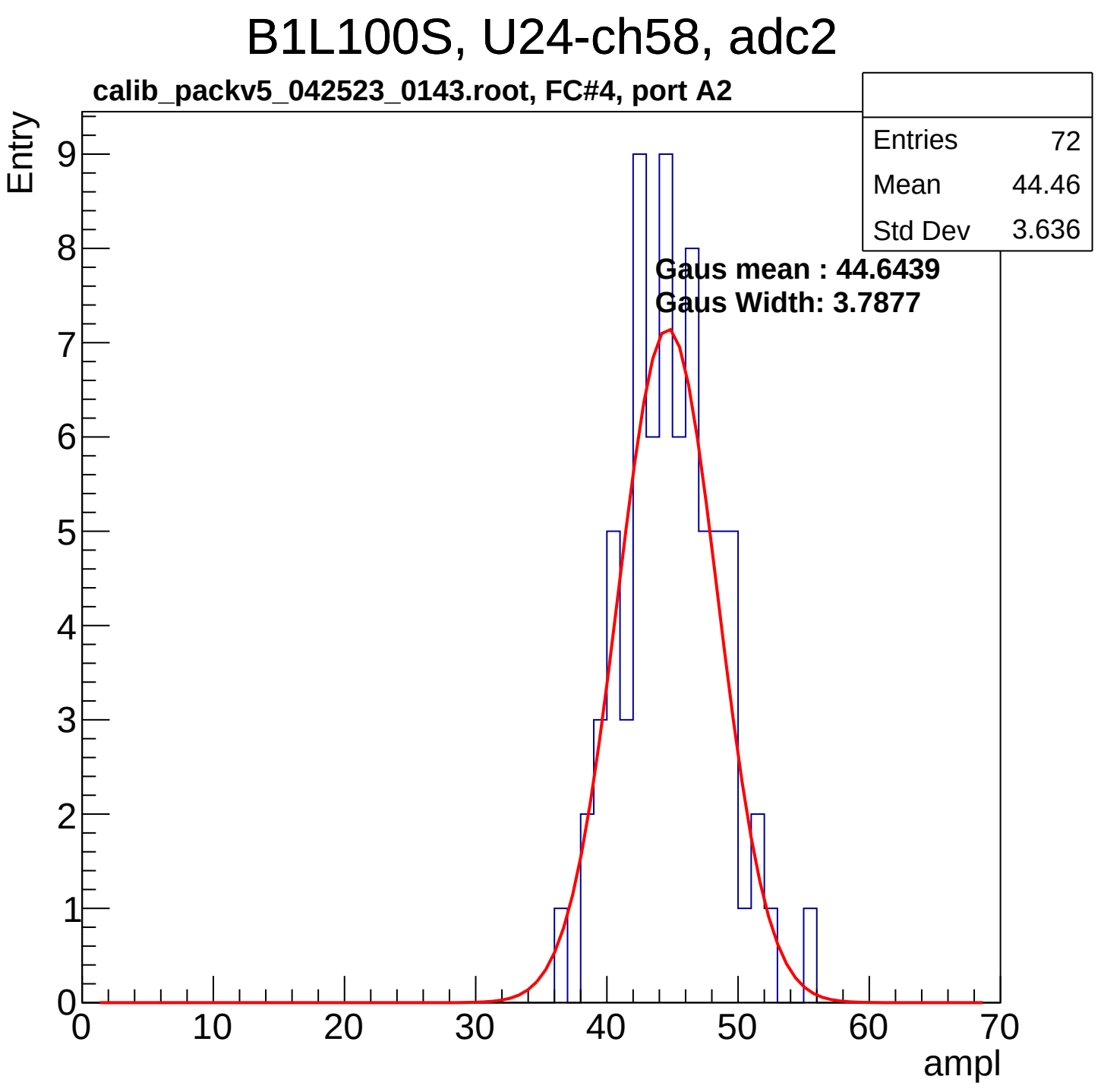

B1L100S, U24-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 38.35
Std Dev 3.356
Gaus mean : 38.8063
Gaus Width: 3.4122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.46
Std Dev 3.636
Gaus mean : 44.6439
Gaus Width: 3.7877
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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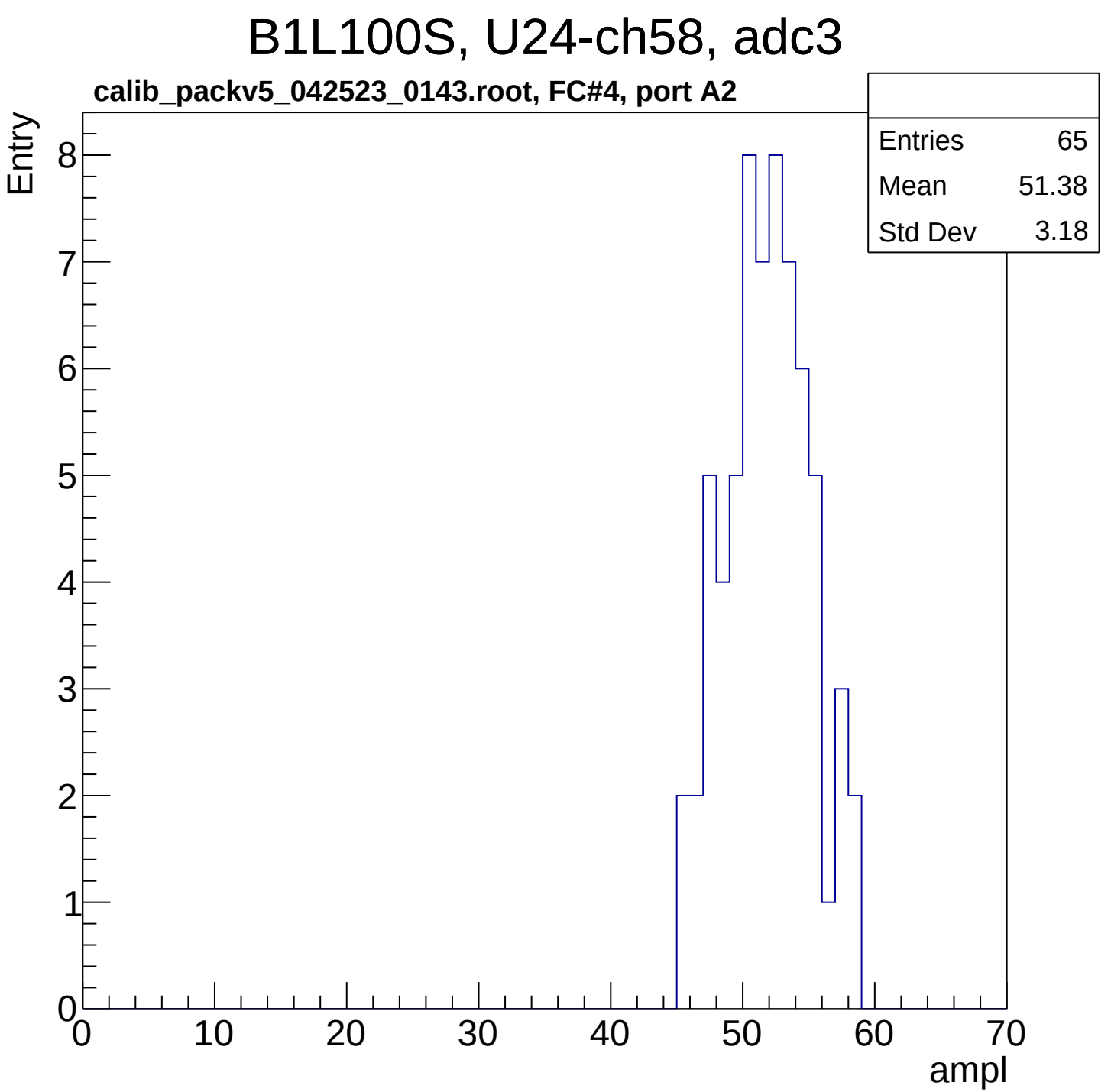
B1L100S, U24-ch58, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 51.38
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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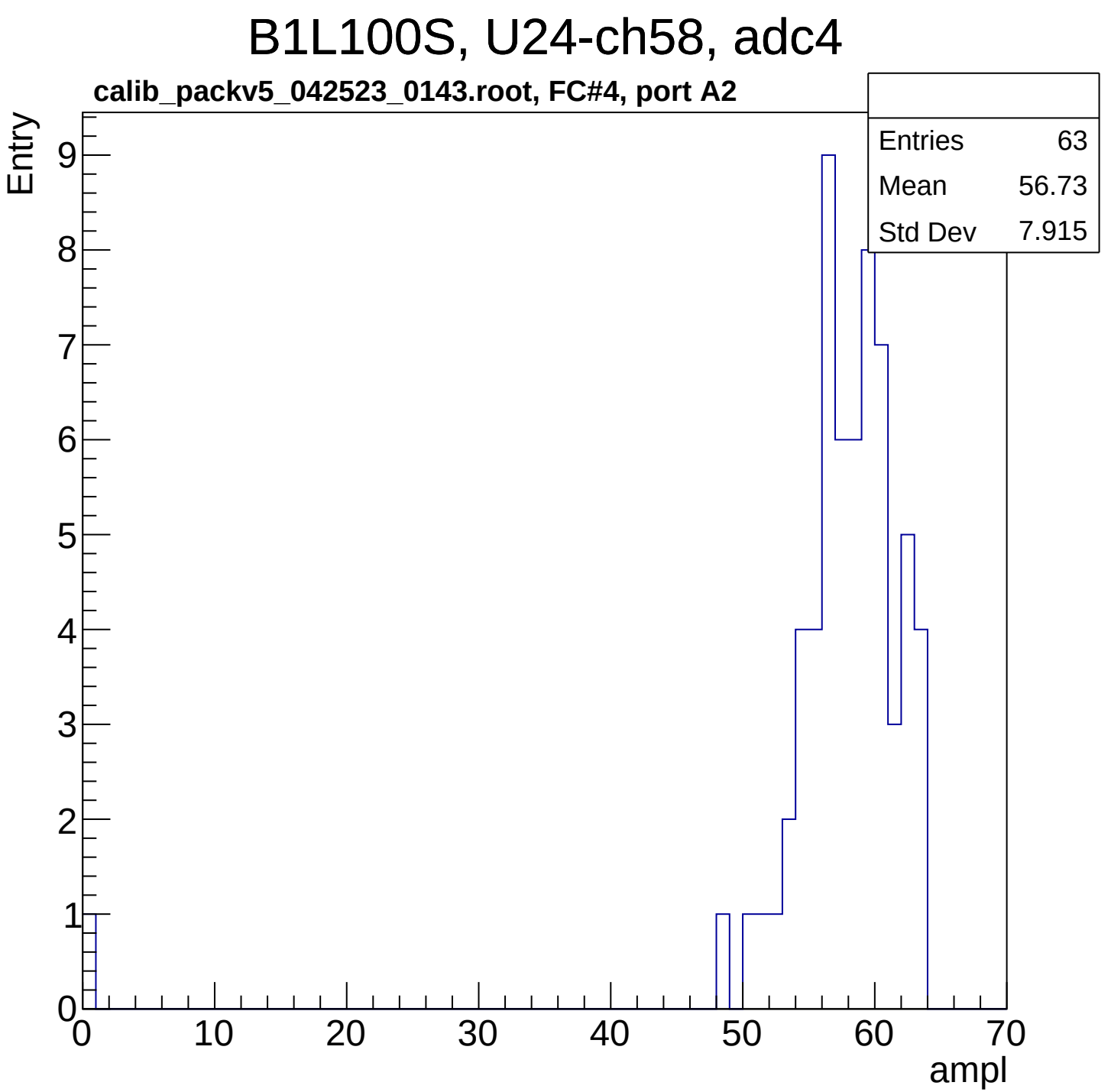
B1L100S, U24-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.73
Std Dev 7.915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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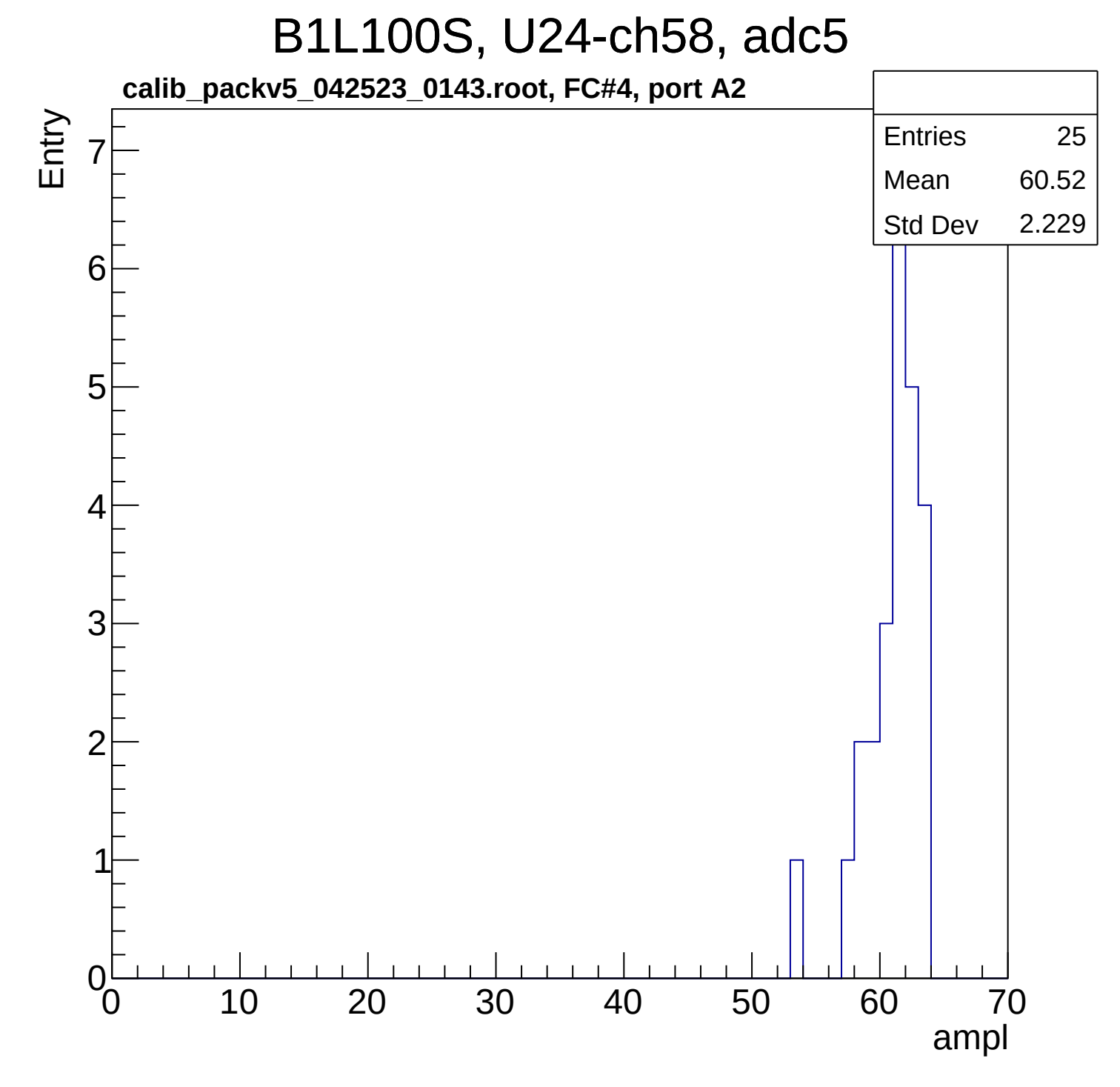
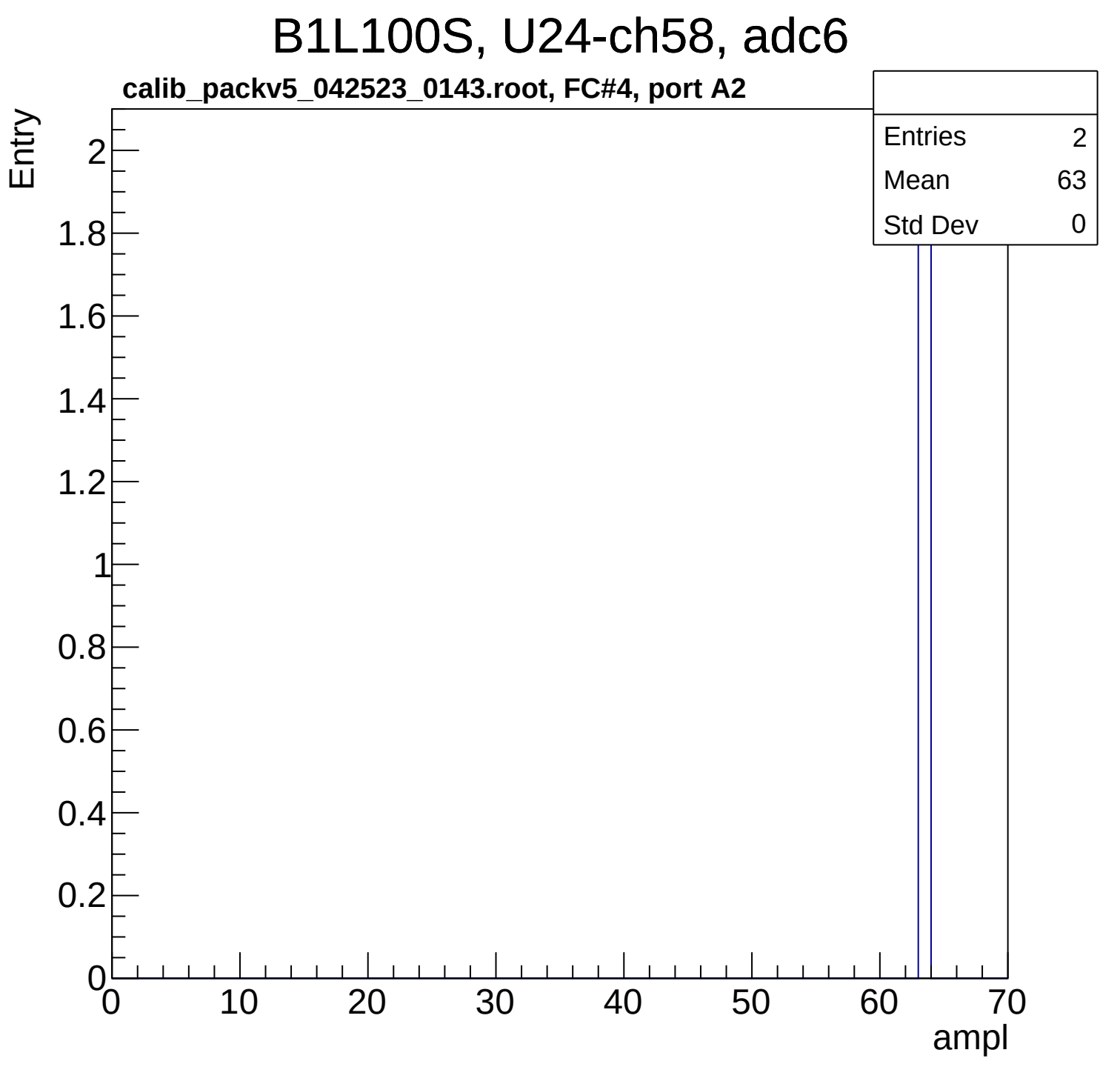
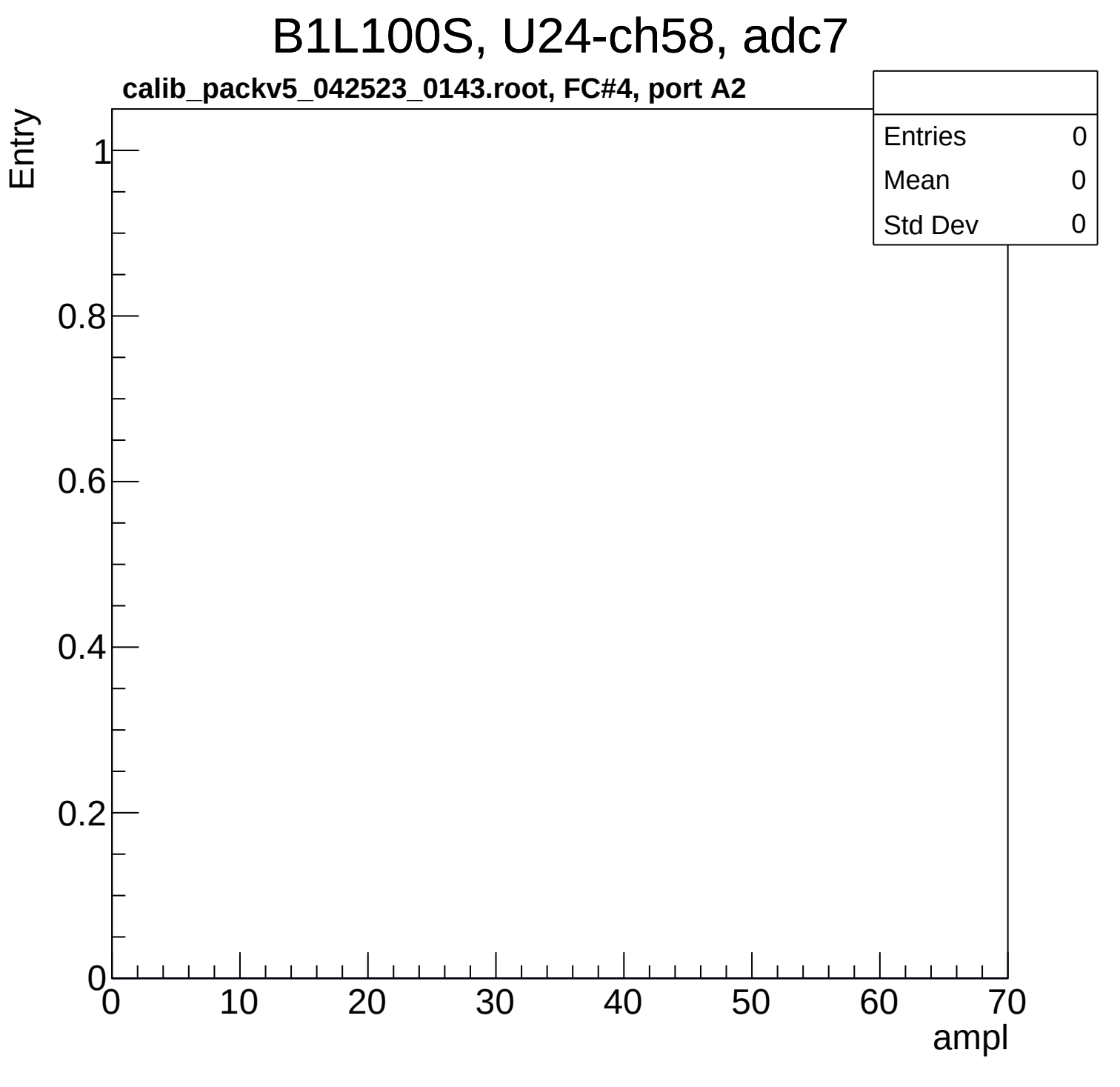
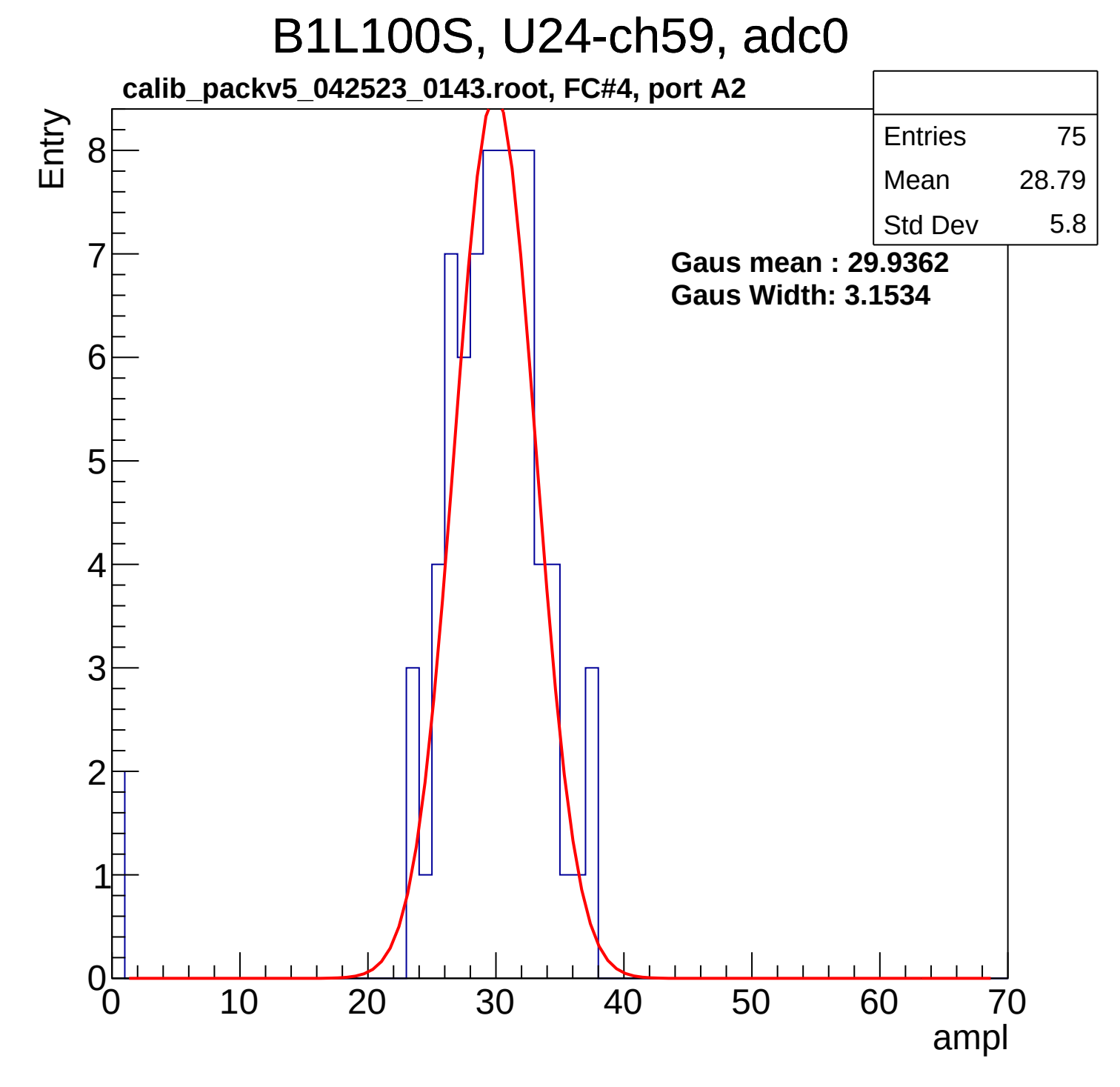
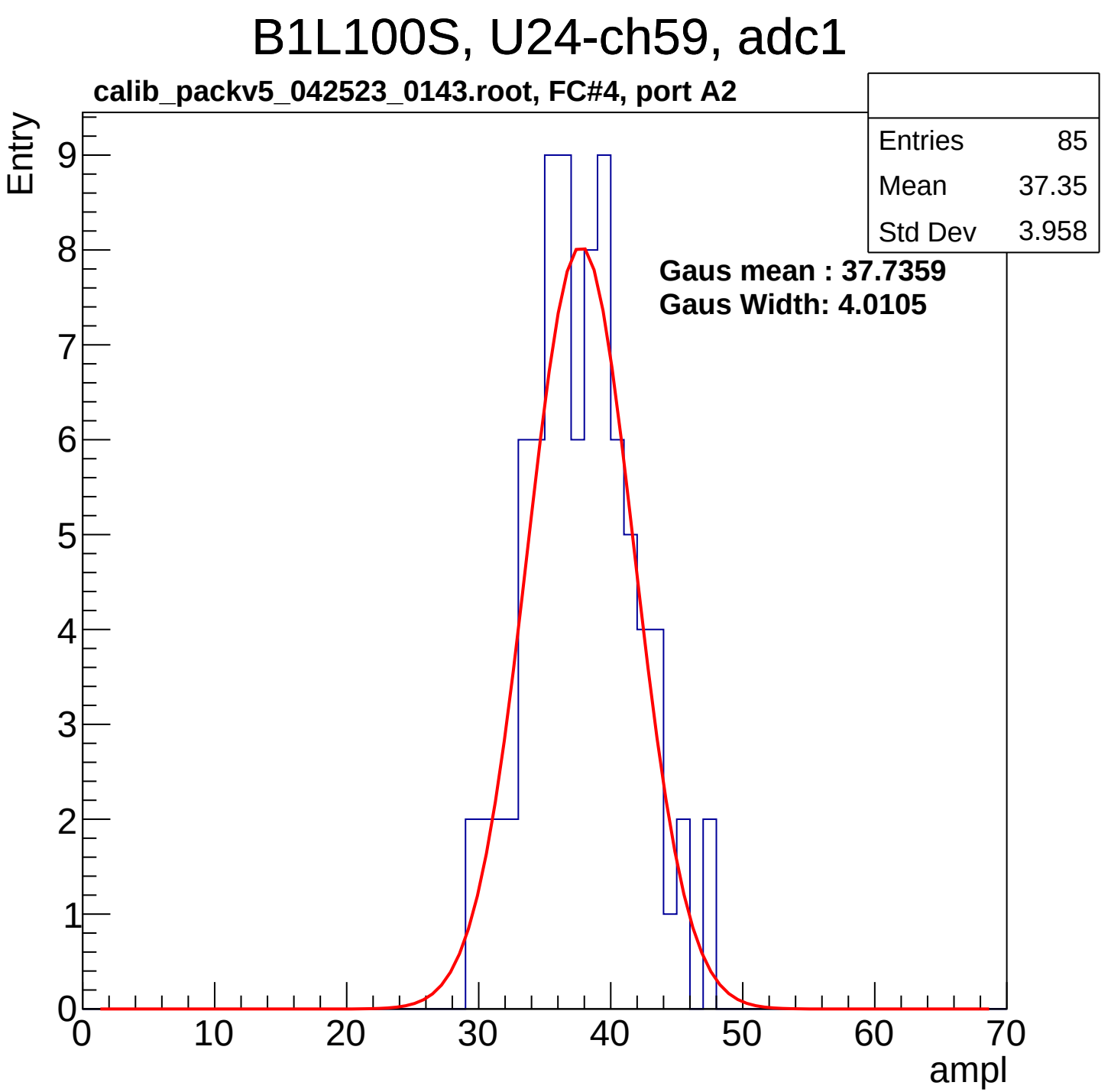

B1L100S, U24-ch58, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 60.52
Std Dev 2.229
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch58, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.79
Std Dev 5.8
Gaus mean : 29.9362
Gaus Width: 3.1534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 37.35
Std Dev 3.958
Gaus mean : 37.7359
Gaus Width: 4.0105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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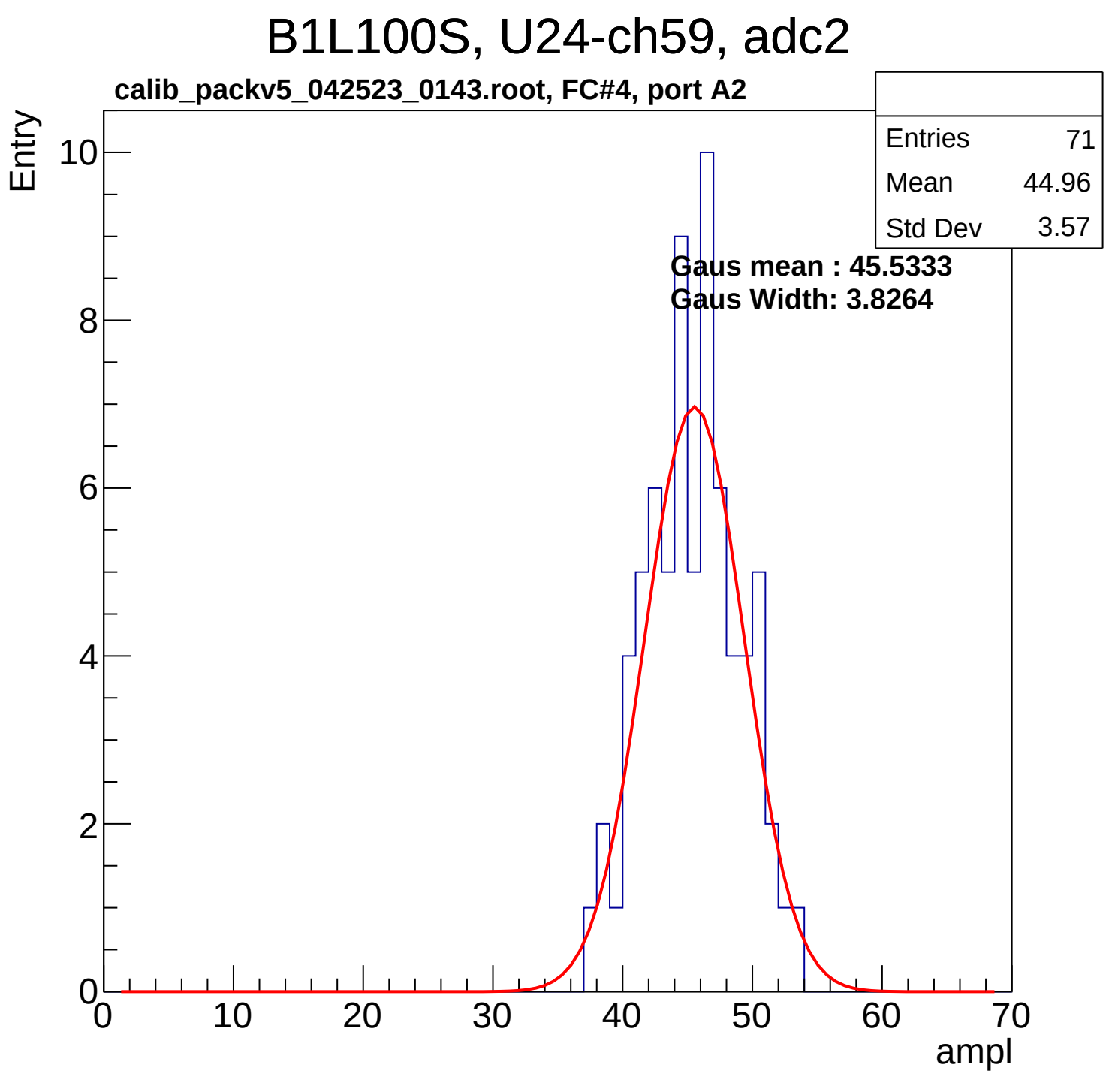
B1L100S, U24-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 44.96
Std Dev 3.57
Gaus mean : 45.5333
Gaus Width: 3.8264
Entry
ampl
0
2
4
6
8
10
0
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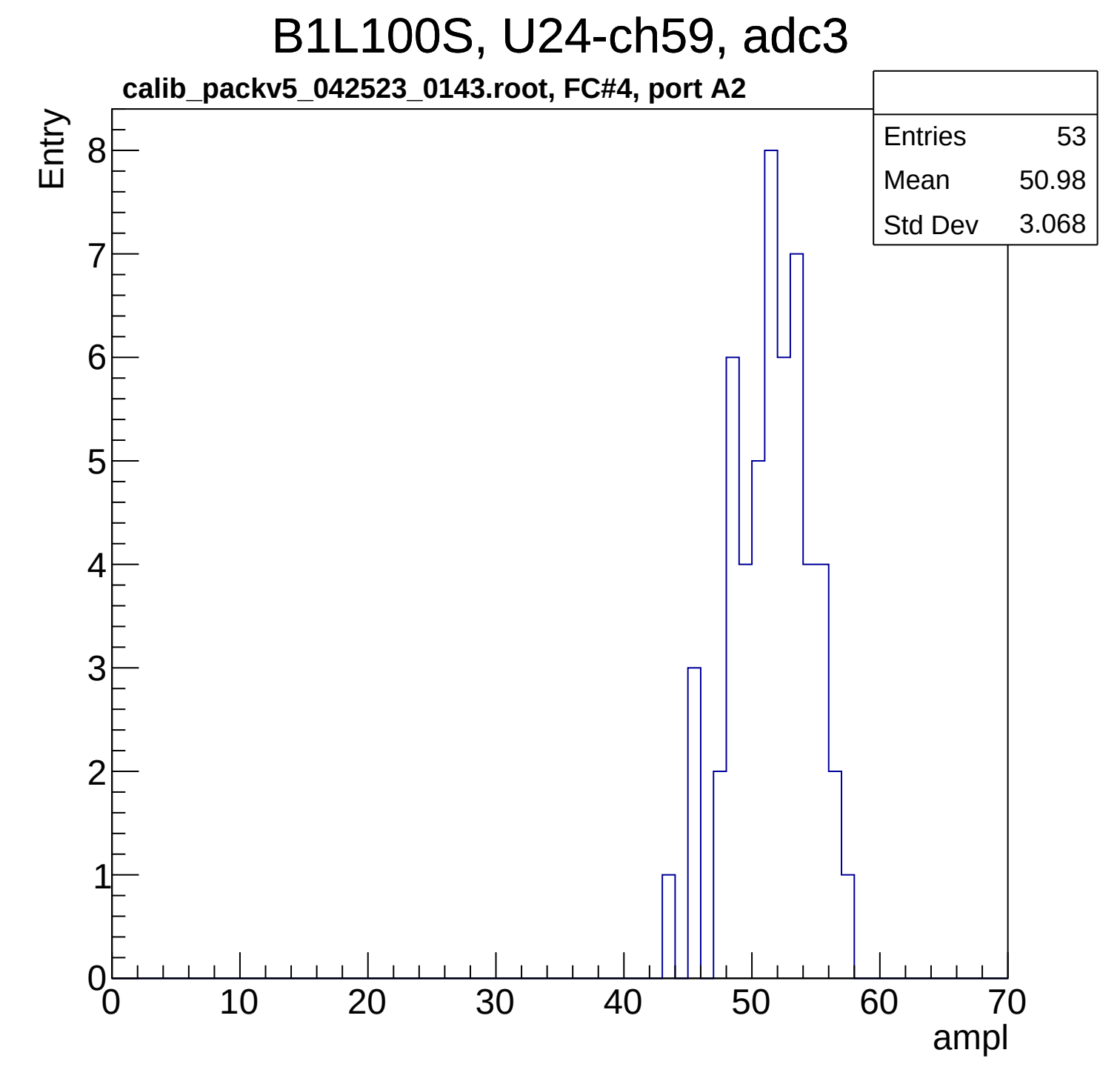
20
30
40
50
60
70


B1L100S, U24-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 50.98
Std Dev 3.068
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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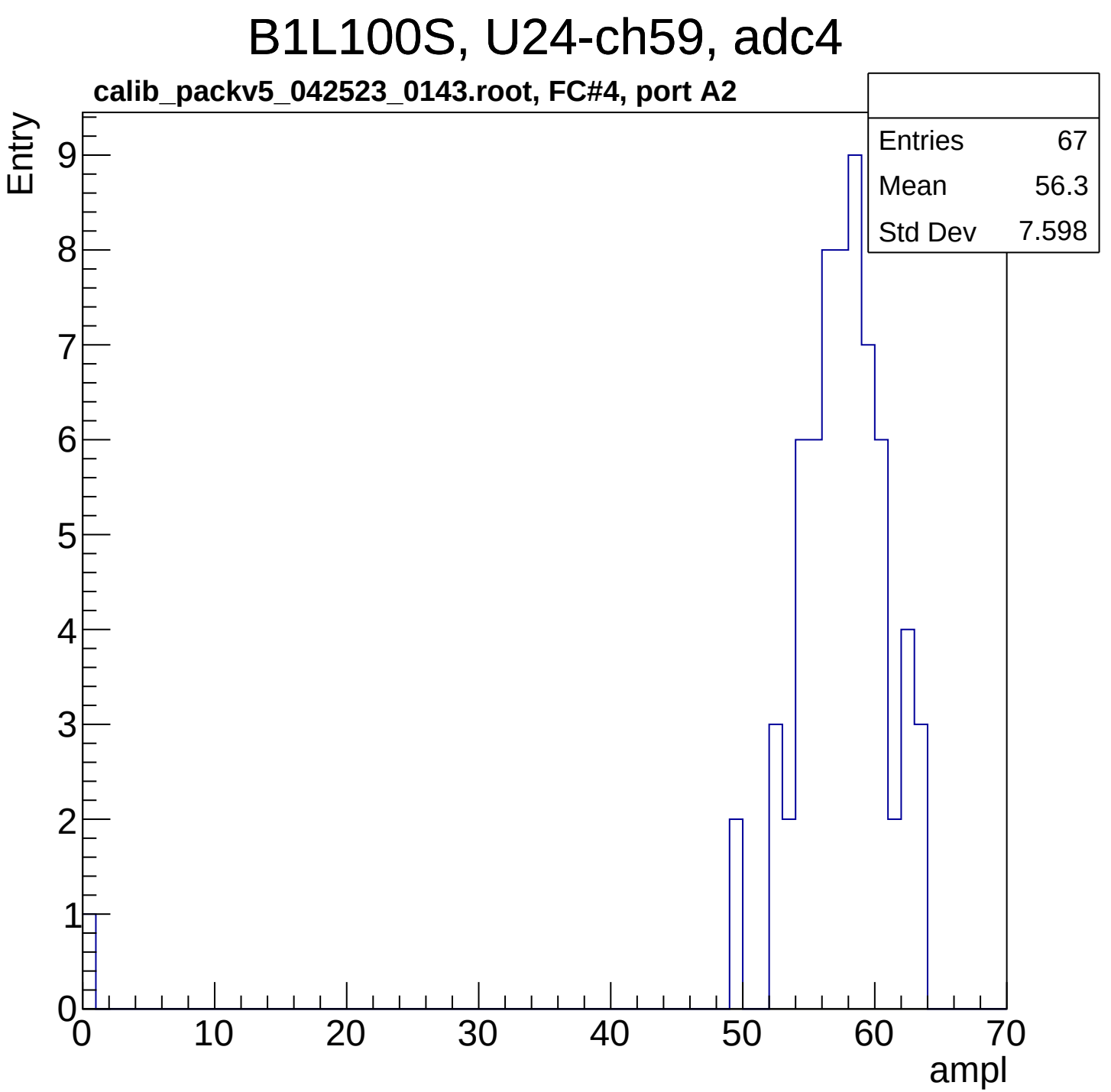
B1L100S, U24-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.3
Std Dev 7.598
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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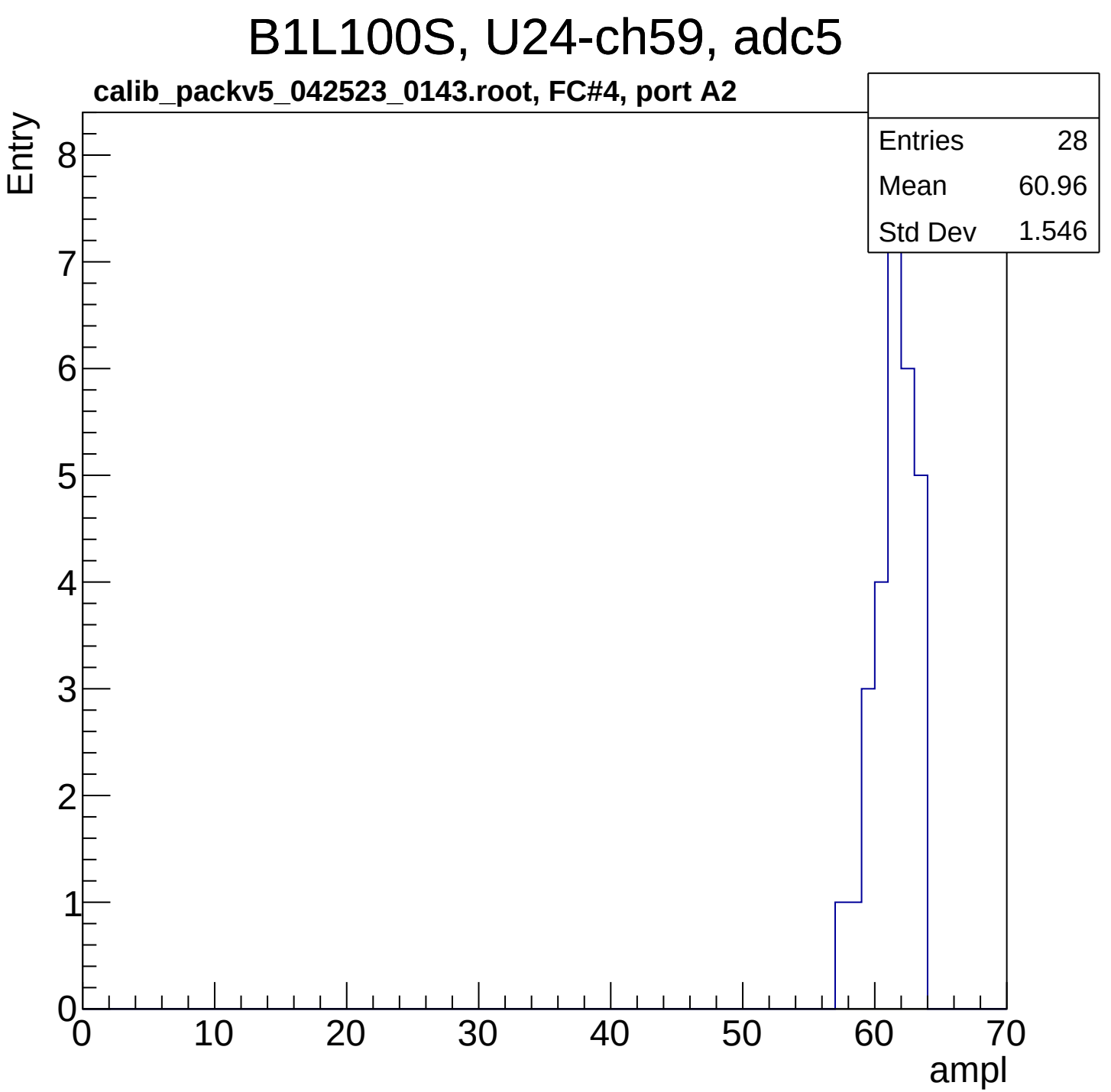
B1L100S, U24-ch59, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 60.96
Std Dev 1.546
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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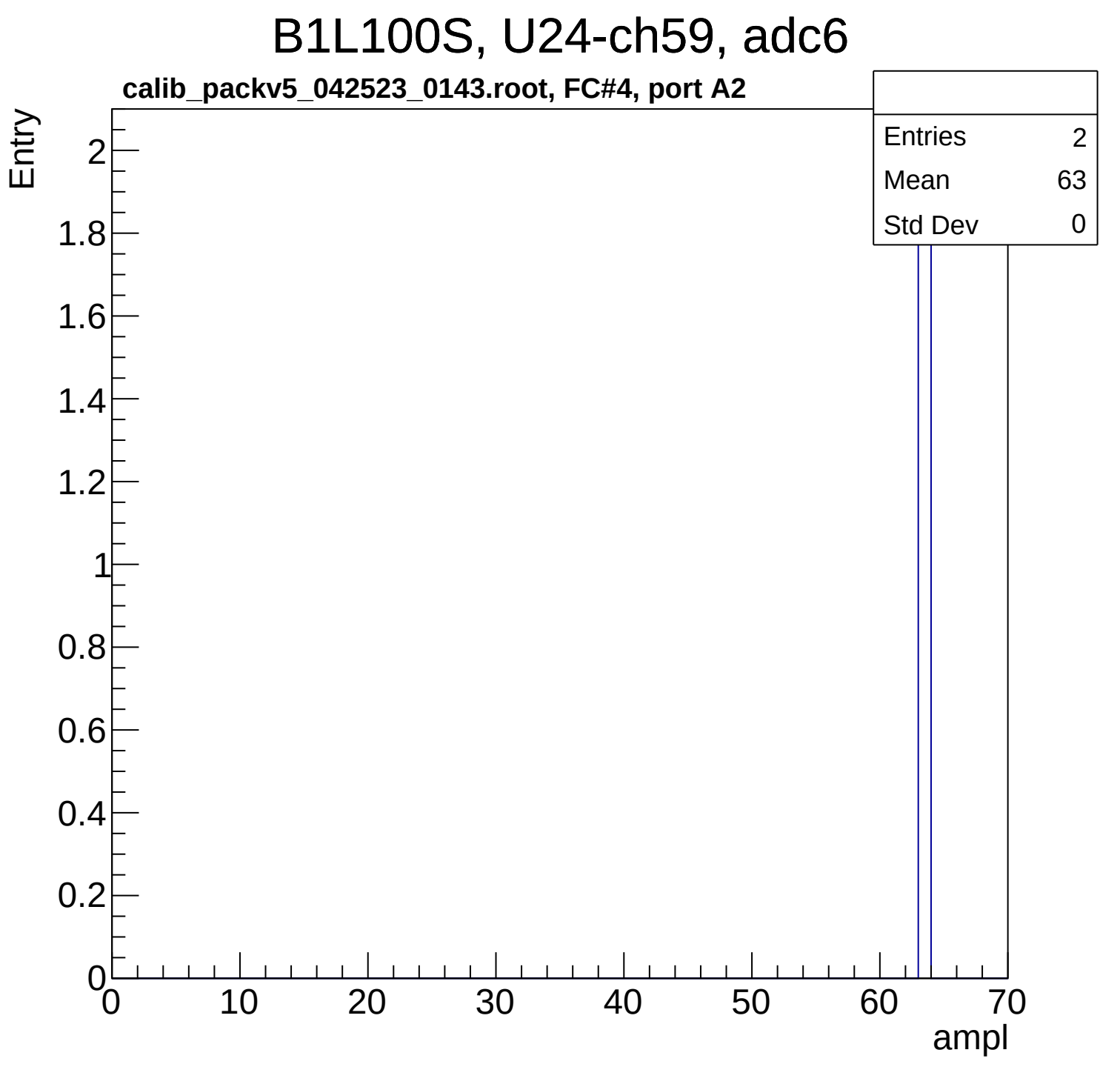
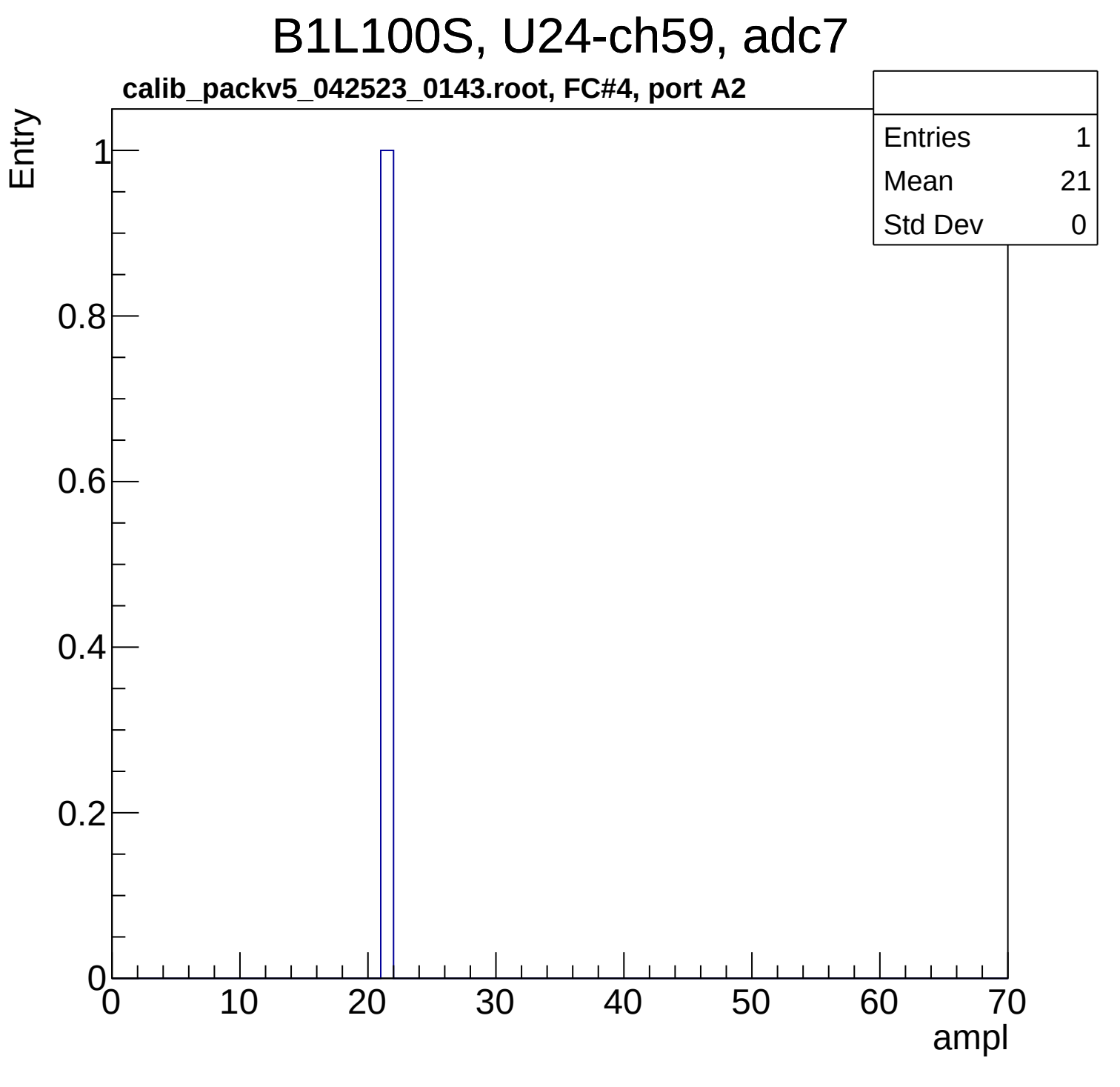
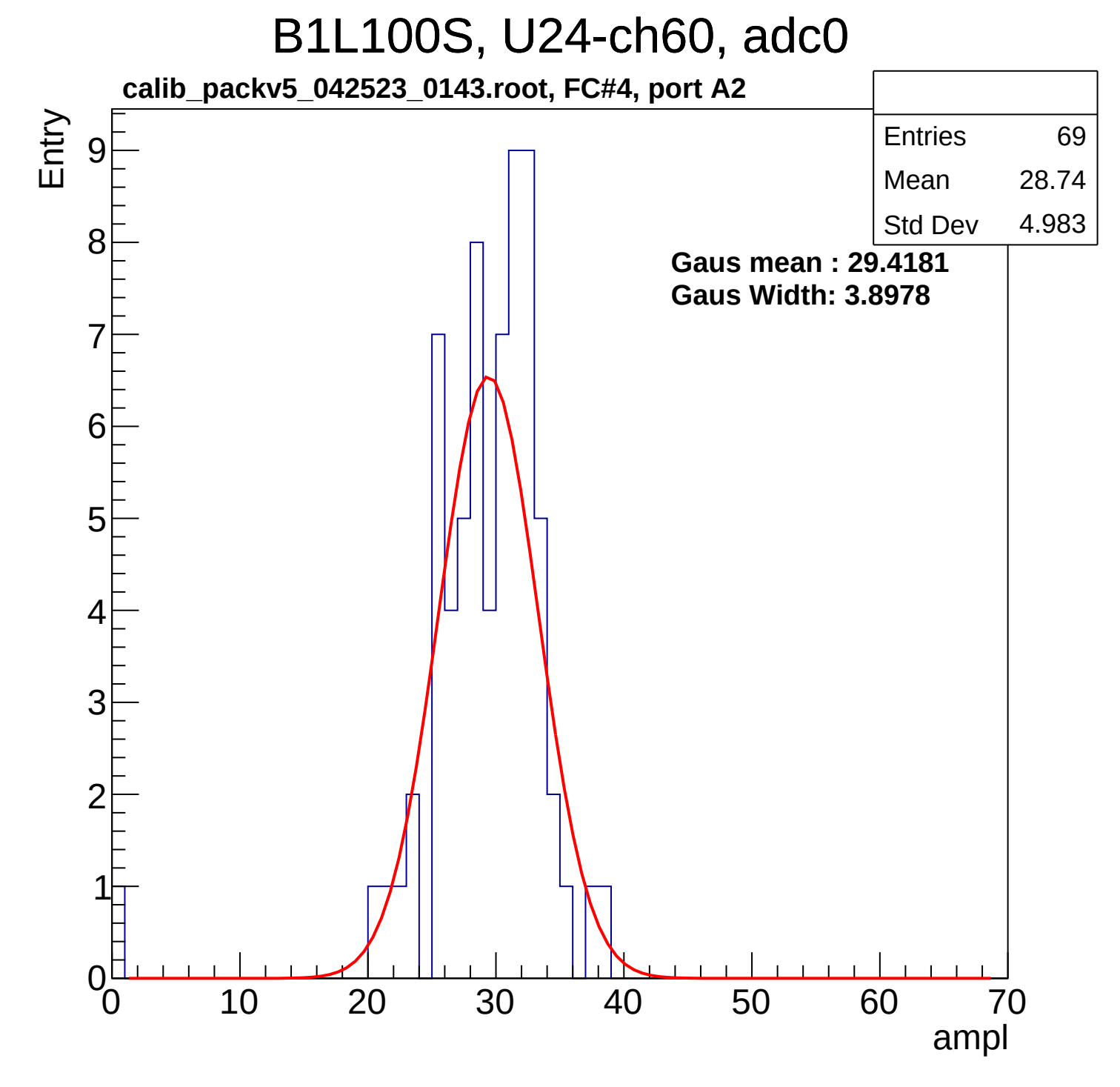
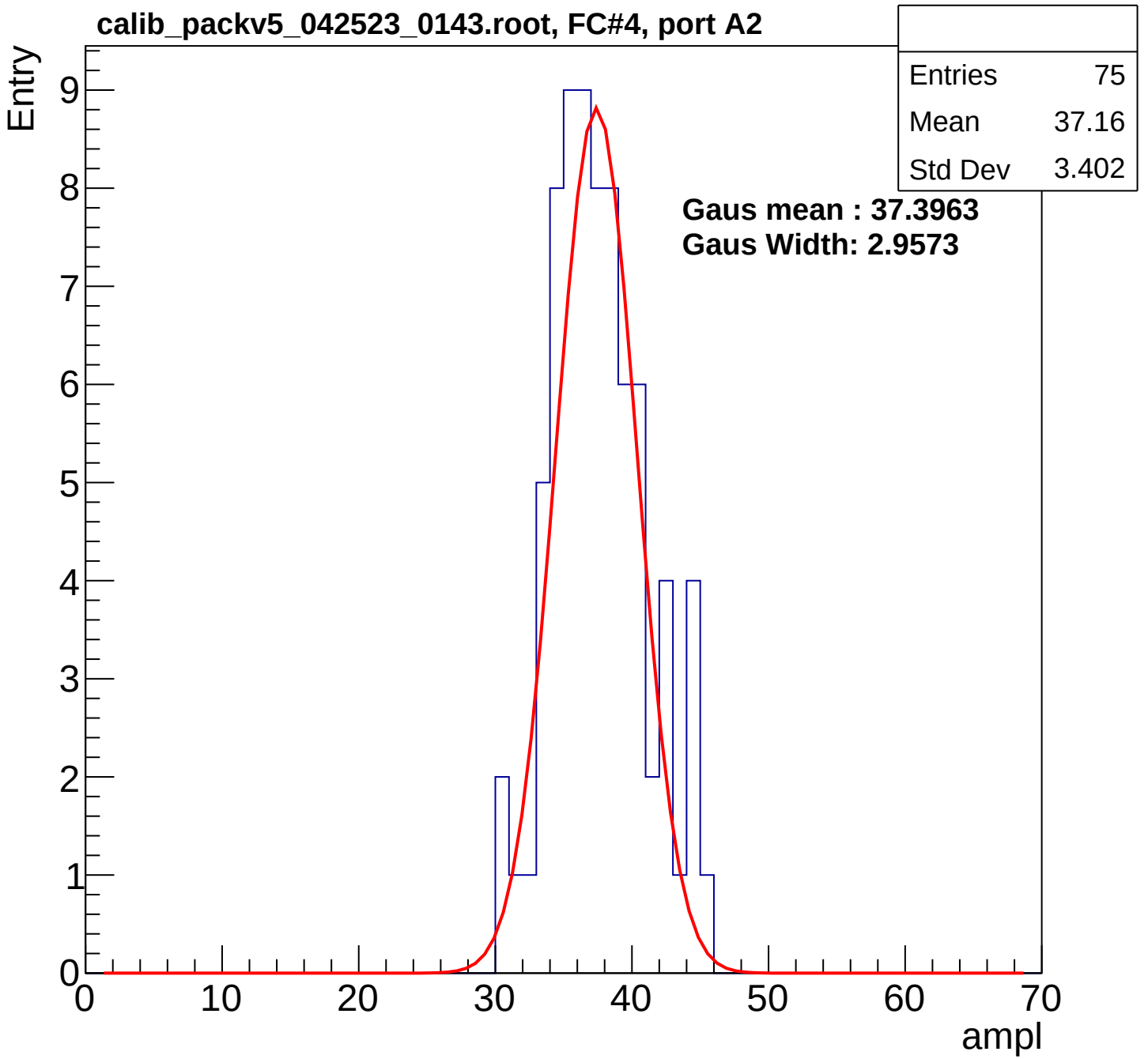

B1L100S, U24-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch59, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.74
Std Dev 4.983
Gaus mean : 29.4181
Gaus Width: 3.8978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.16
Std Dev 3.402
Gaus mean : 37.3963
Gaus Width: 2.9573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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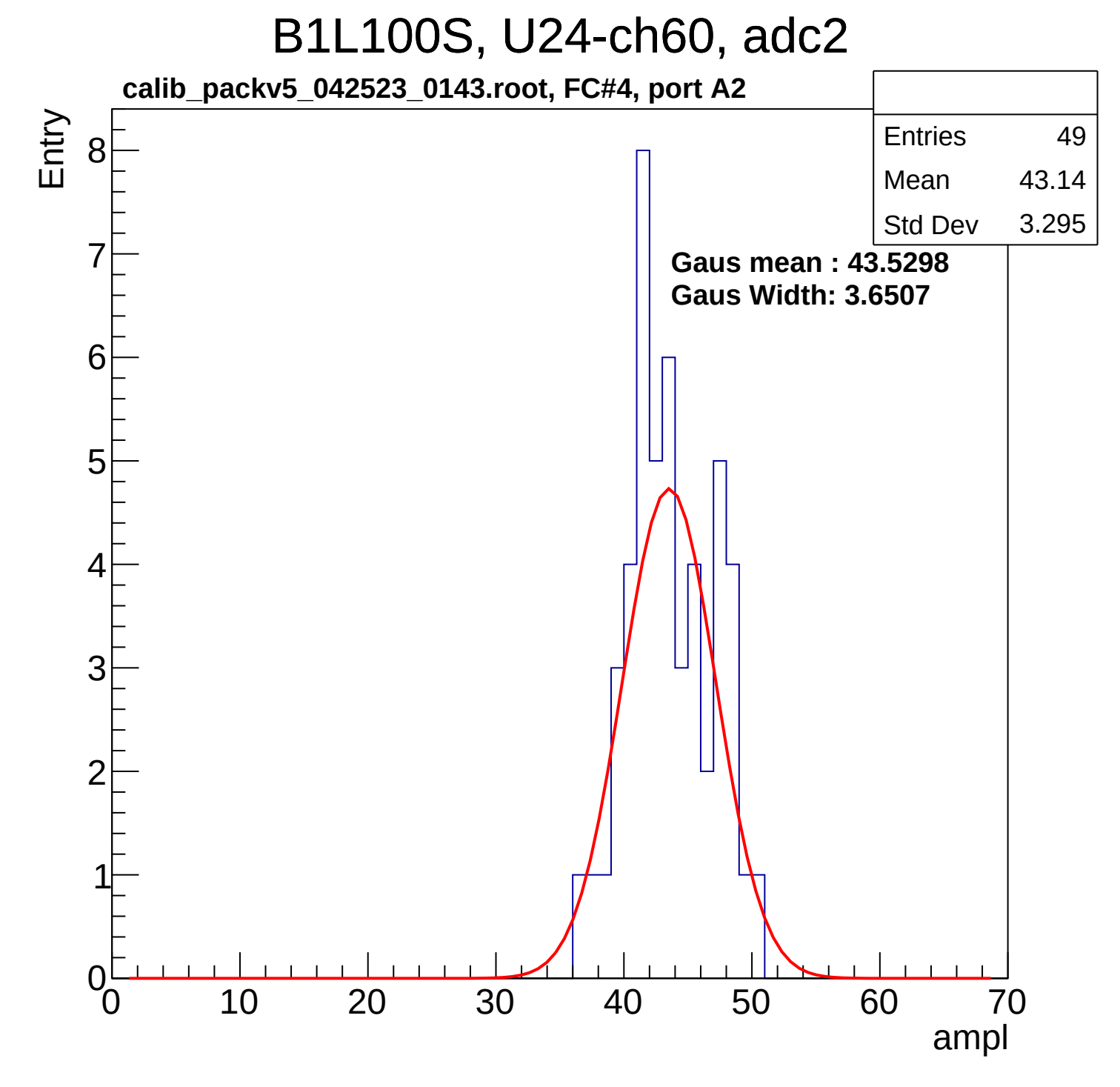
40
50
60
70


B1L100S, U24-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 43.14
Std Dev 3.295
Gaus mean : 43.5298
Gaus Width: 3.6507
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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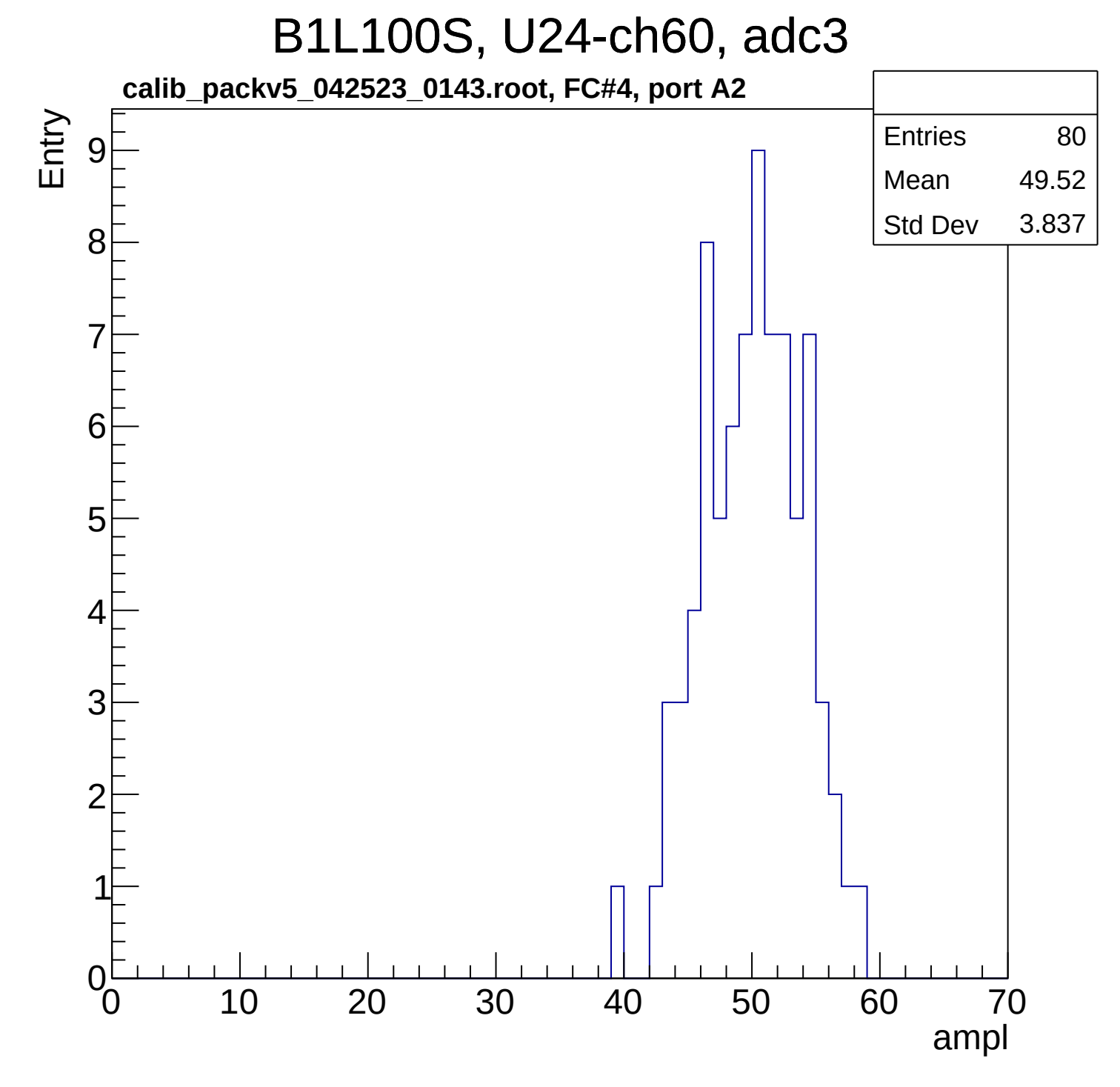
B1L100S, U24-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 49.52
Std Dev 3.837
Entry
ampl
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4
5
6
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0
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20
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40
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60
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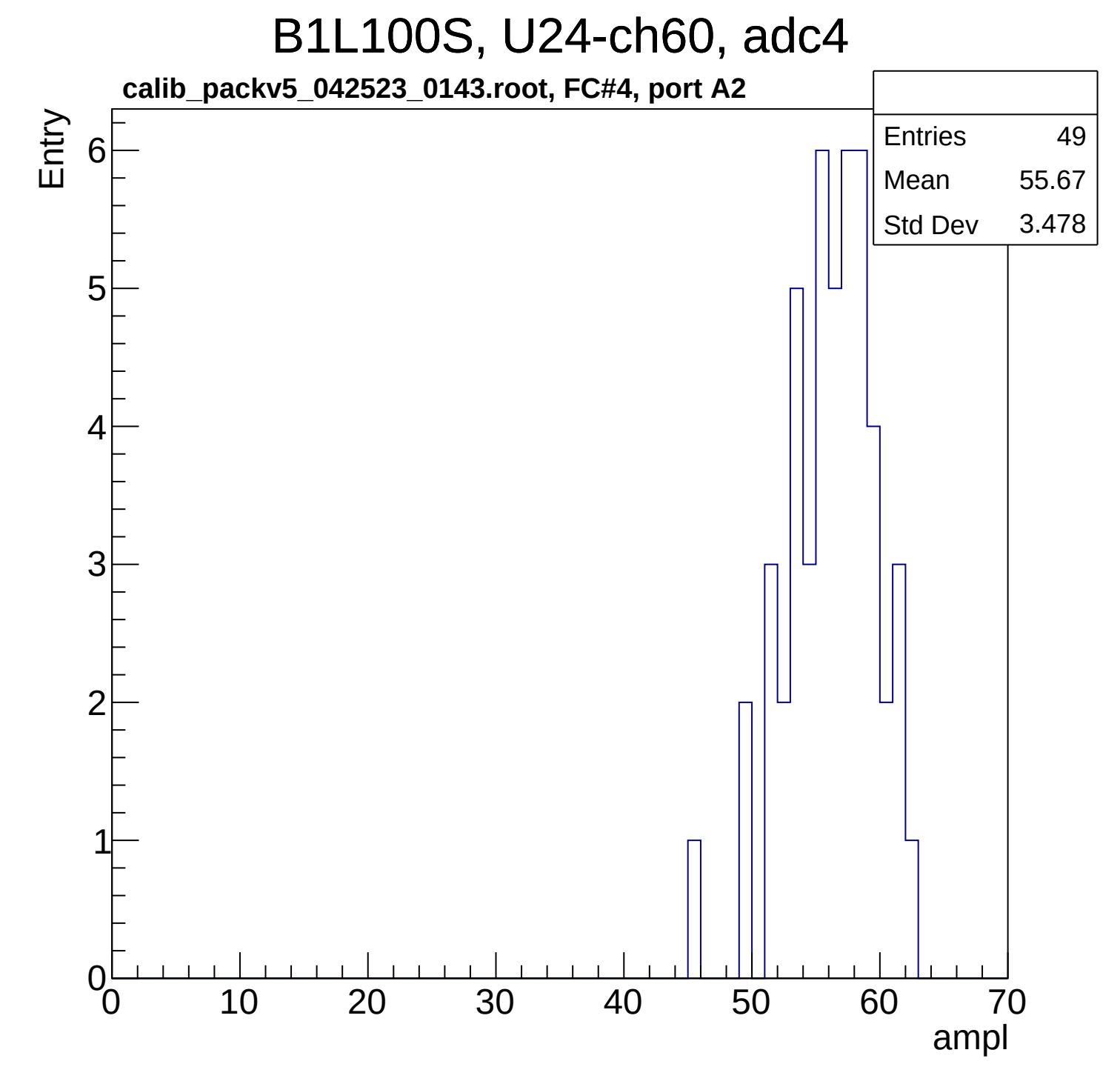
B1L100S, U24-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.67
Std Dev 3.478
Entry
ampl
0
1
2
3
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0
10
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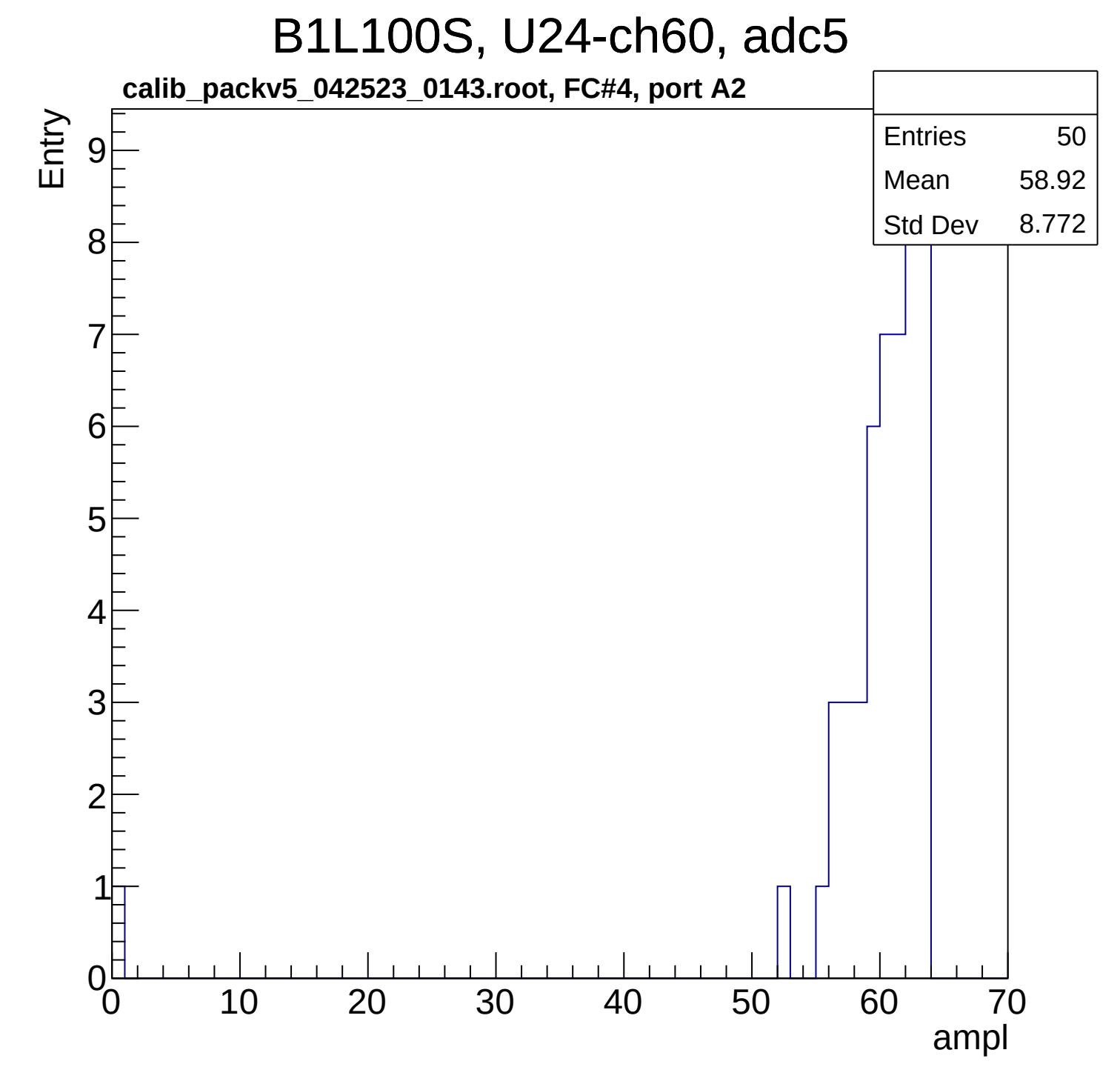
40
50
60
70


B1L100S, U24-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.92
Std Dev 8.772
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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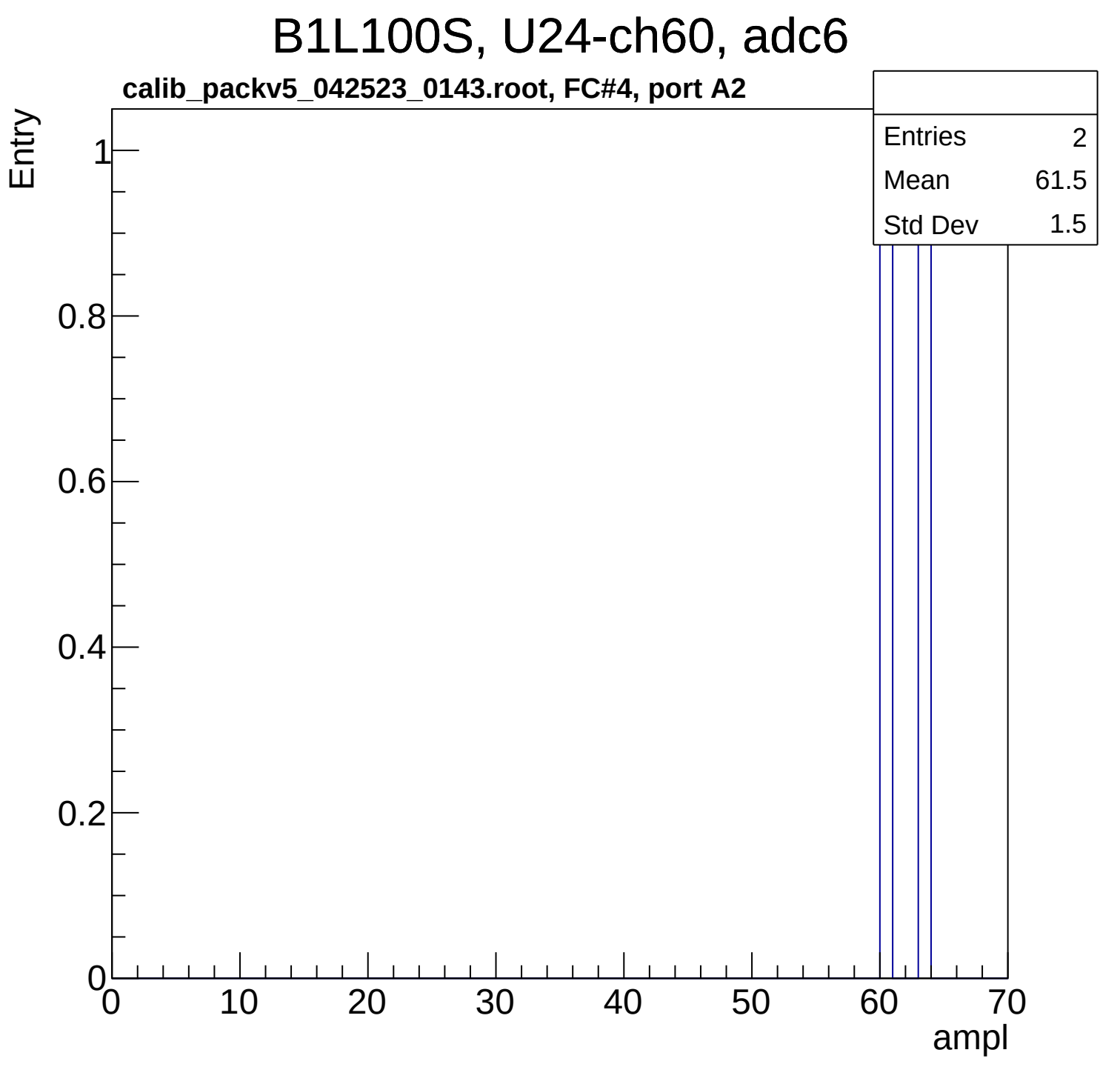
40
50
60
70


B1L100S, U24-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
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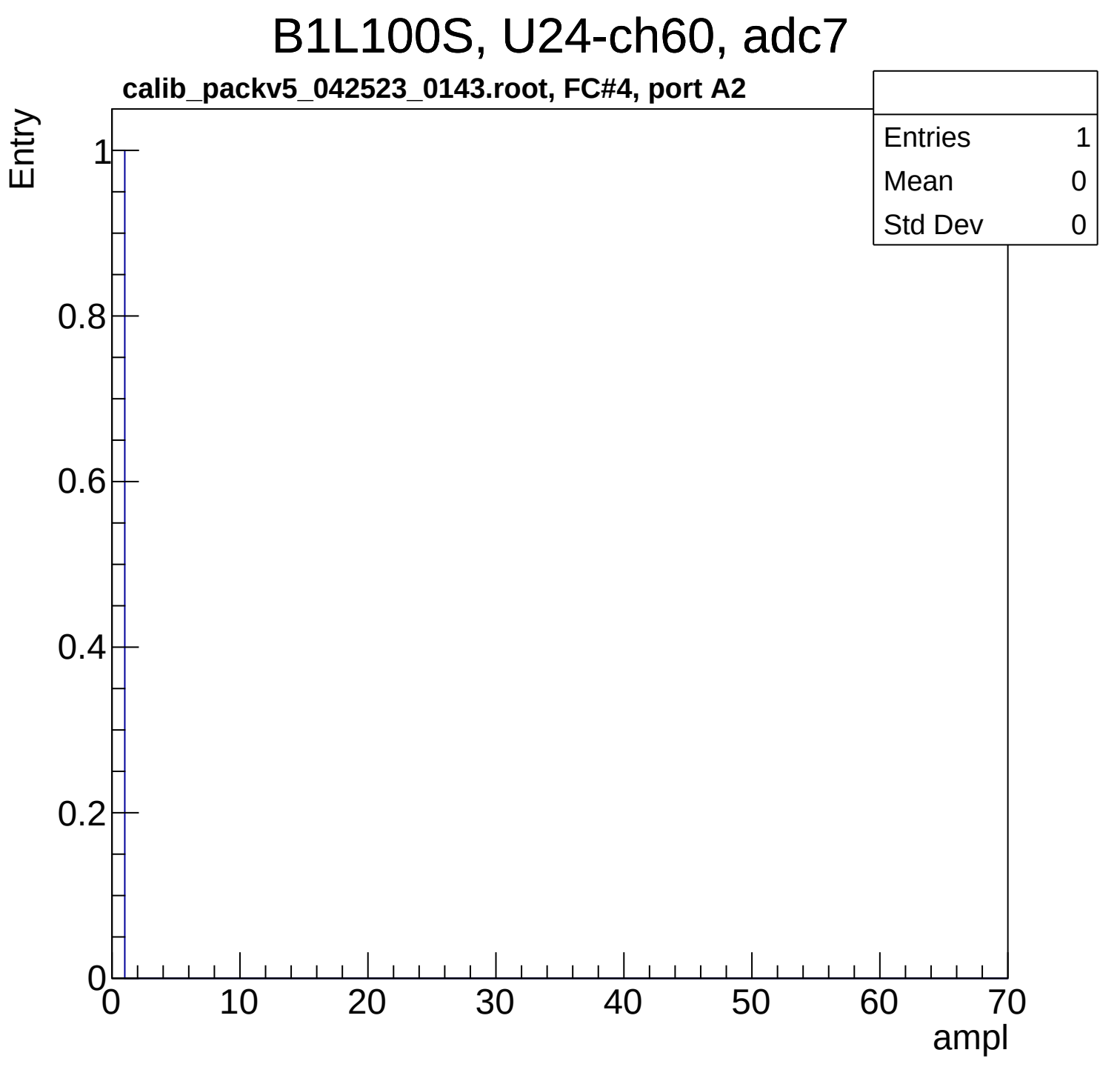
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U24-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
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10
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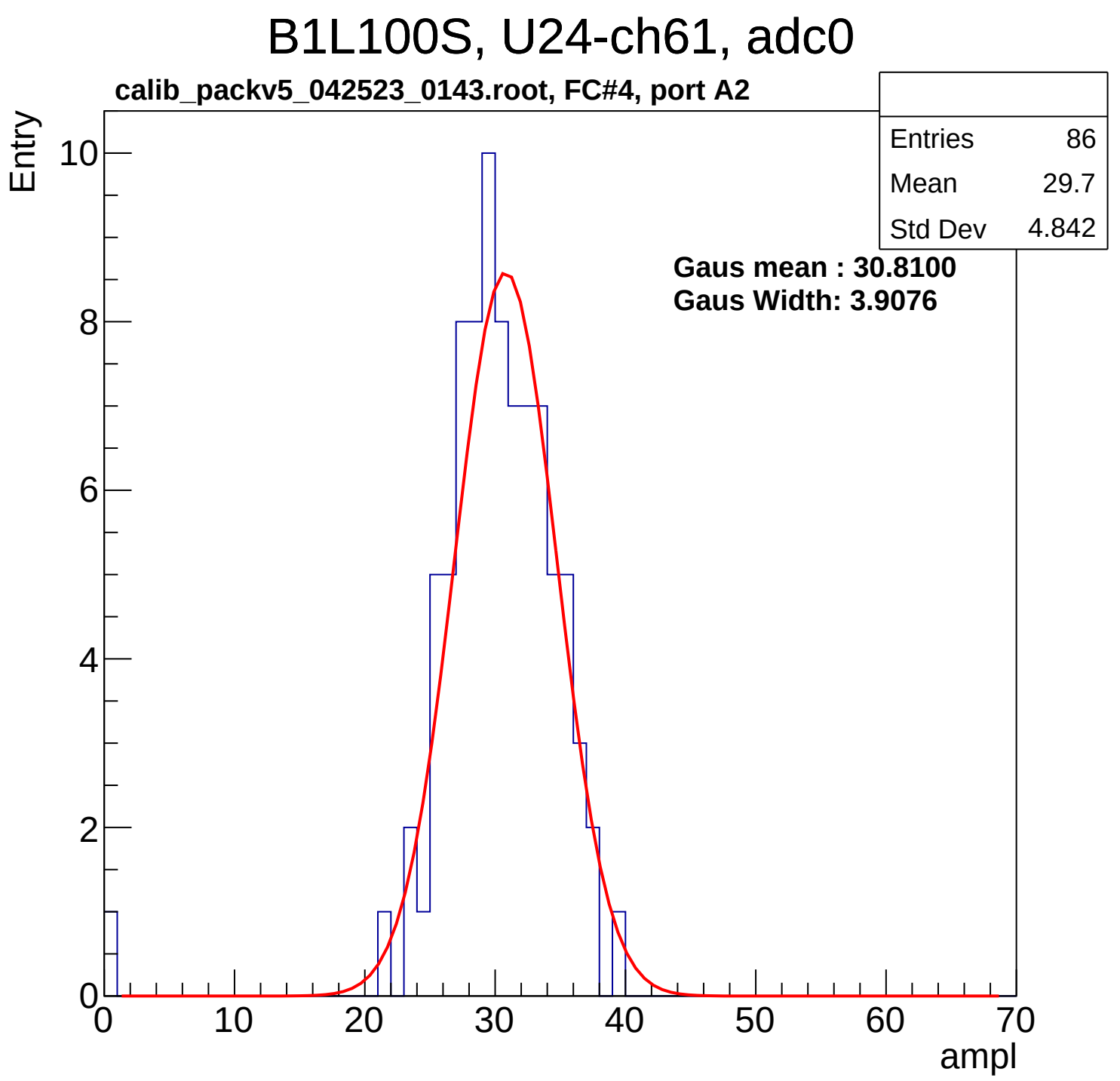
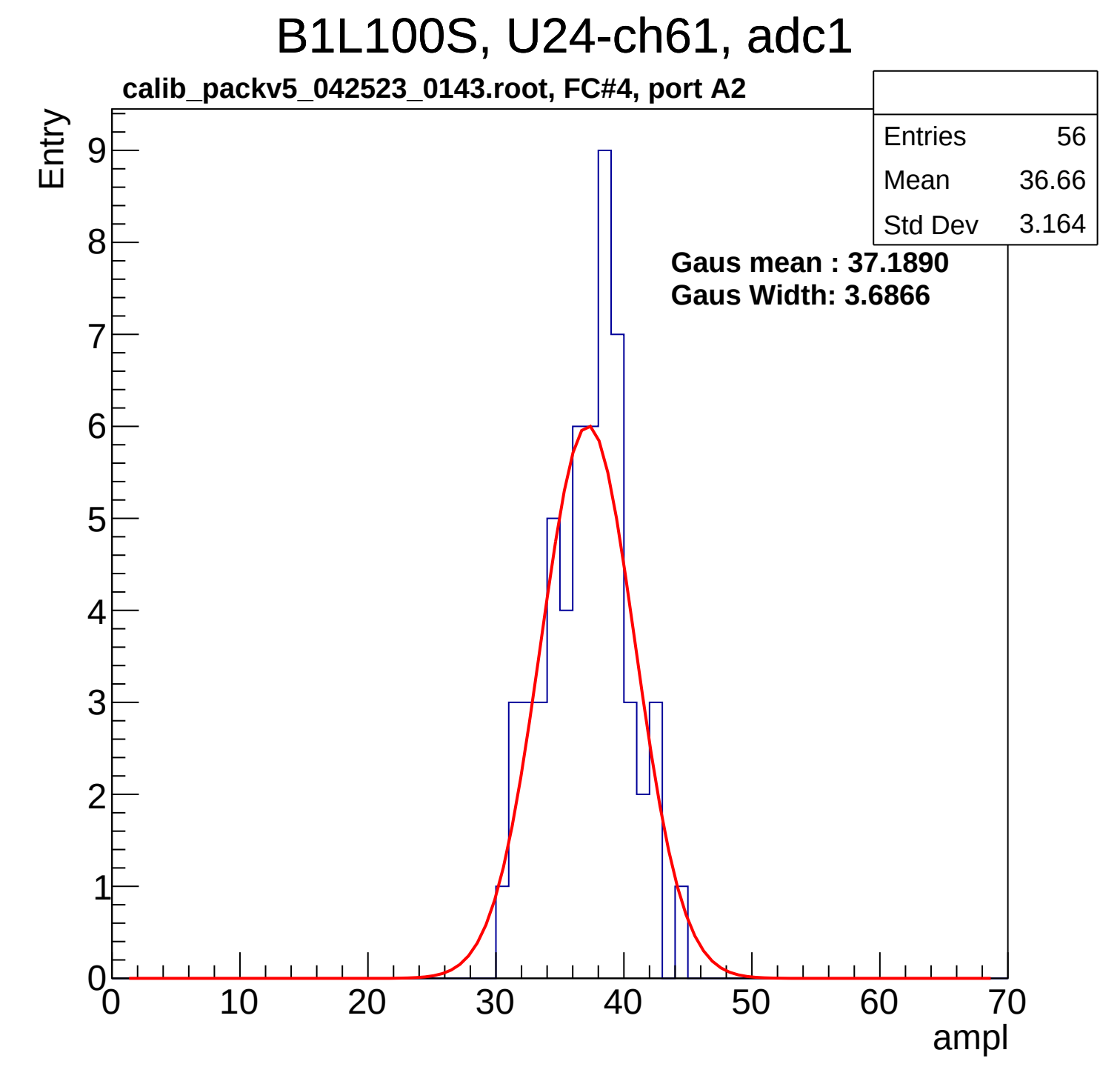
50
60
70
ampl


B1L100S, U24-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 29.7
Std Dev 4.842
Gaus mean : 30.8100
Gaus Width: 3.9076
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 36.66
Std Dev 3.164
Gaus mean : 37.1890
Gaus Width: 3.6866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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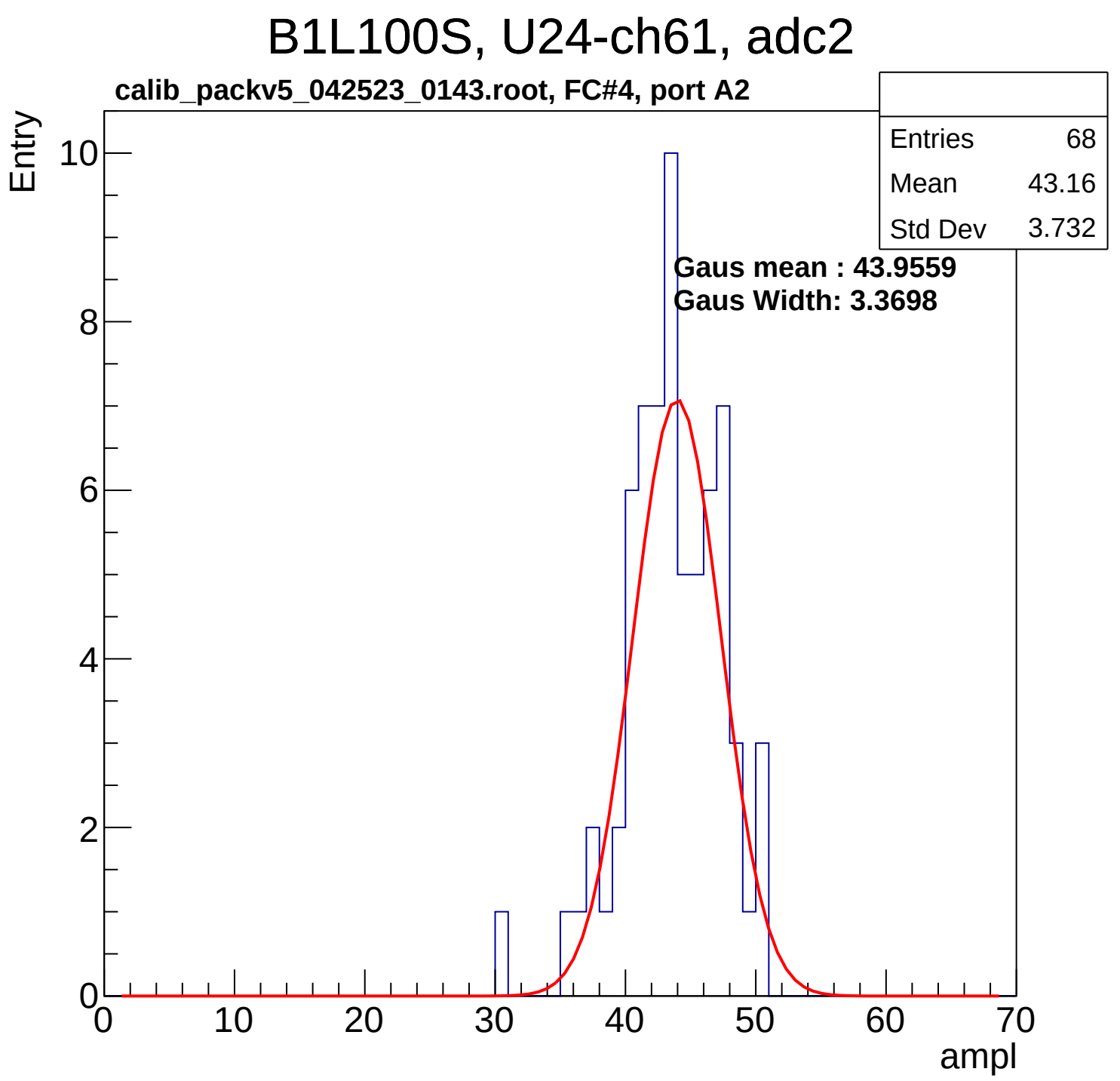
B1L100S, U24-ch61, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.16
Std Dev 3.732
Gaus mean : 43.9559
Gaus Width: 3.3698
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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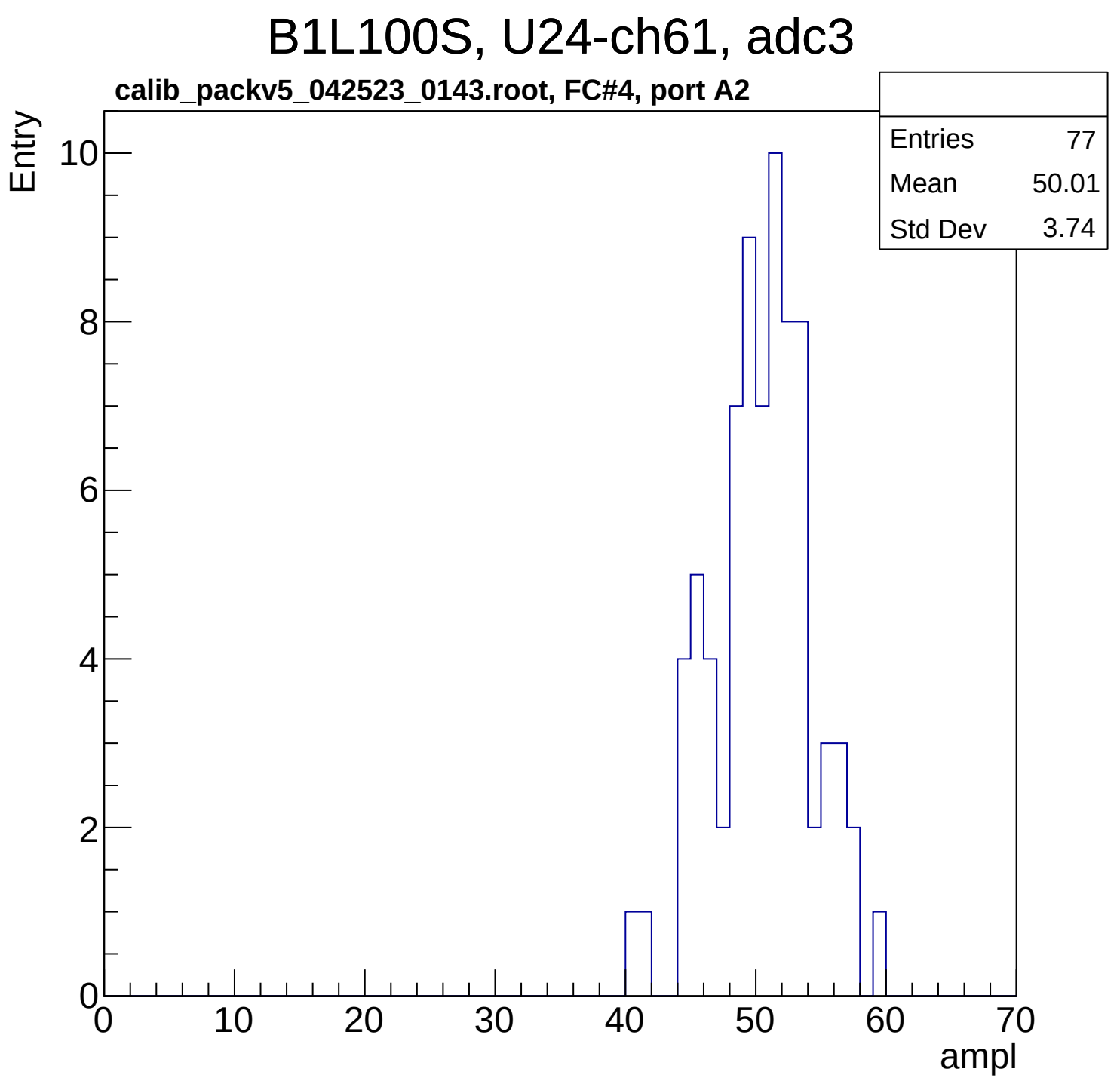
B1L100S, U24-ch61, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 50.01
Std Dev 3.74
Entry
ampl
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60
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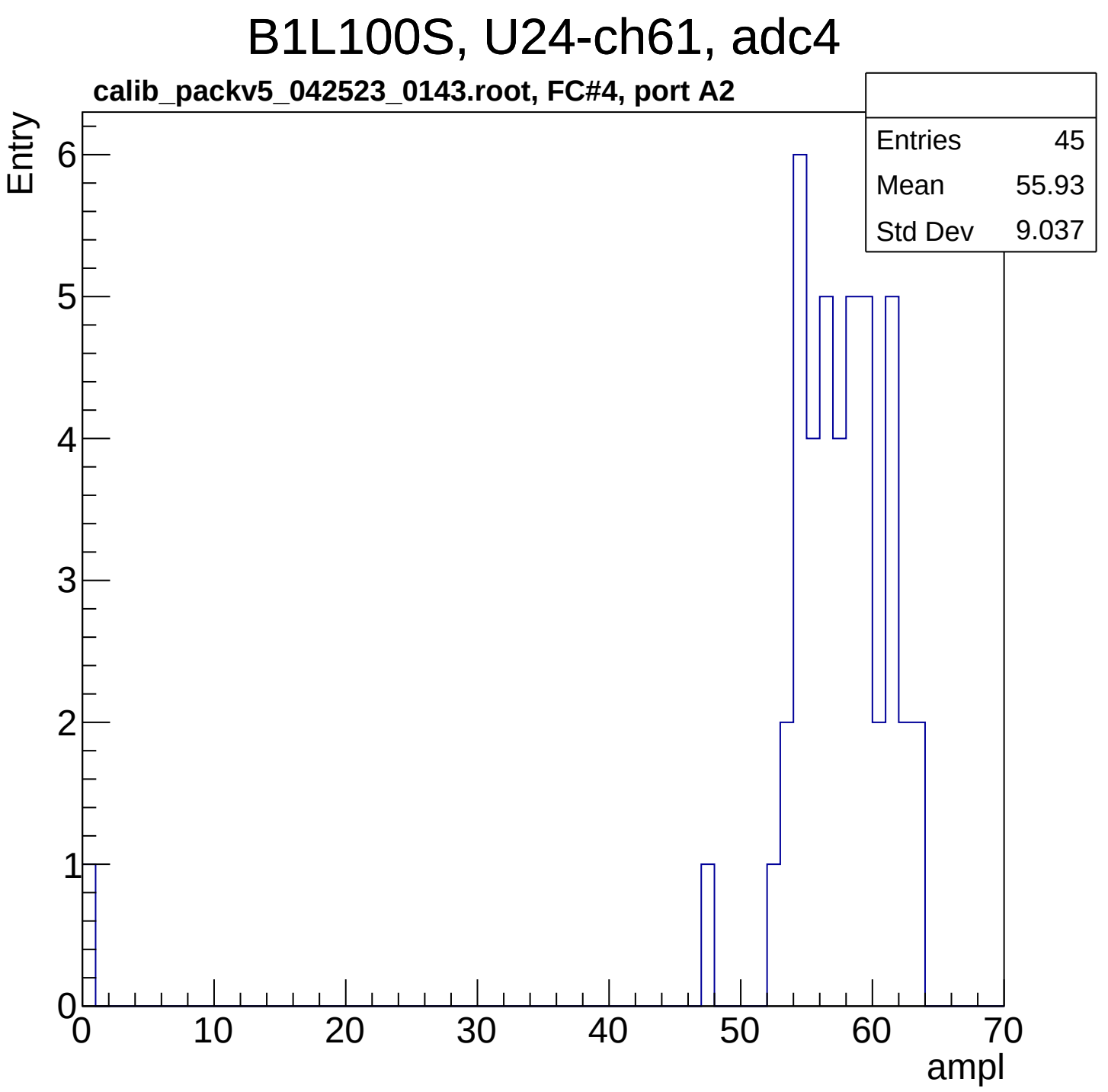
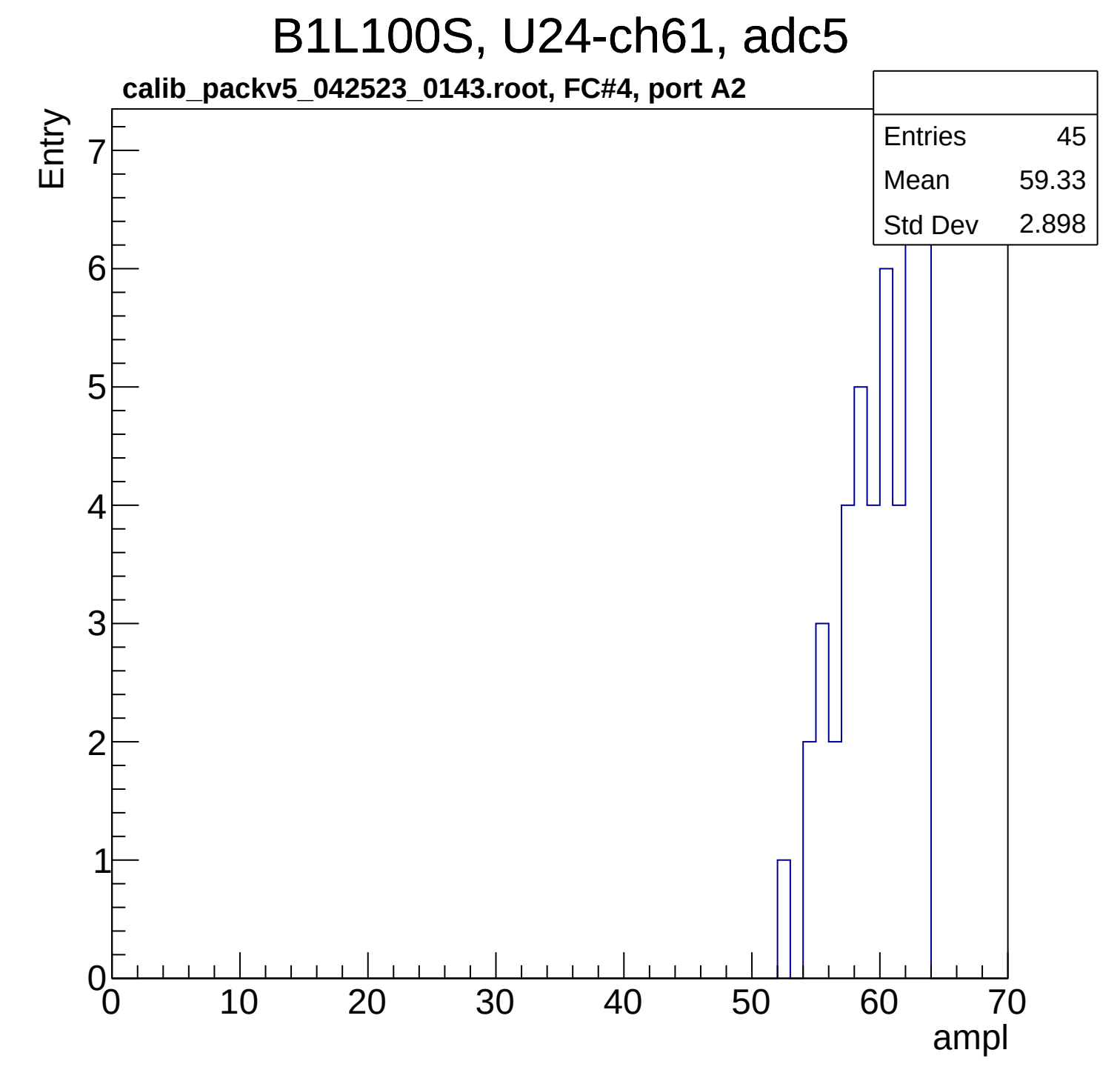

B1L100S, U24-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 55.93
Std Dev 9.037
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch61, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.33
Std Dev 2.898
Entry
ampl
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2
3
4
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0
10
20
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40
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60
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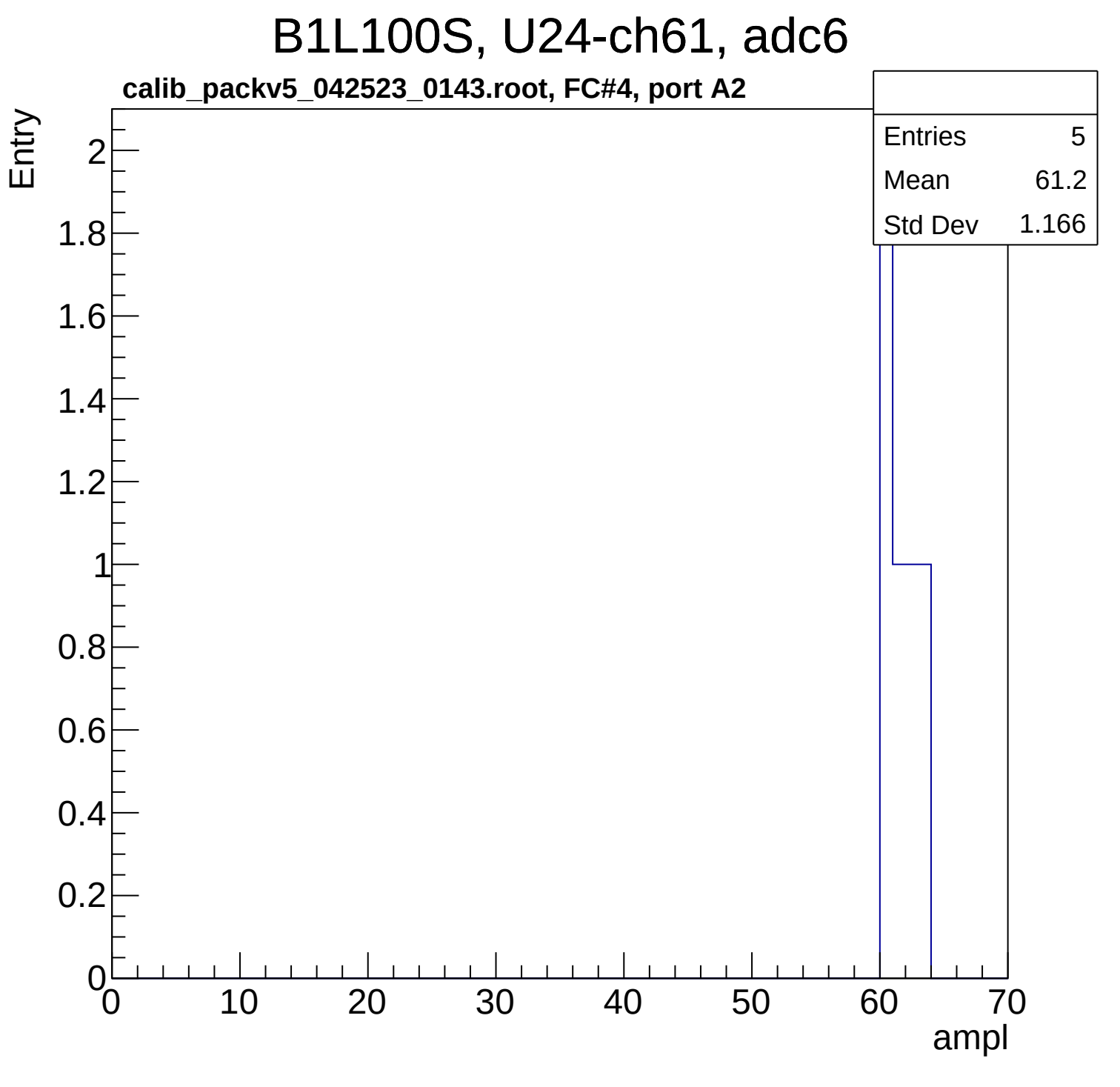
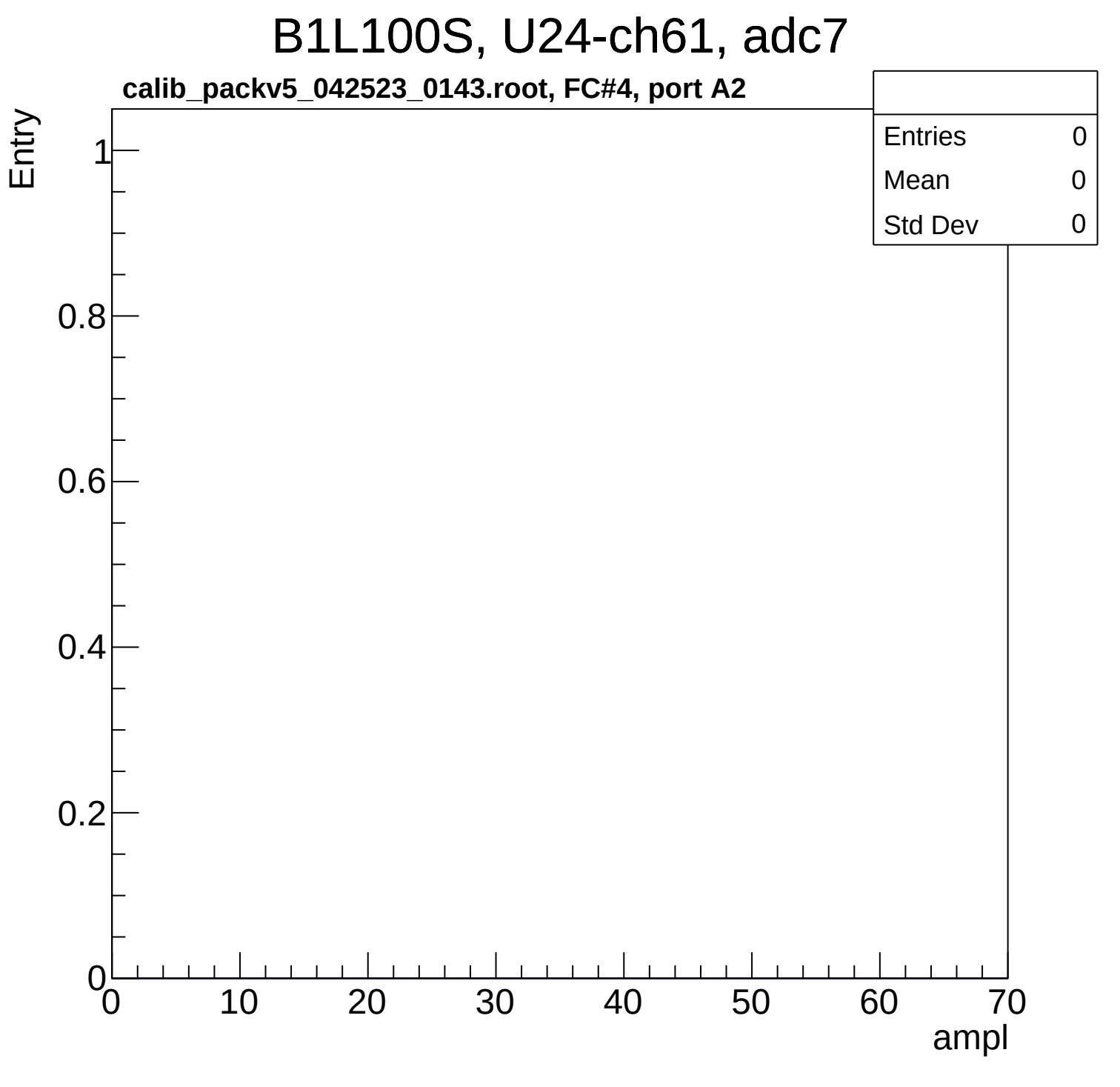
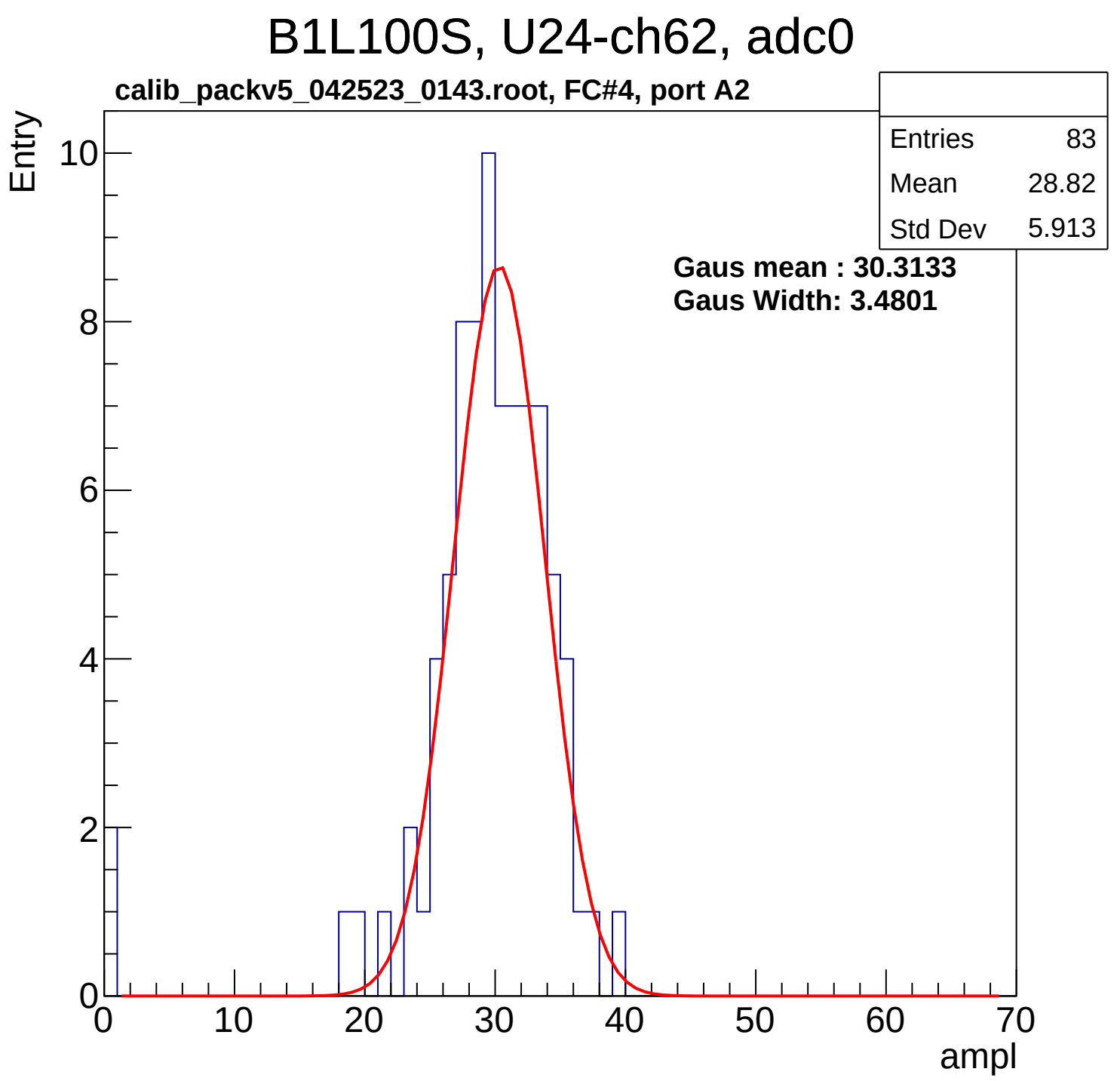
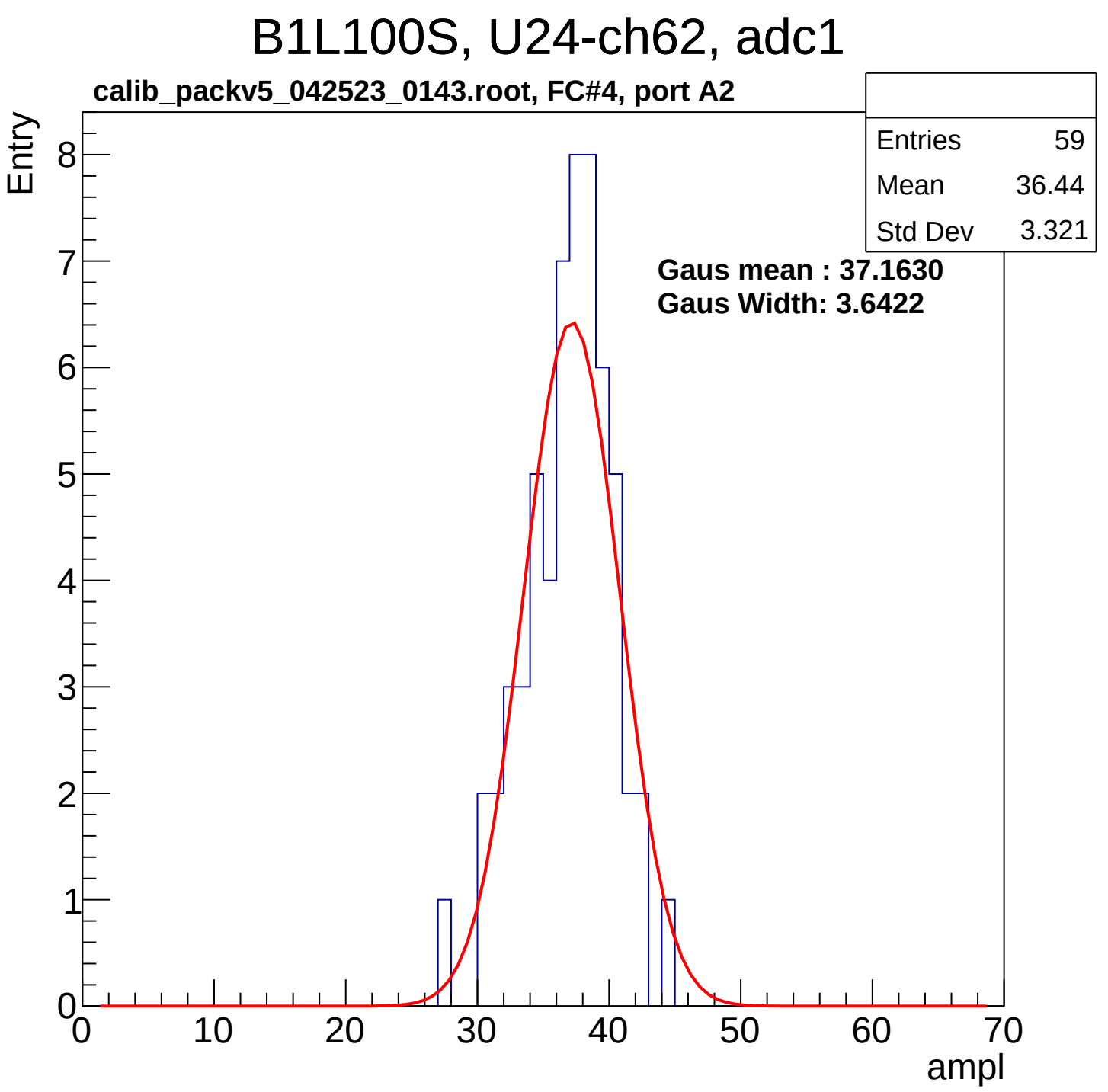

B1L100S, U24-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.2
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 28.82
Std Dev 5.913
Gaus mean : 30.3133
Gaus Width: 3.4801
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.44
Std Dev 3.321
Gaus mean : 37.1630
Gaus Width: 3.6422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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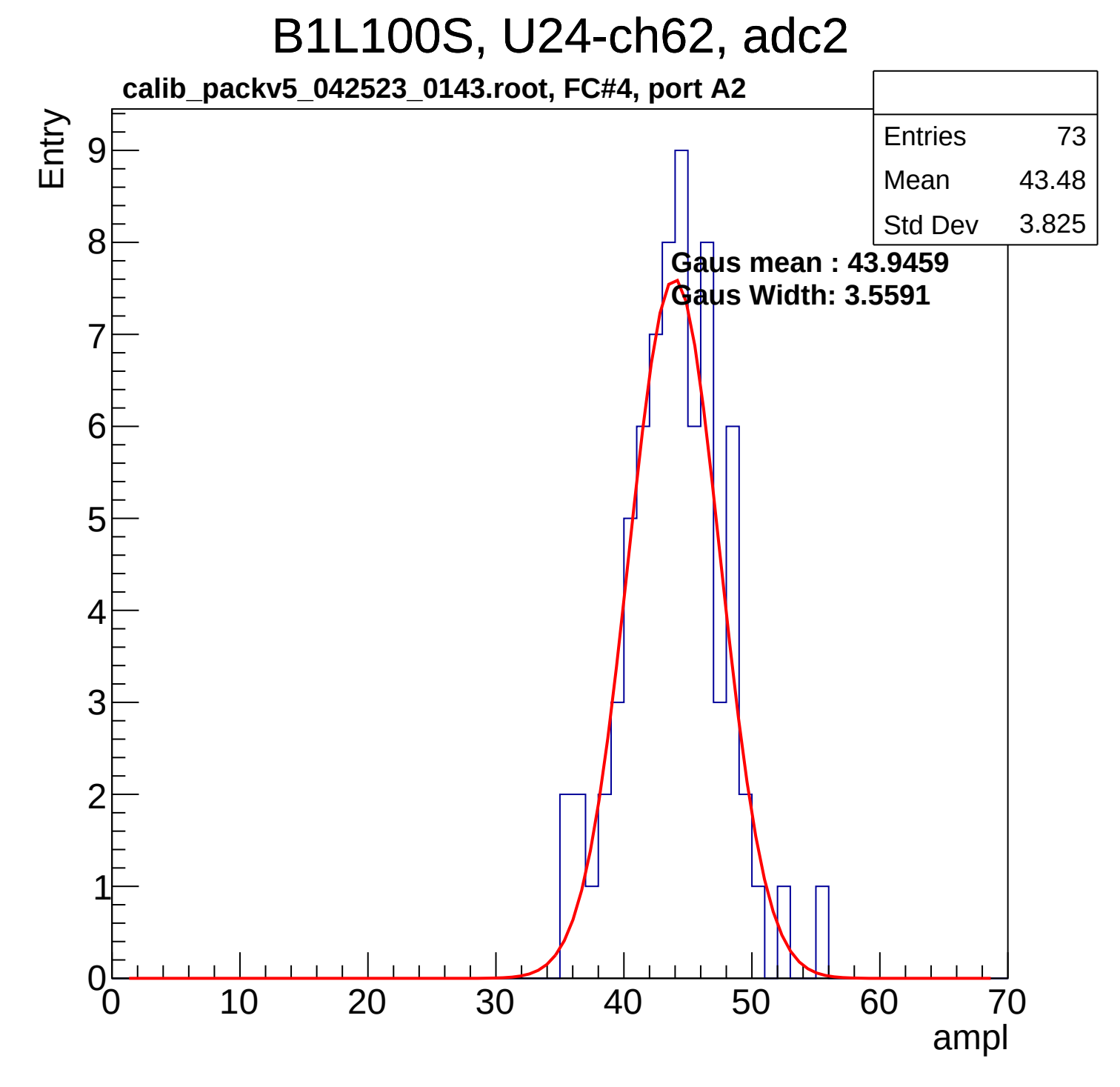
B1L100S, U24-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.48
Std Dev 3.825
Gaus mean : 43.9459
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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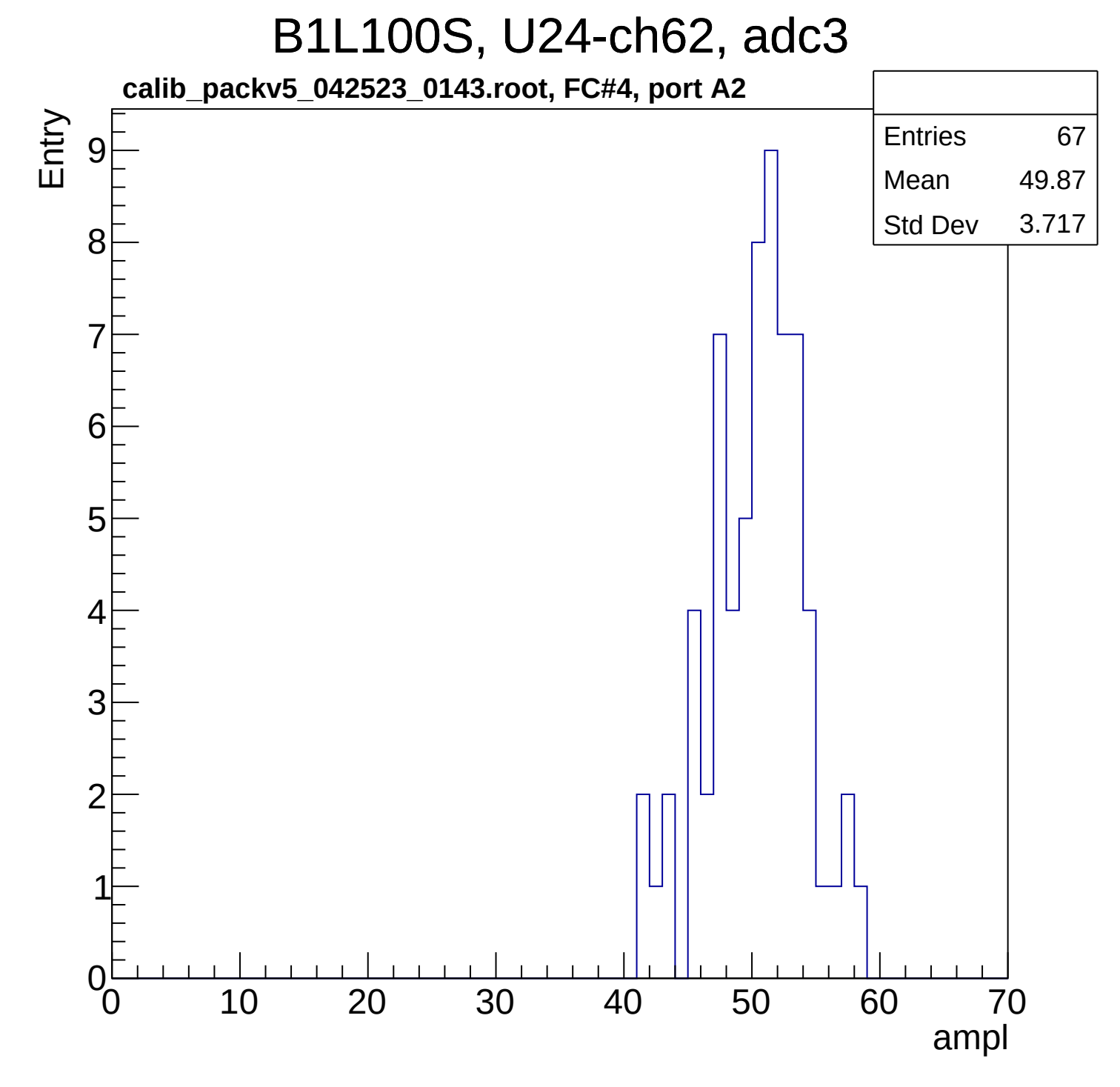
B1L100S, U24-ch62, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.87
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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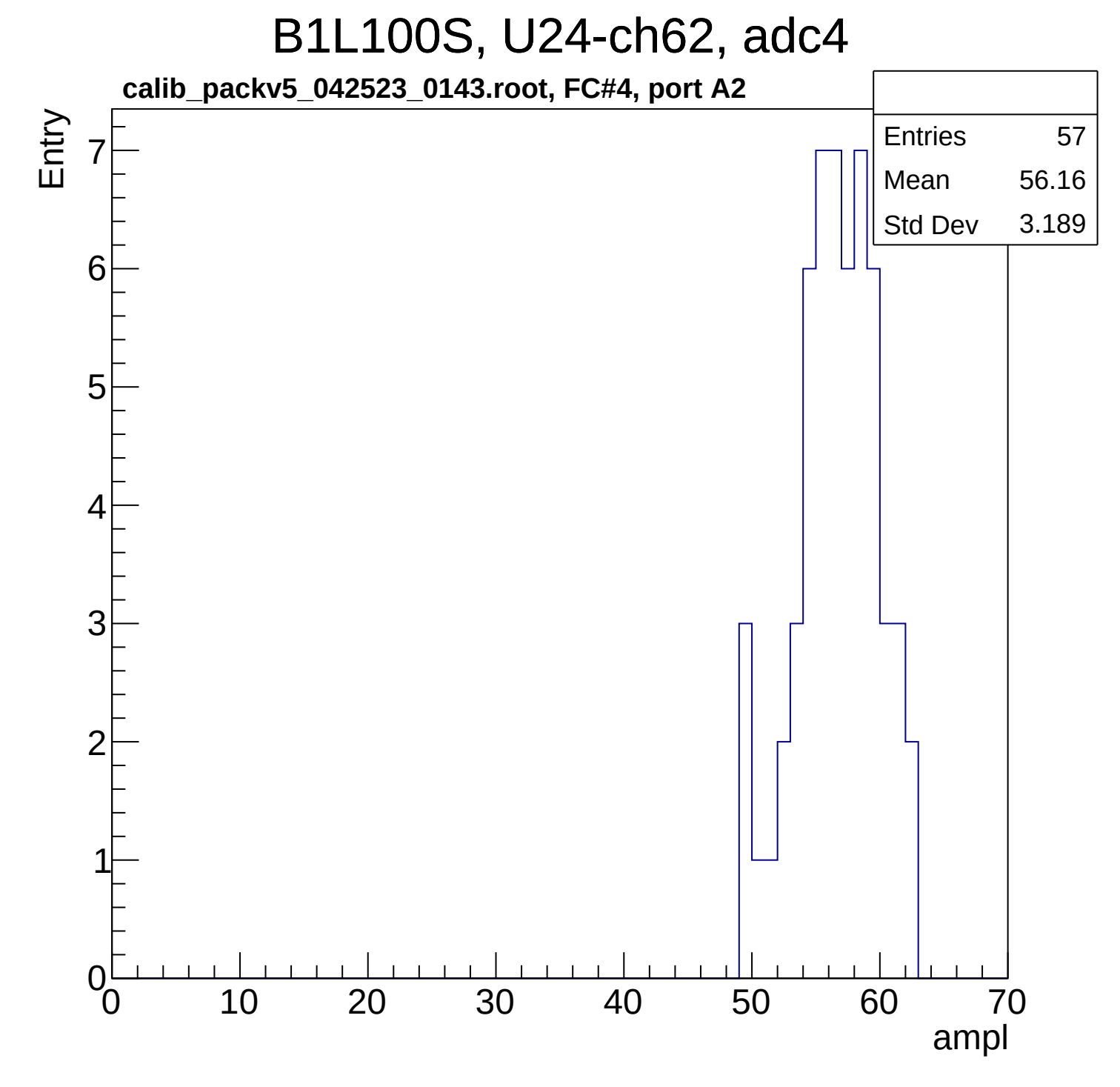
B1L100S, U24-ch62, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.16
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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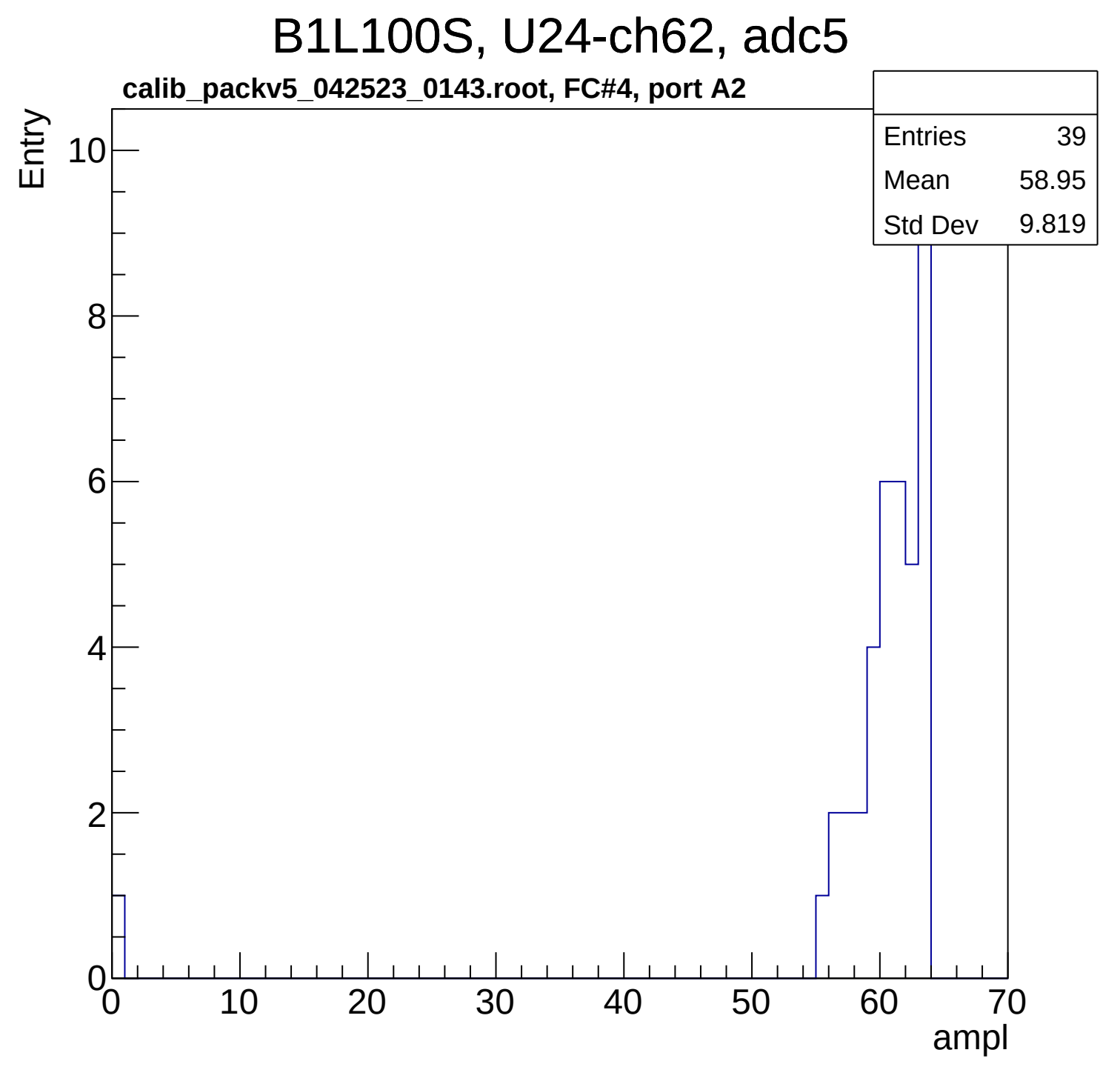
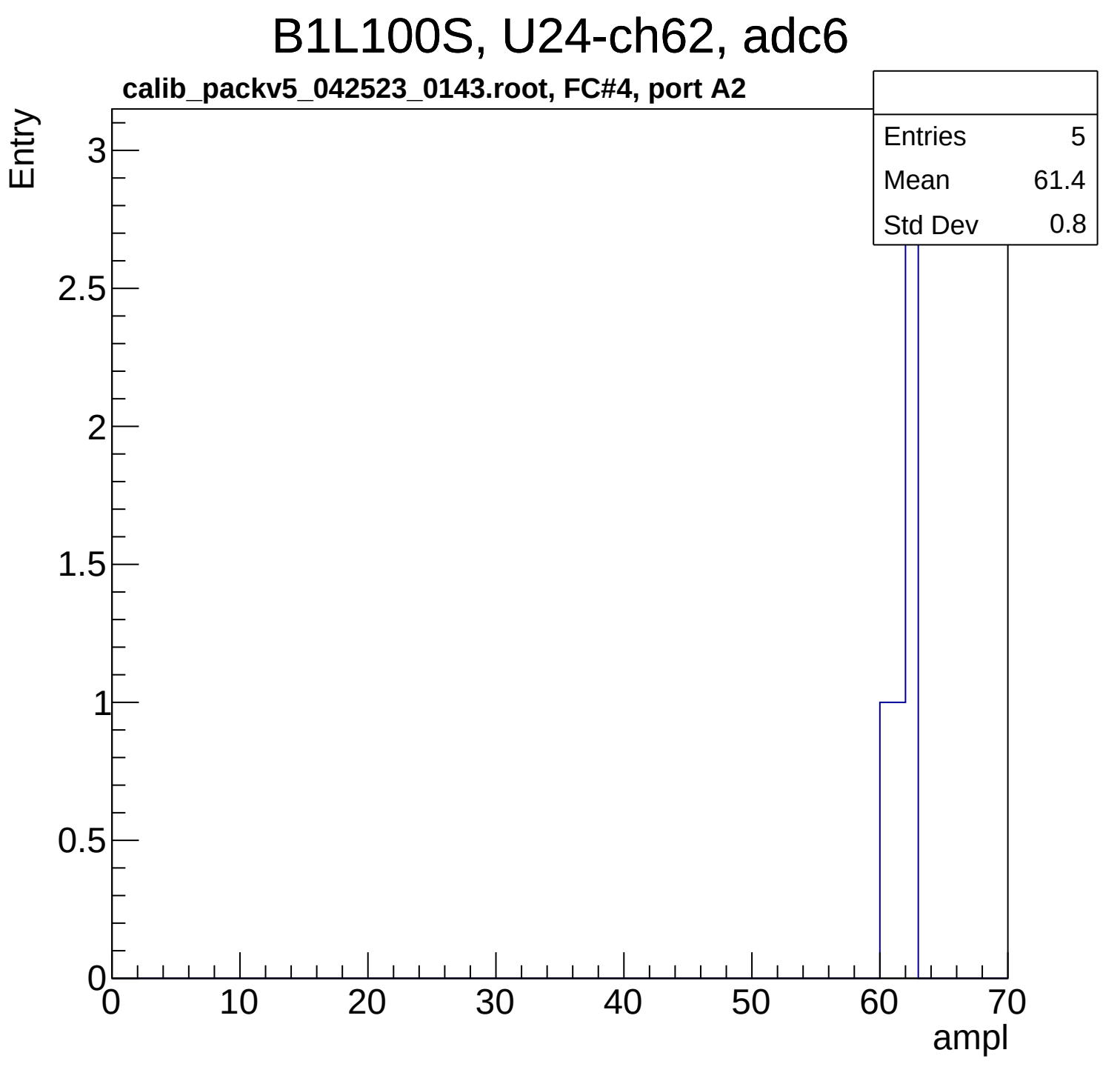

B1L100S, U24-ch62, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.95
Std Dev 9.819
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 0.8
Entry
ampl
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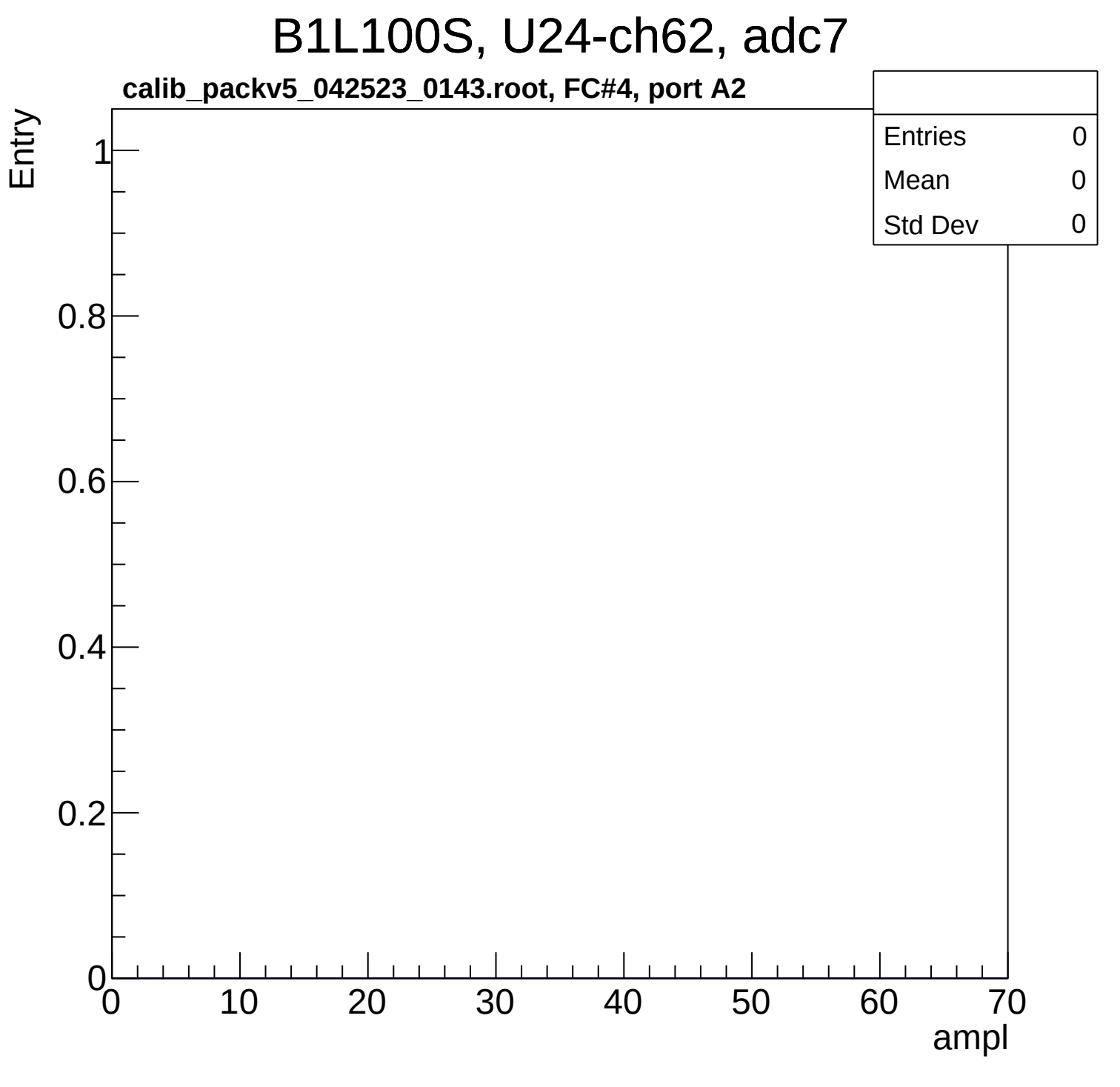
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch62, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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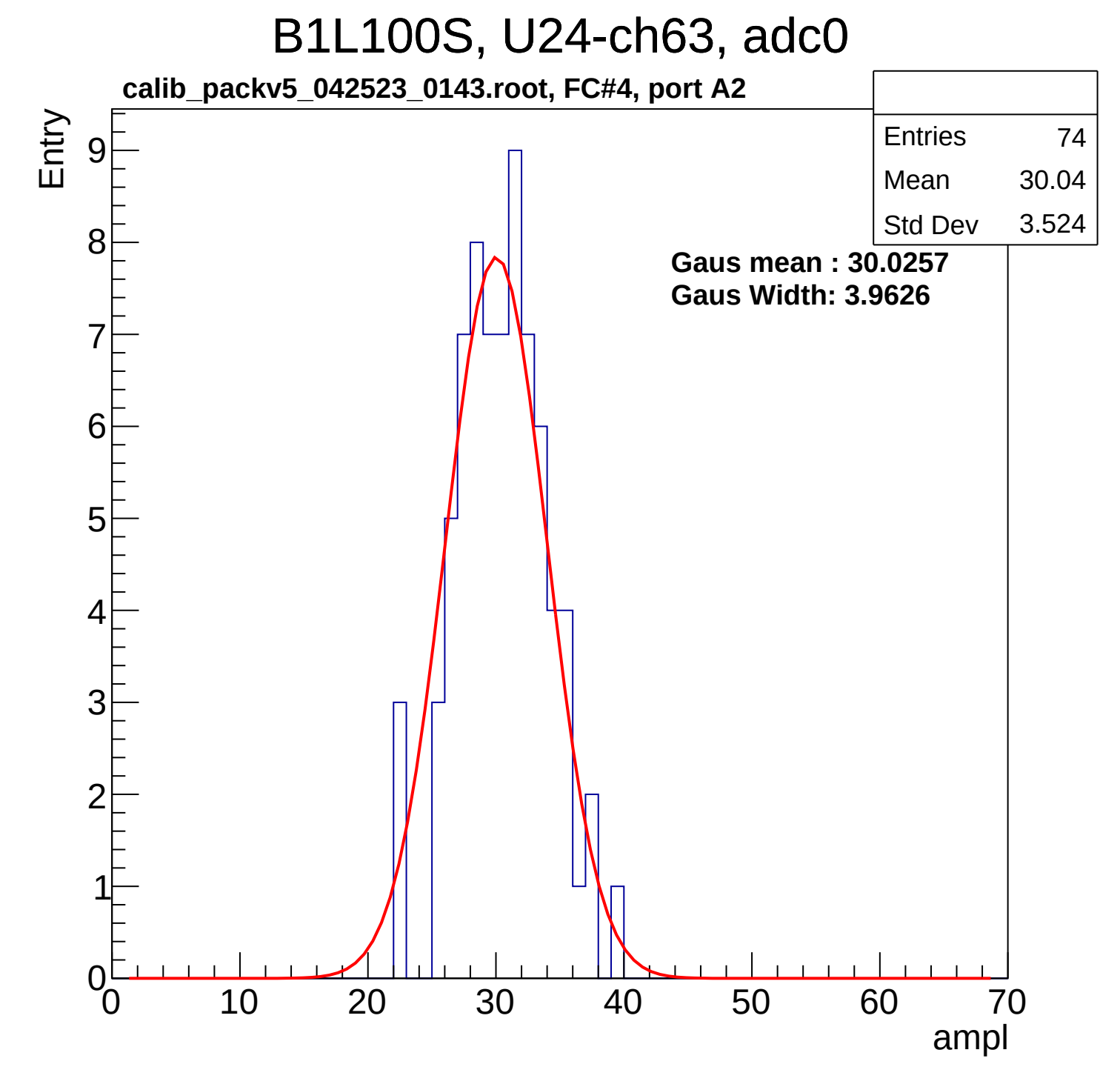
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch63, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 30.04
Std Dev 3.524
Gaus mean : 30.0257
Gaus Width: 3.9626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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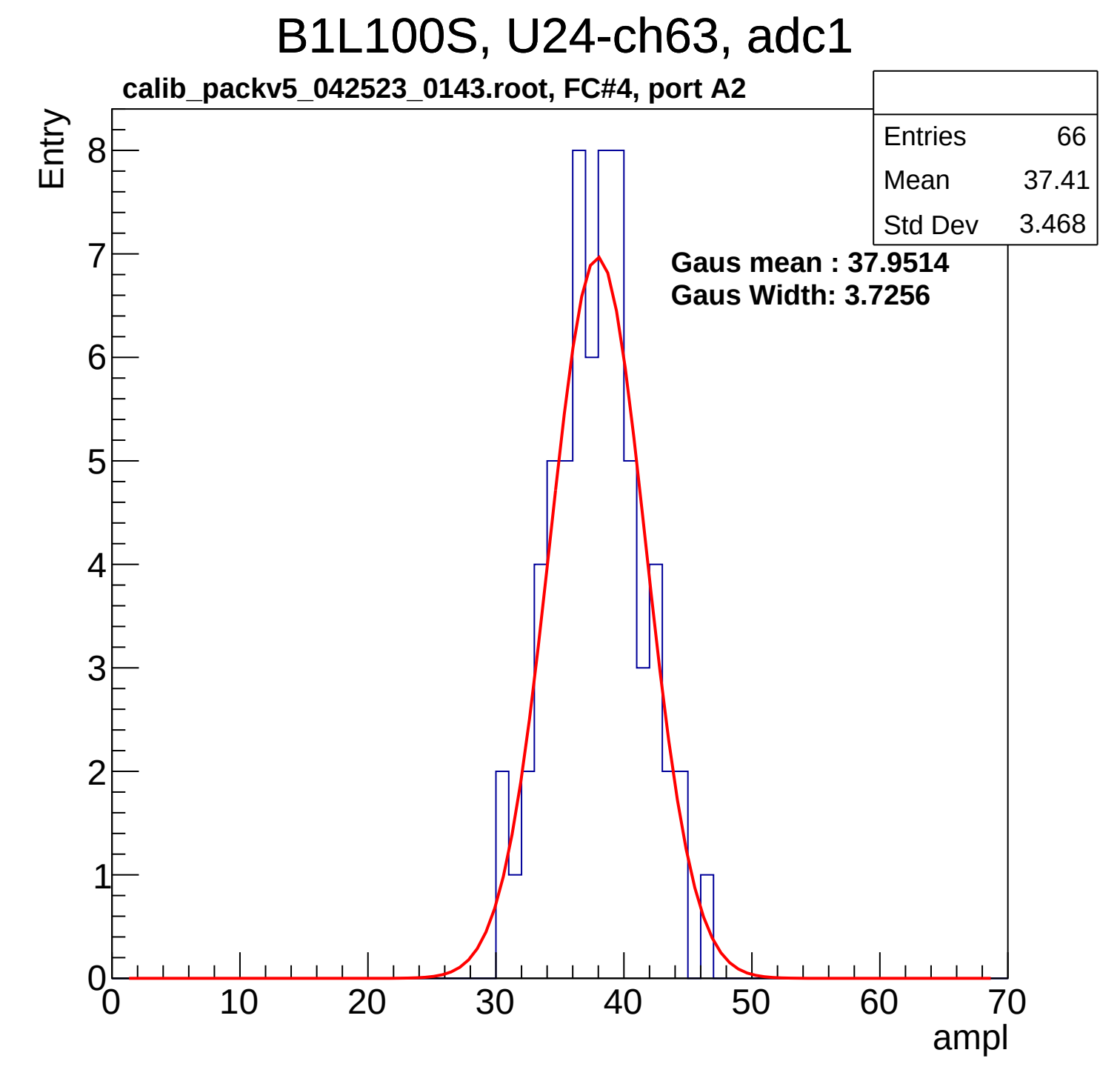
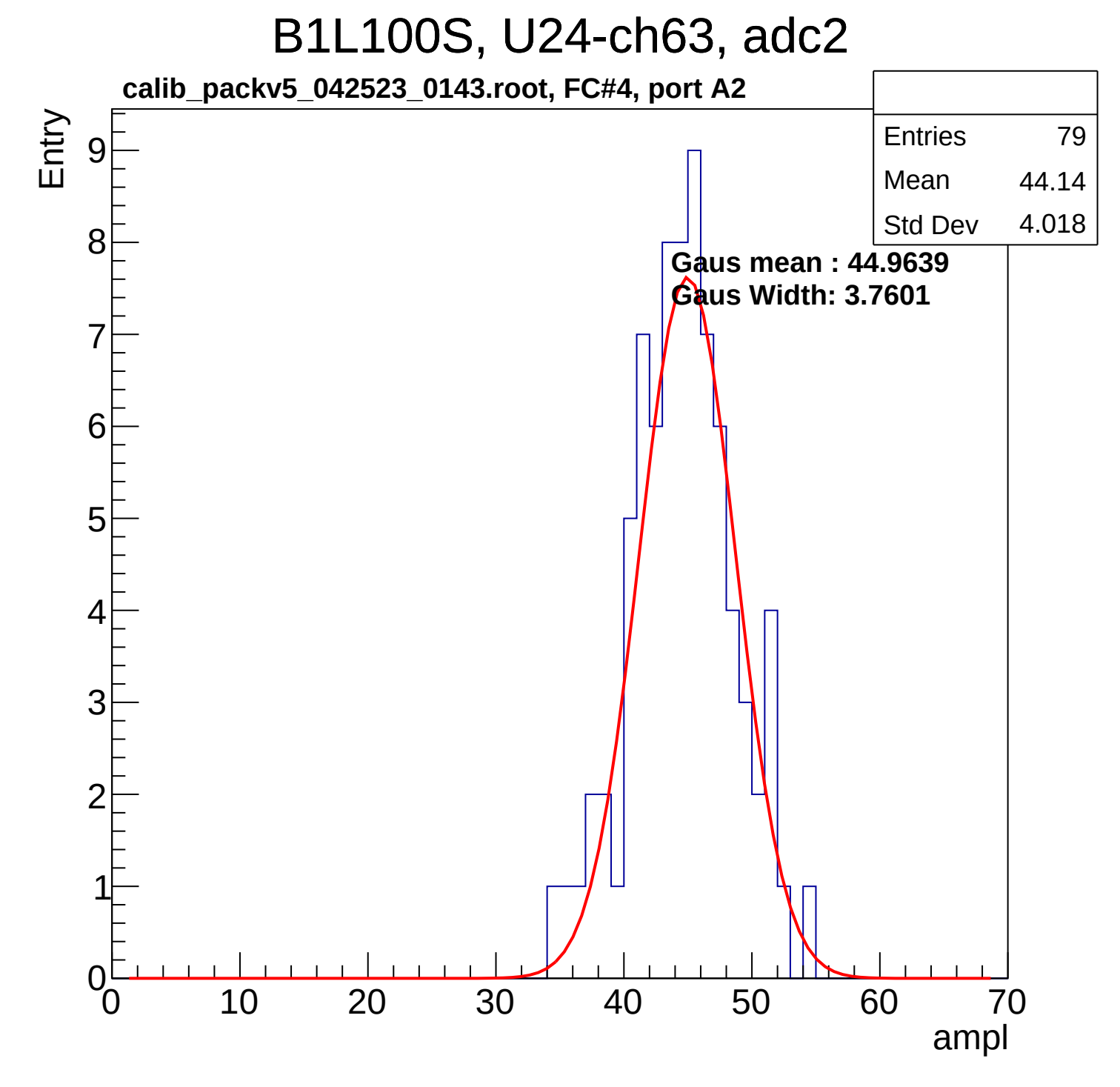

B1L100S, U24-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 37.41
Std Dev 3.468
Gaus mean : 37.9514
Gaus Width: 3.7256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch63, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 44.14
Std Dev 4.018
Gaus mean : 44.9639
Gaus Width: 3.7601
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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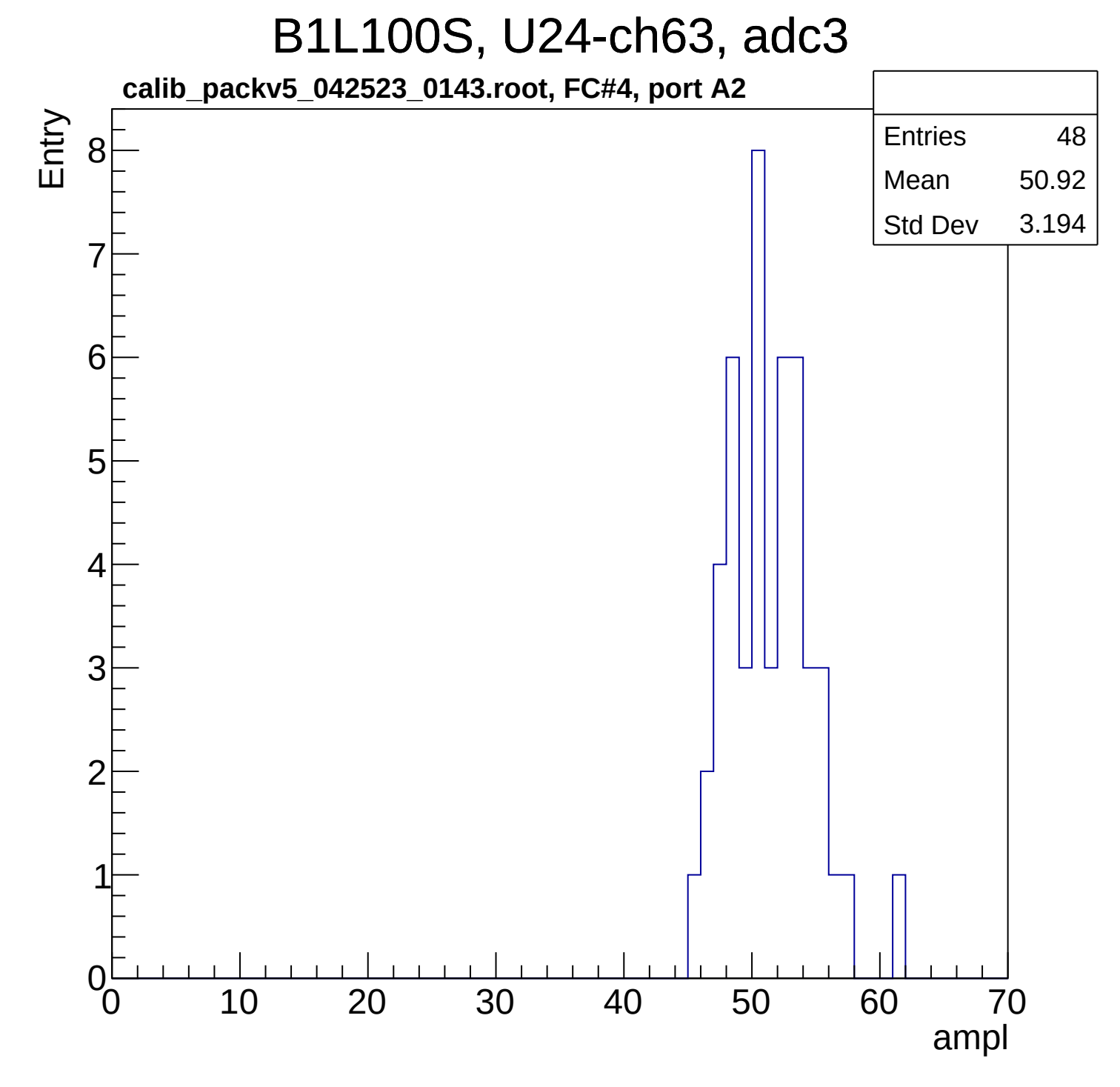
B1L100S, U24-ch63, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 50.92
Std Dev 3.194
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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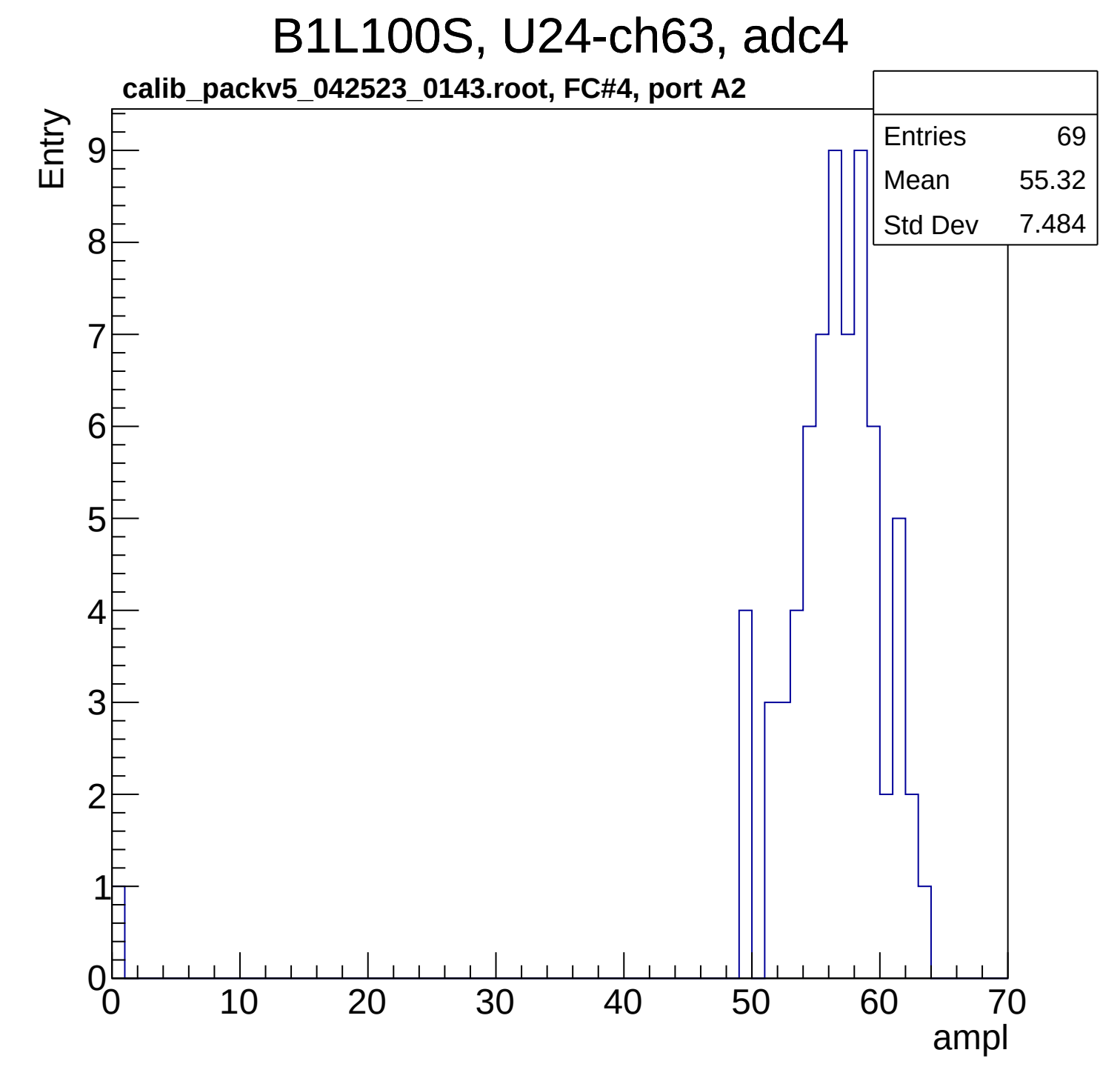
B1L100S, U24-ch63, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.32
Std Dev 7.484
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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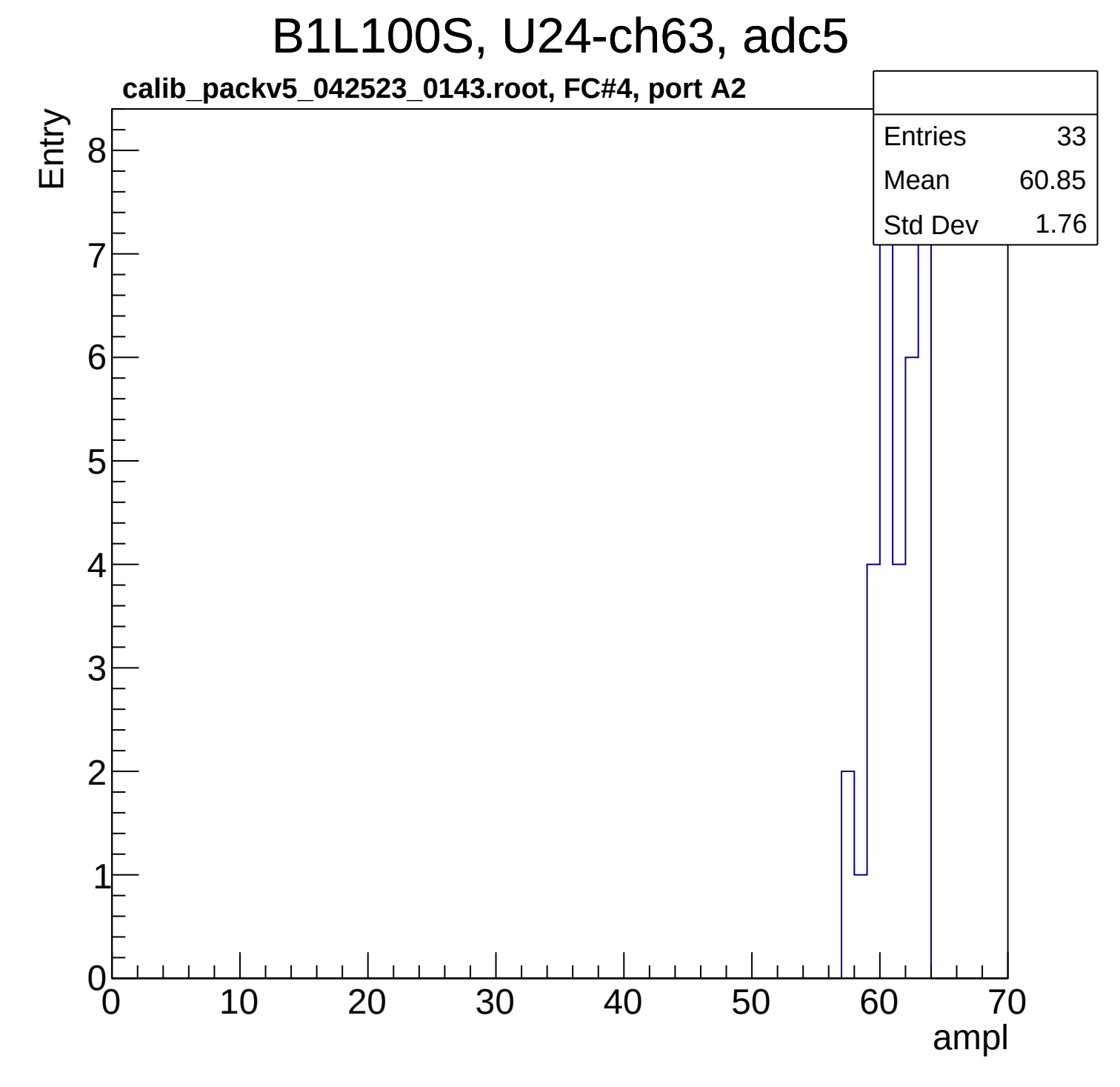
B1L100S, U24-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 60.85
Std Dev 1.76
Entry
ampl
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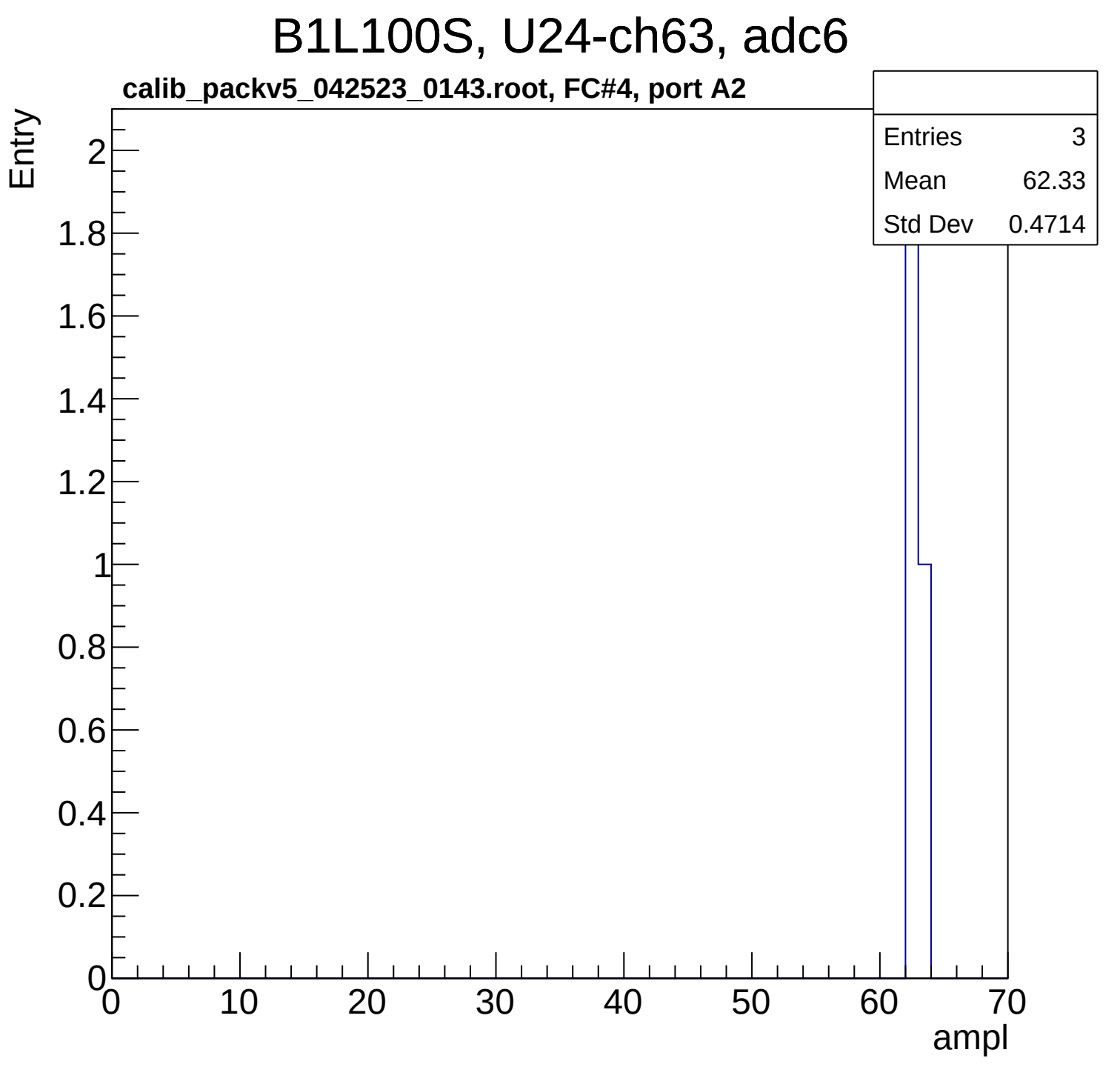
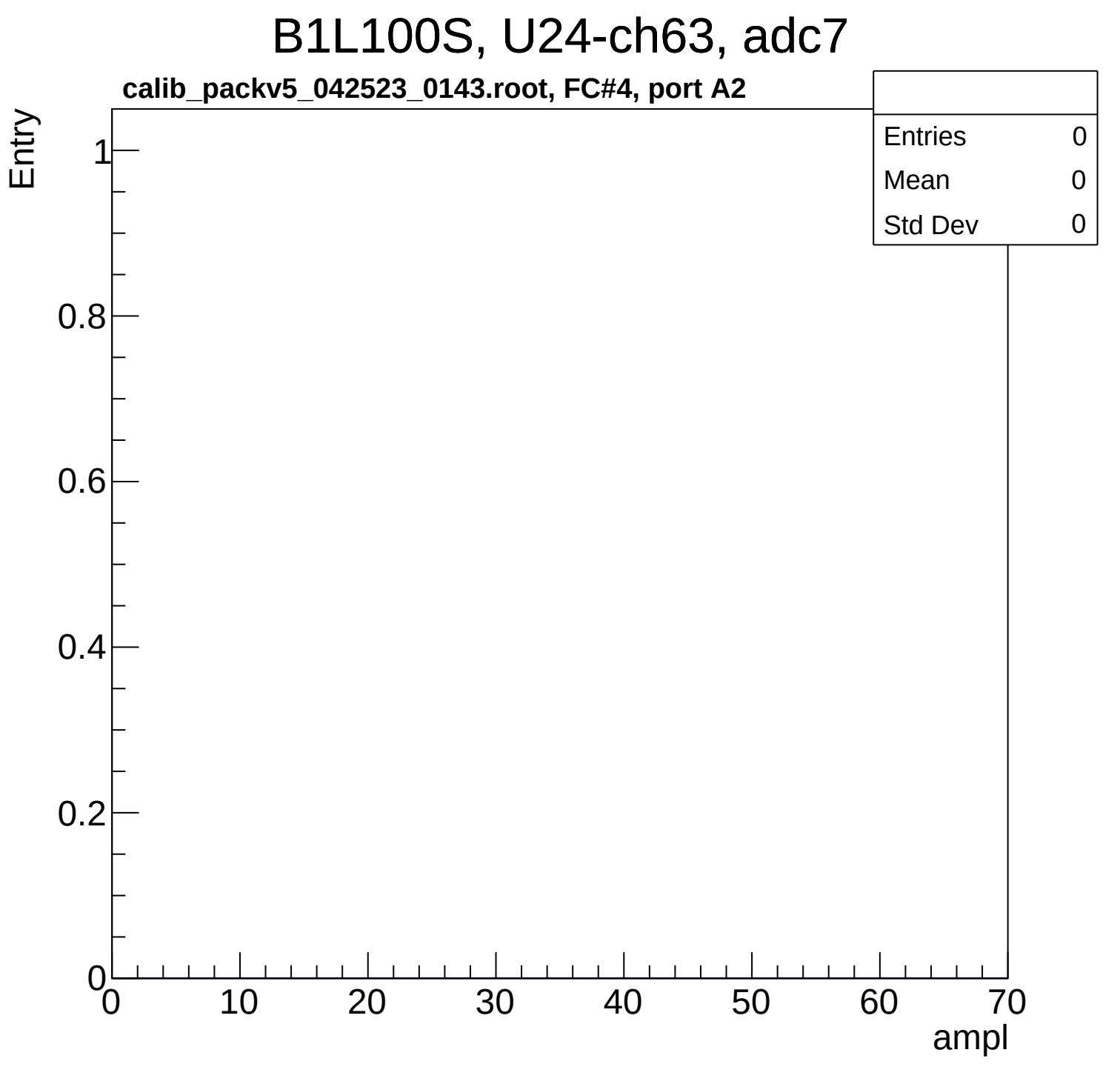
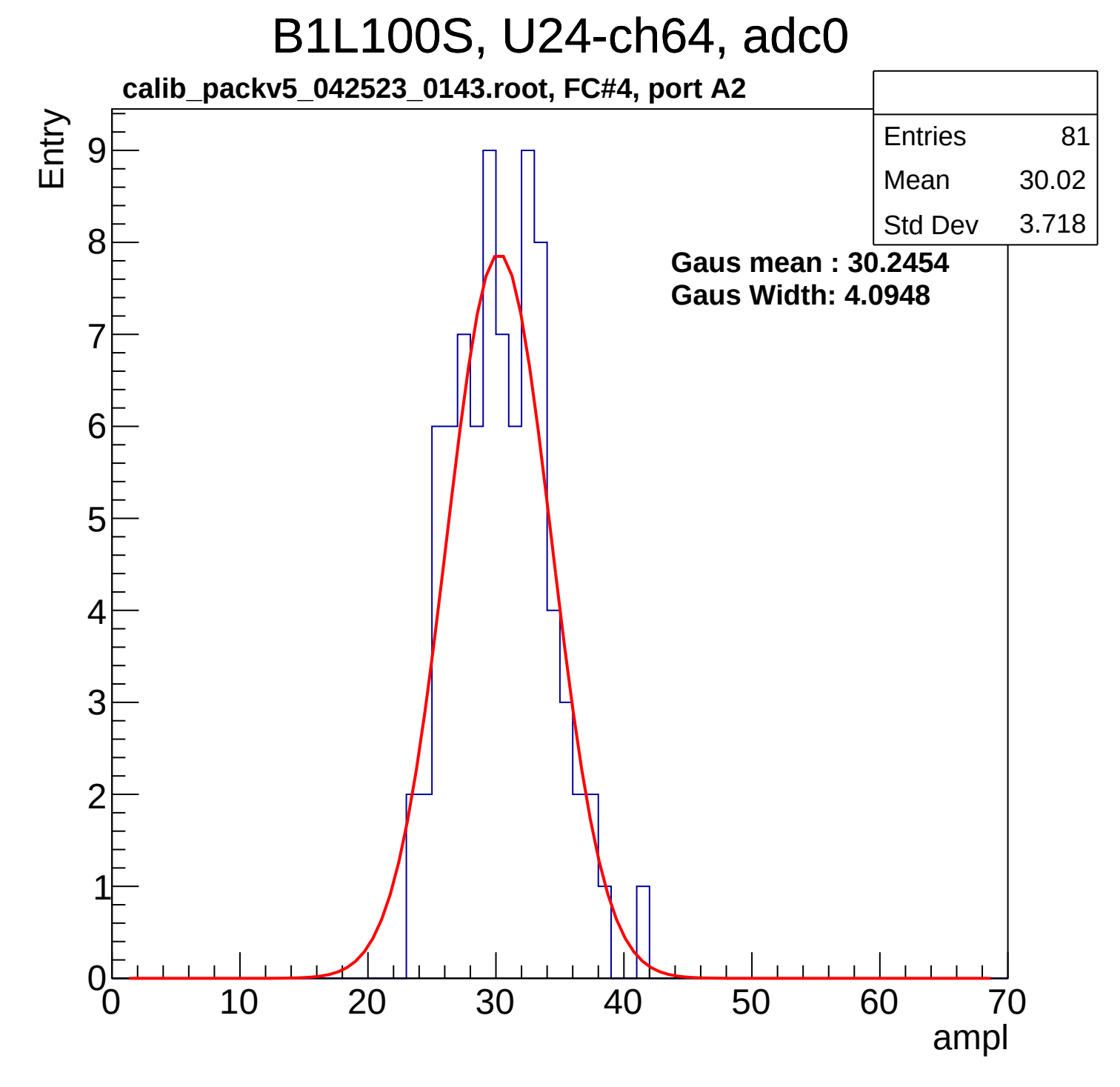
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch63, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch63, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.02
Std Dev 3.718
Gaus mean : 30.2454
Gaus Width: 4.0948
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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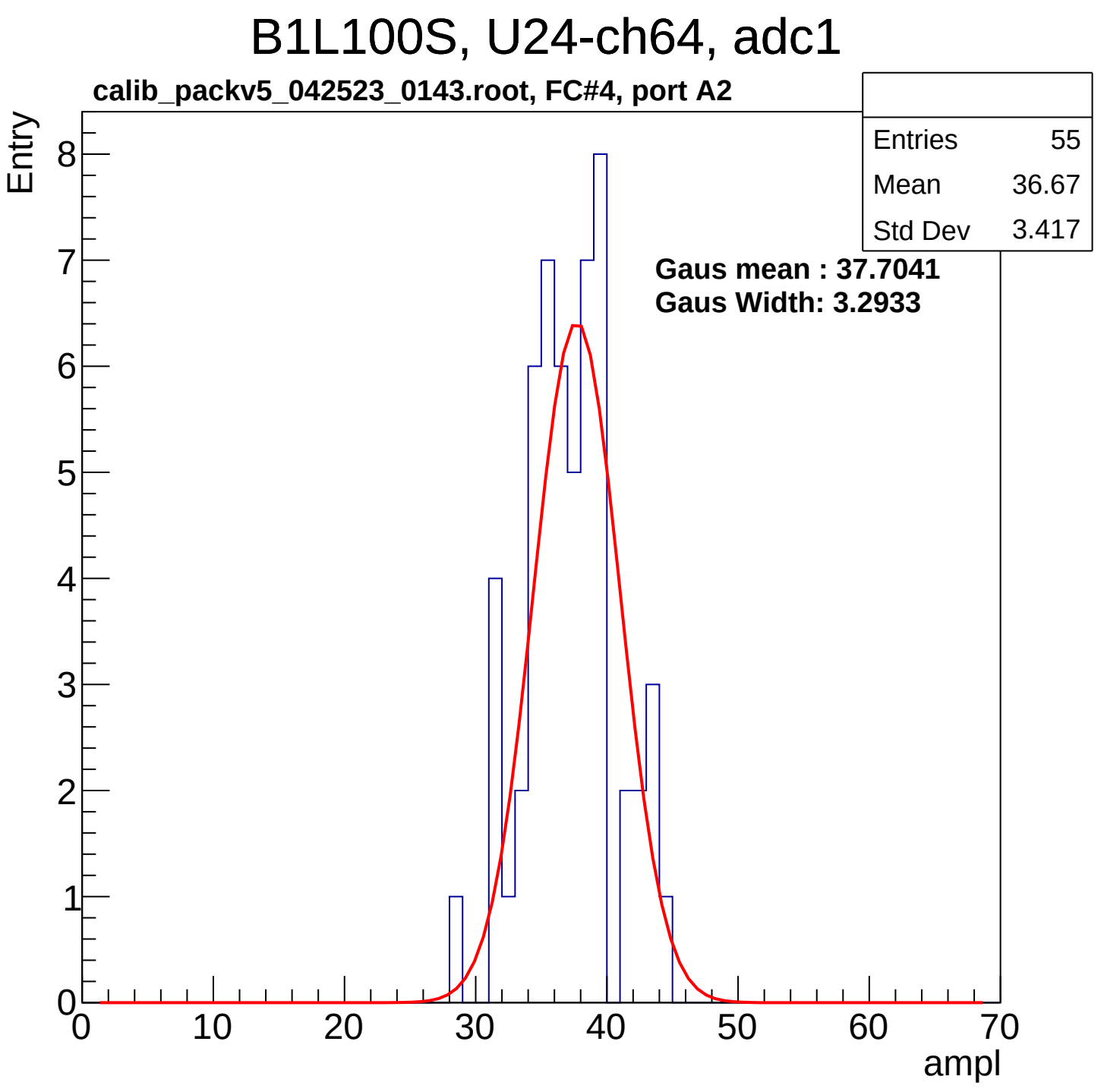
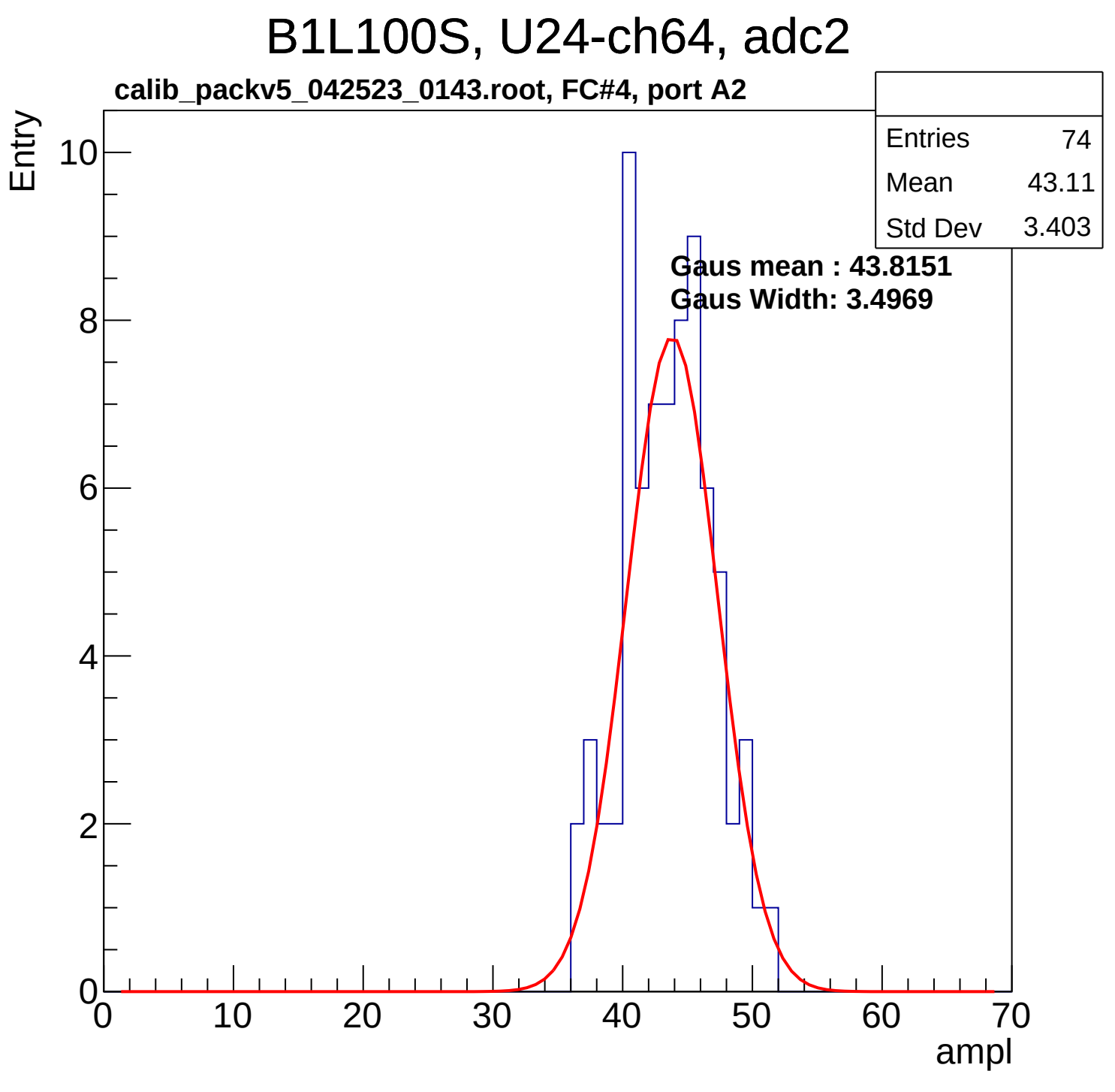
20
30
40
50
60
70


B1L100S, U24-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 36.67
Std Dev 3.417
Gaus mean : 37.7041
Gaus Width: 3.2933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.11
Std Dev 3.403
Gaus mean : 43.8151
Gaus Width: 3.4969
Entry
ampl
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2
4
6
8
10
0
10
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40
50
60
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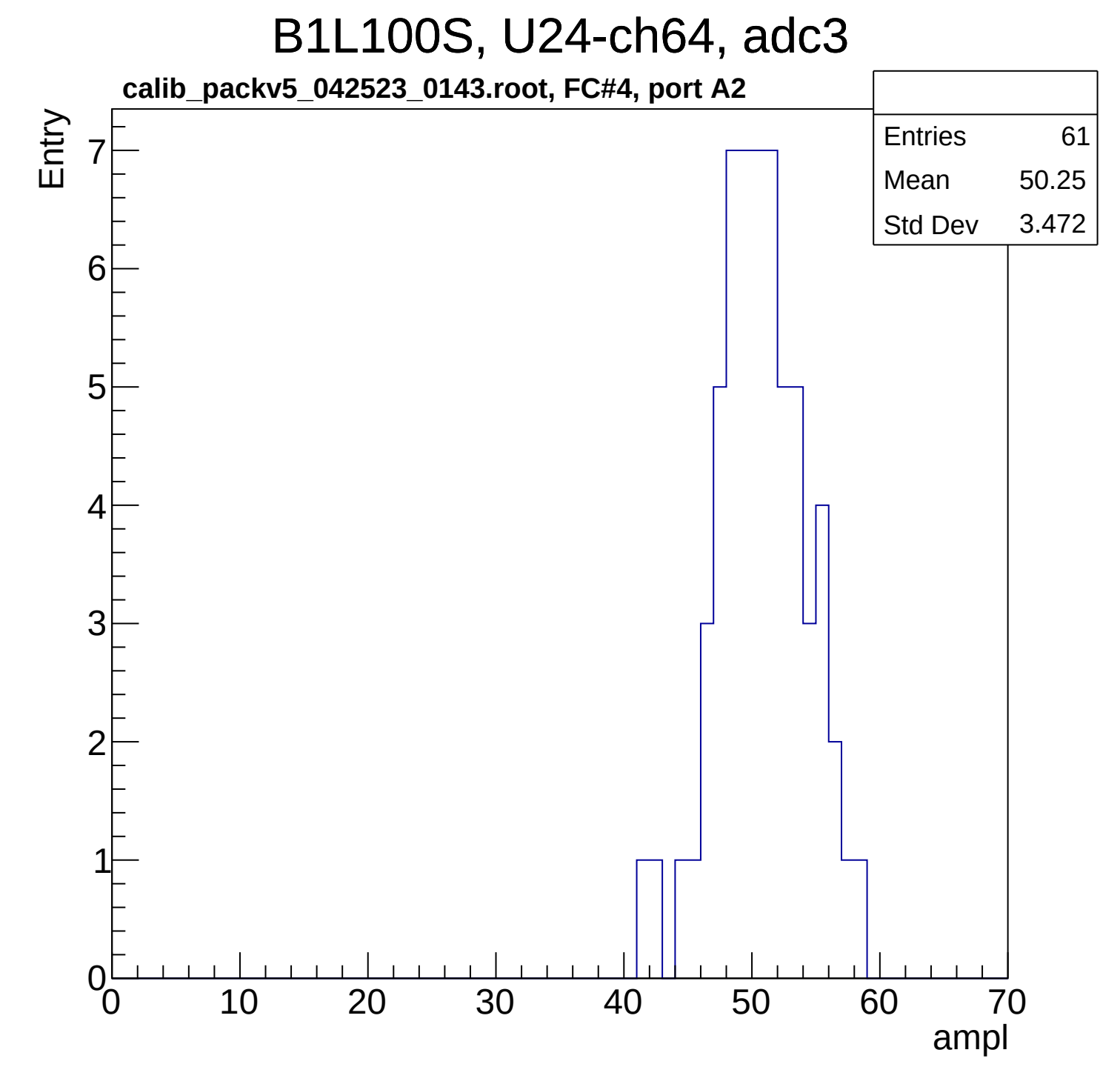
B1L100S, U24-ch64, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.25
Std Dev 3.472
Entry
ampl
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1
2
3
4
5
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0
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20
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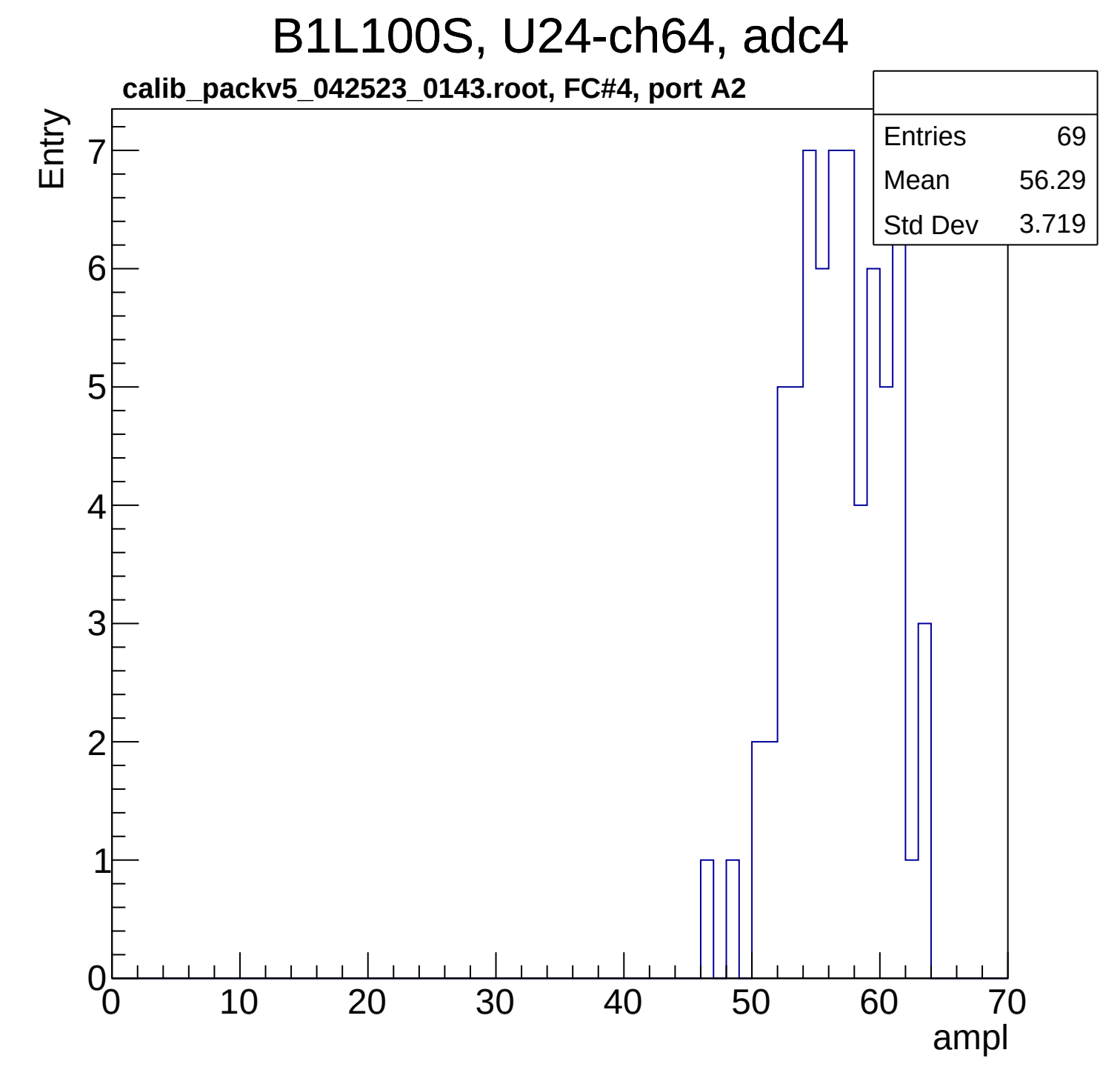
40
50
60
70


B1L100S, U24-ch64, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.29
Std Dev 3.719
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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40
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60
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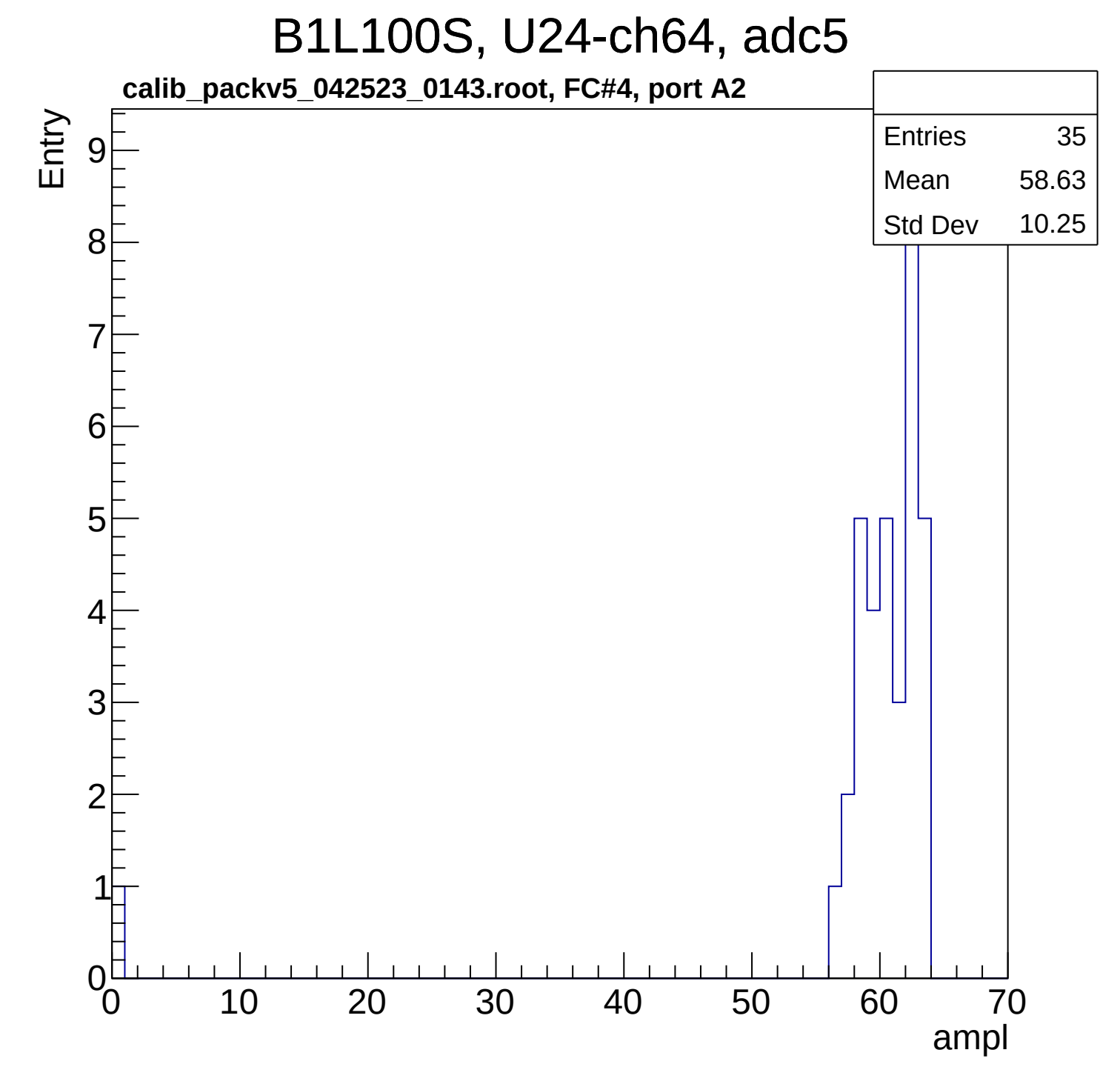
B1L100S, U24-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.63
Std Dev 10.25
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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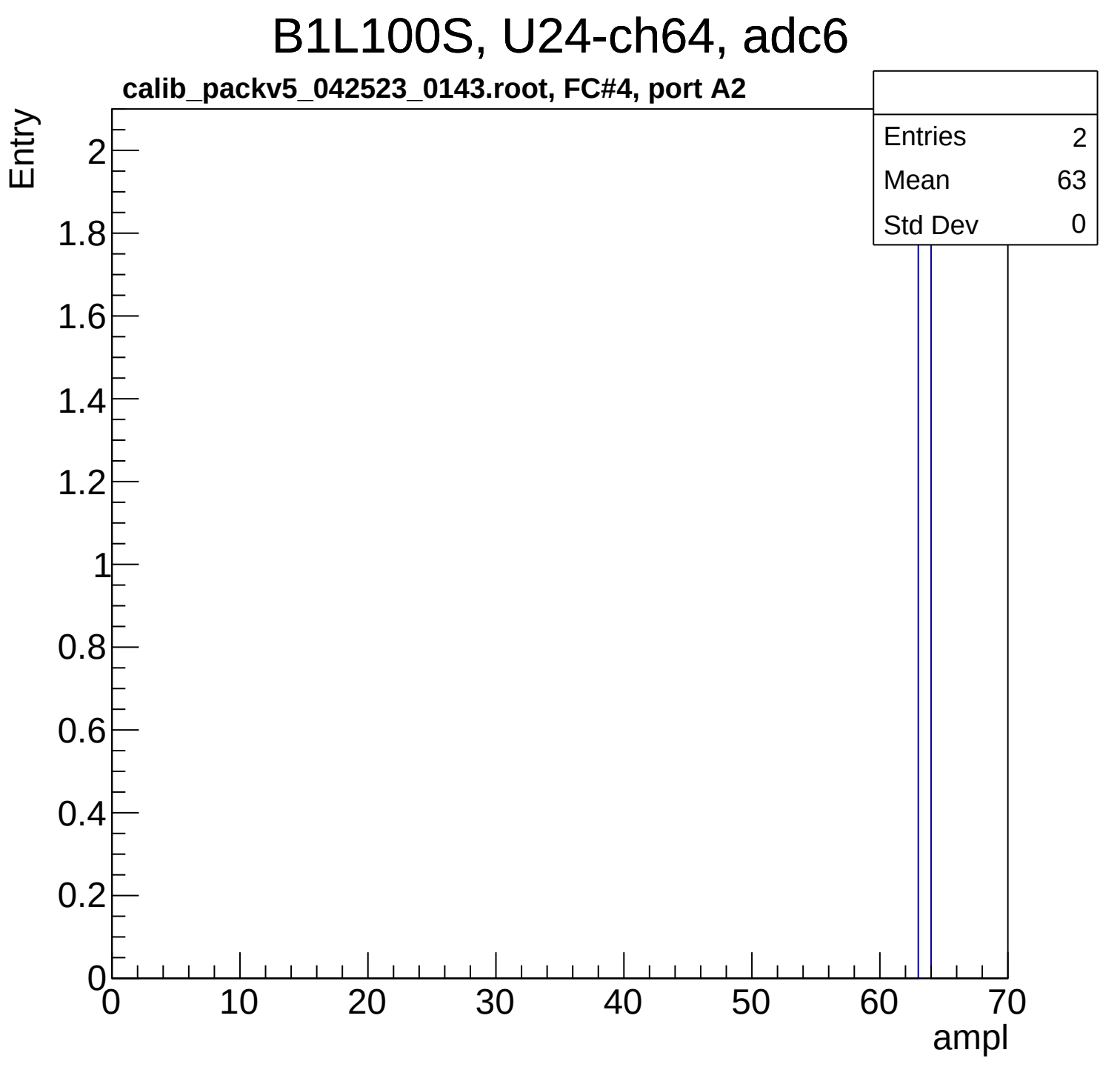
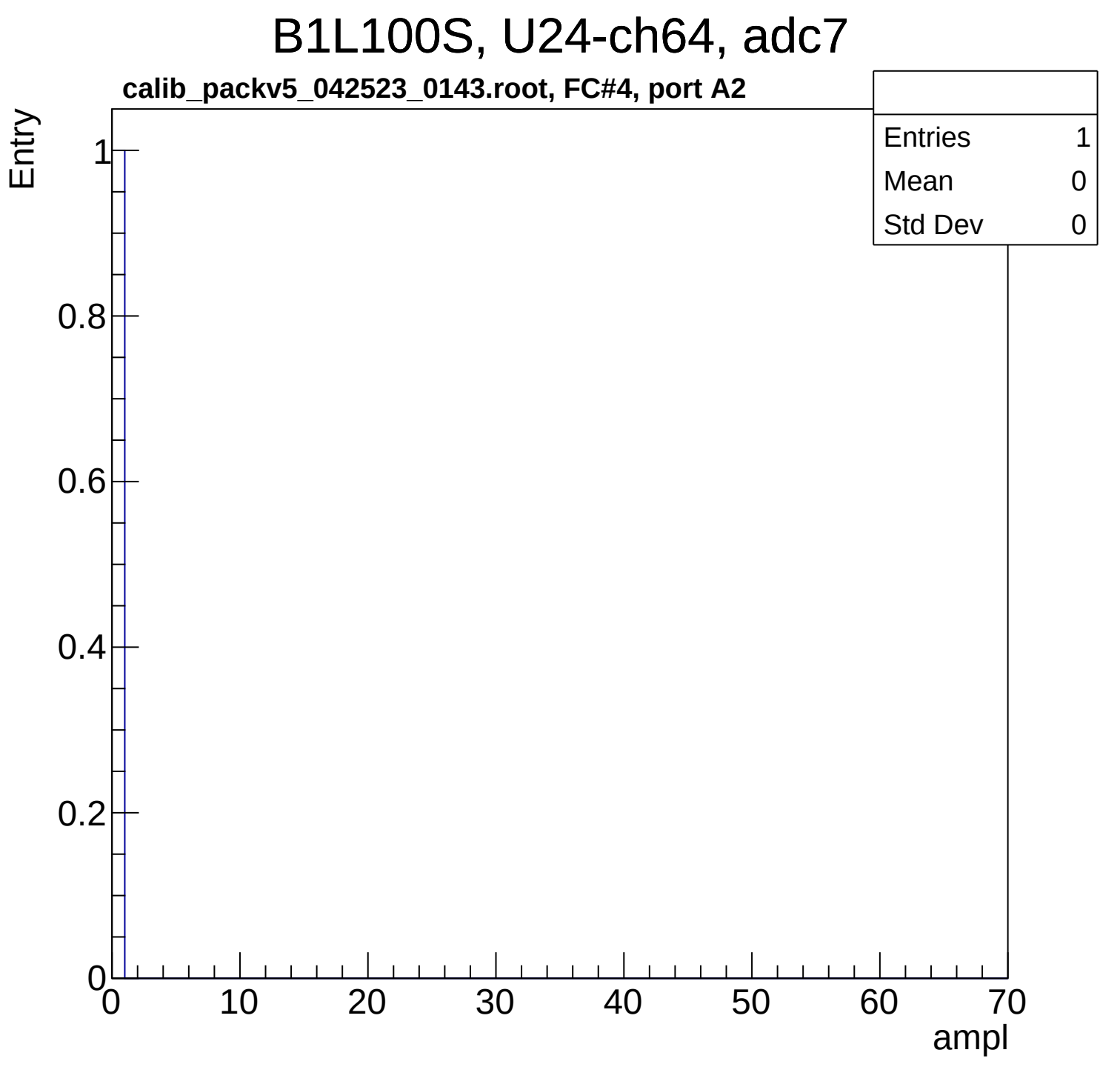

B1L100S, U24-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch64, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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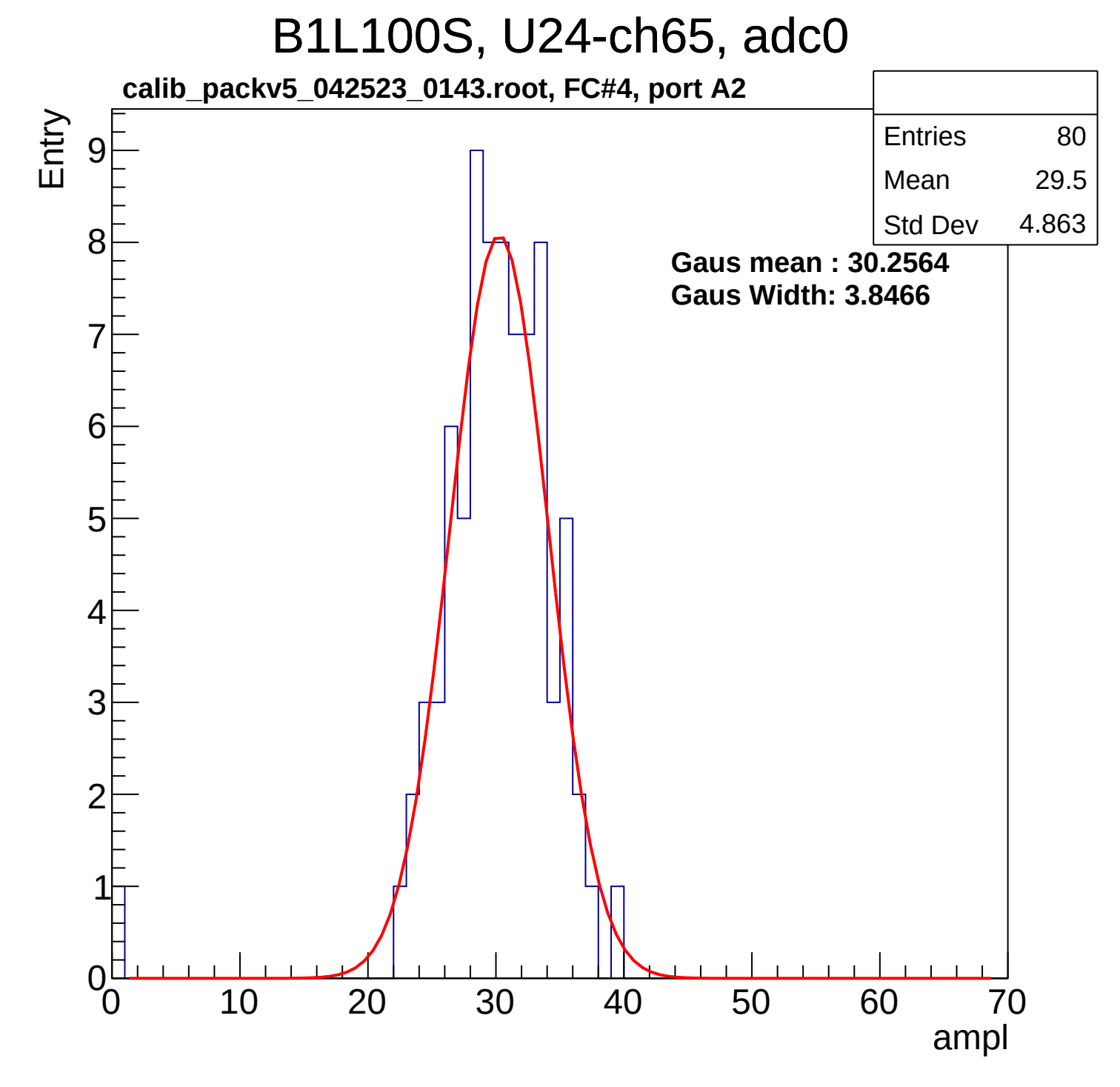
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.5
Std Dev 4.863
Gaus mean : 30.2564
Gaus Width: 3.8466
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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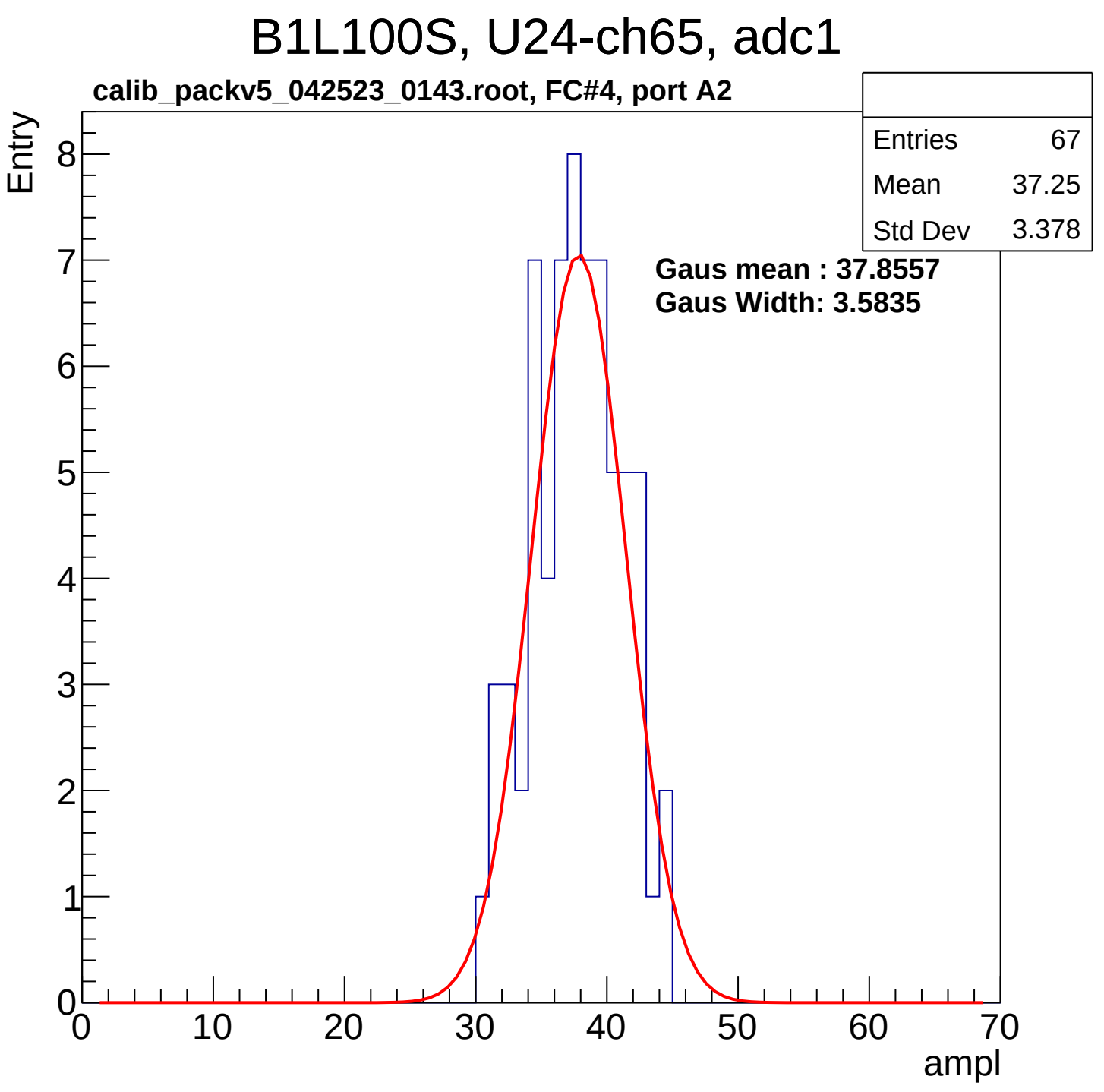
B1L100S, U24-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.25
Std Dev 3.378
Gaus mean : 37.8557
Gaus Width: 3.5835
Entry
ampl
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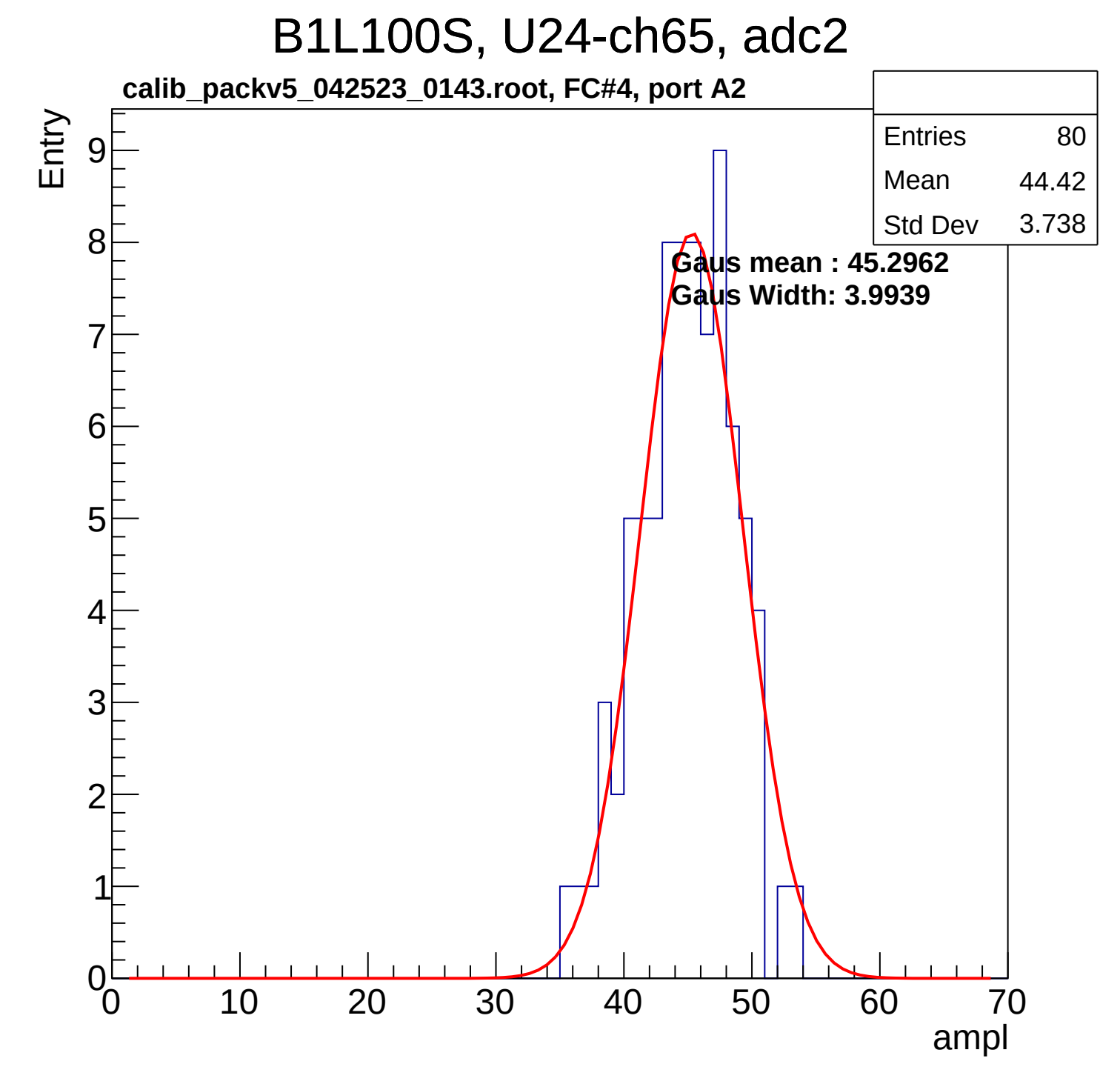
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 44.42
Std Dev 3.738
Gaus mean : 45.2962
Gaus Width: 3.9939
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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40
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60
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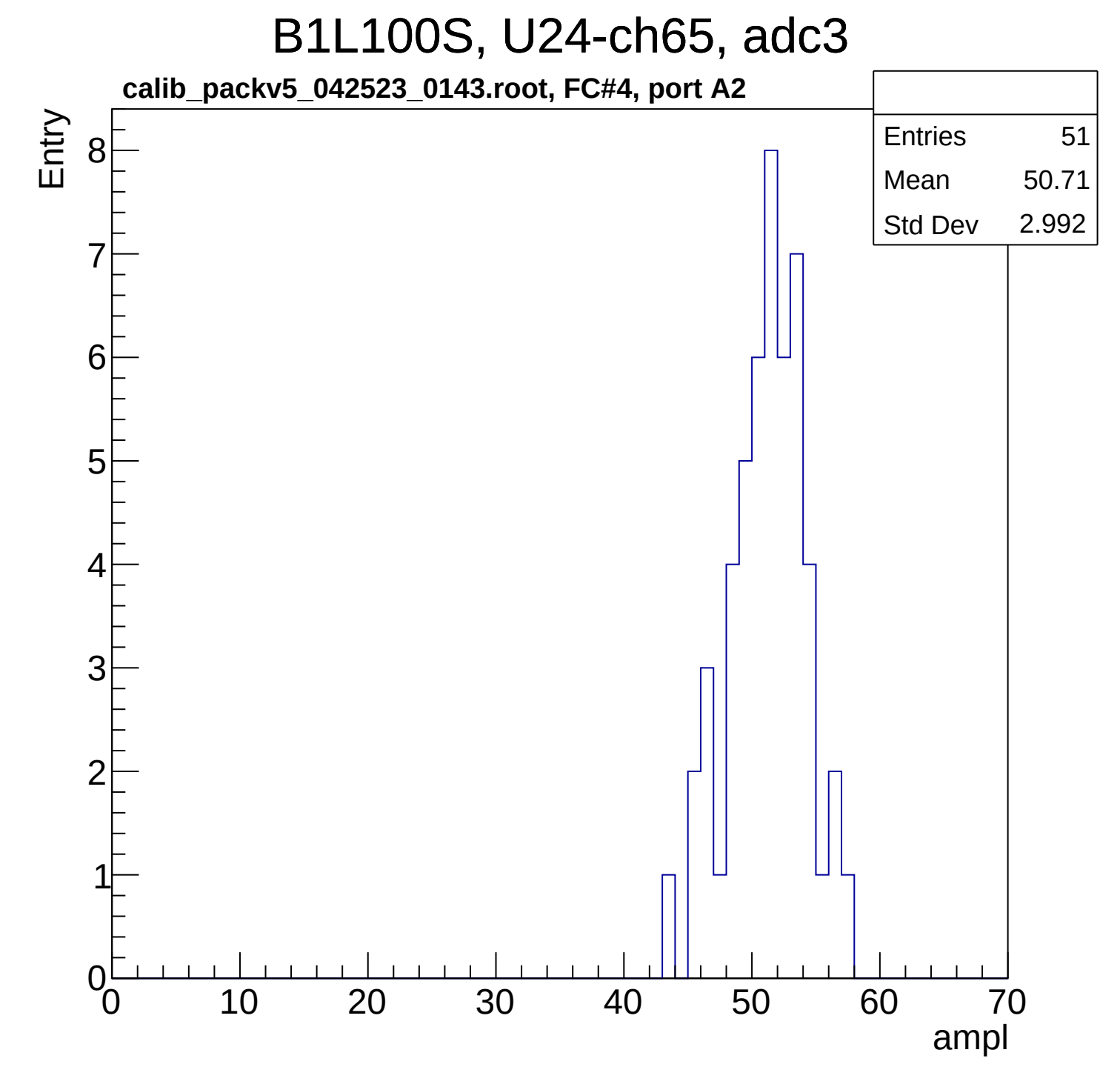
B1L100S, U24-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 50.71
Std Dev 2.992
Entry
ampl
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2
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4
5
6
7
8
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10
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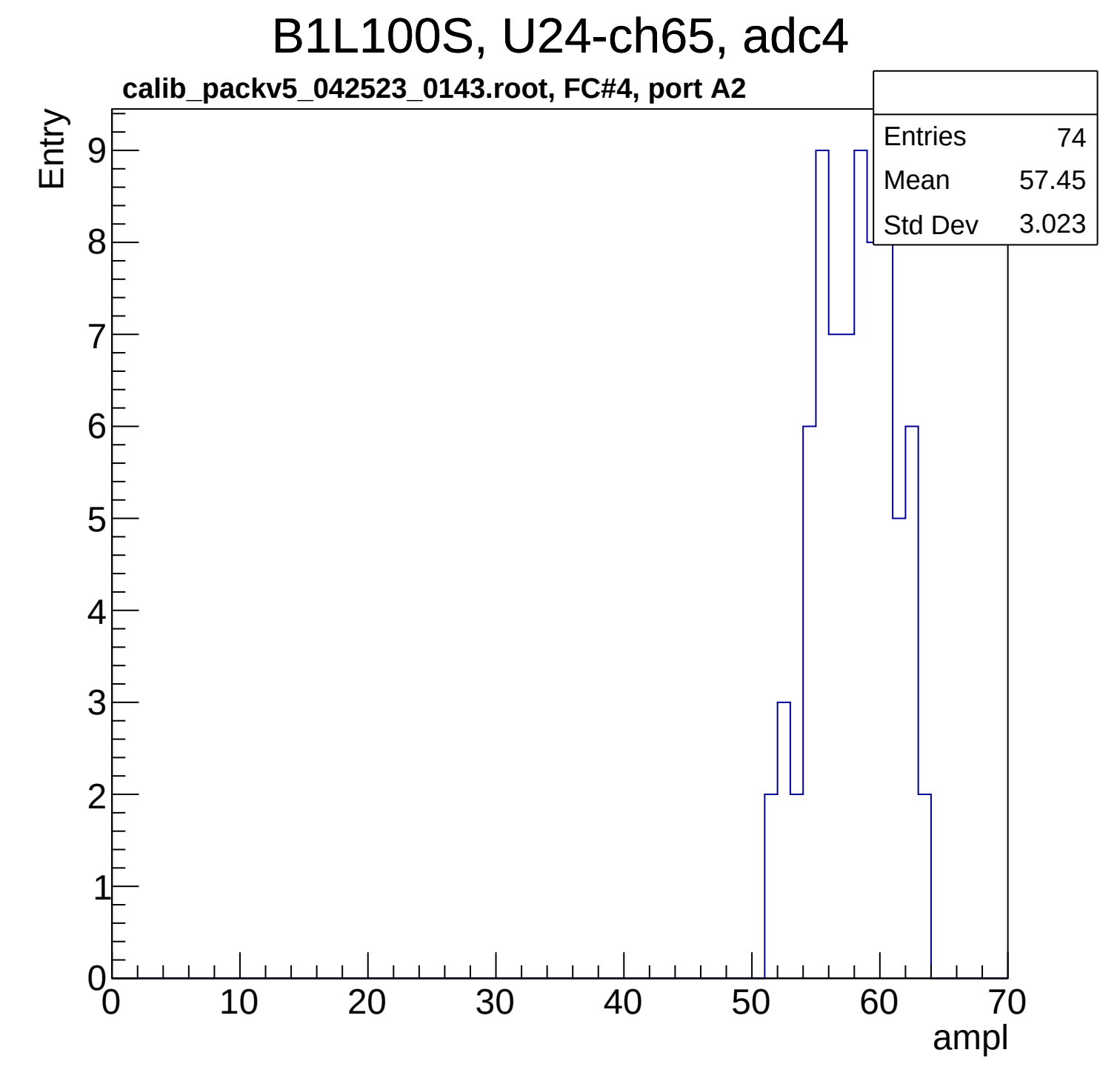
50
60
70


B1L100S, U24-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 57.45
Std Dev 3.023
Entry
ampl
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3
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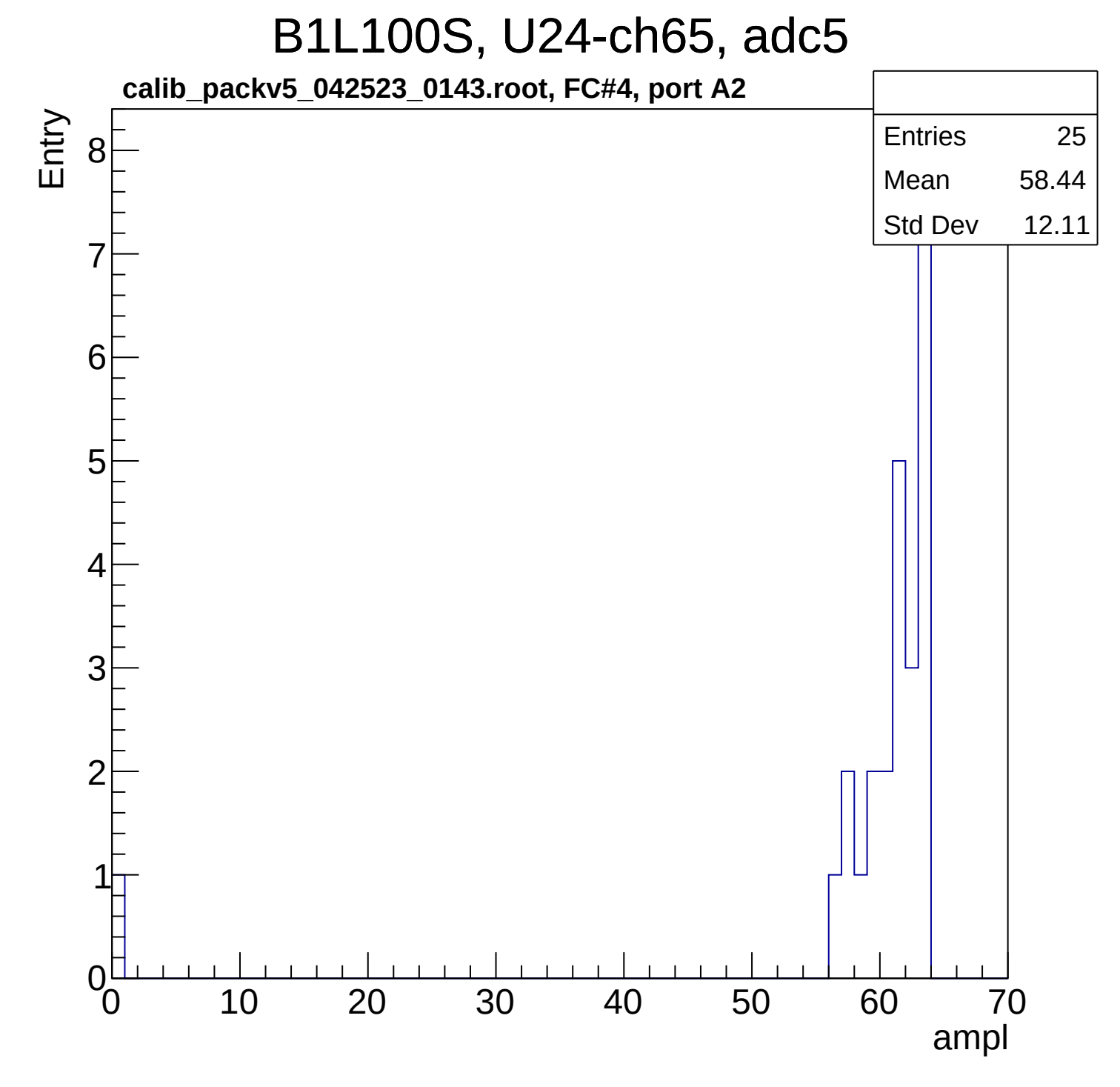
B1L100S, U24-ch65, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.44
Std Dev 12.11
Entry
ampl
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1
2
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5
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0
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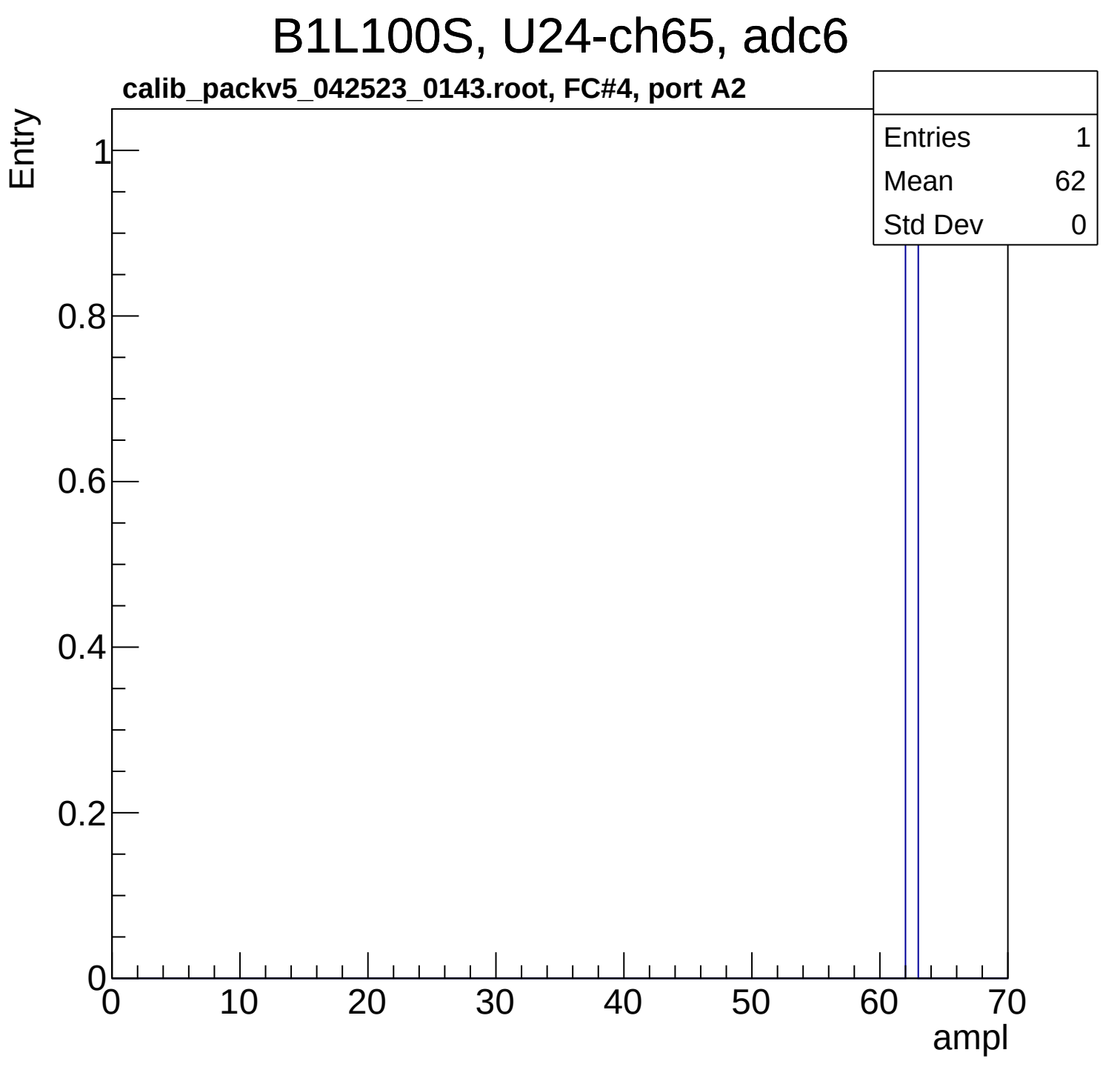
B1L100S, U24-ch65, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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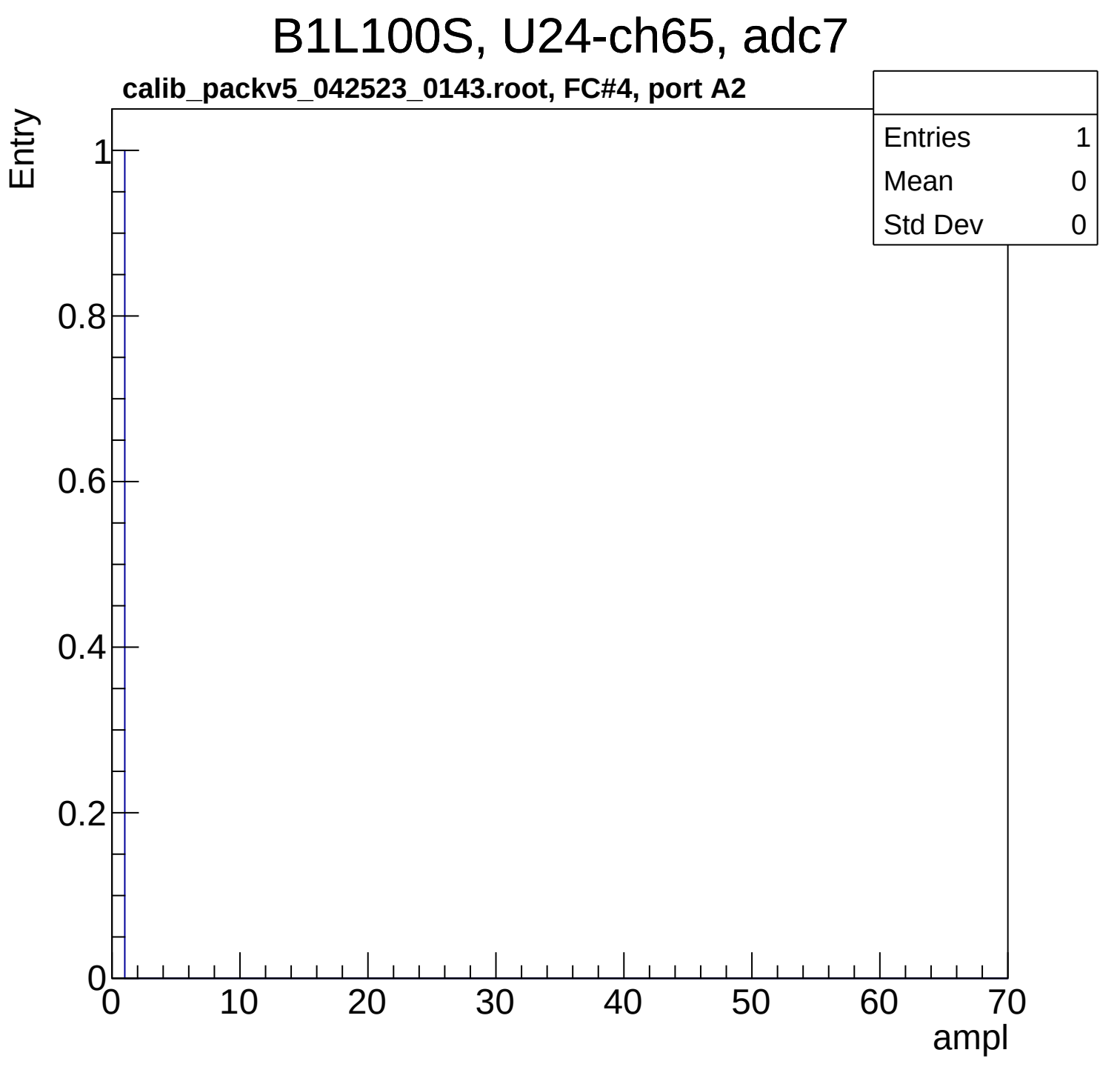
40
50
60
70
ampl


B1L100S, U24-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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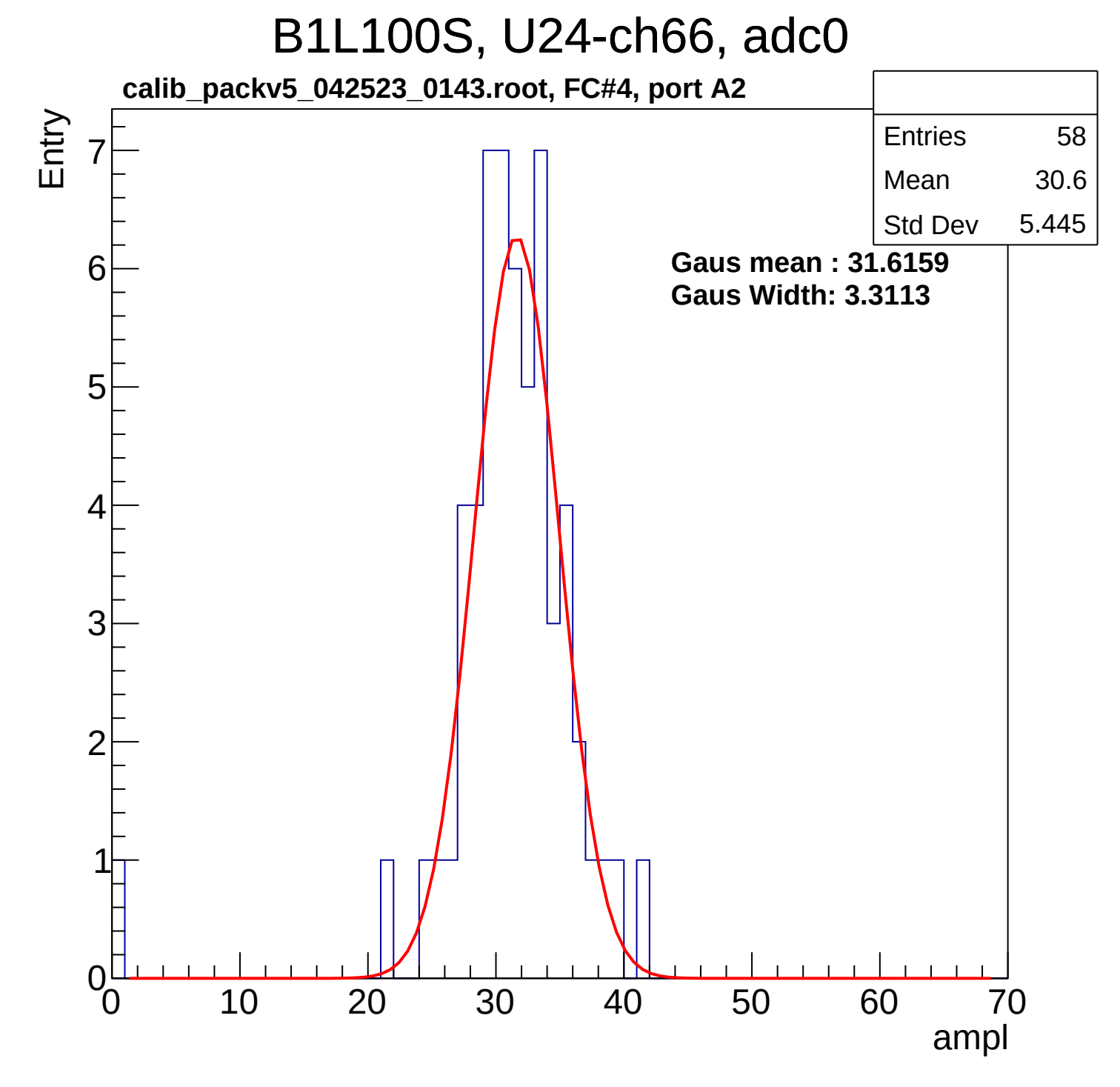
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 30.6
Std Dev 5.445
Gaus mean : 31.6159
Gaus Width: 3.3113
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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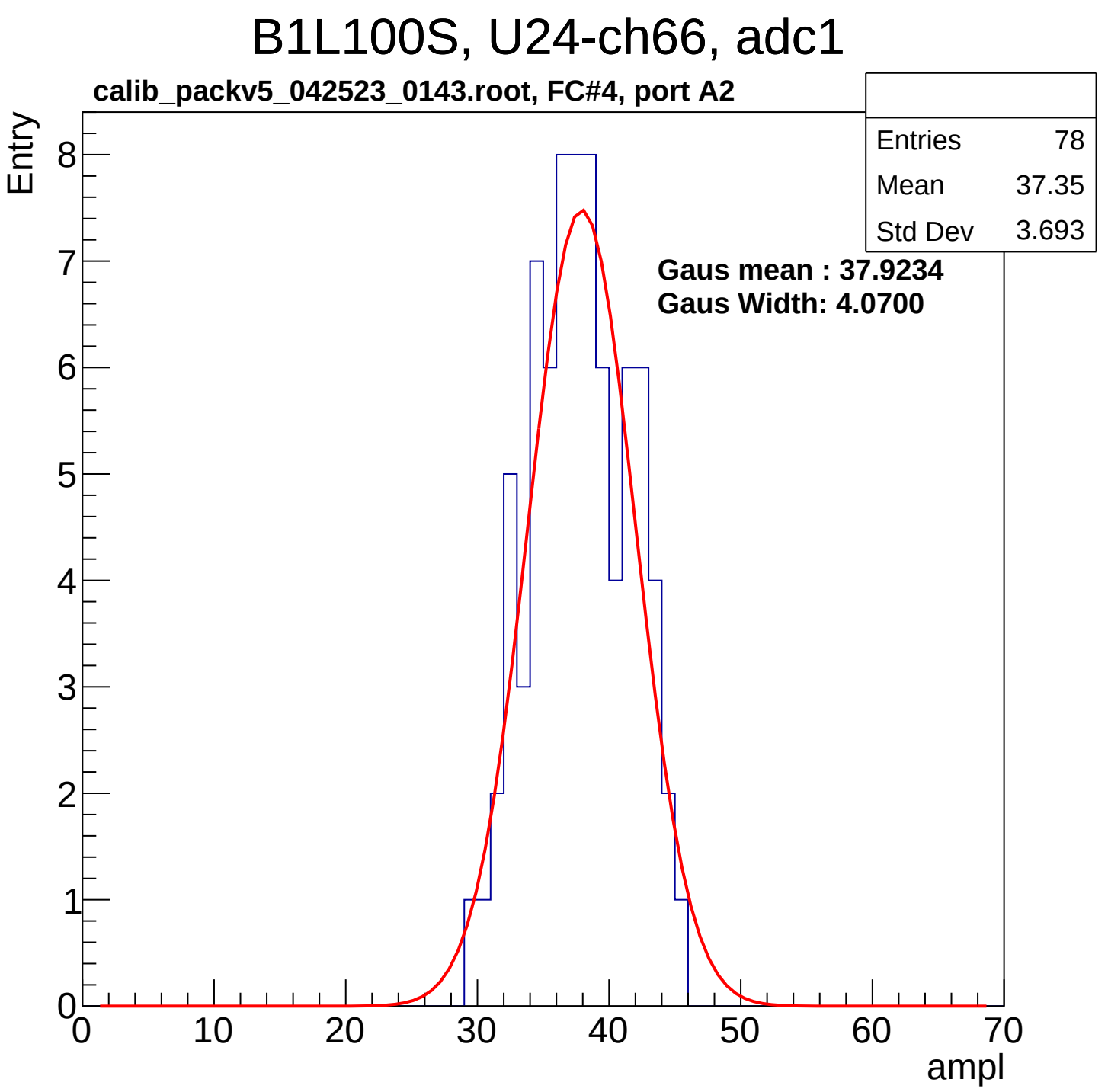
B1L100S, U24-ch66, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.35
Std Dev 3.693
Gaus mean : 37.9234
Gaus Width: 4.0700
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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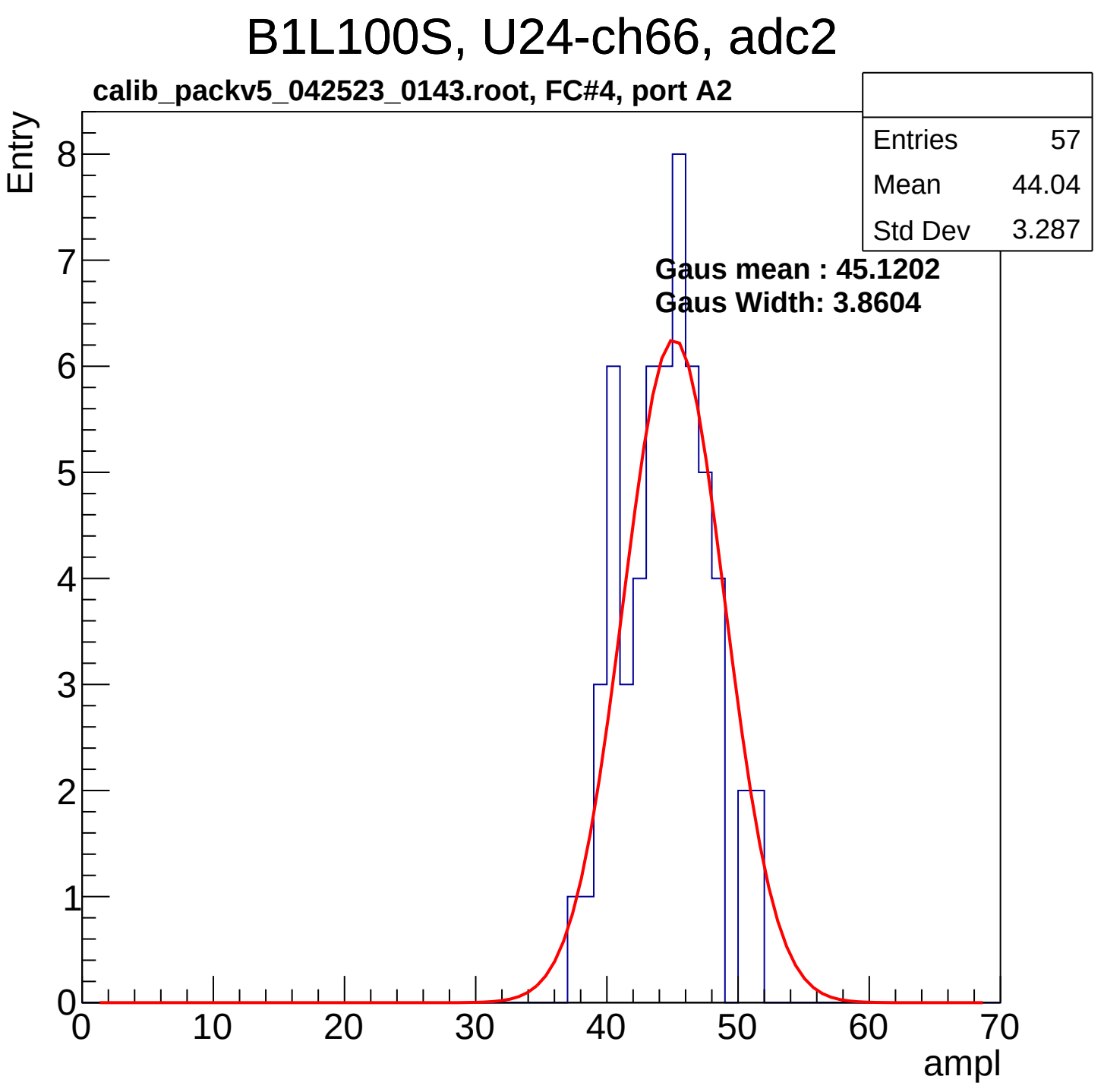
B1L100S, U24-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.04
Std Dev 3.287
Gaus mean : 45.1202
Gaus Width: 3.8604
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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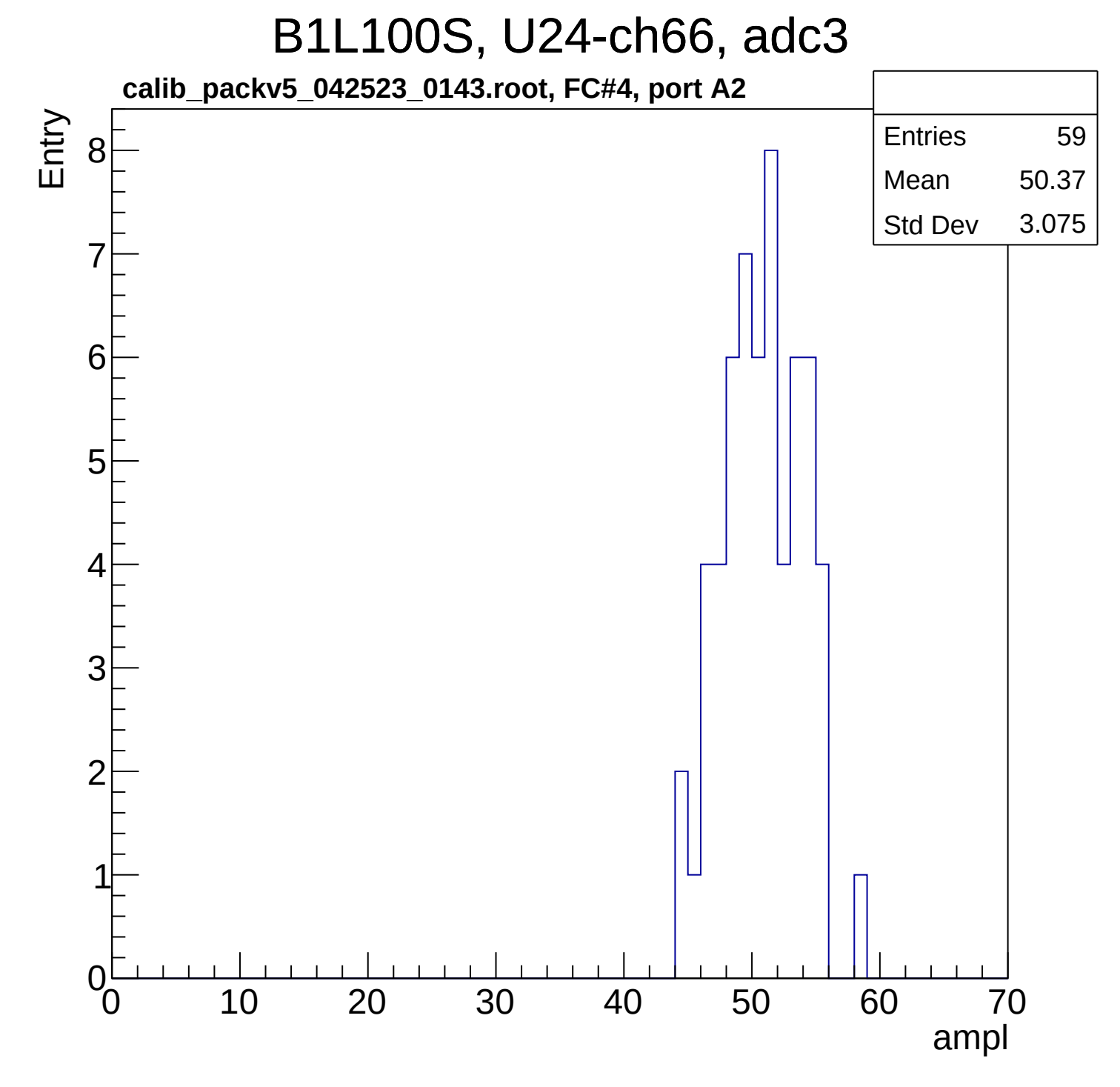
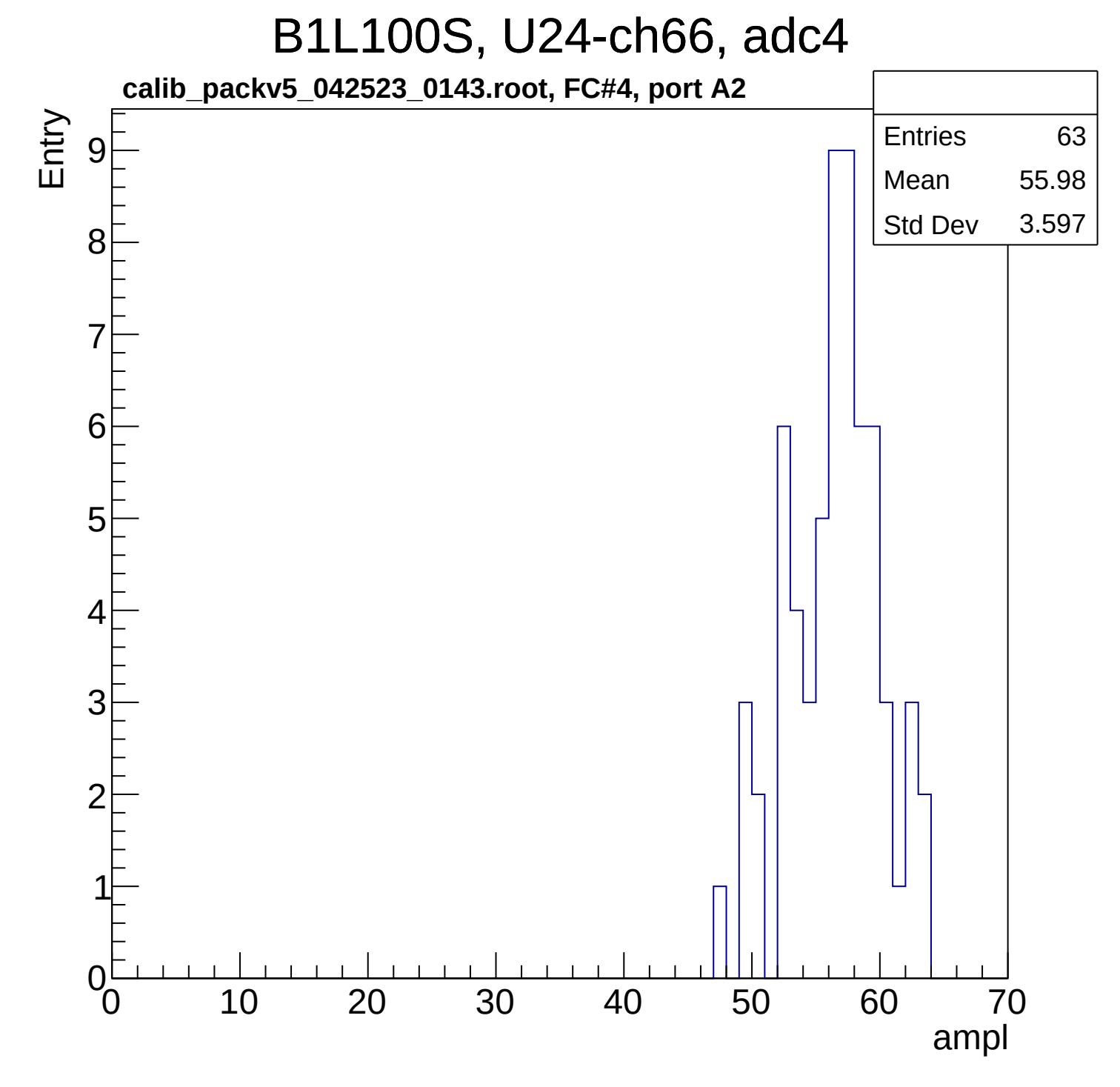

B1L100S, U24-ch66, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 50.37
Std Dev 3.075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.98
Std Dev 3.597
Entry
ampl
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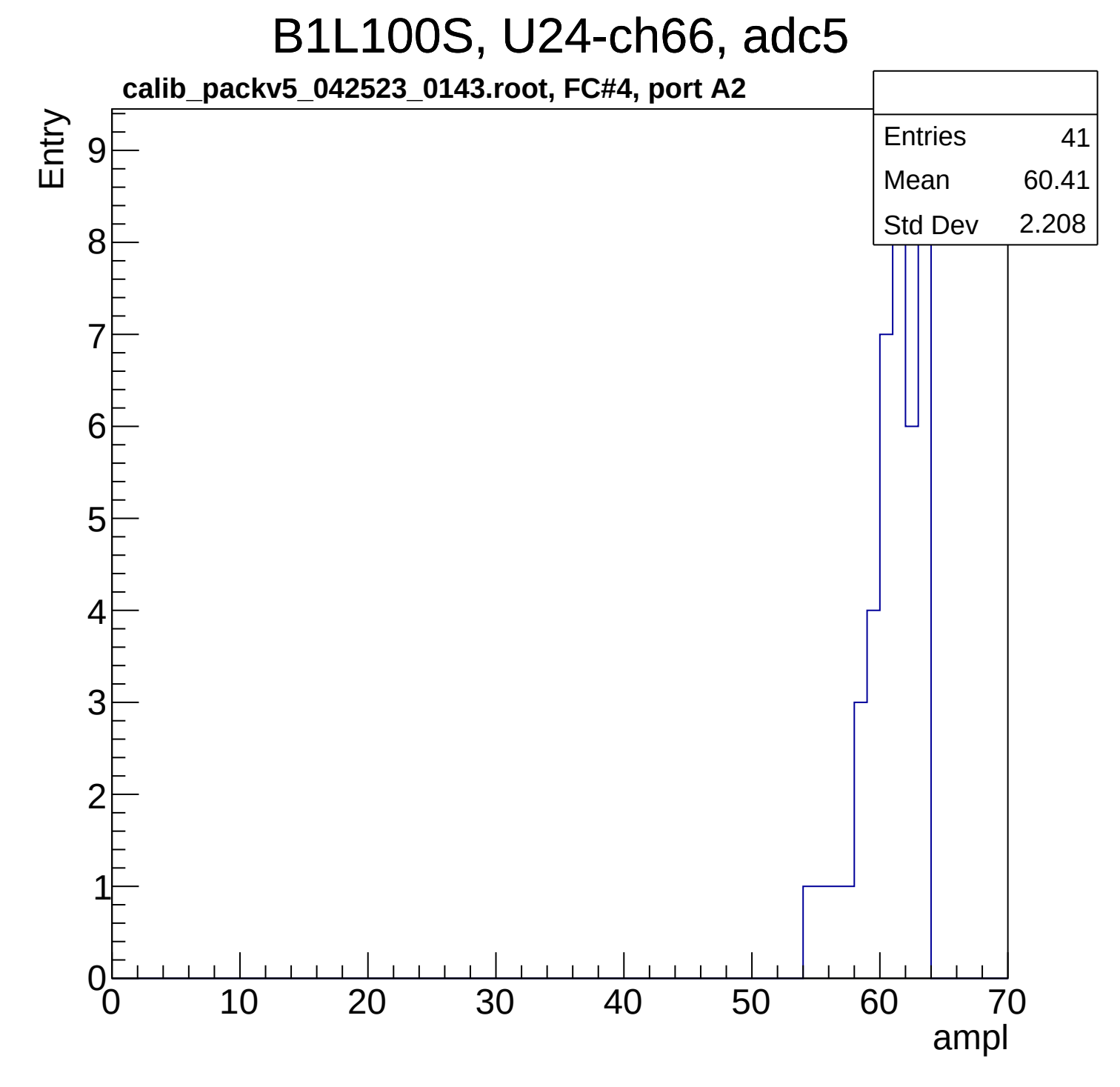
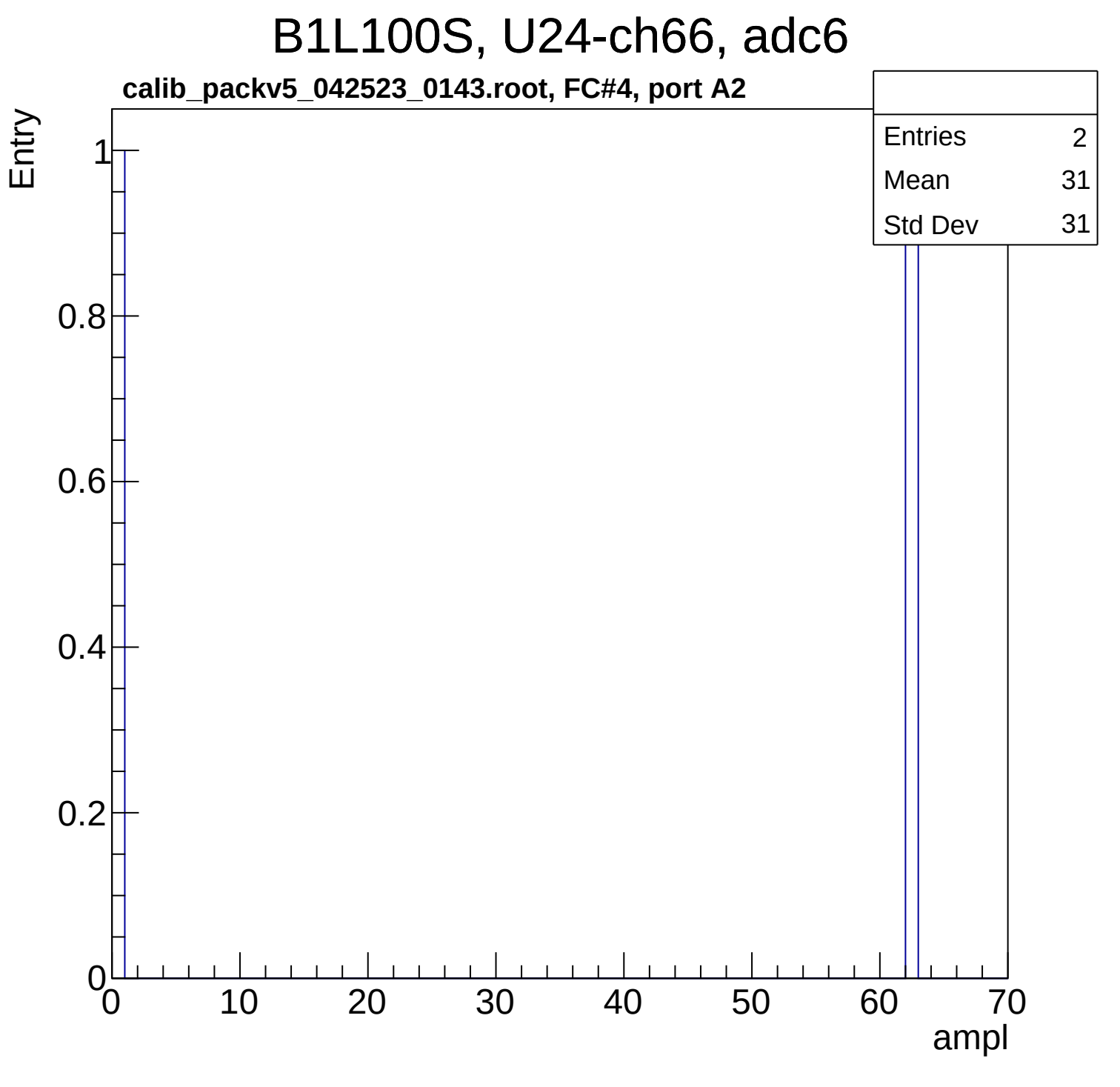
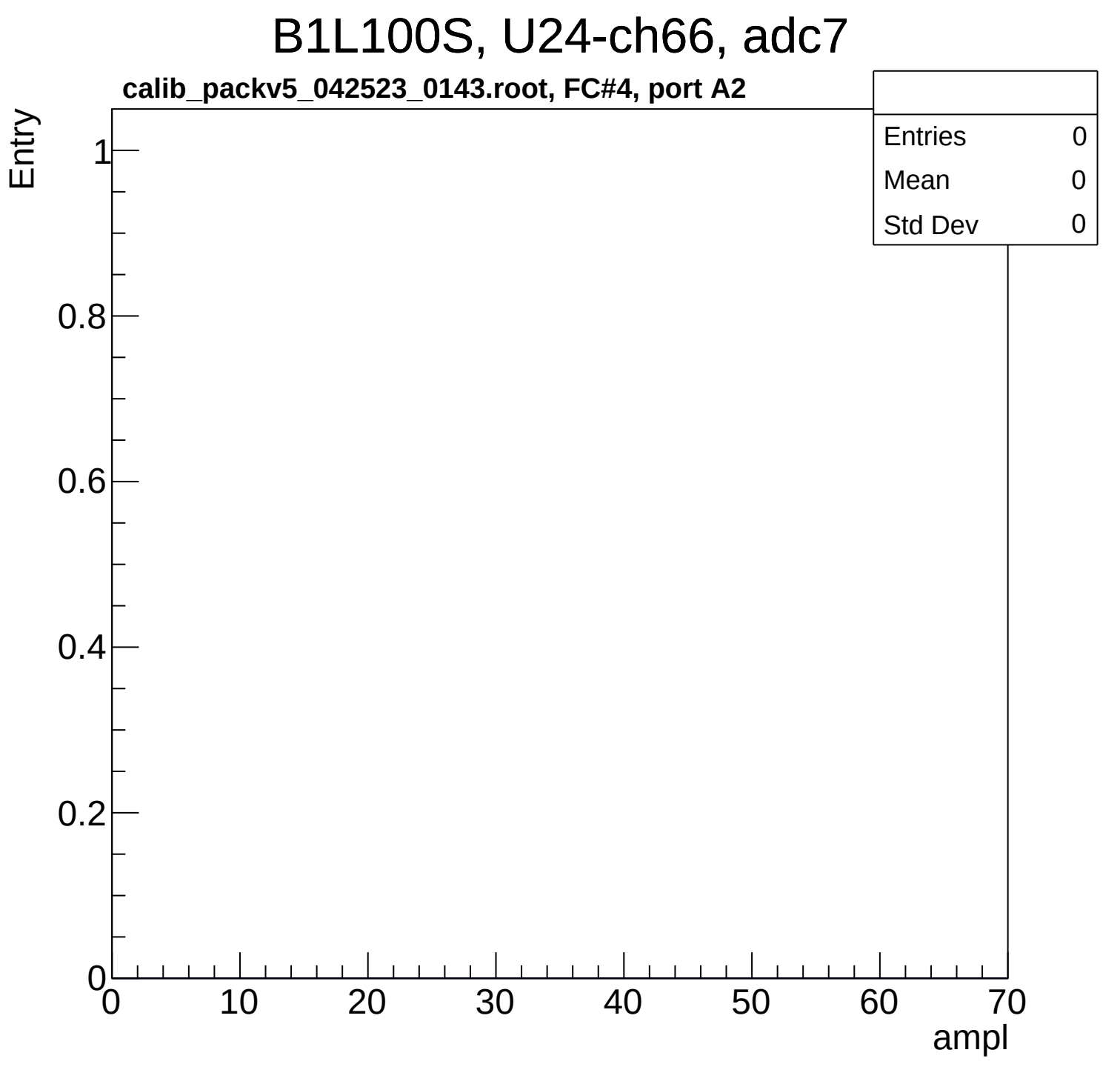
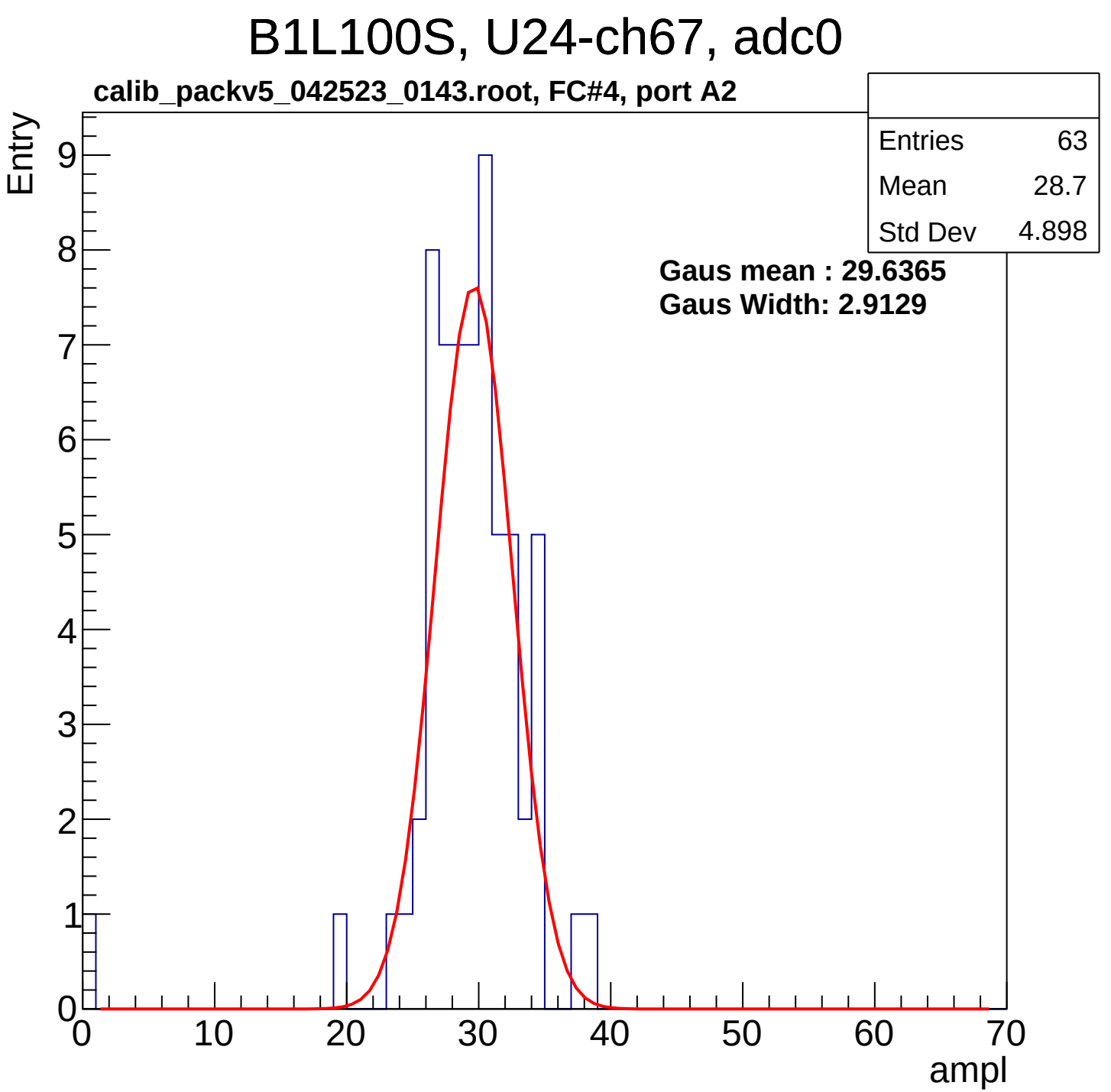
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch66, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 60.41
Std Dev 2.208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch66, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.7
Std Dev 4.898
Gaus mean : 29.6365
Gaus Width: 2.9129
Entry
ampl
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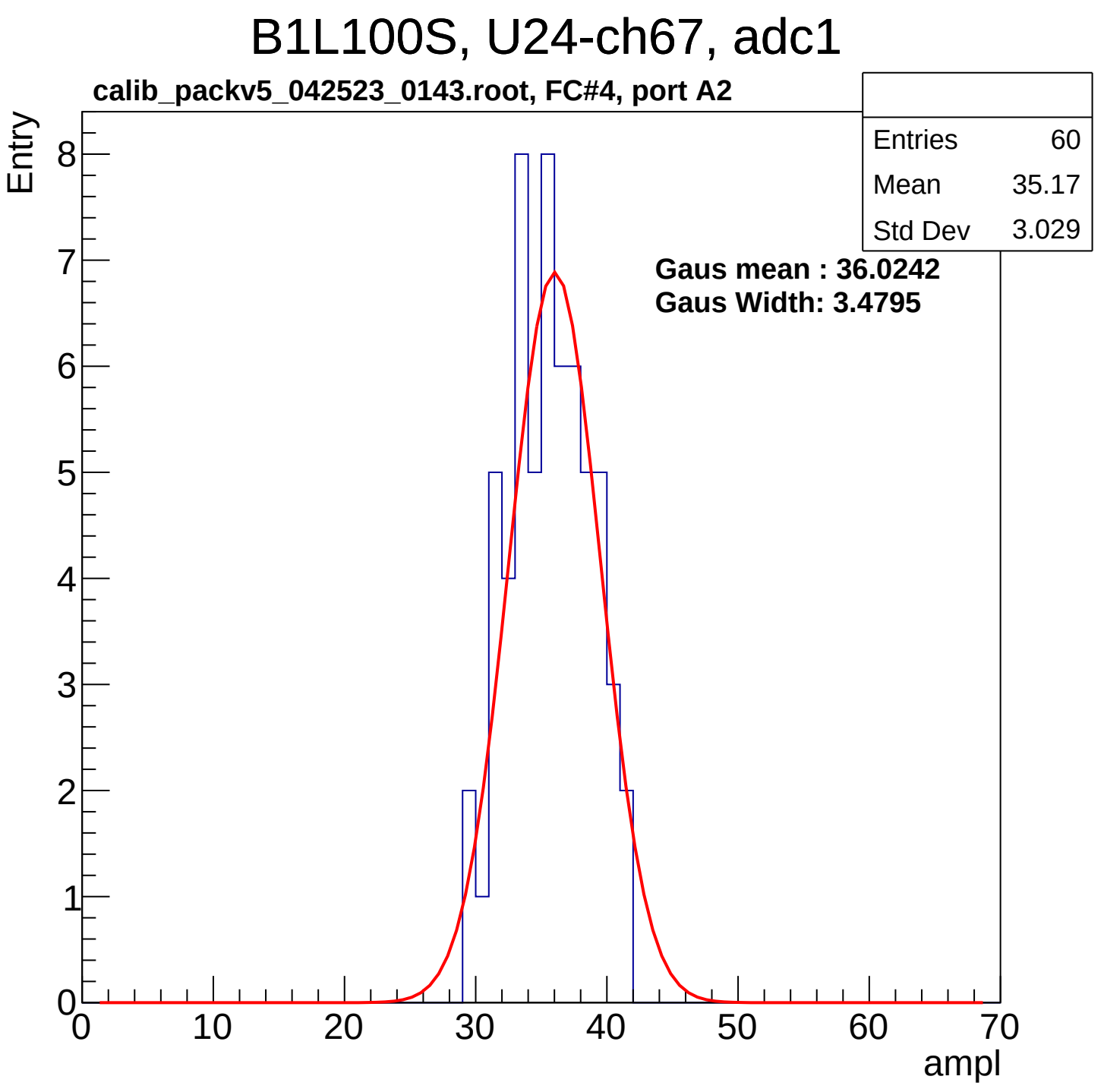
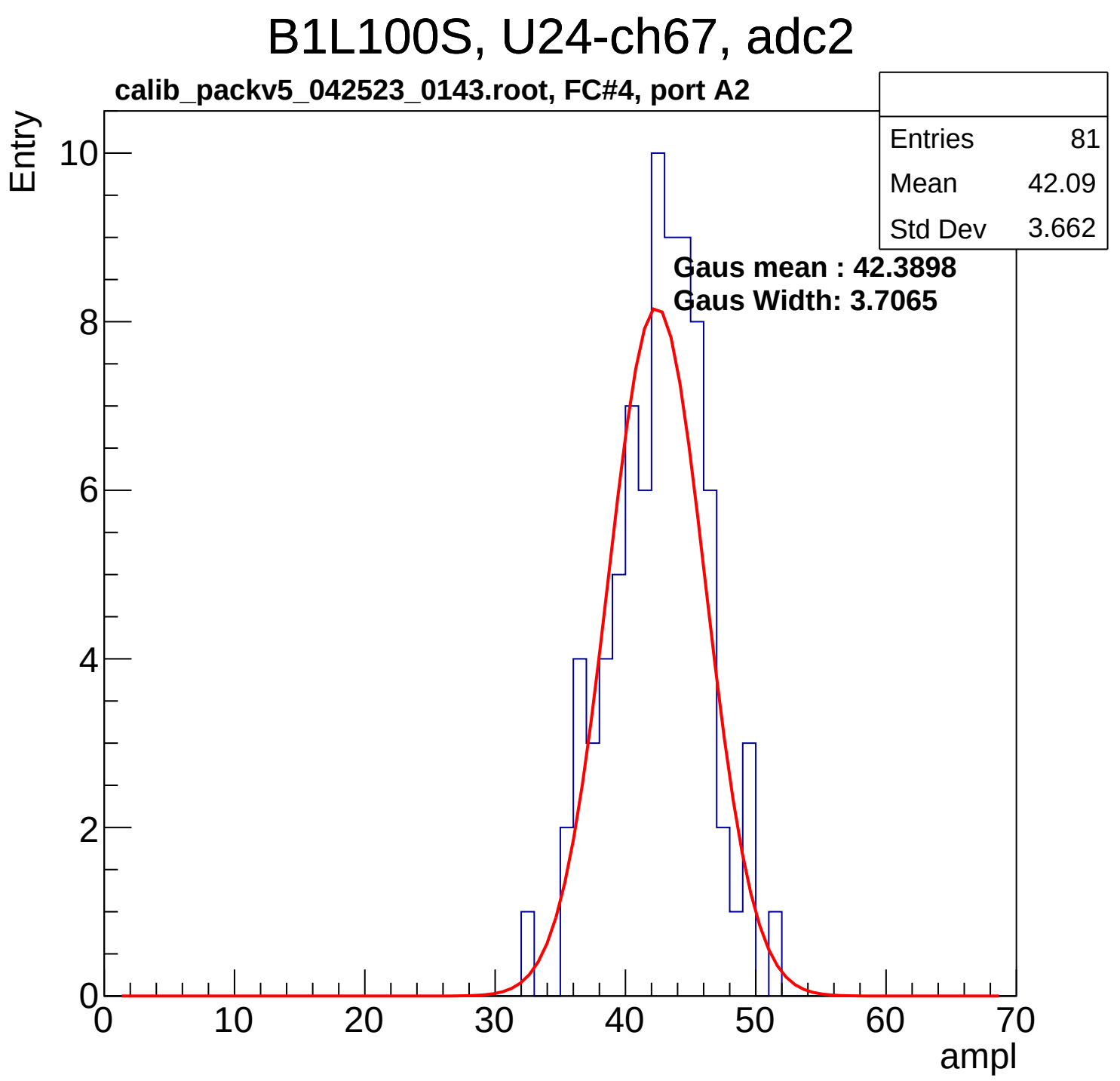
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.17
Std Dev 3.029
Gaus mean : 36.0242
Gaus Width: 3.4795
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 42.09
Std Dev 3.662
Gaus mean : 42.3898
Gaus Width: 3.7065
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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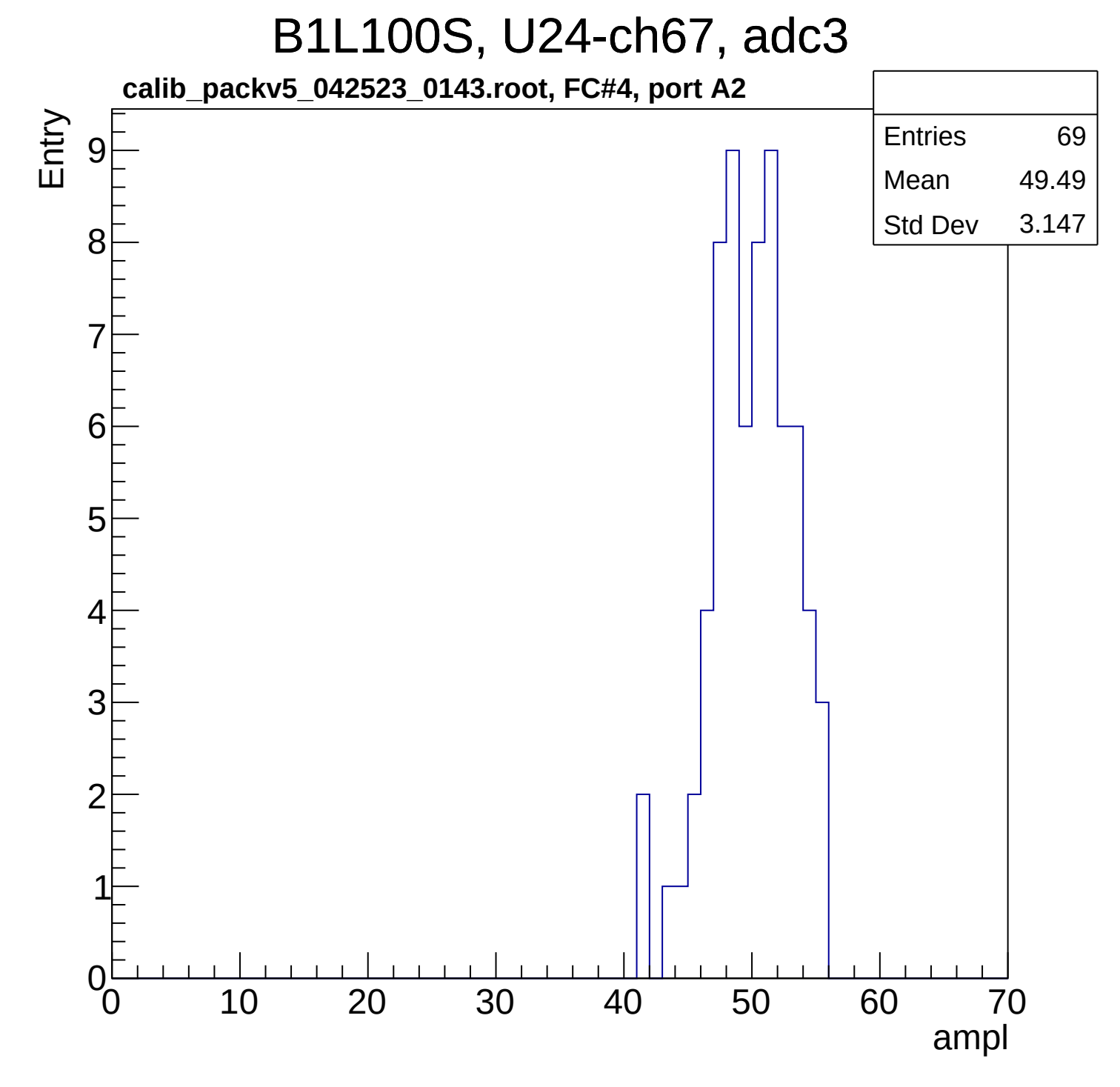
B1L100S, U24-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.49
Std Dev 3.147
Entry
ampl
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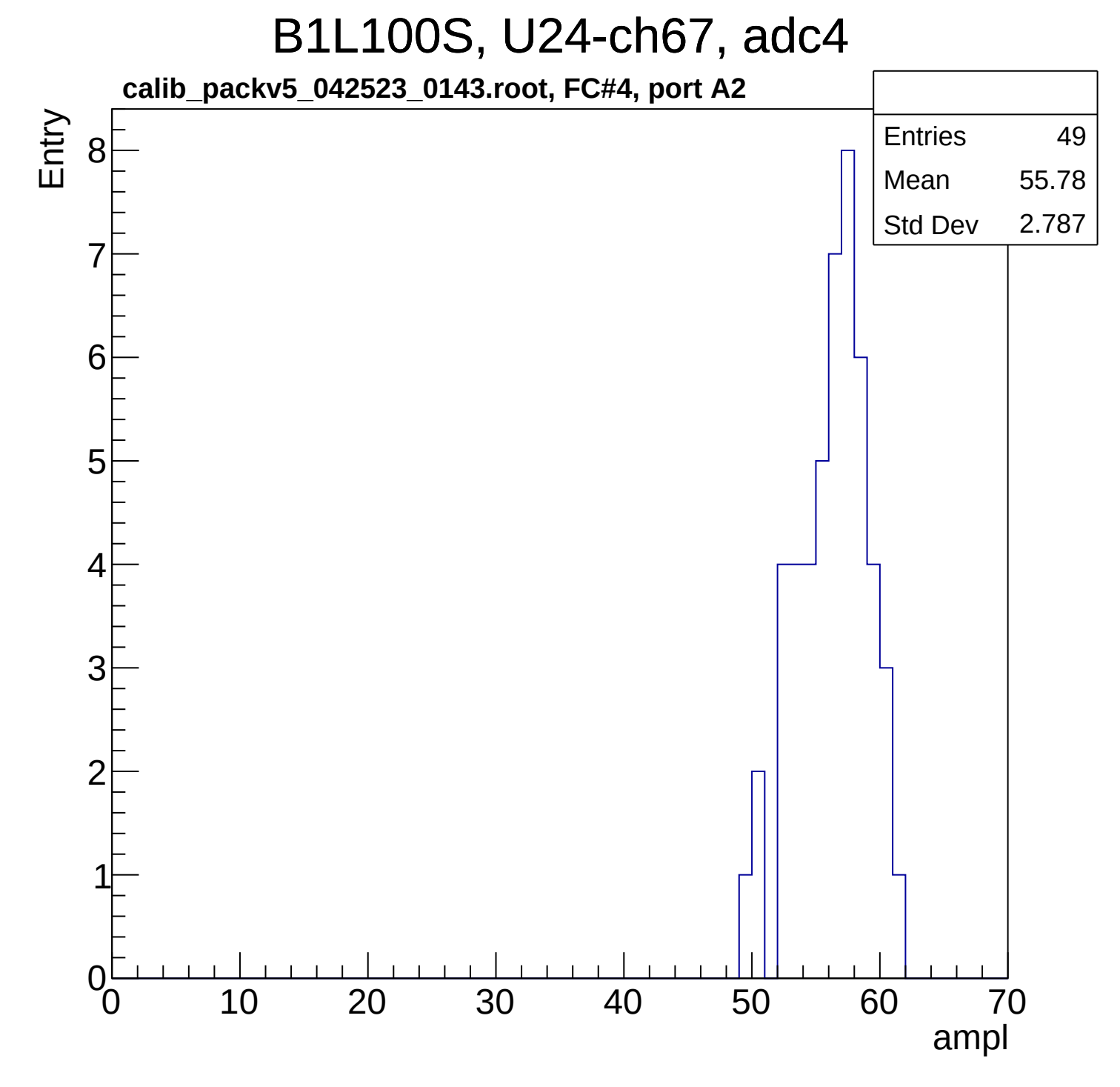
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch67, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.78
Std Dev 2.787
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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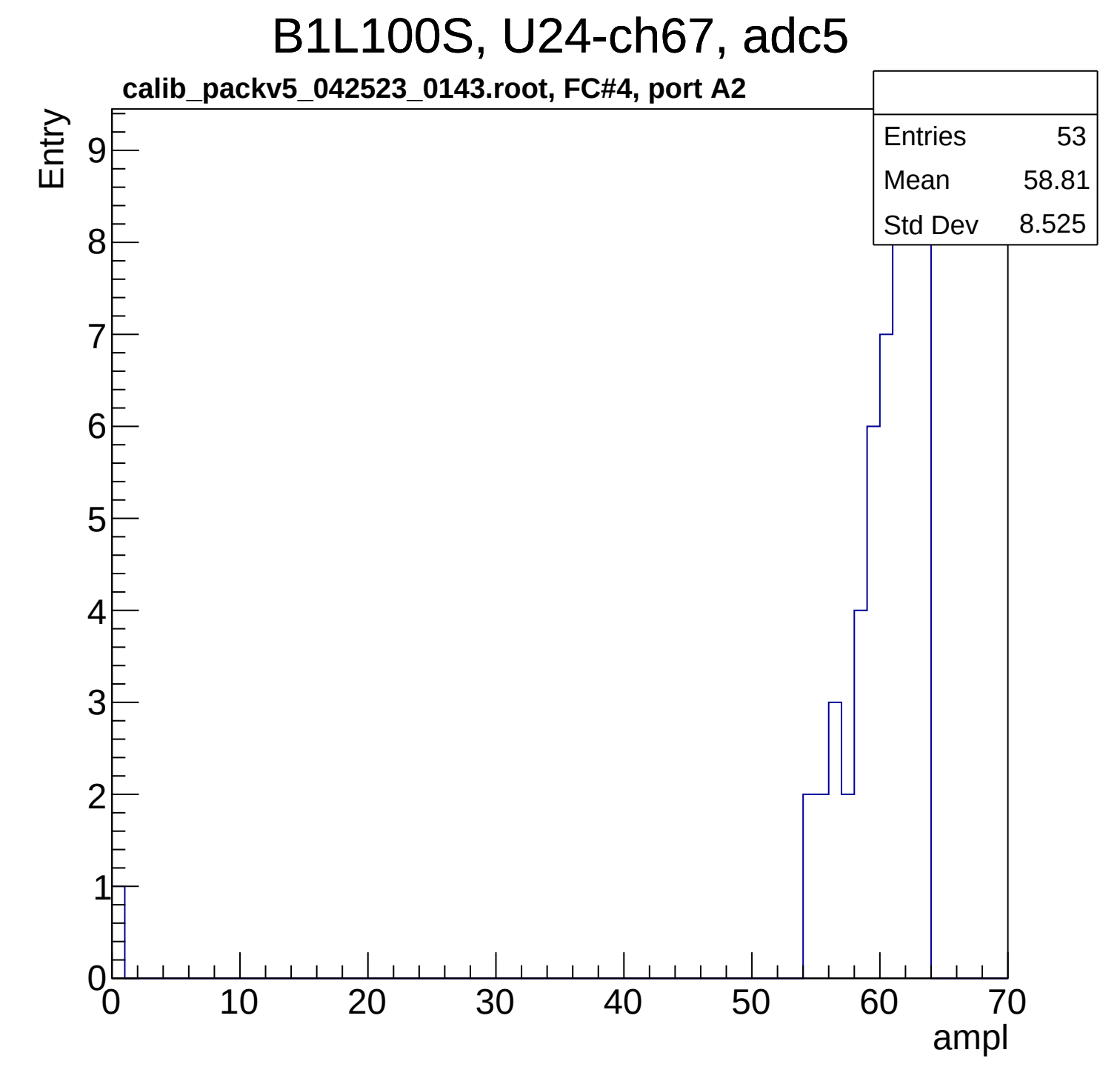
B1L100S, U24-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.81
Std Dev 8.525
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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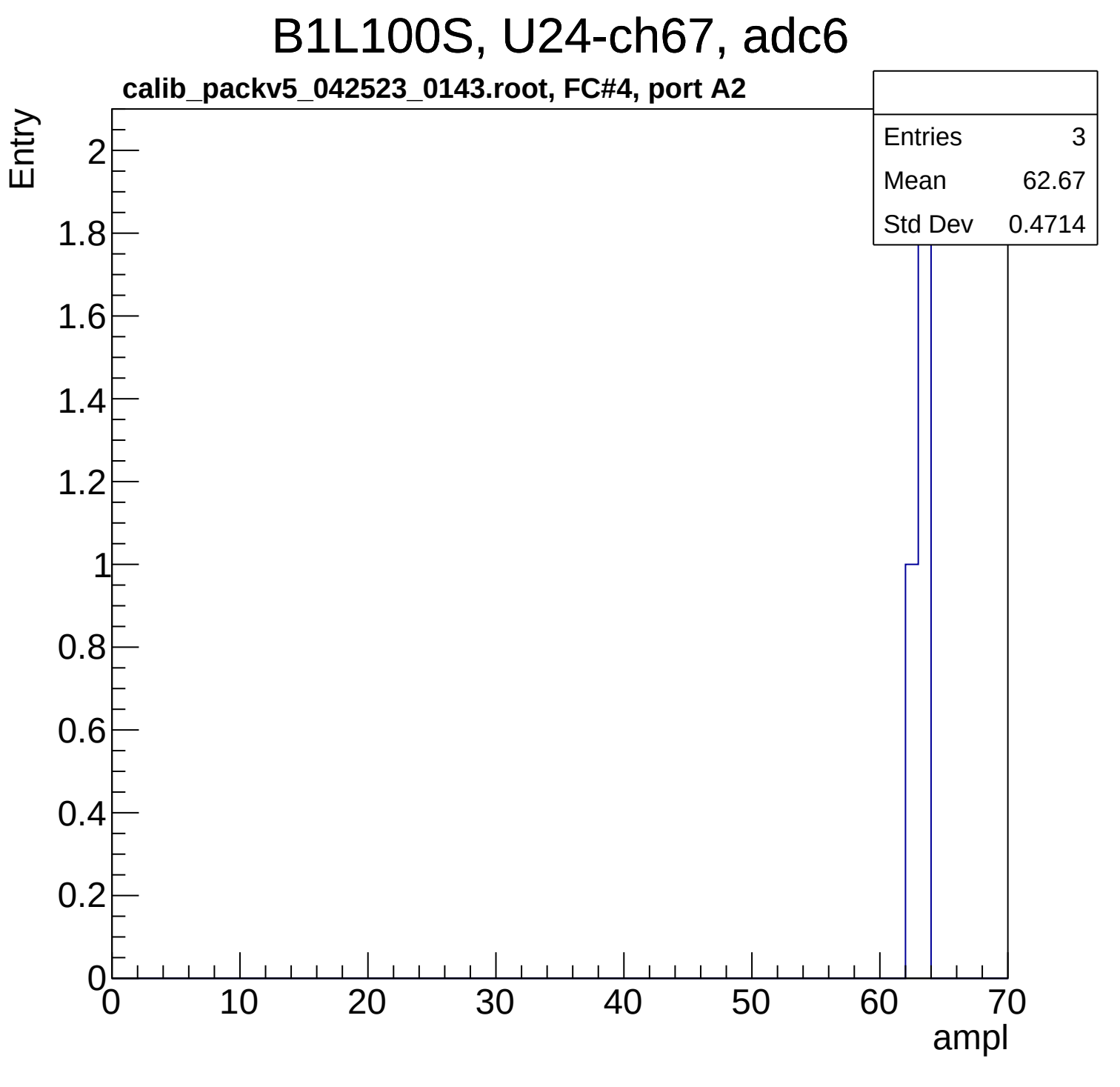
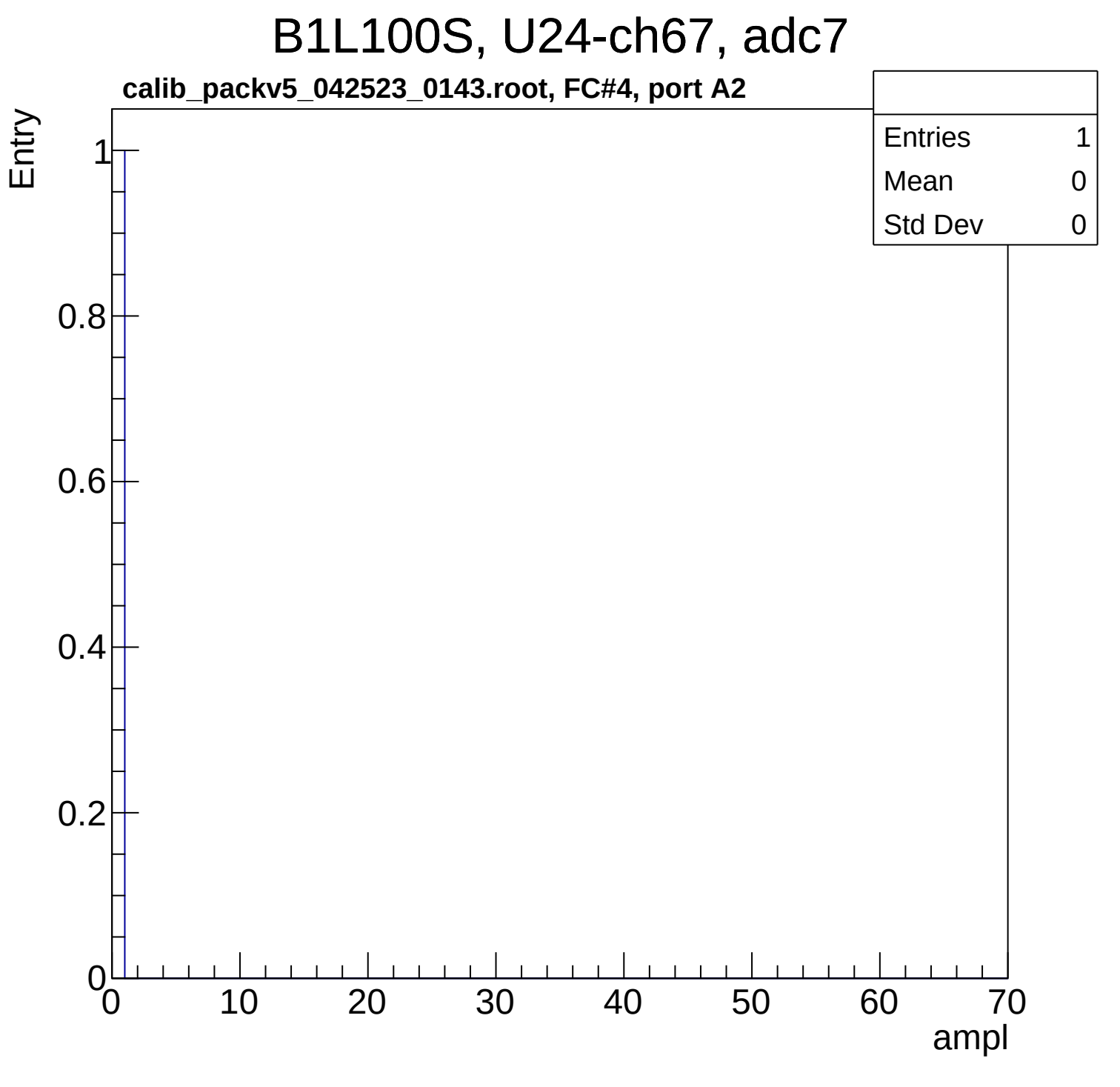

B1L100S, U24-ch67, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch67, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
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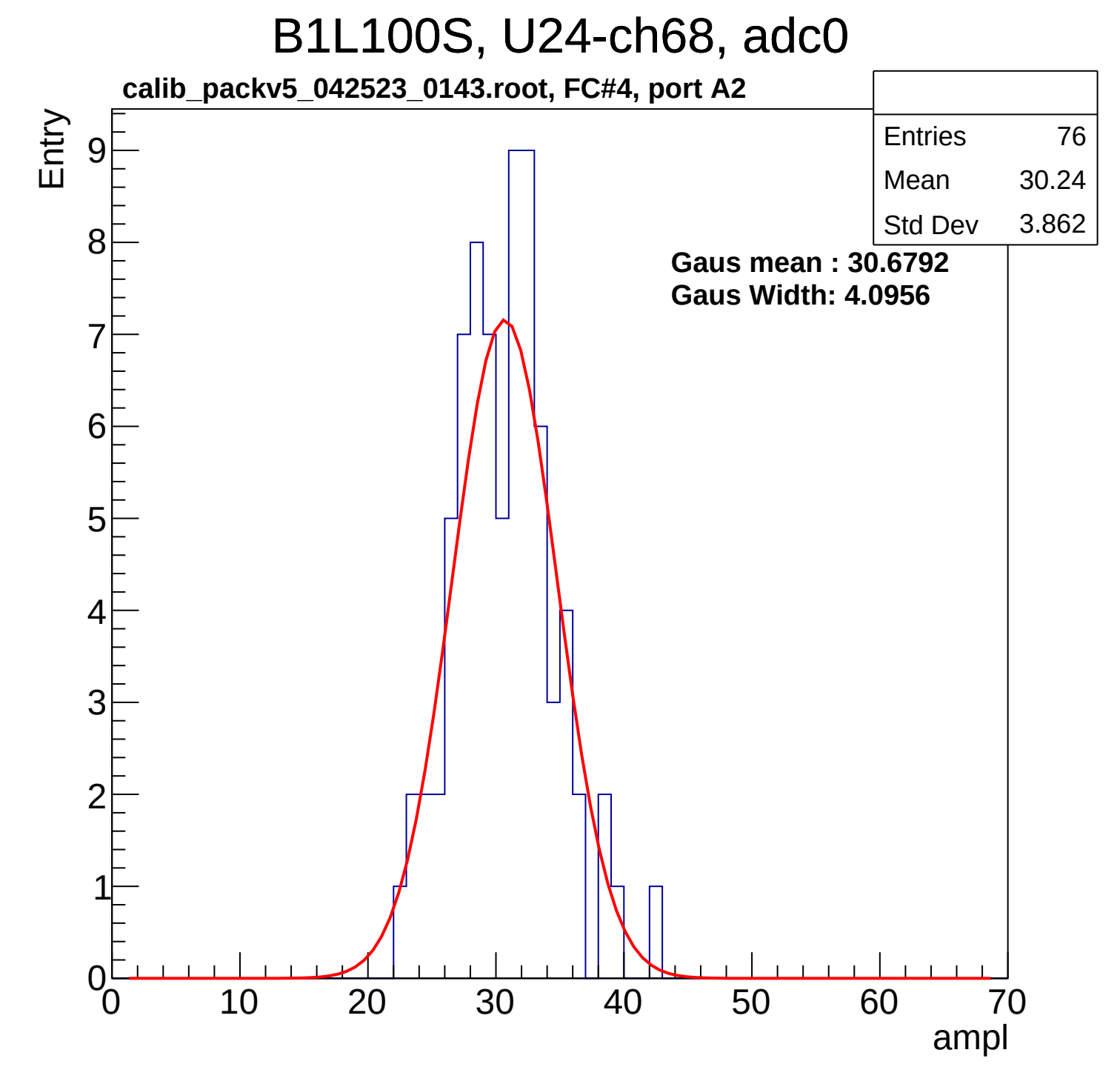
50
60
70
ampl


B1L100S, U24-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.24
Std Dev 3.862
Gaus mean : 30.6792
Gaus Width: 4.0956
Entry
ampl
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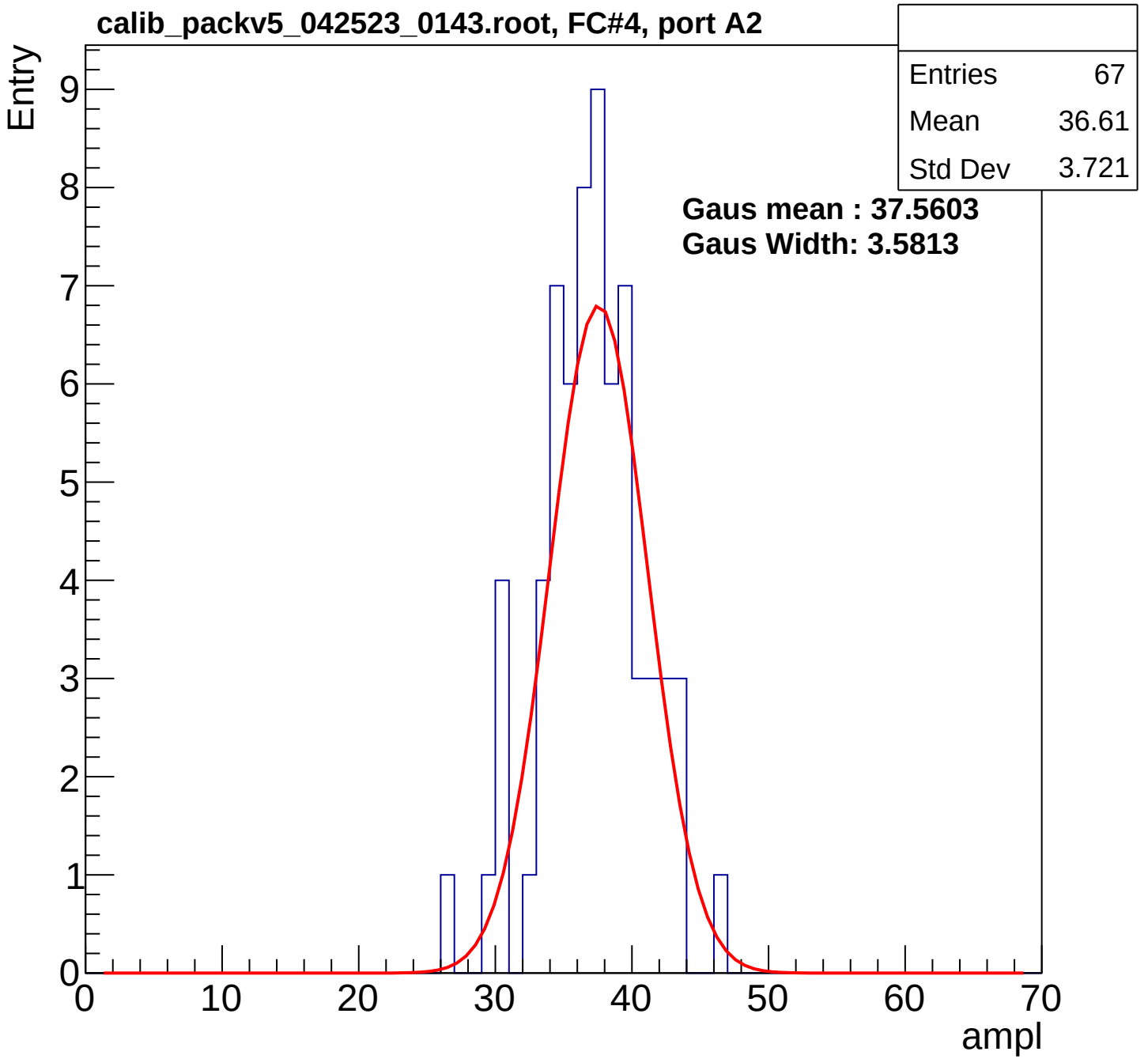
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.61
Std Dev 3.721
Gaus mean : 37.5603
Gaus Width: 3.5813
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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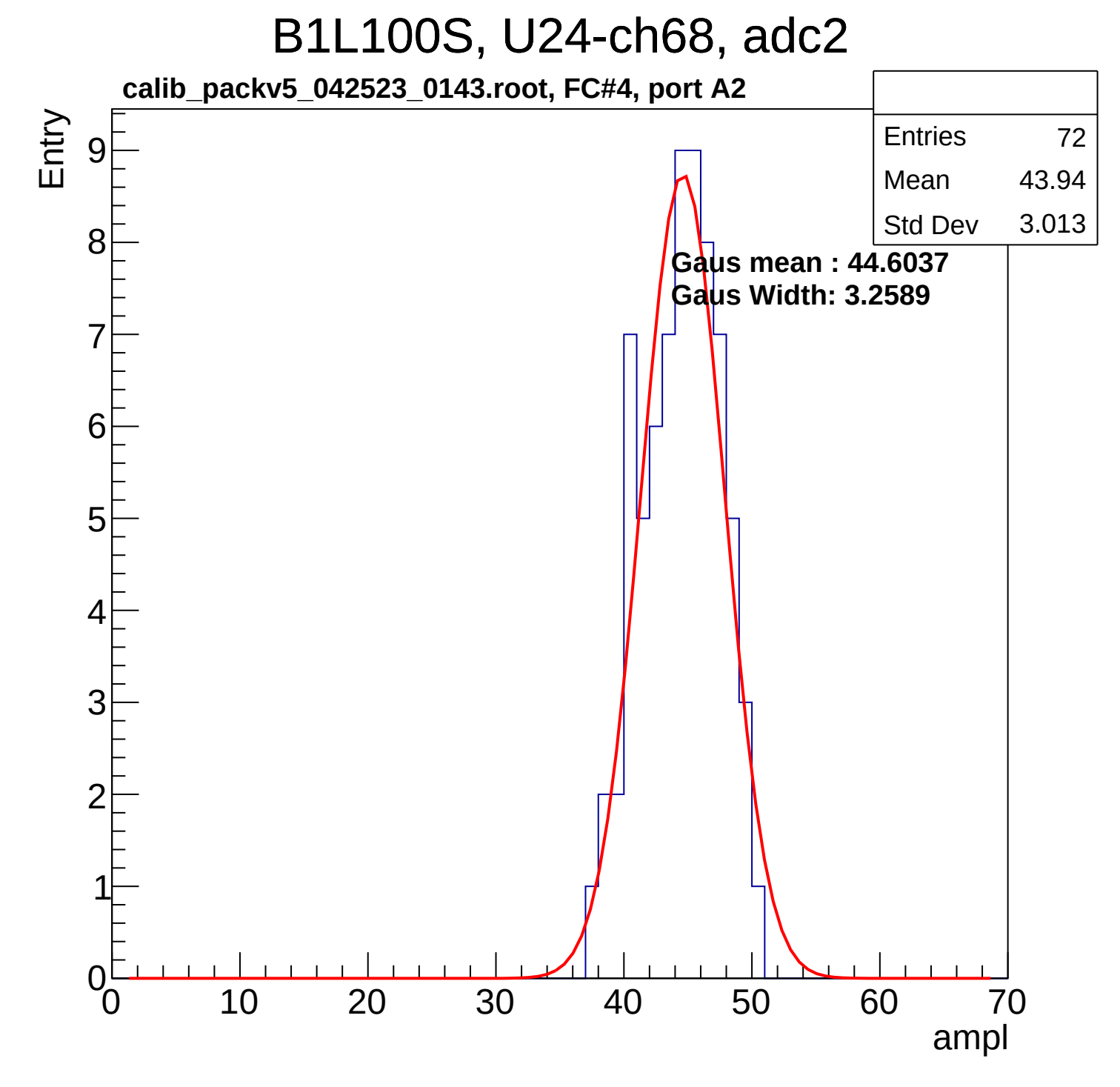
60
70


B1L100S, U24-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 43.94
Std Dev 3.013
Gaus mean : 44.6037
Gaus Width: 3.2589
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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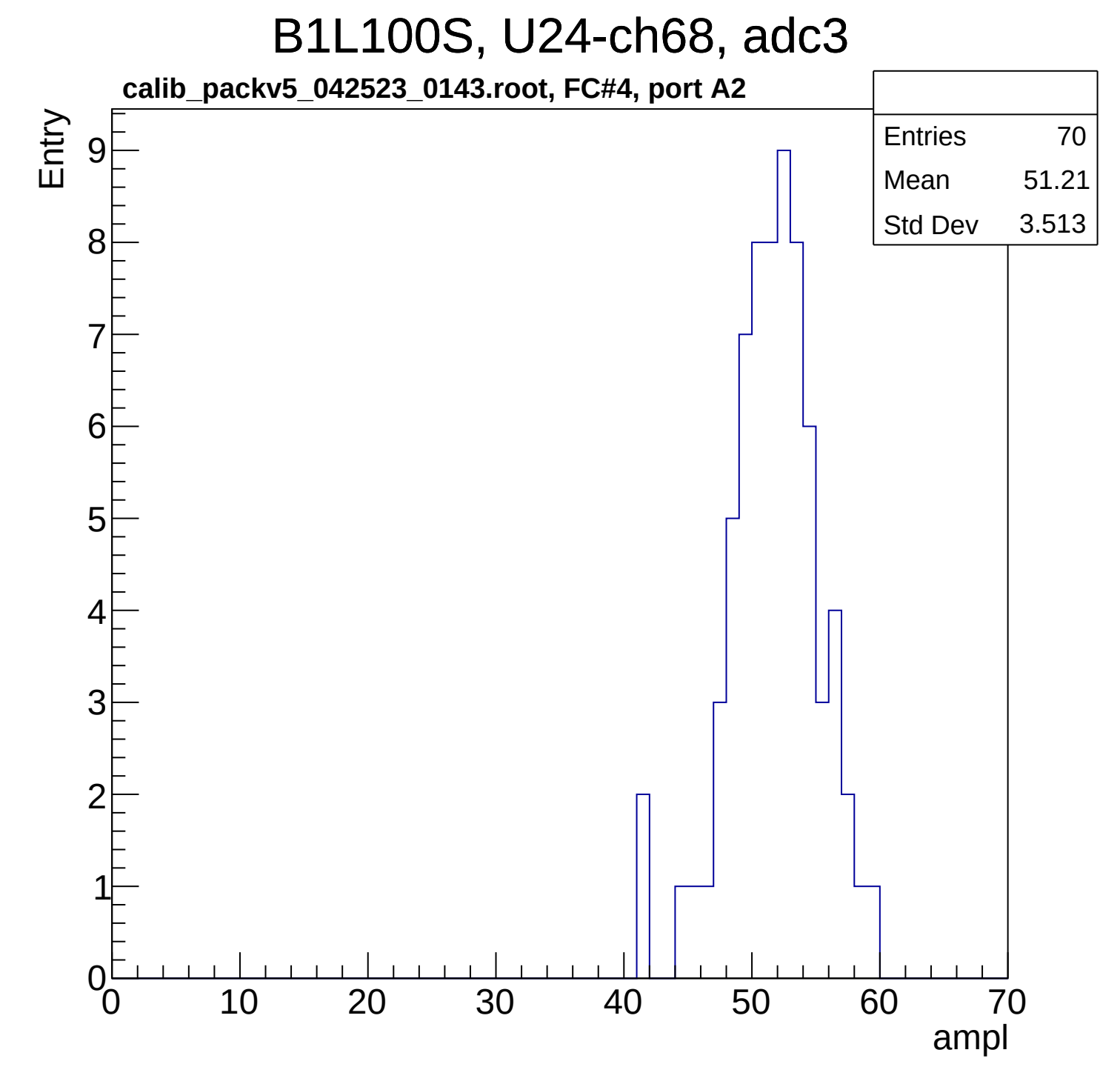
B1L100S, U24-ch68, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 51.21
Std Dev 3.513
Entry
ampl
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7
8
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0
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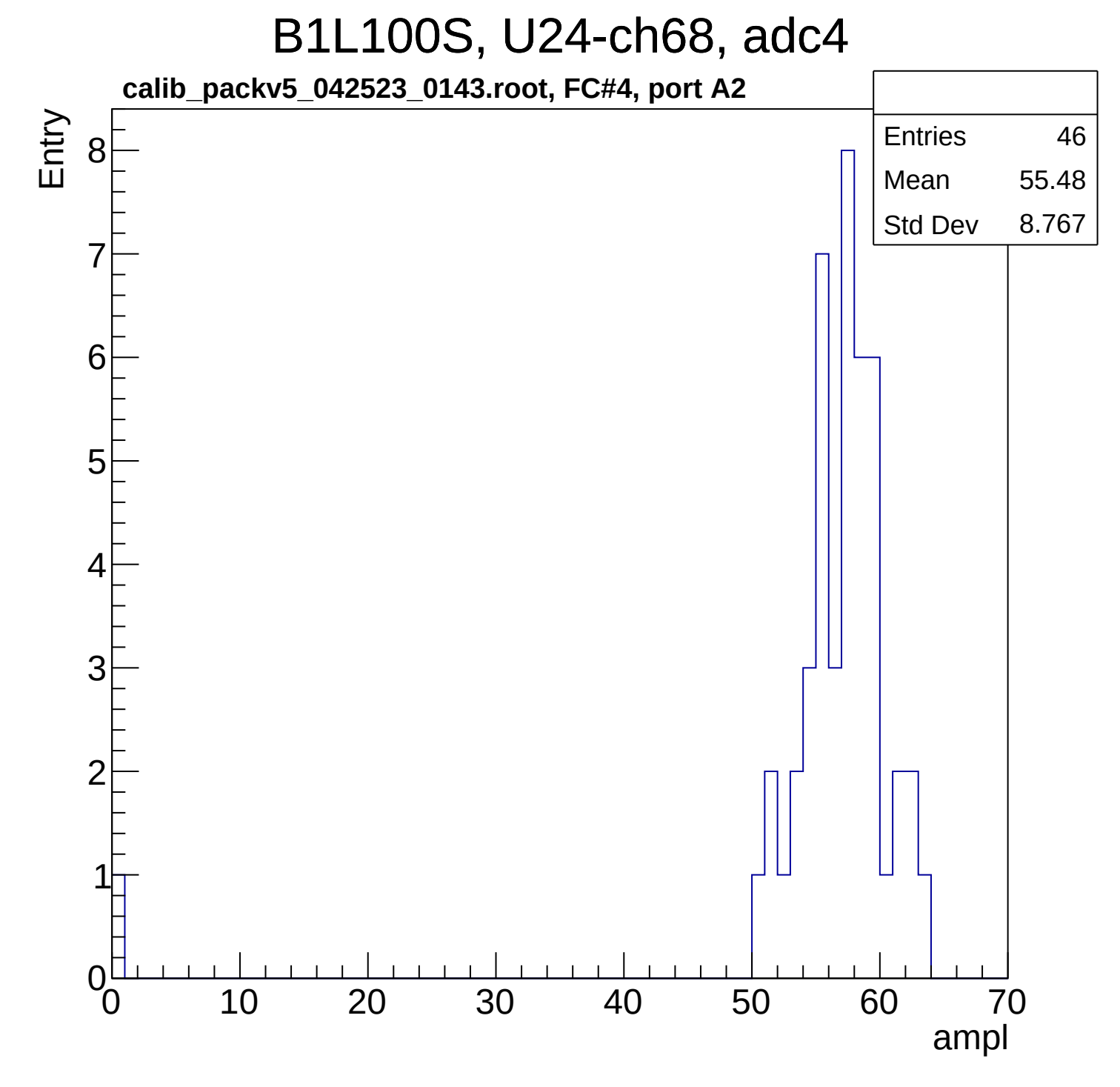
B1L100S, U24-ch68, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 55.48
Std Dev 8.767
Entry
ampl
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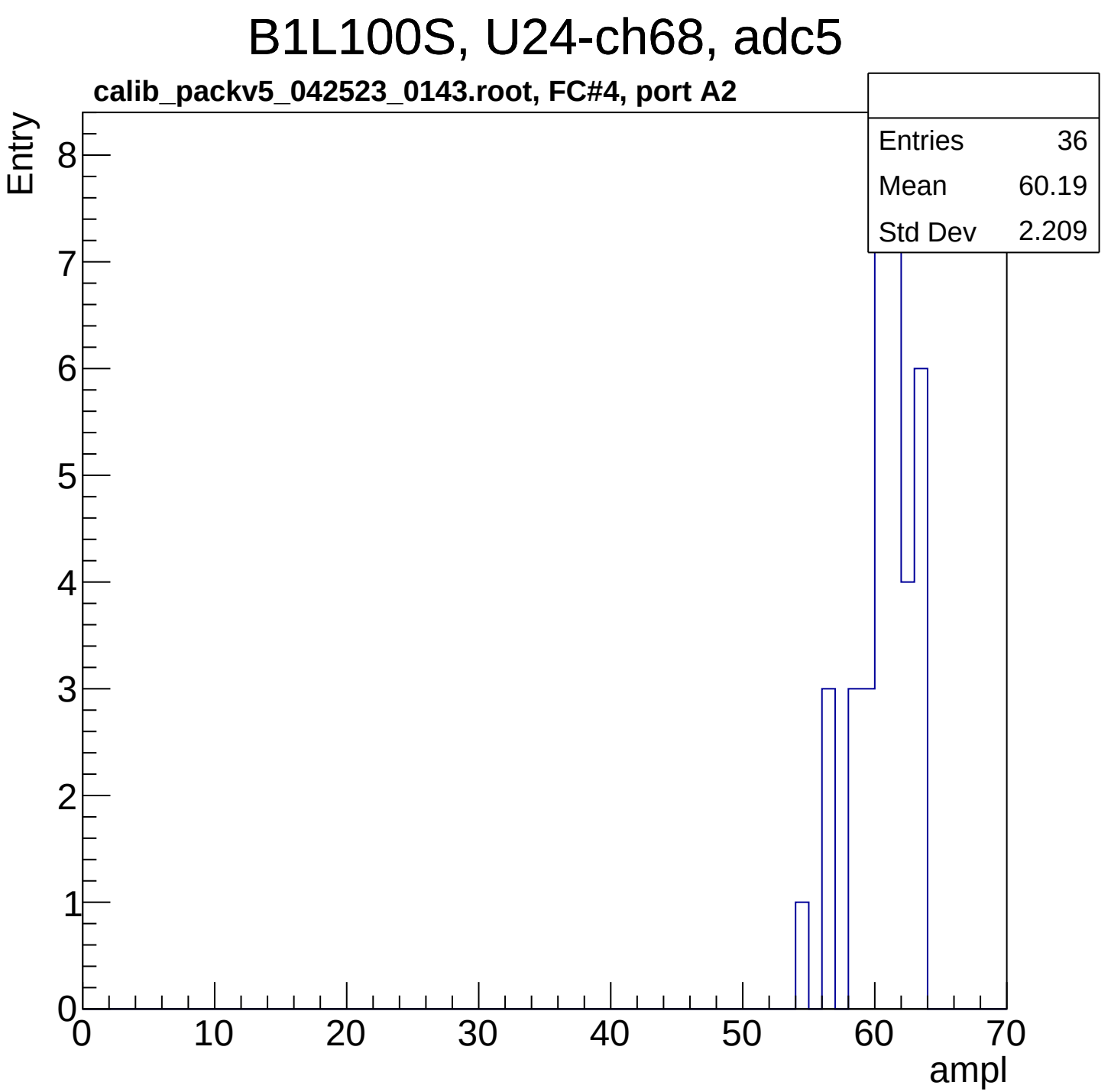
B1L100S, U24-ch68, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.19
Std Dev 2.209
Entry
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6
7
8
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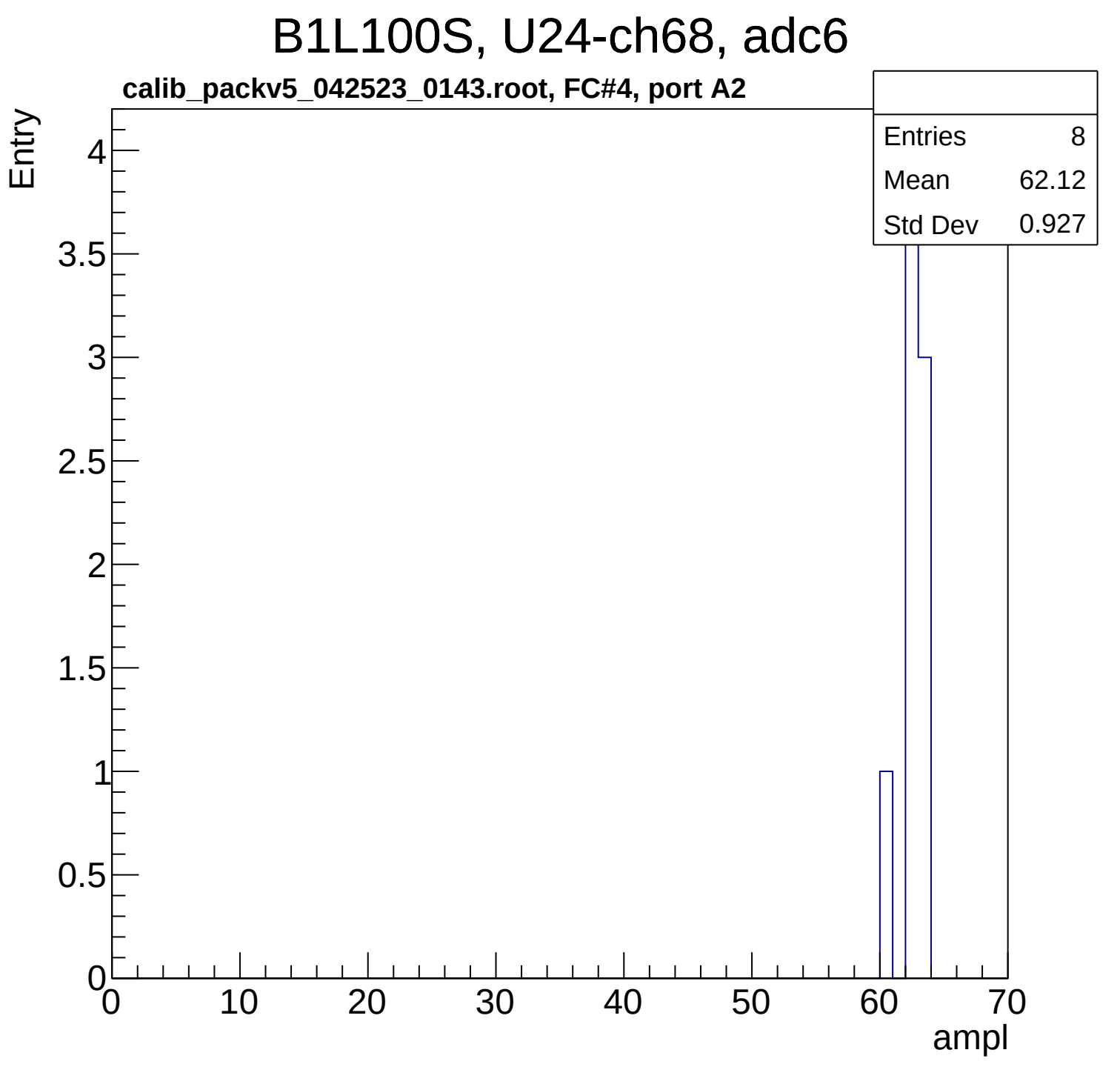
20
30
40
50
60
70
ampl


B1L100S, U24-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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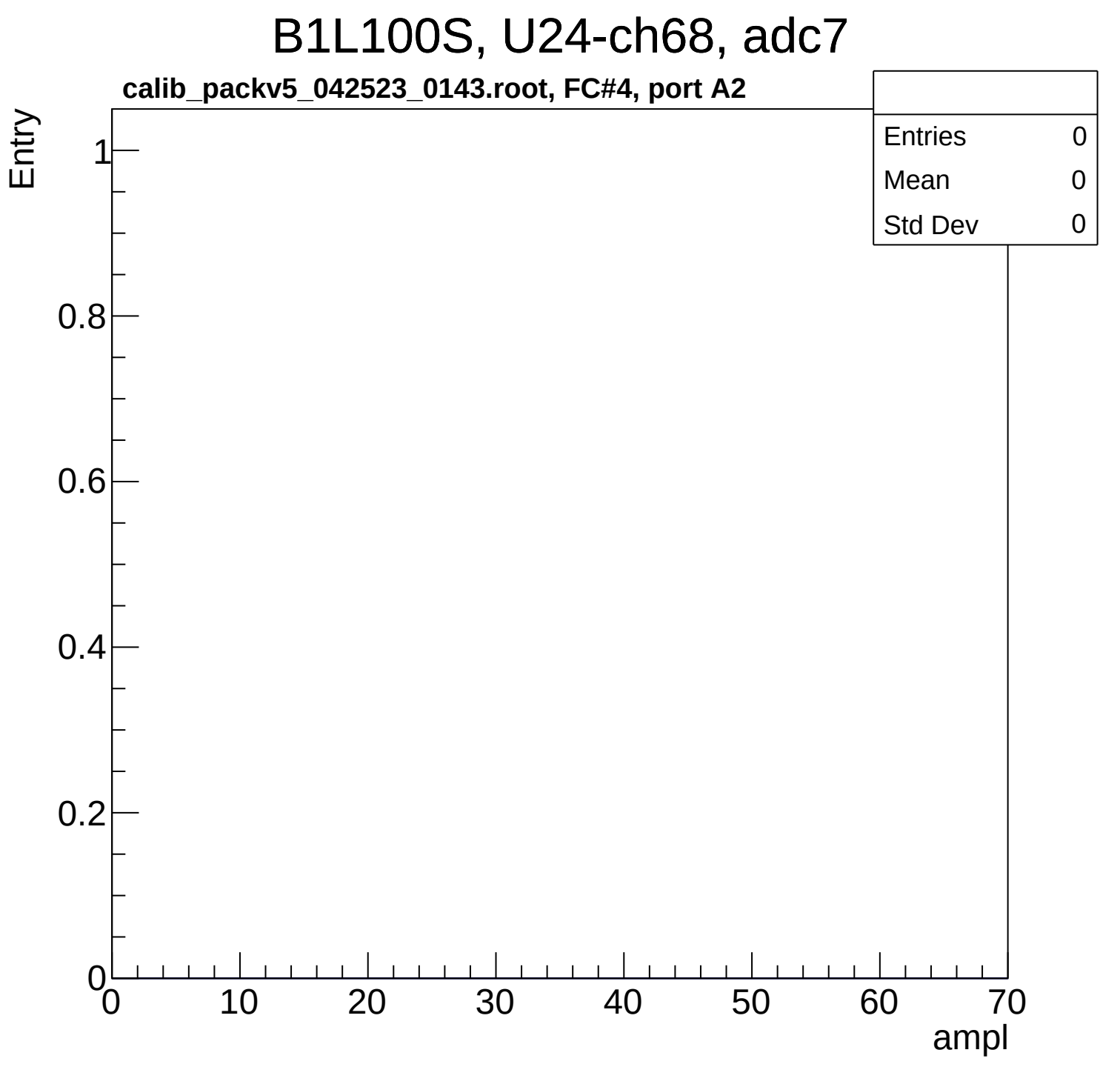
40
50
60
70


B1L100S, U24-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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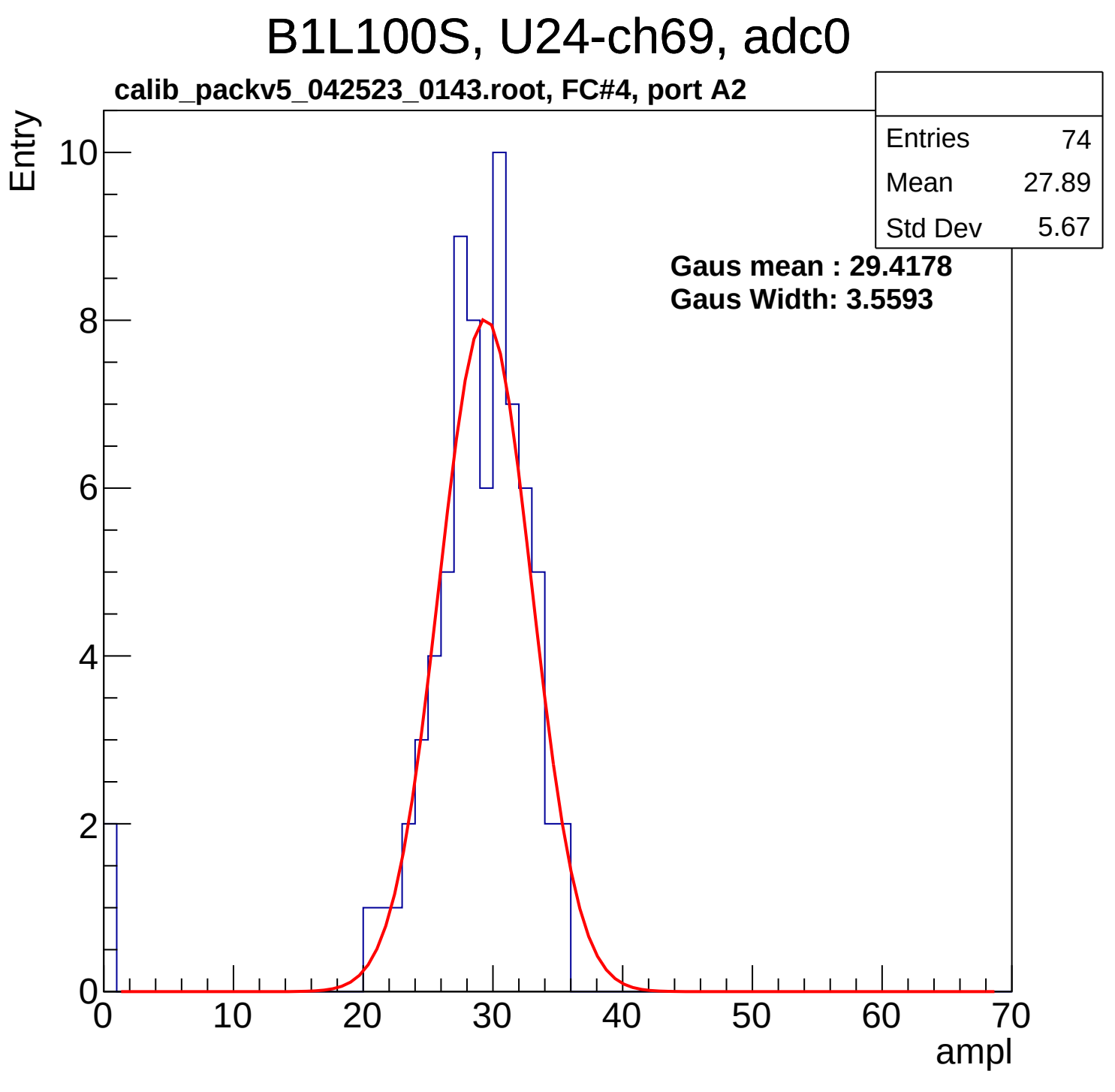
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 27.89
Std Dev 5.67
Gaus mean : 29.4178
Gaus Width: 3.5593
Entry
ampl
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4
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0
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40
50
60
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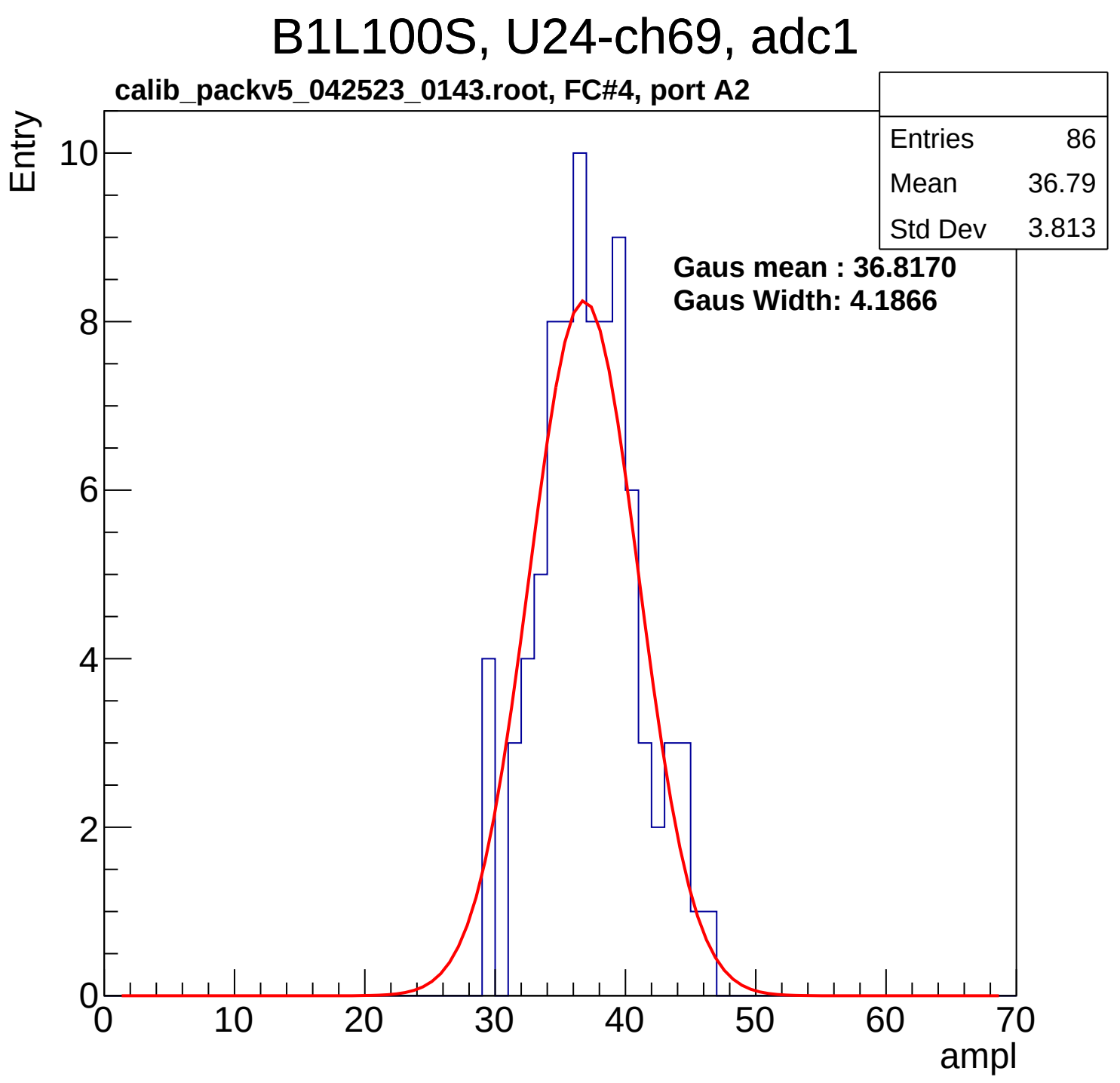
B1L100S, U24-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 36.79
Std Dev 3.813
Gaus mean : 36.8170
Gaus Width: 4.1866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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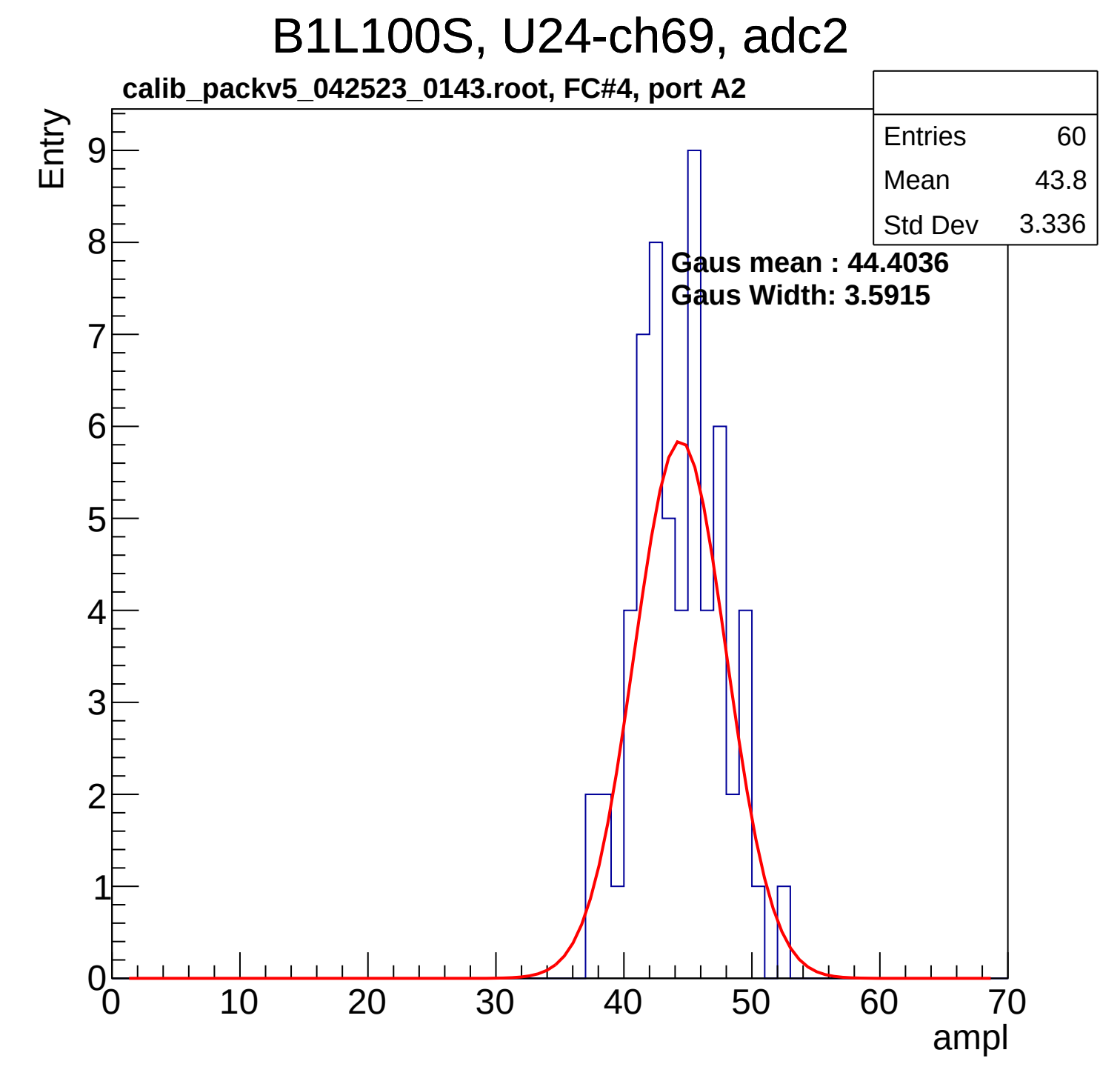
B1L100S, U24-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.8
Std Dev 3.336
Gaus mean : 44.4036
Gaus Width: 3.5915
Entry
ampl
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7
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0
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60
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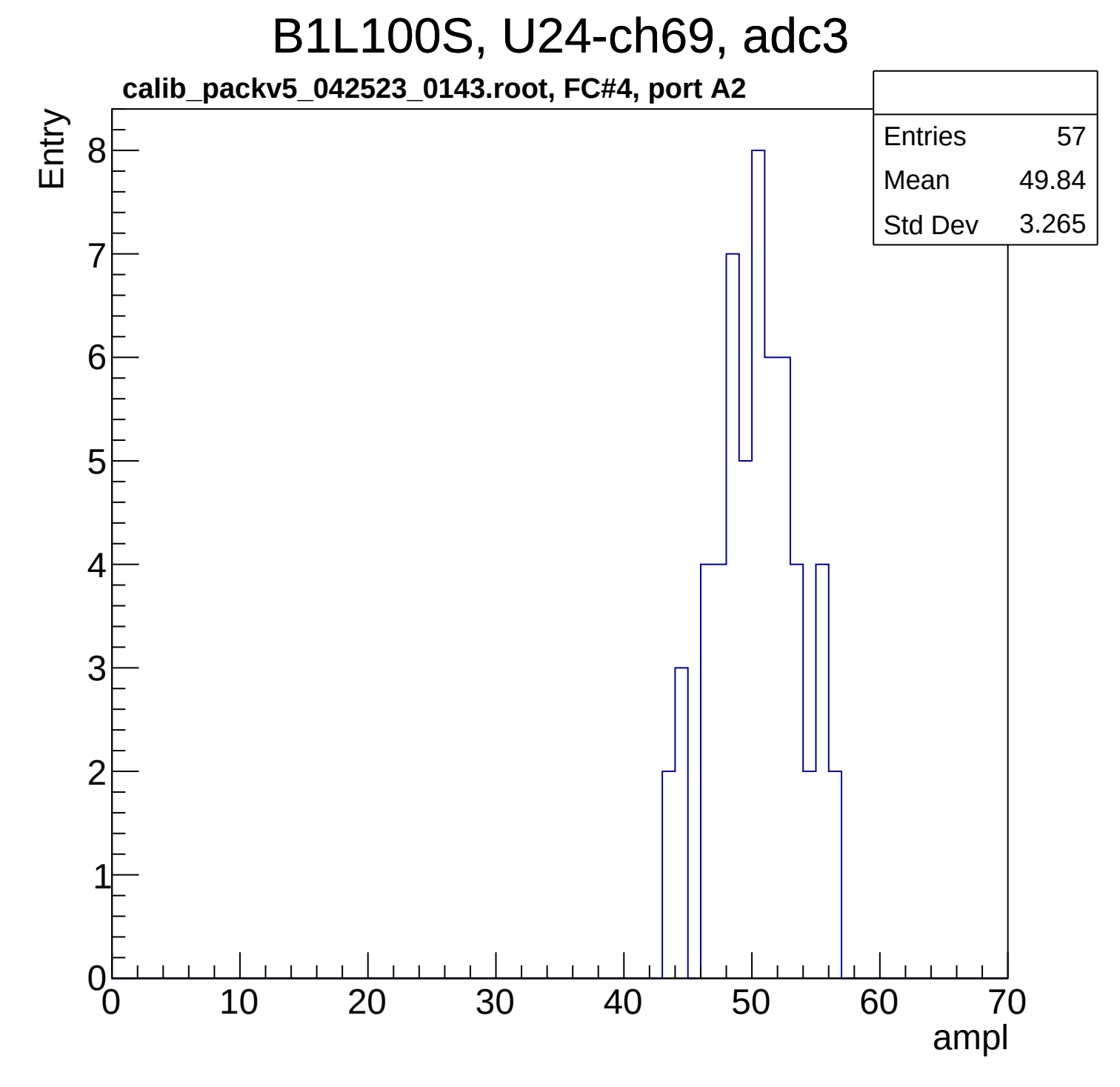
B1L100S, U24-ch69, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.84
Std Dev 3.265
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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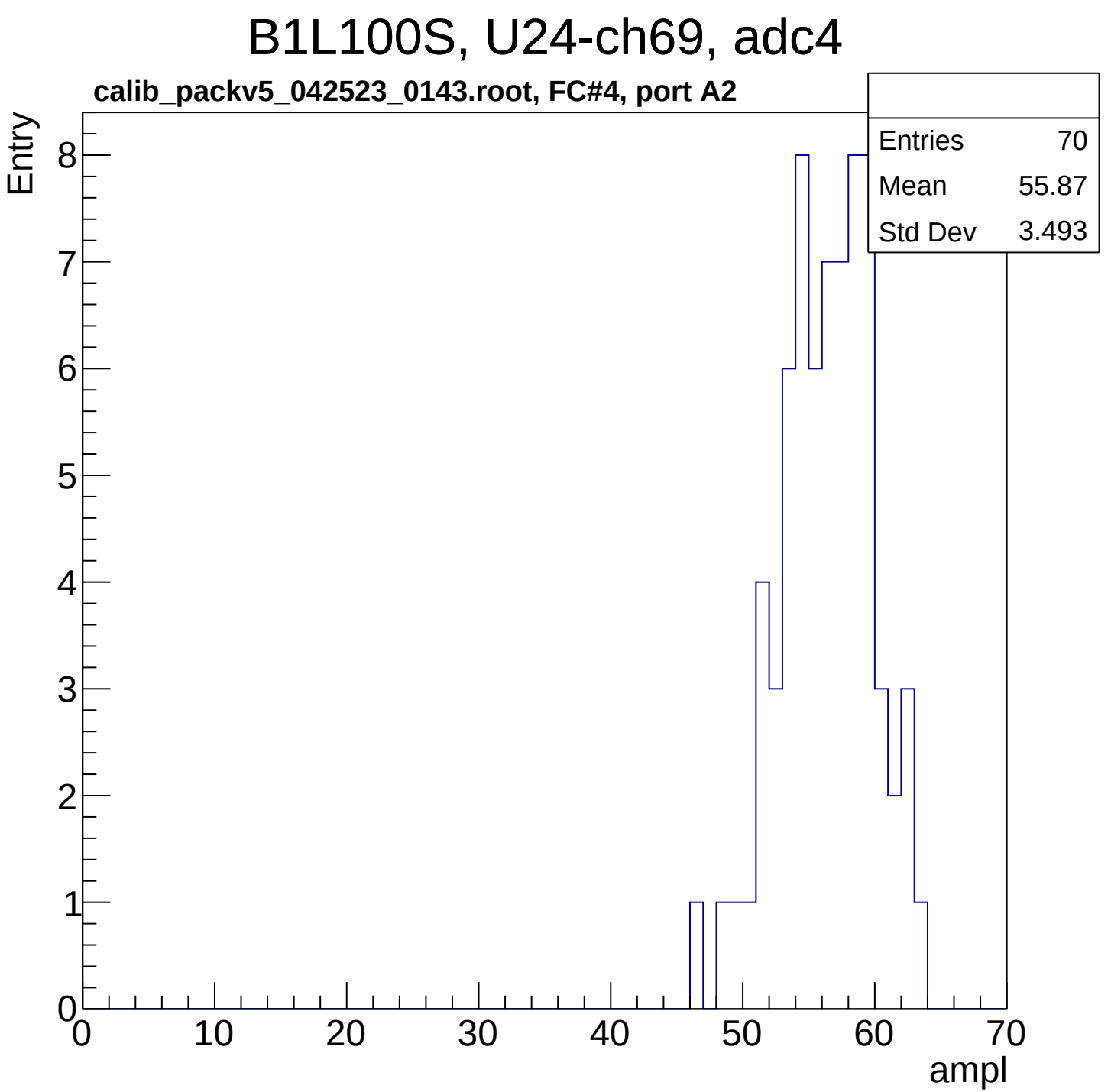
B1L100S, U24-ch69, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 55.87
Std Dev 3.493
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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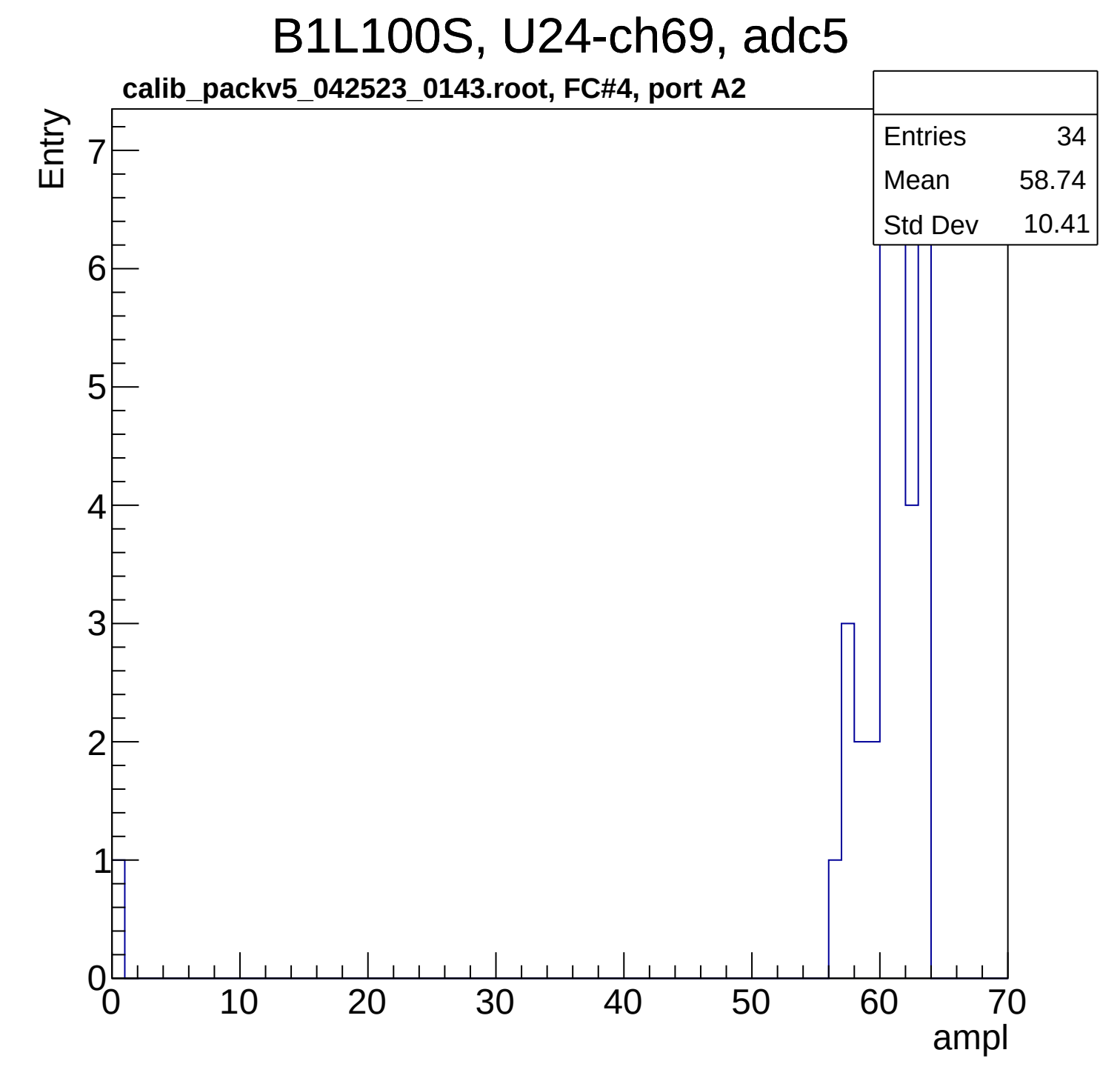
B1L100S, U24-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.74
Std Dev 10.41
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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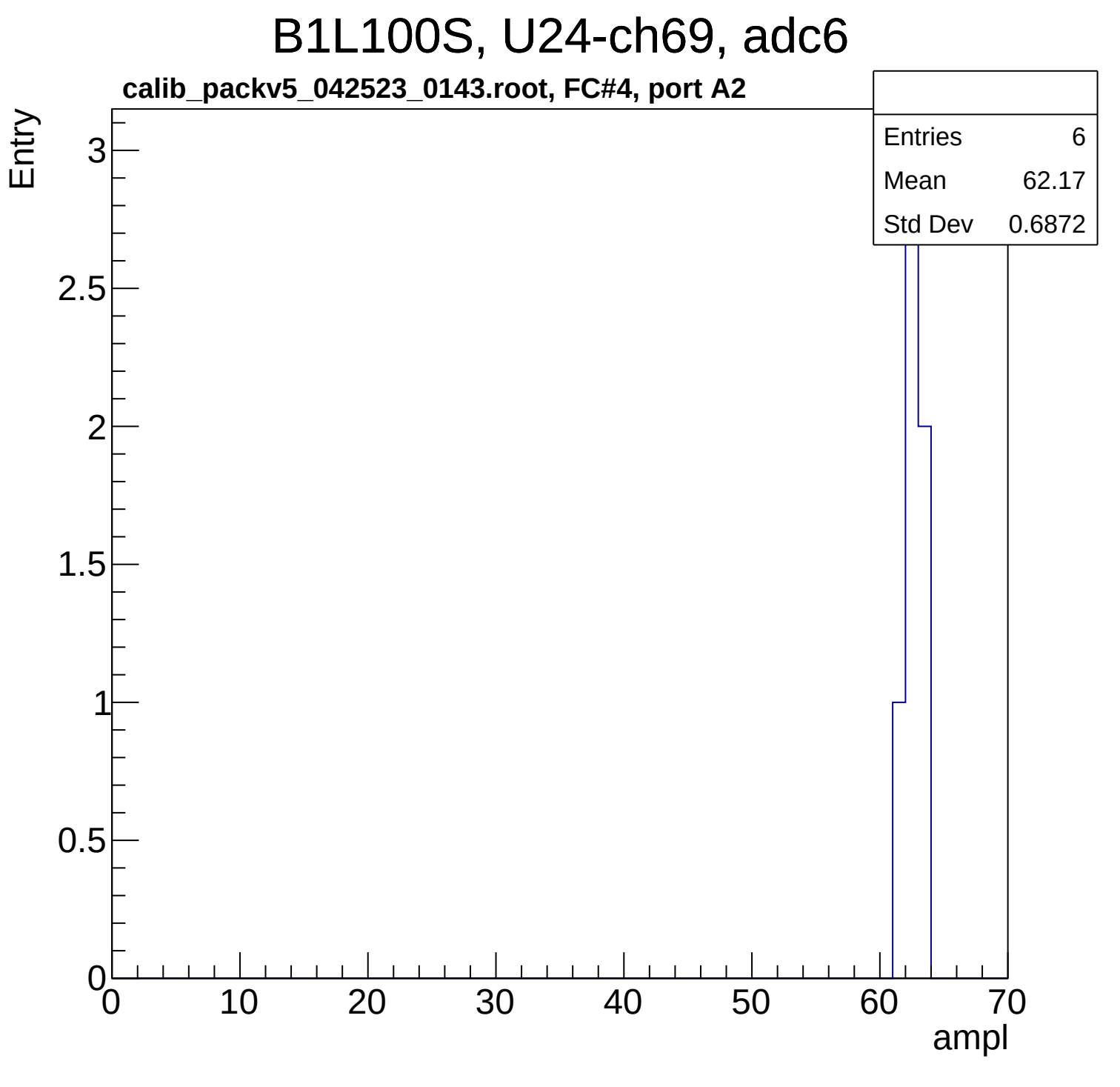
B1L100S, U24-ch69, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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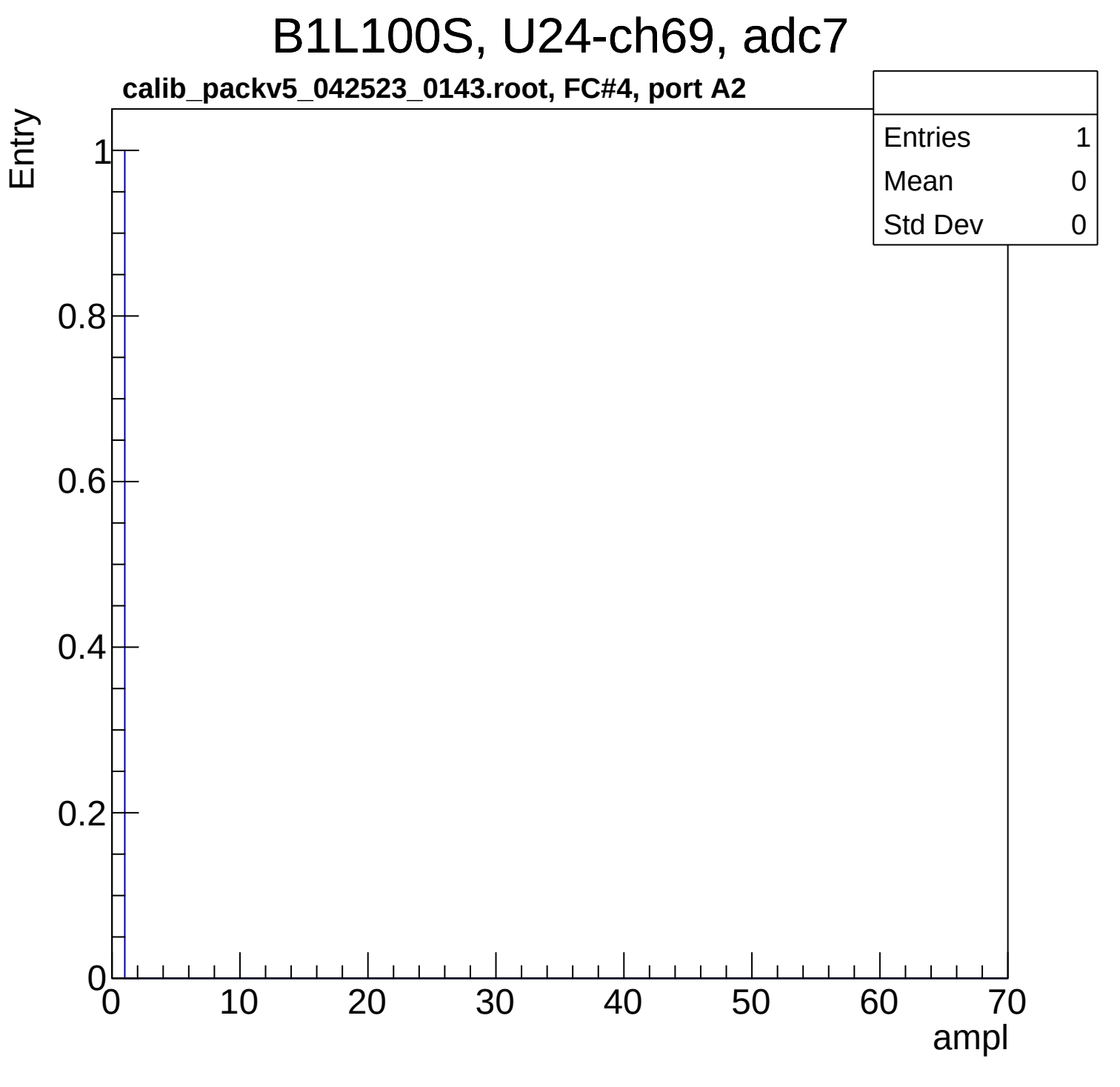
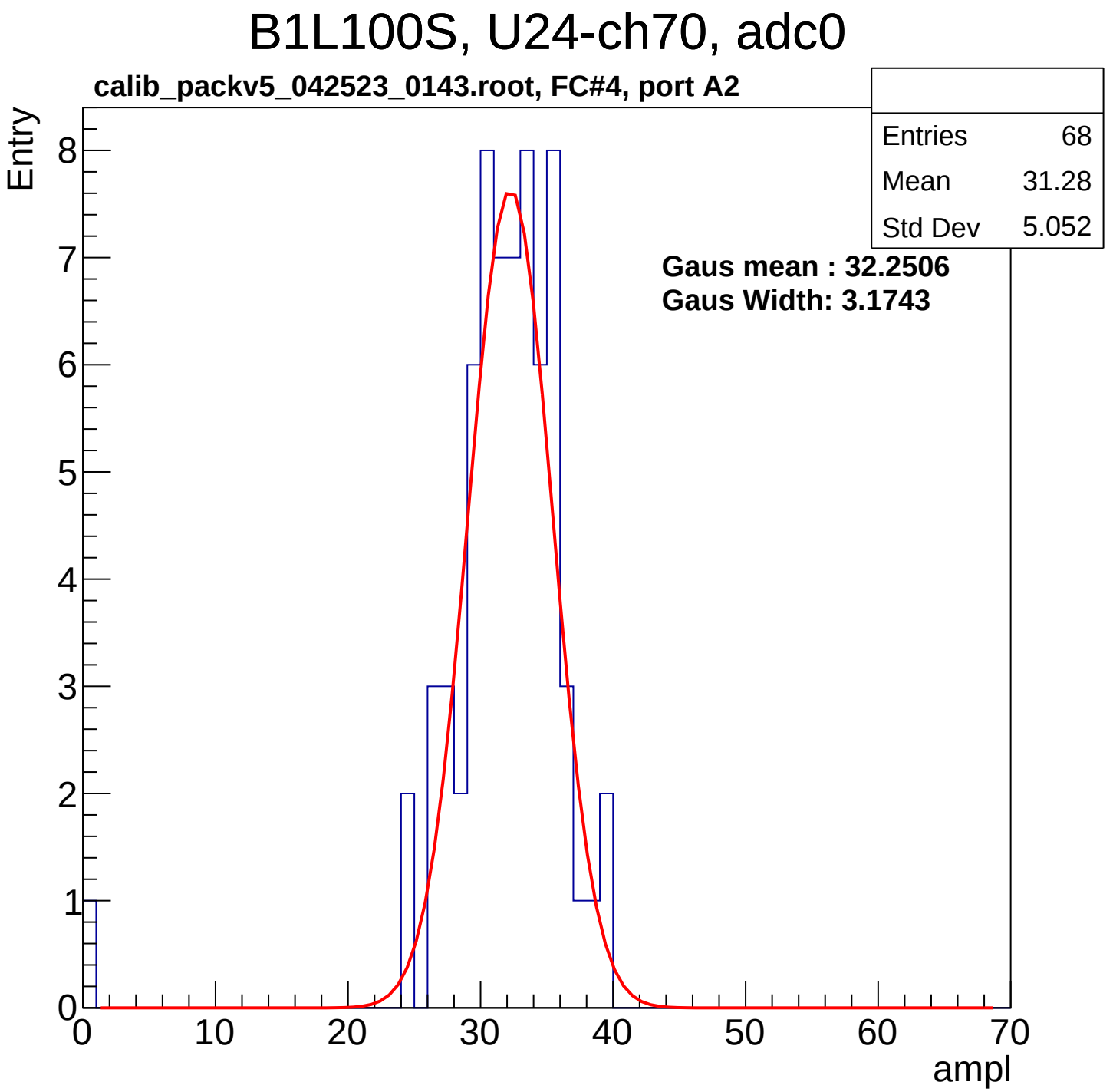

B1L100S, U24-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 31.28
Std Dev 5.052
Gaus mean : 32.2506
Gaus Width: 3.1743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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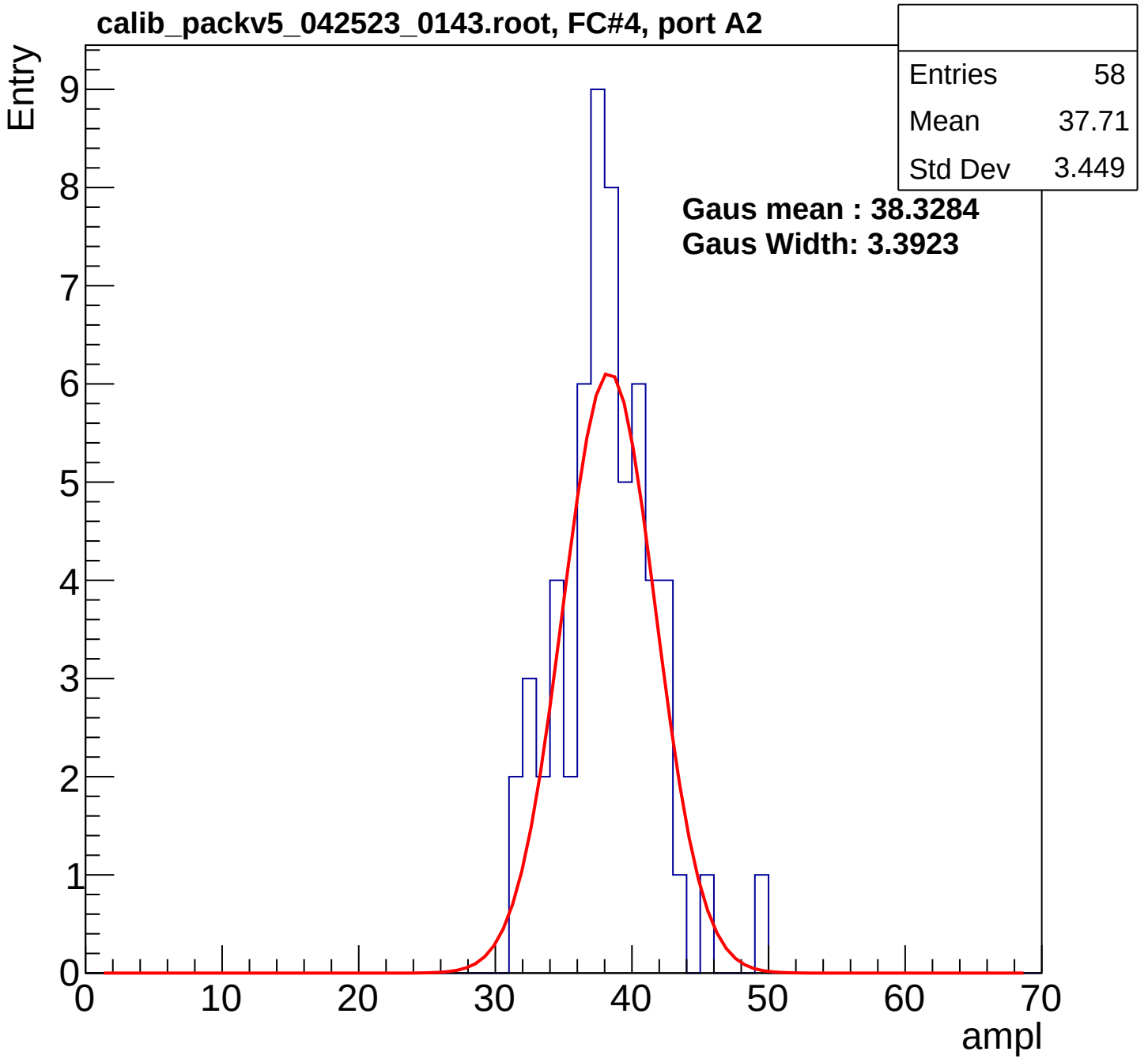
B1L100S, U24-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 37.71
Std Dev 3.449
Gaus mean : 38.3284
Gaus Width: 3.3923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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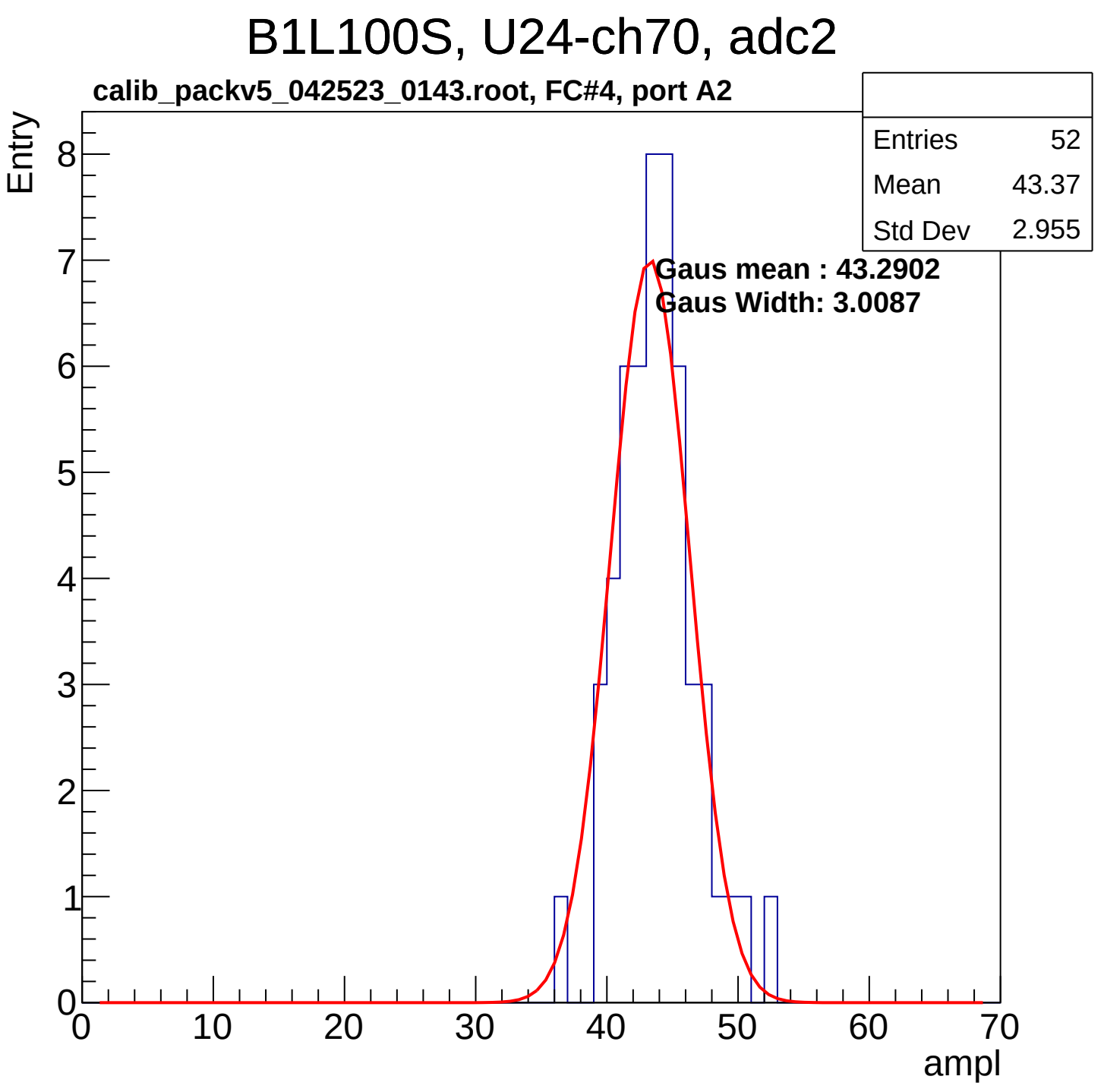
B1L100S, U24-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.37
Std Dev 2.955
Gaus mean : 43.2902
Gaus Width: 3.0087
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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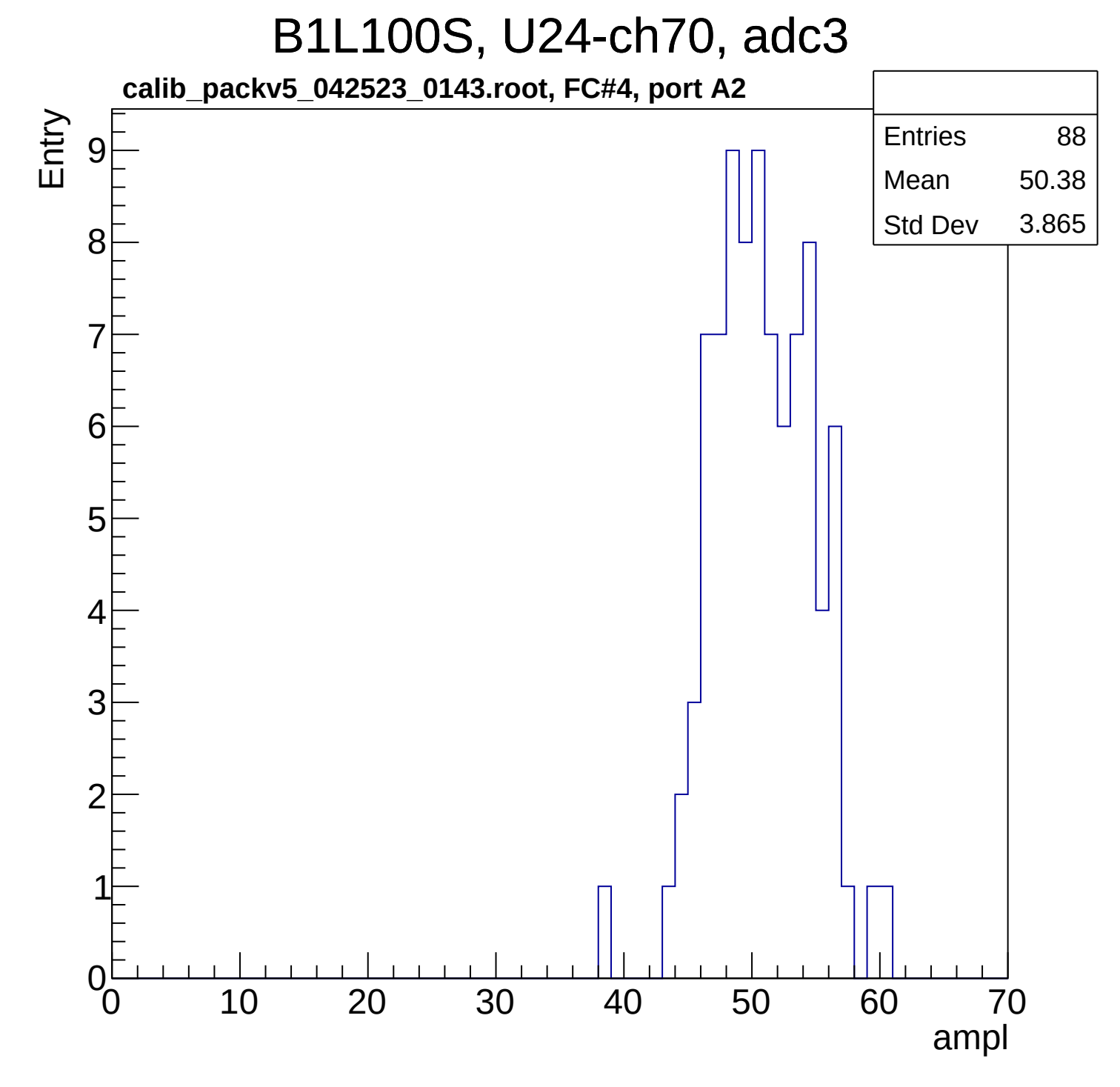
B1L100S, U24-ch70, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 50.38
Std Dev 3.865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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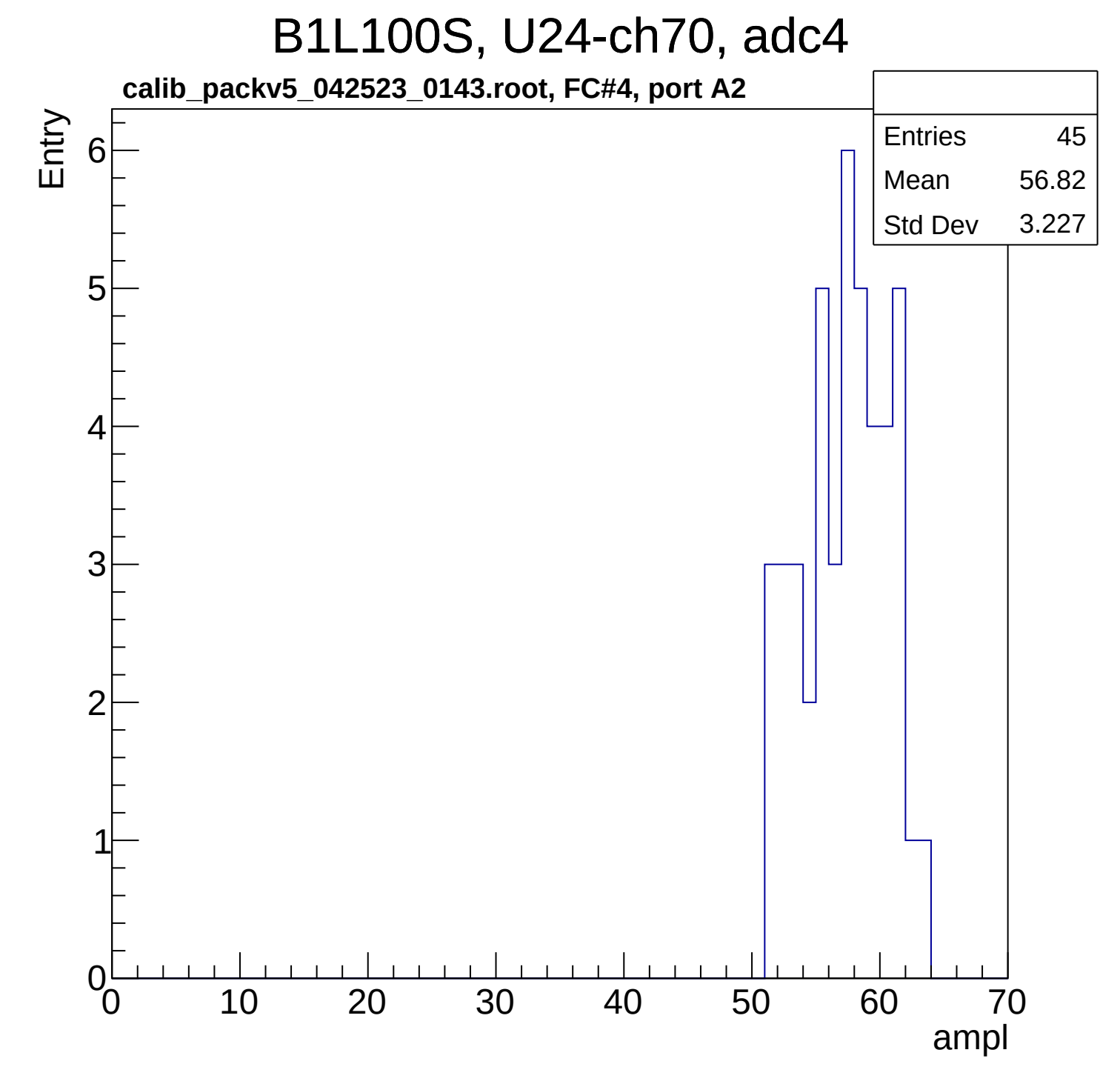
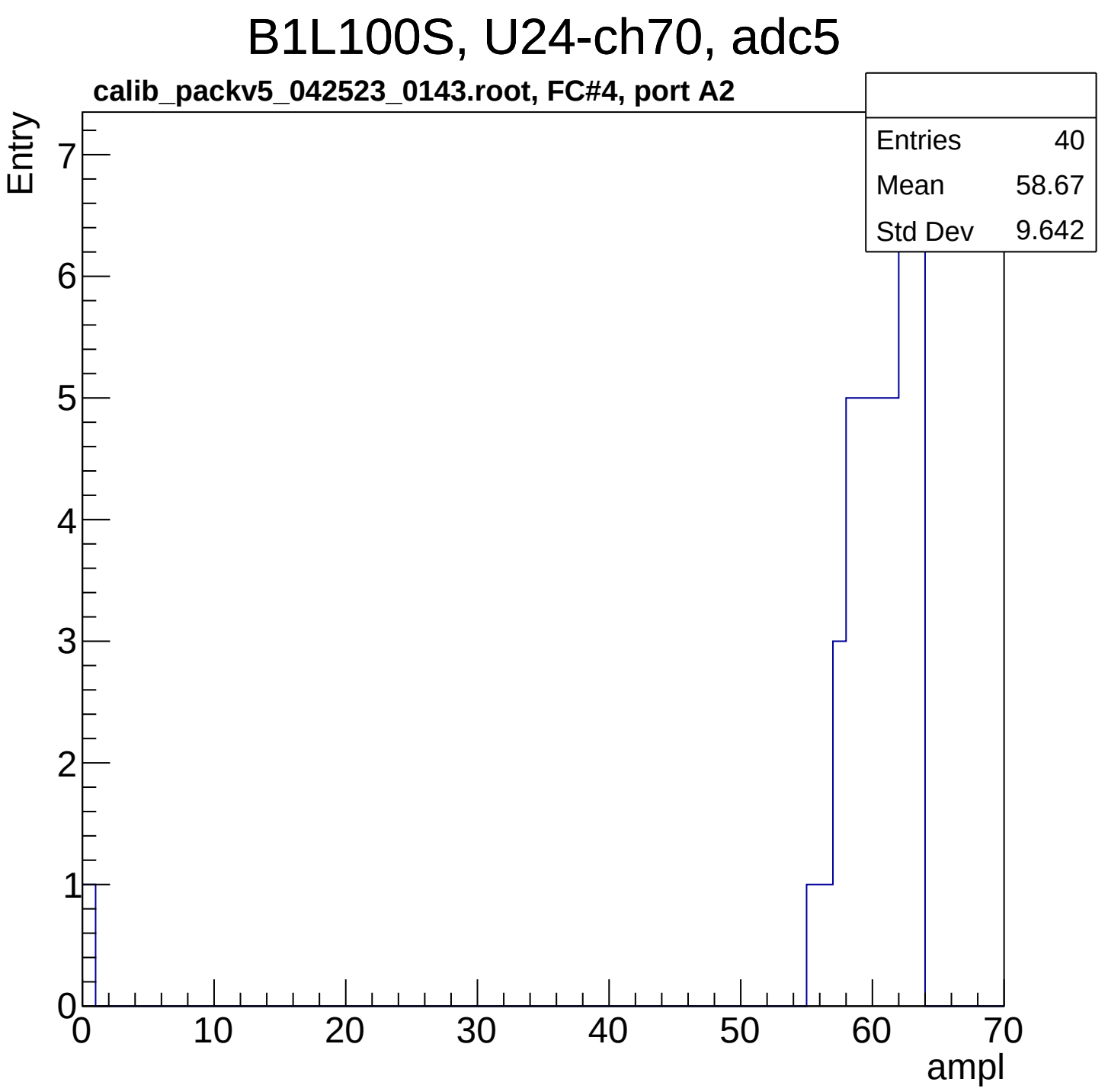
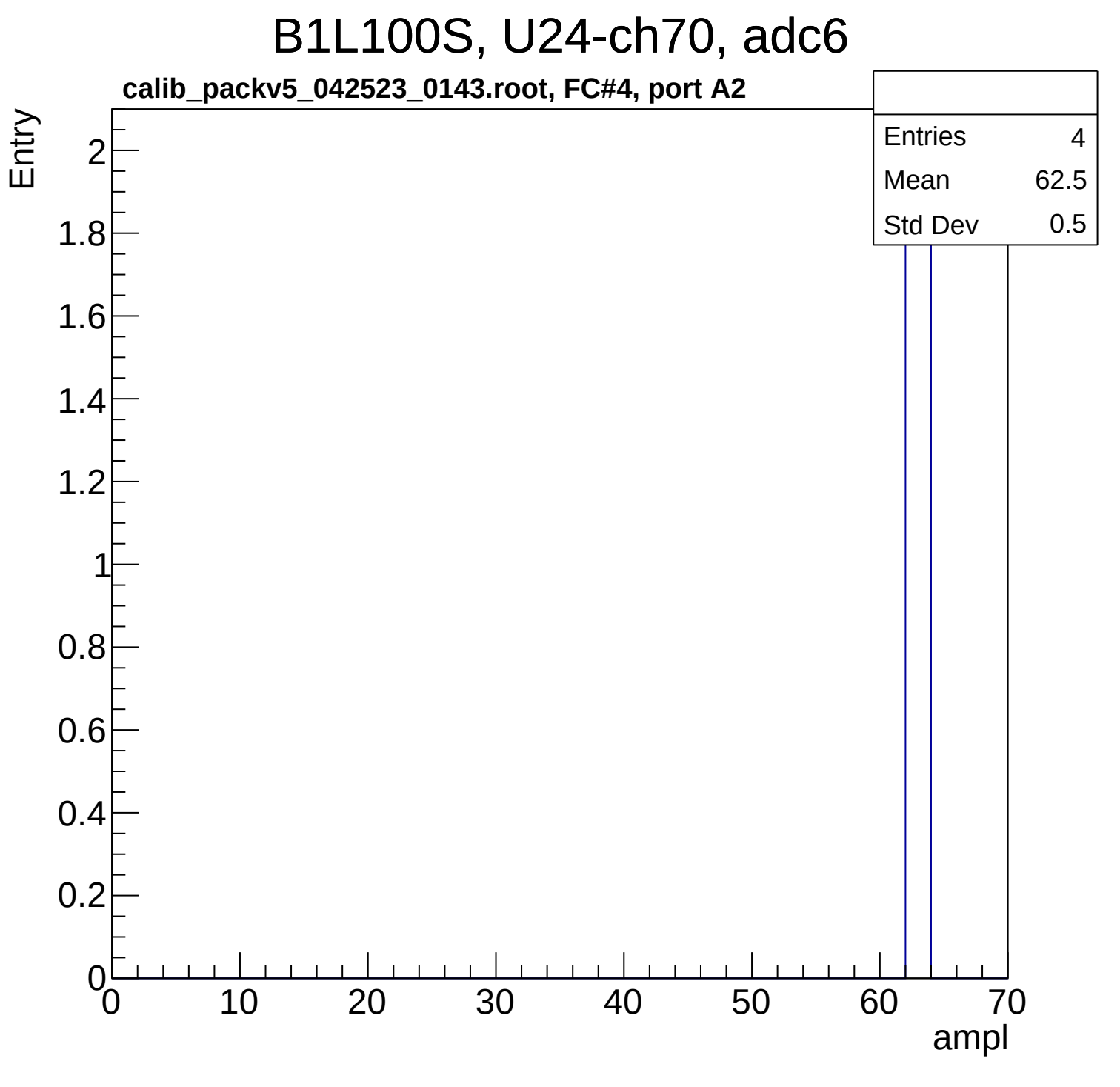
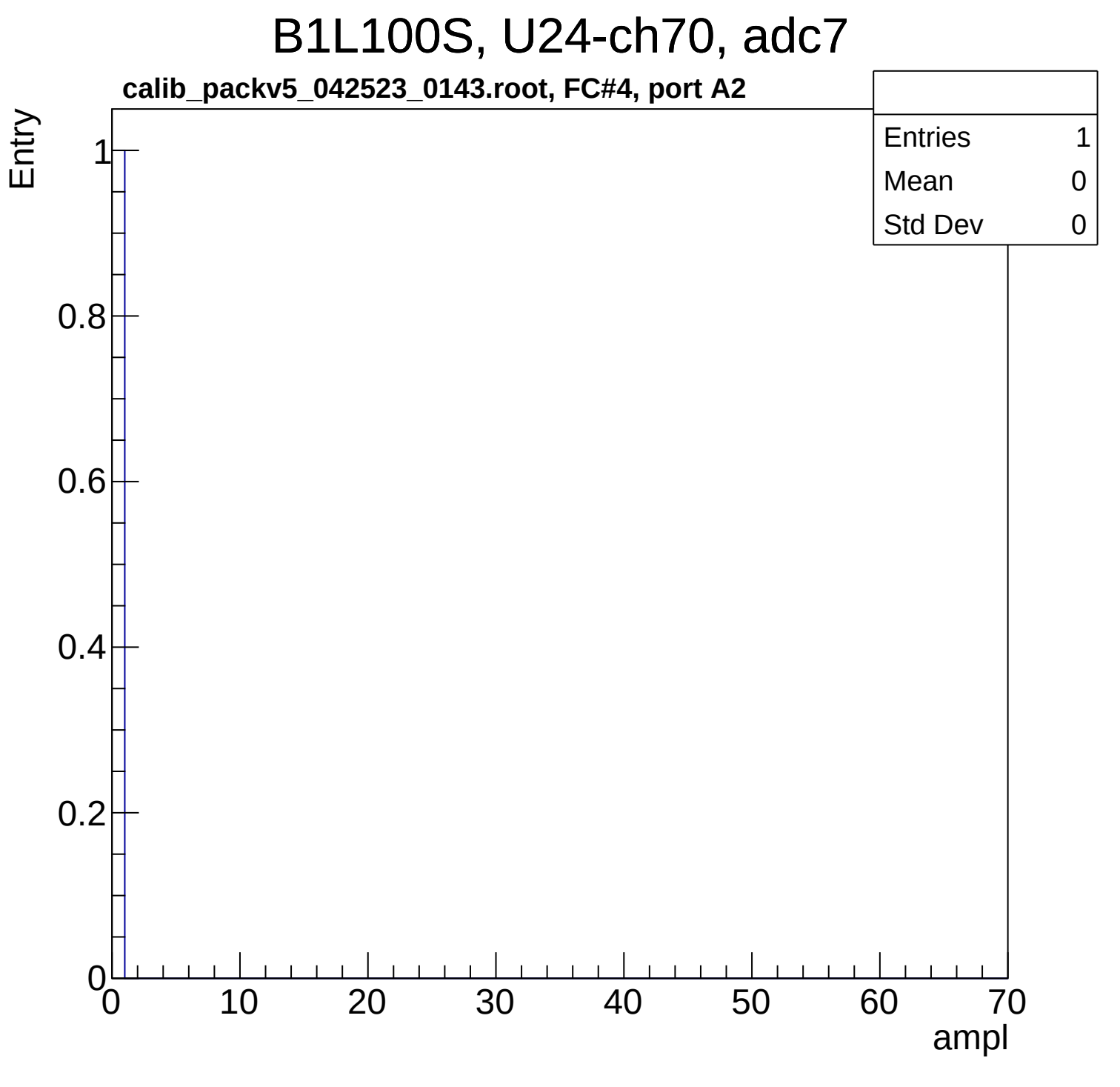
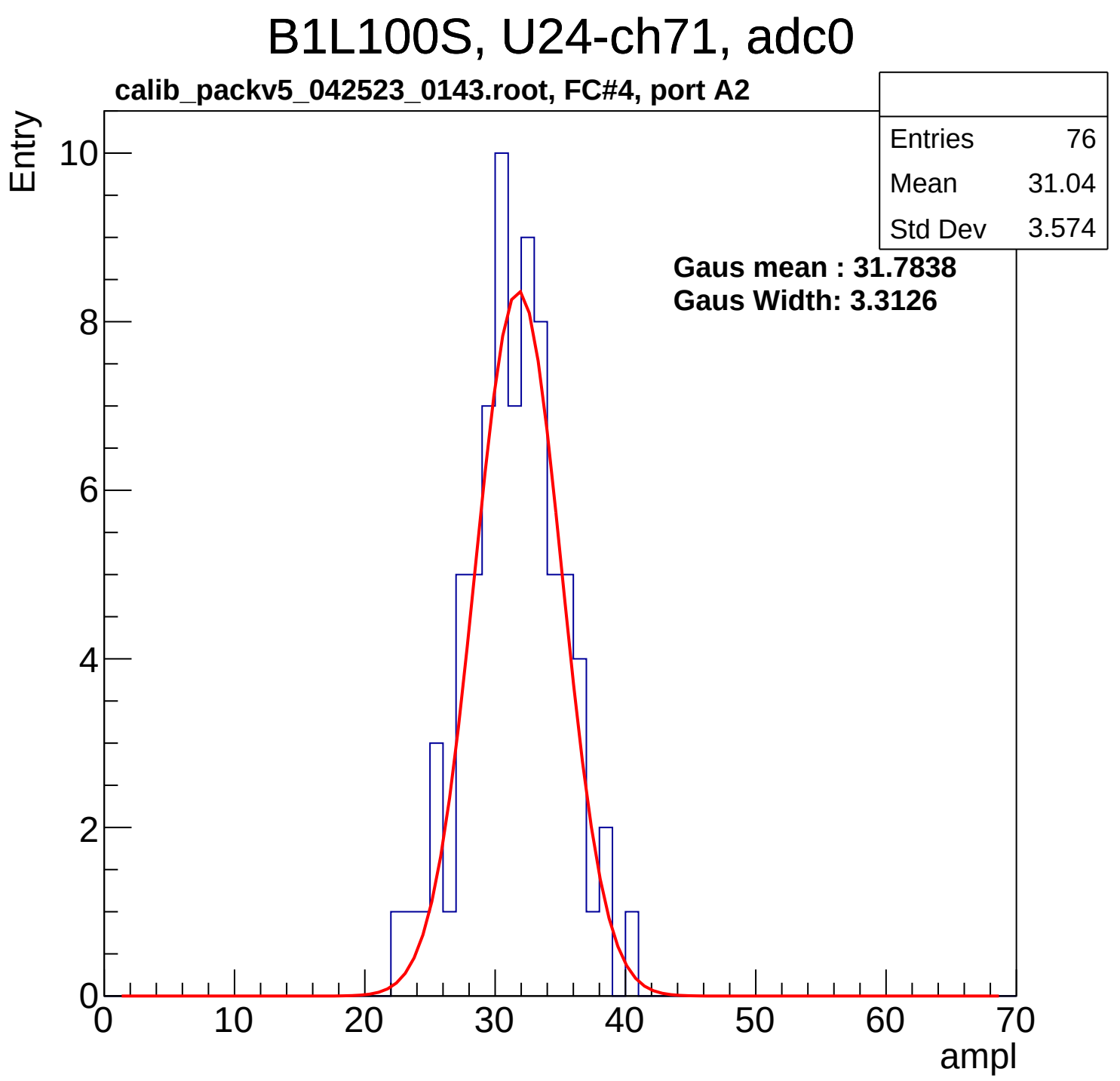
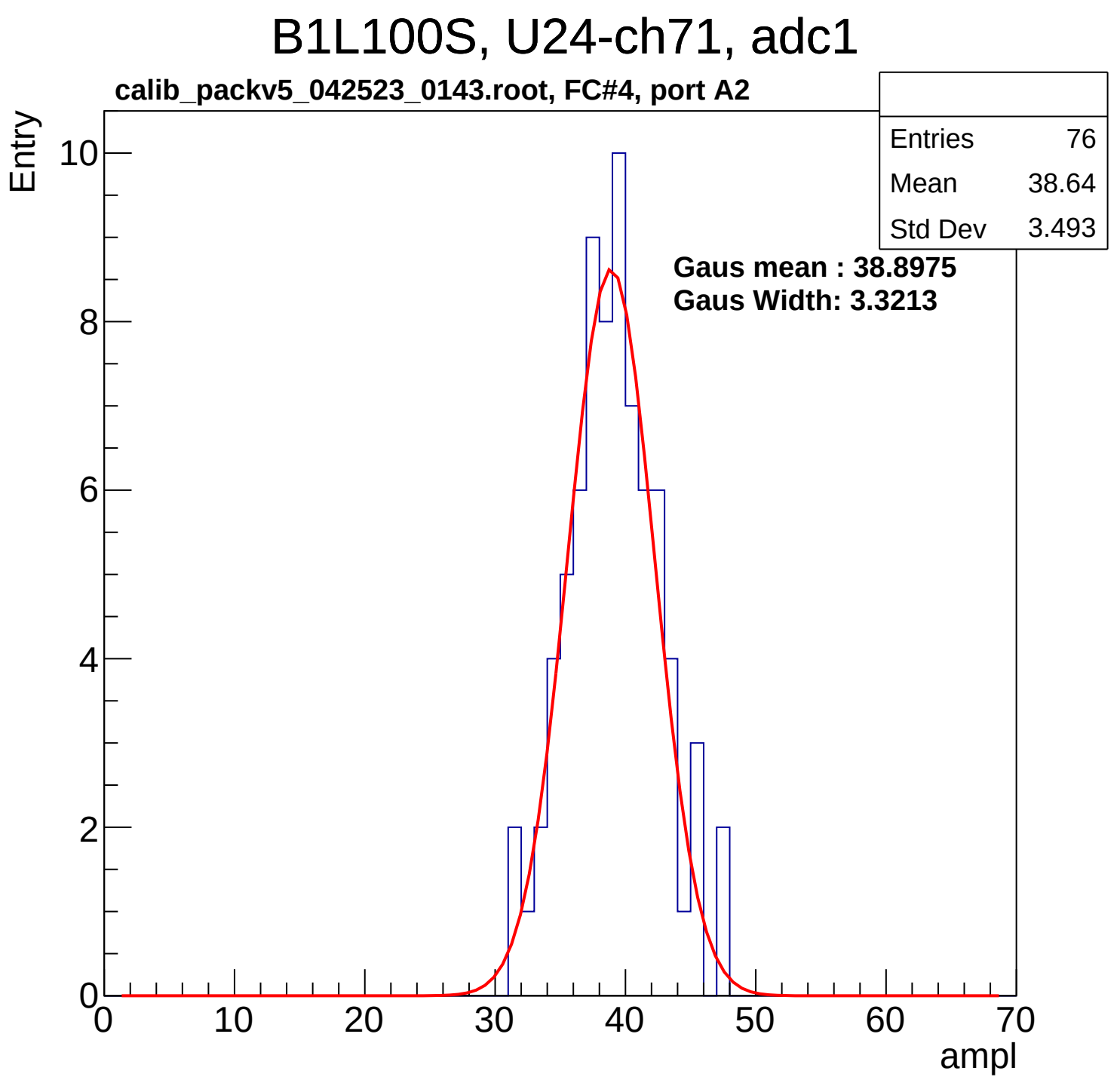

B1L100S, U24-ch70, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 56.82
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

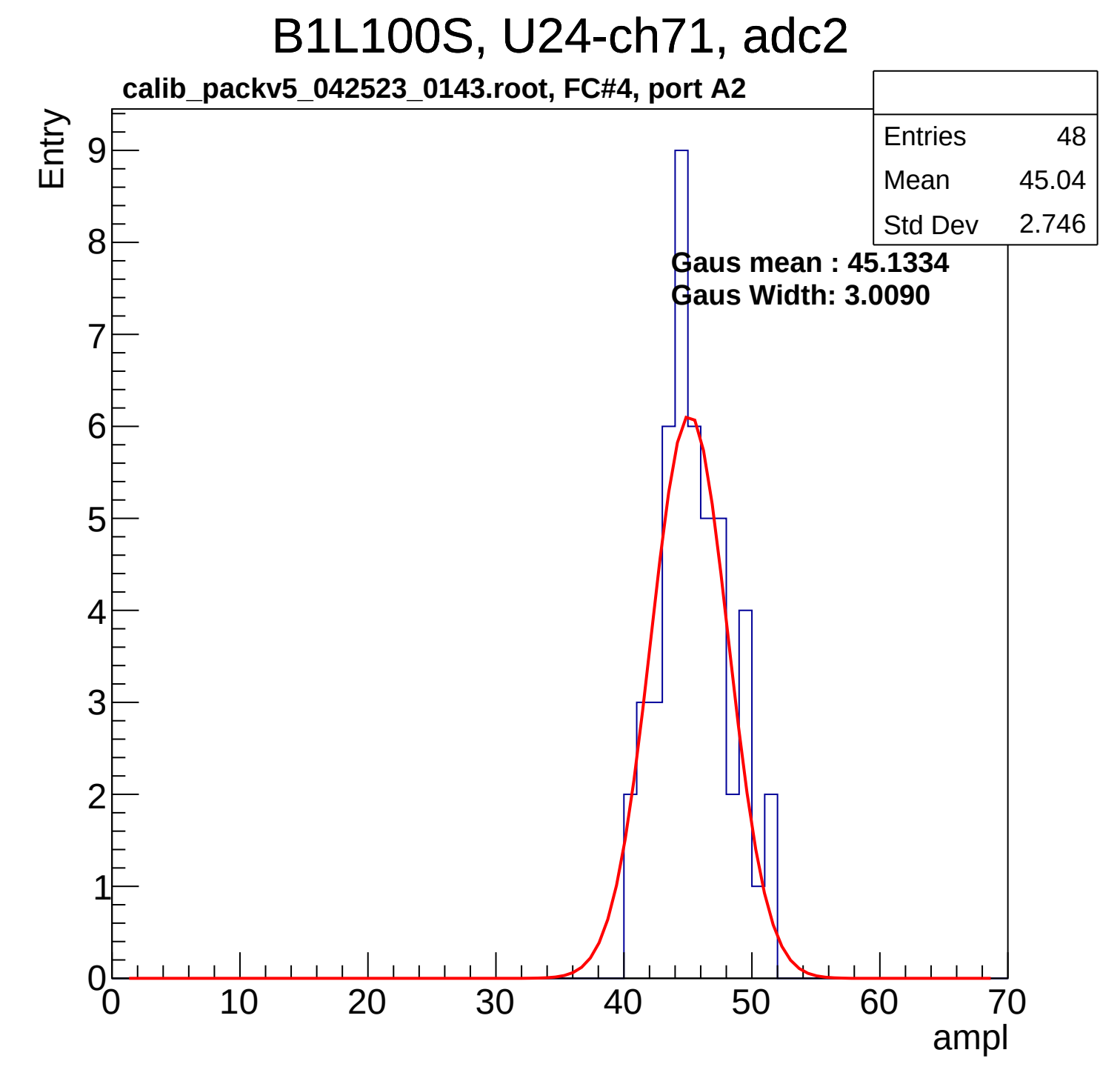

B1L100S, U24-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.67
Std Dev 9.642
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch70, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 31.04
Std Dev 3.574
Gaus mean : 31.7838
Gaus Width: 3.3126
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 38.64
Std Dev 3.493
Gaus mean : 38.8975
Gaus Width: 3.3213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 45.04
Std Dev 2.746
Gaus mean : 45.1334
Gaus Width: 3.0090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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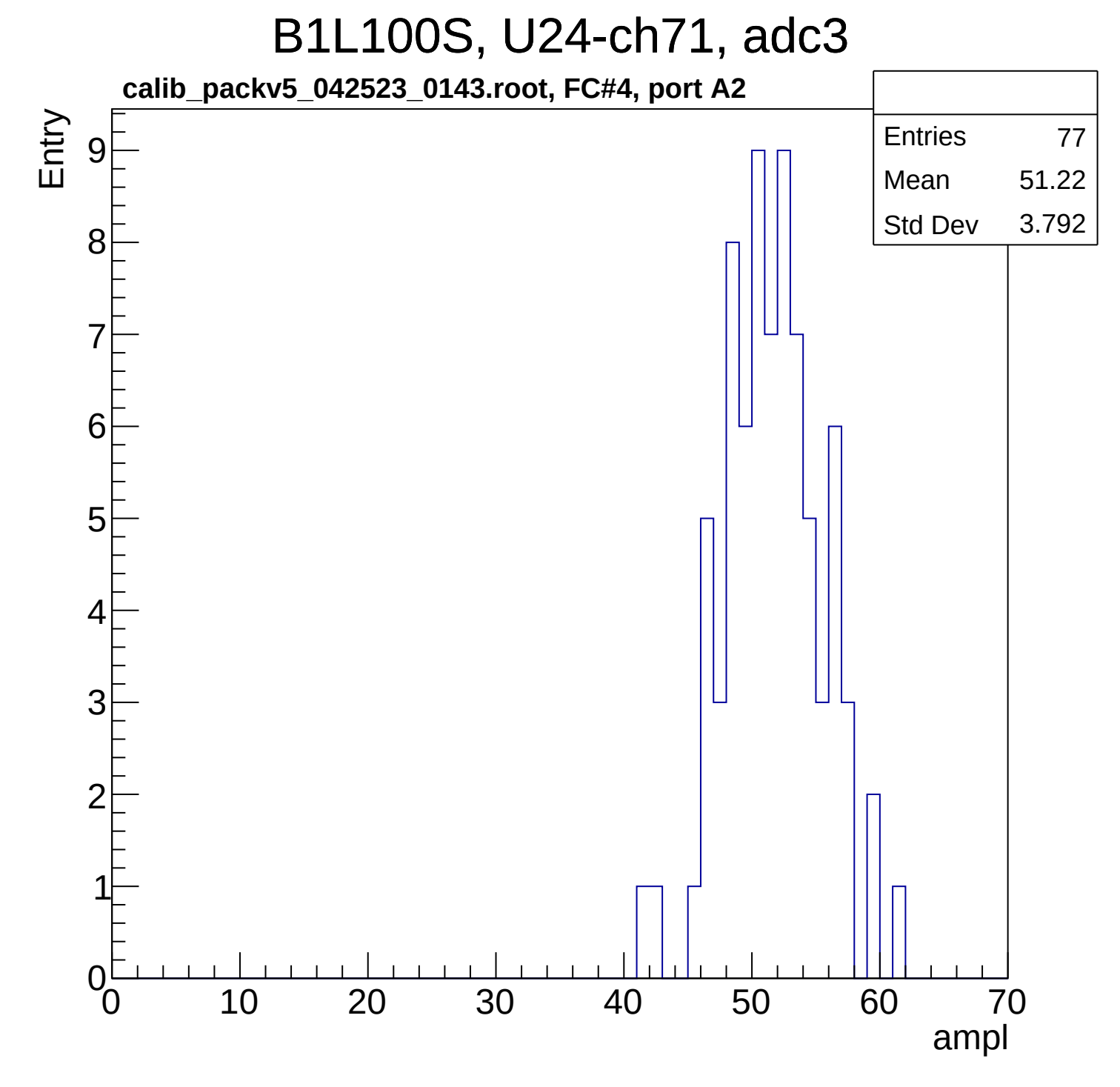
B1L100S, U24-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 51.22
Std Dev 3.792
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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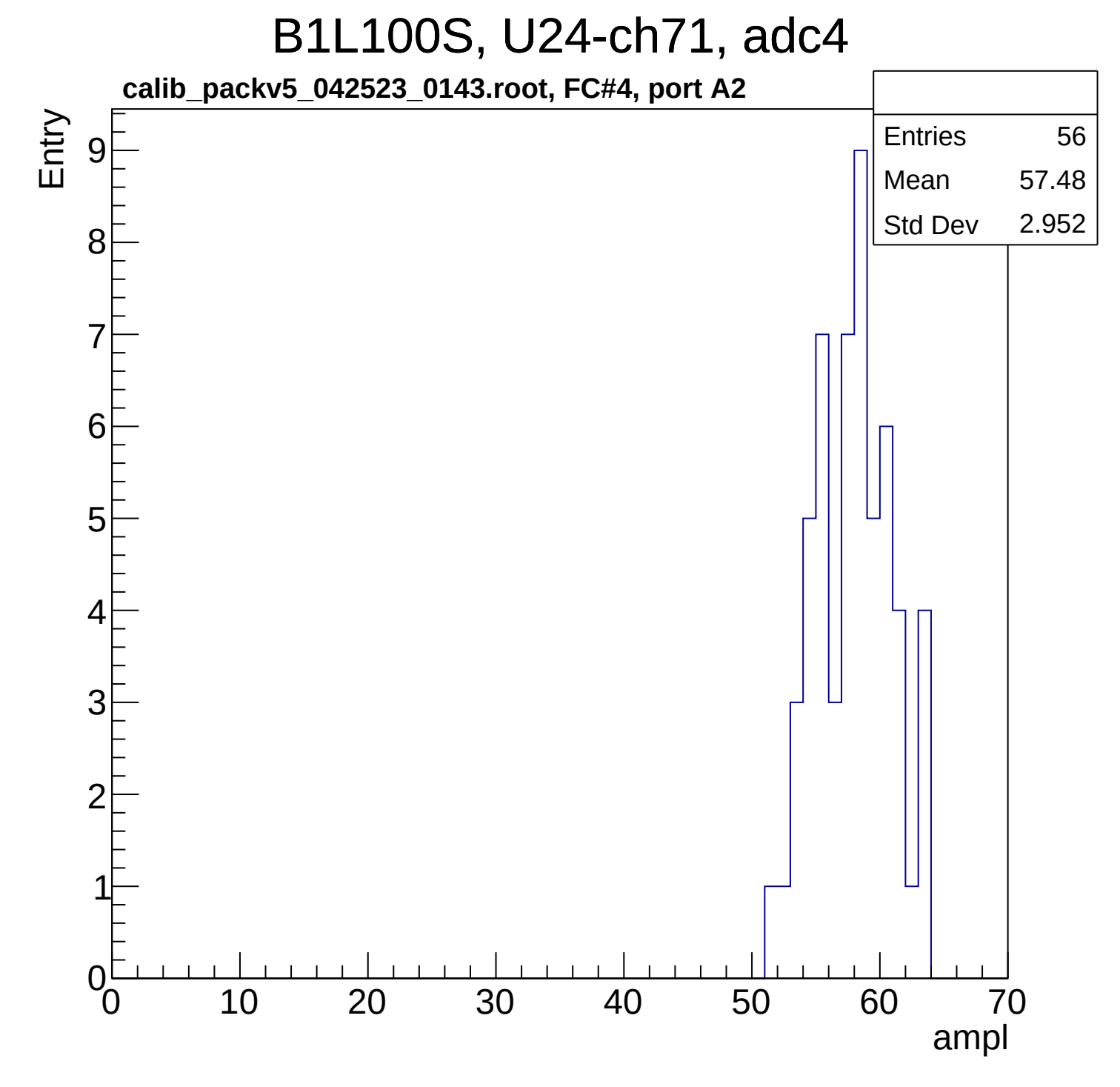
B1L100S, U24-ch71, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.48
Std Dev 2.952
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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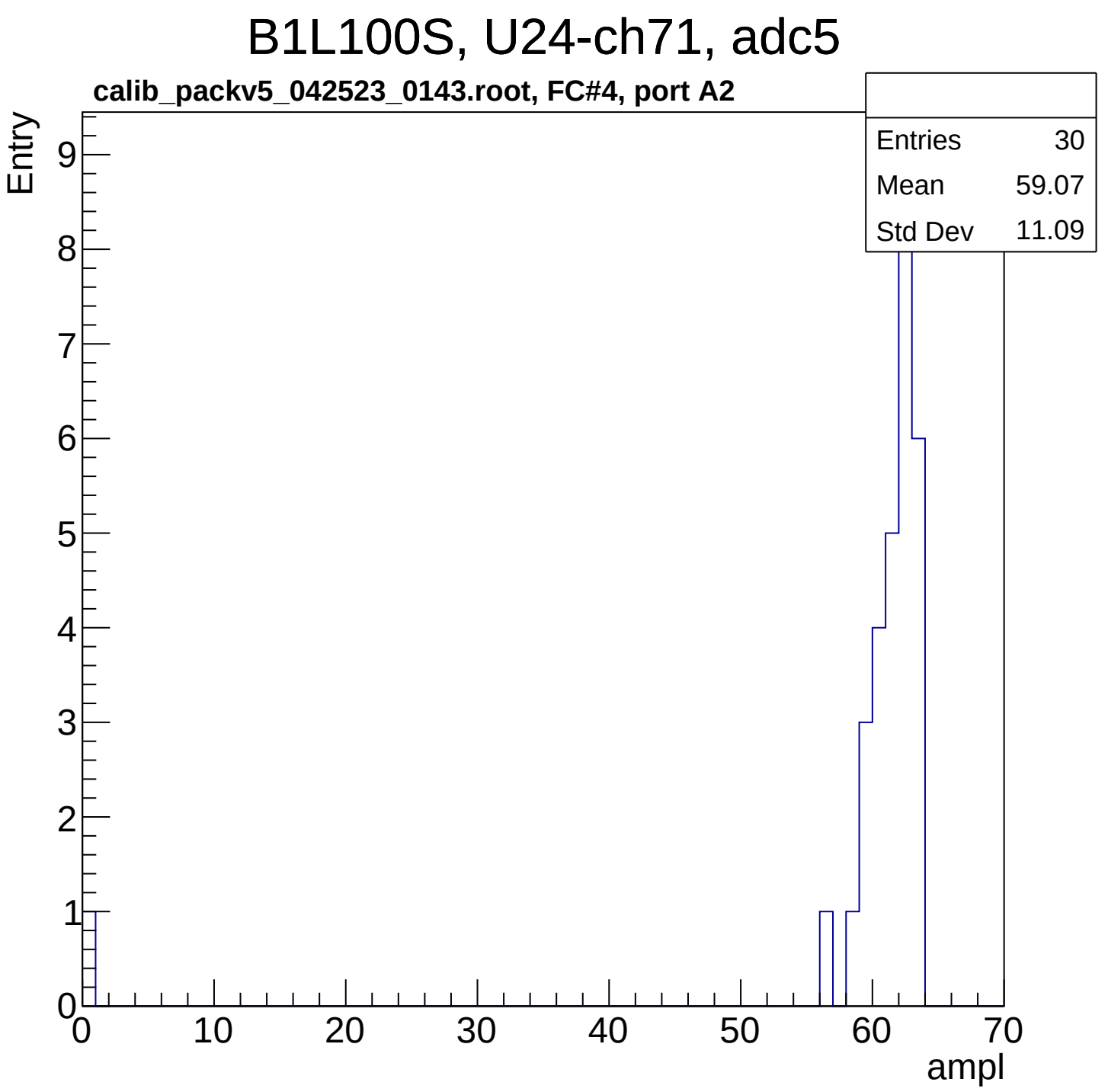
B1L100S, U24-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 59.07
Std Dev 11.09
Entry
ampl
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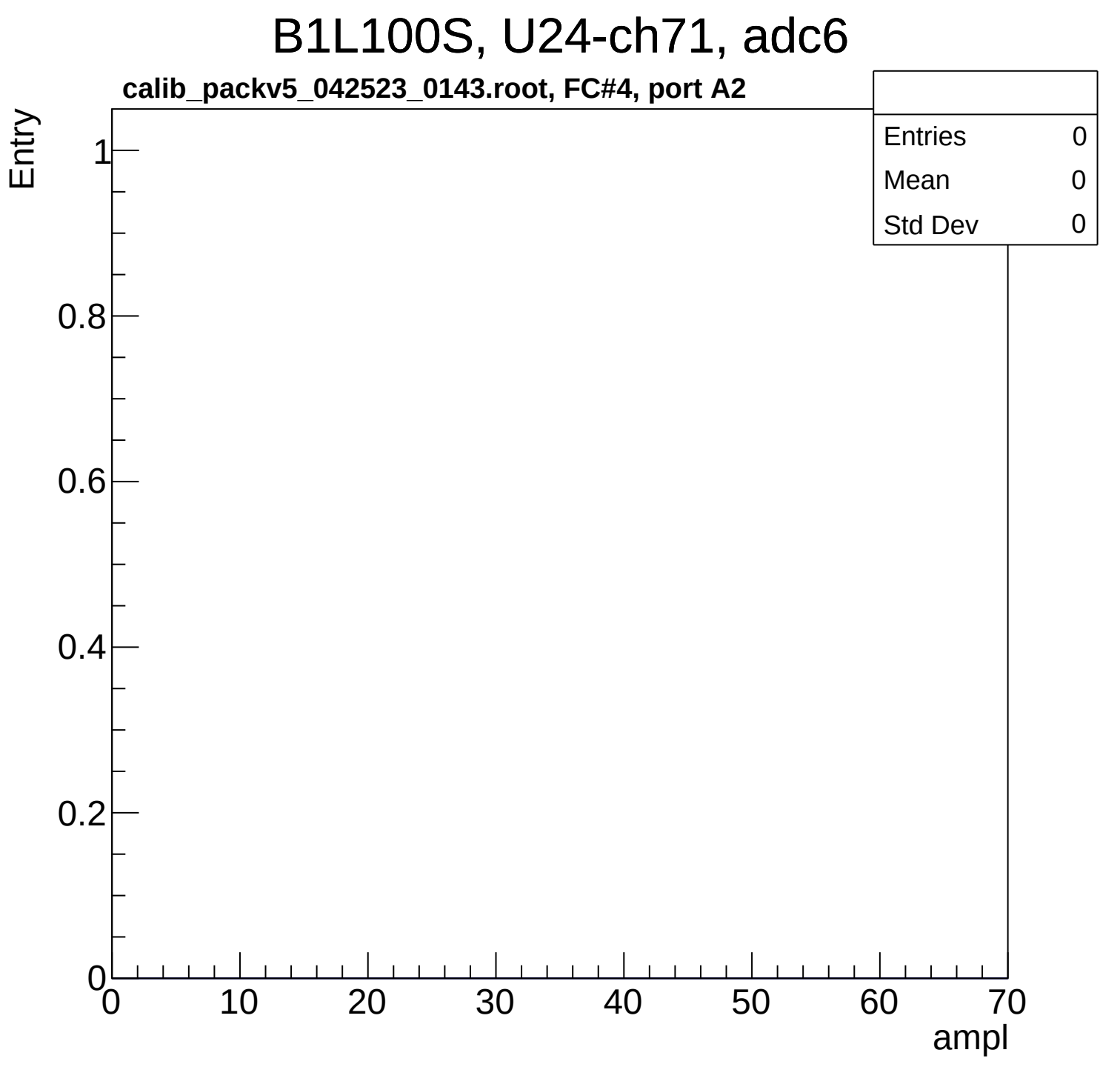
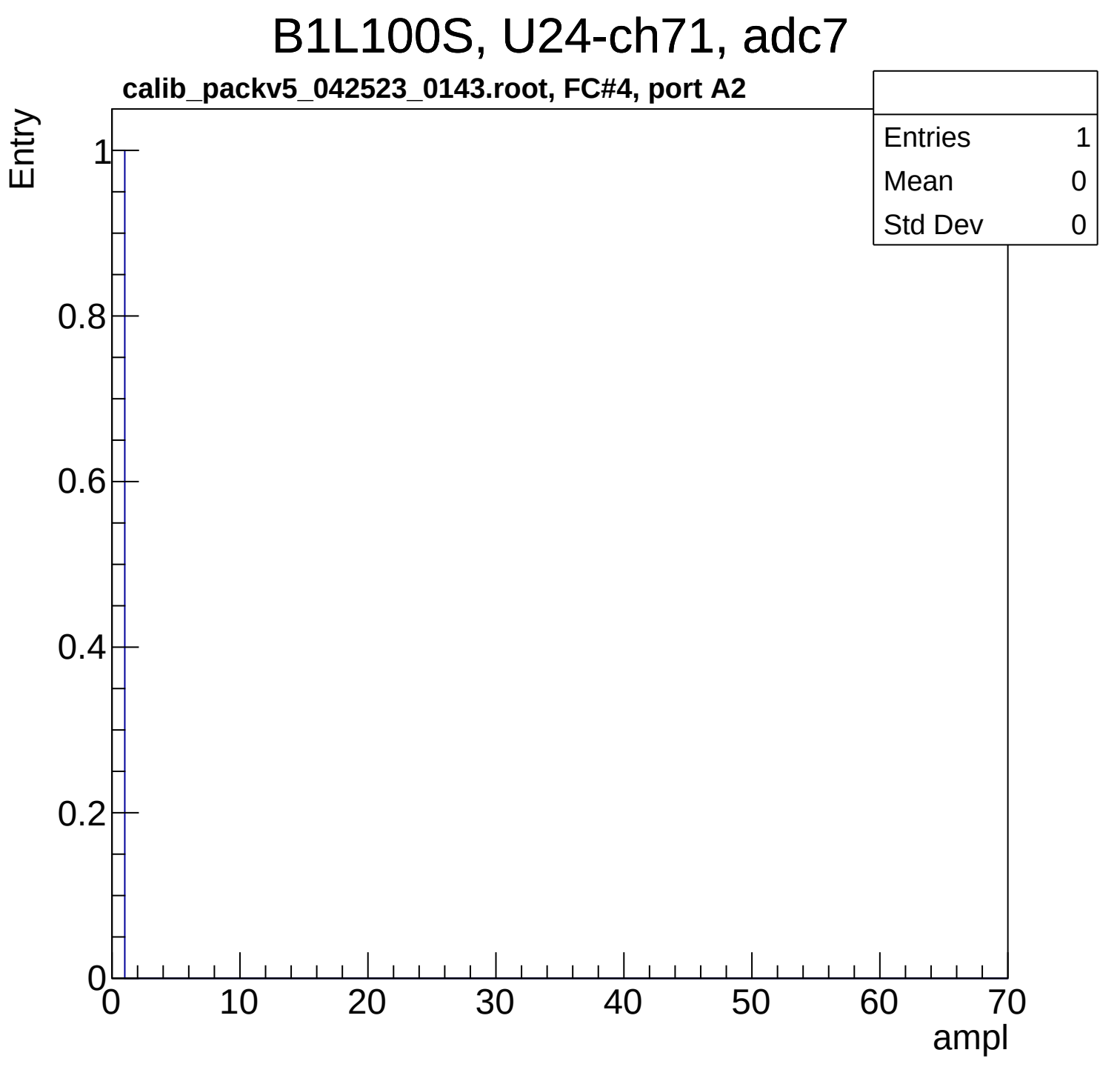
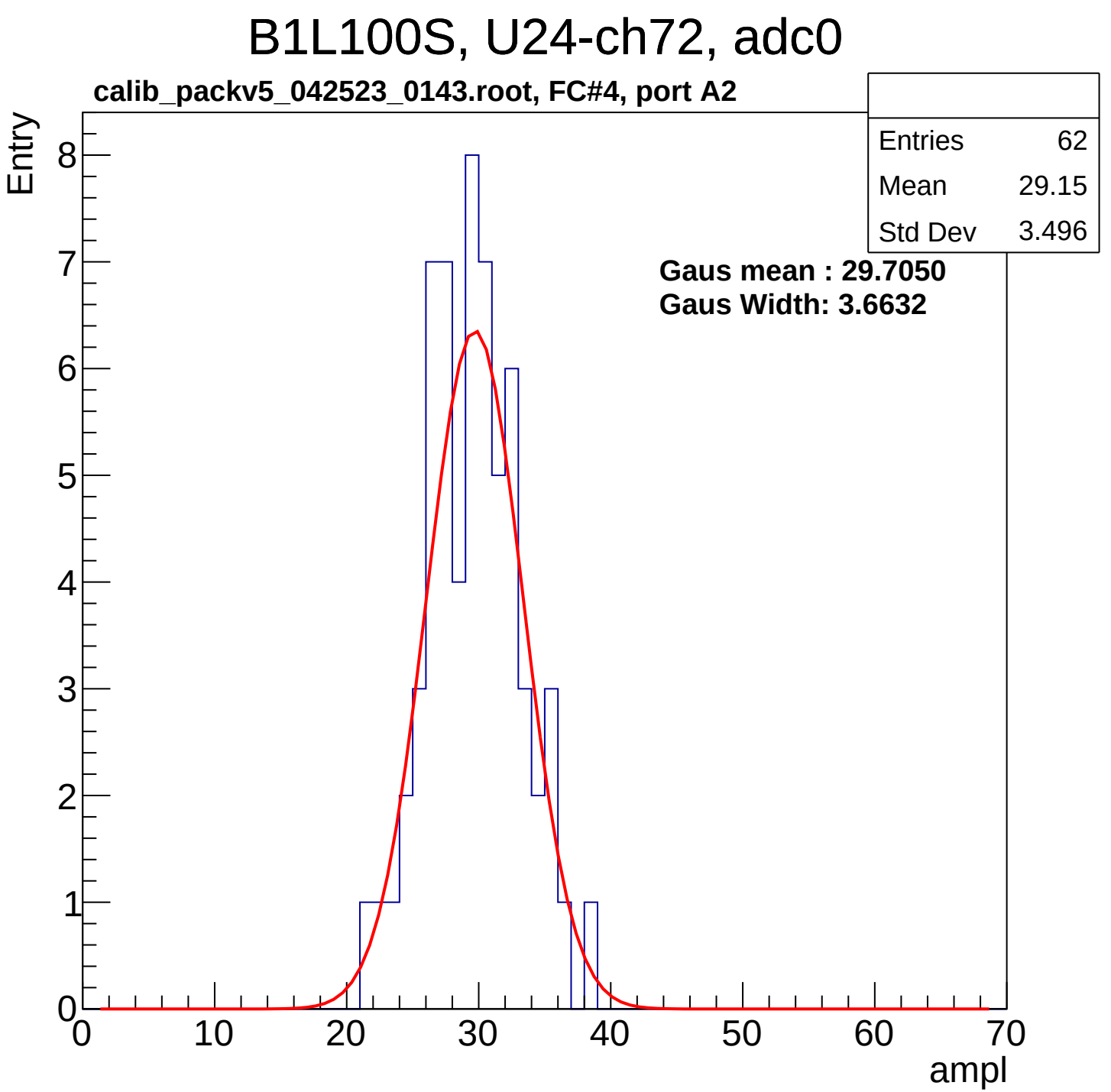
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch71, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.15
Std Dev 3.496
Gaus mean : 29.7050
Gaus Width: 3.6632
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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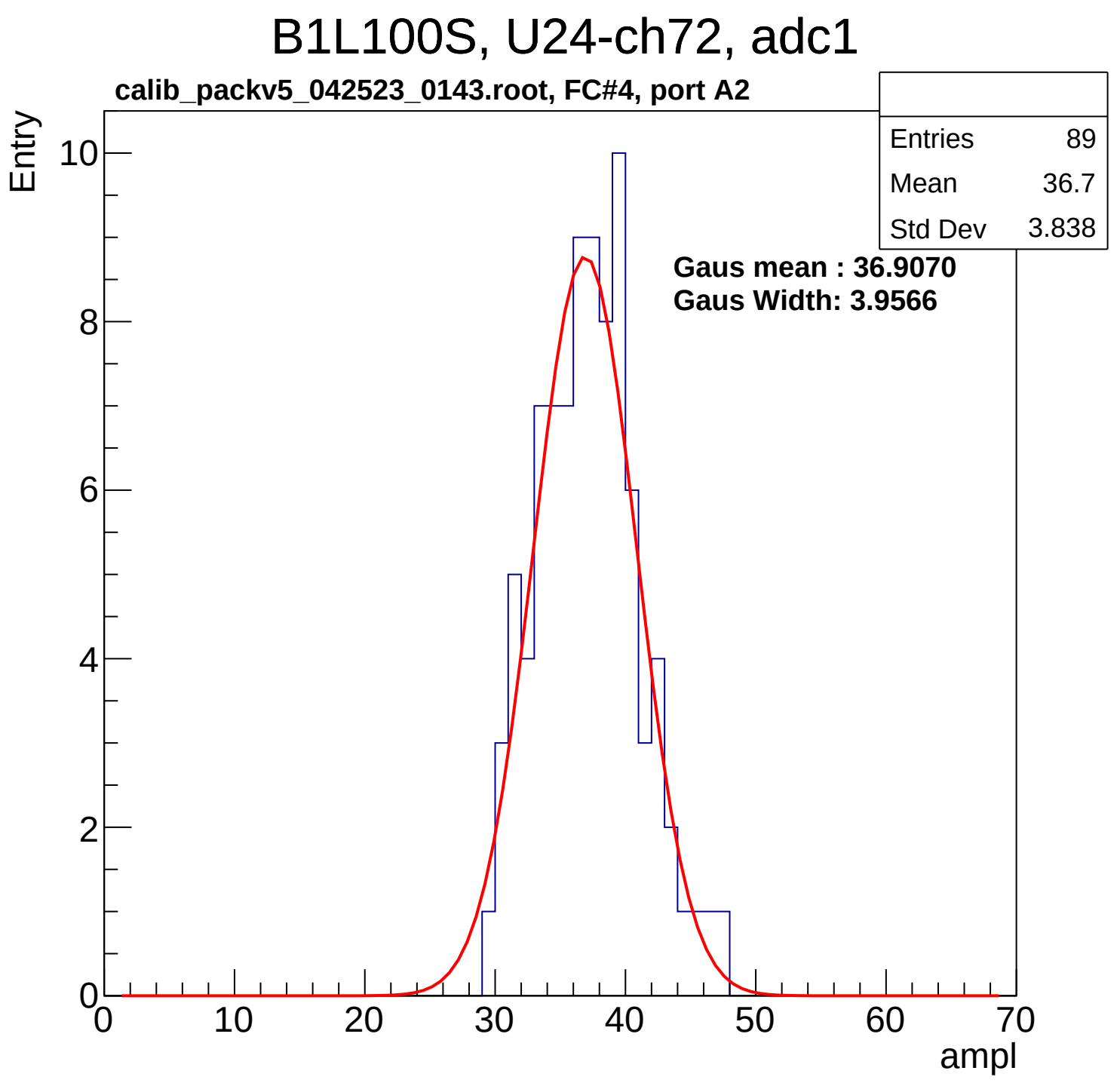
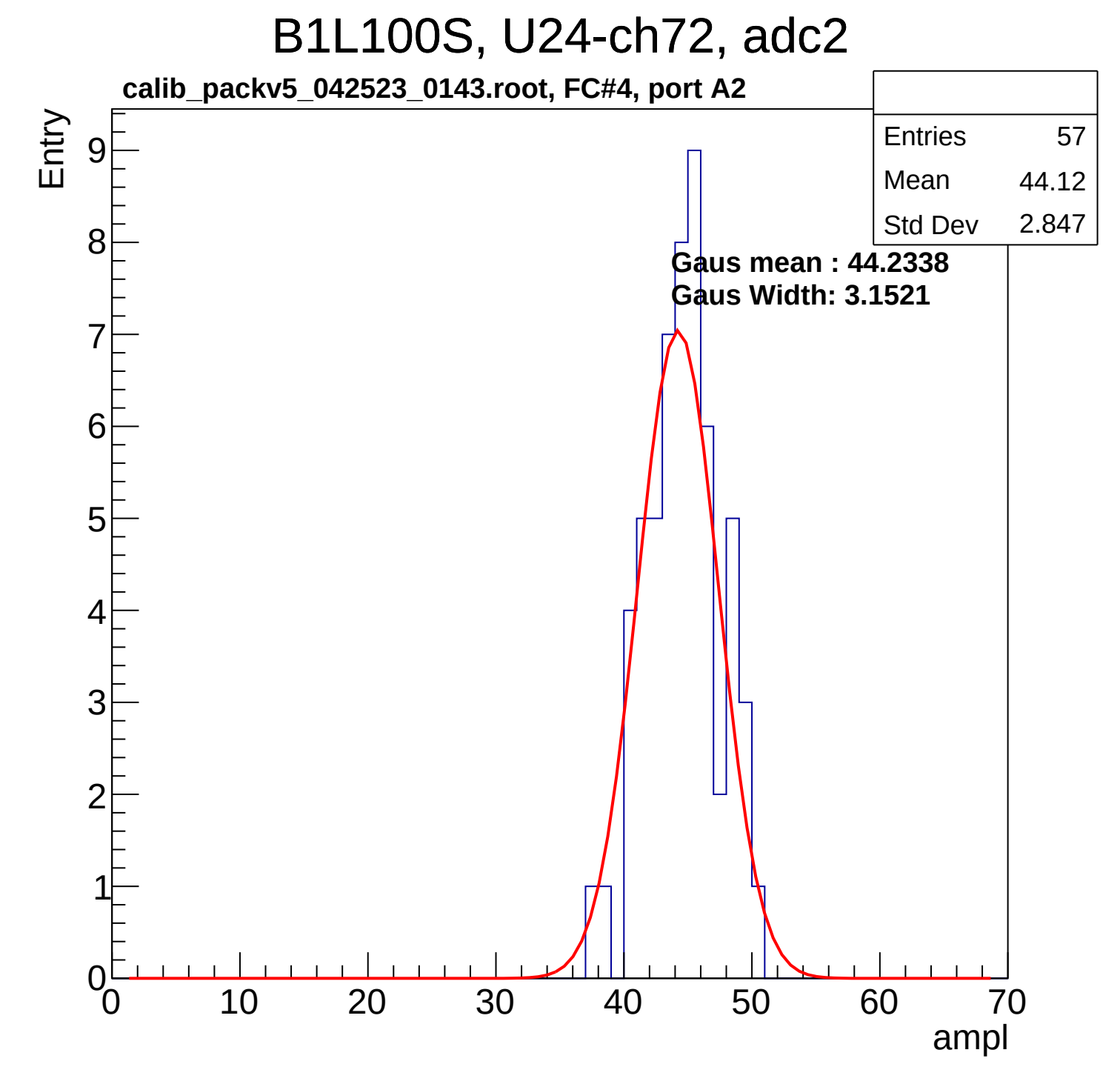

B1L100S, U24-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 36.7
Std Dev 3.838
Gaus mean : 36.9070
Gaus Width: 3.9566
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.12
Std Dev 2.847
Gaus mean : 44.2338
Gaus Width: 3.1521
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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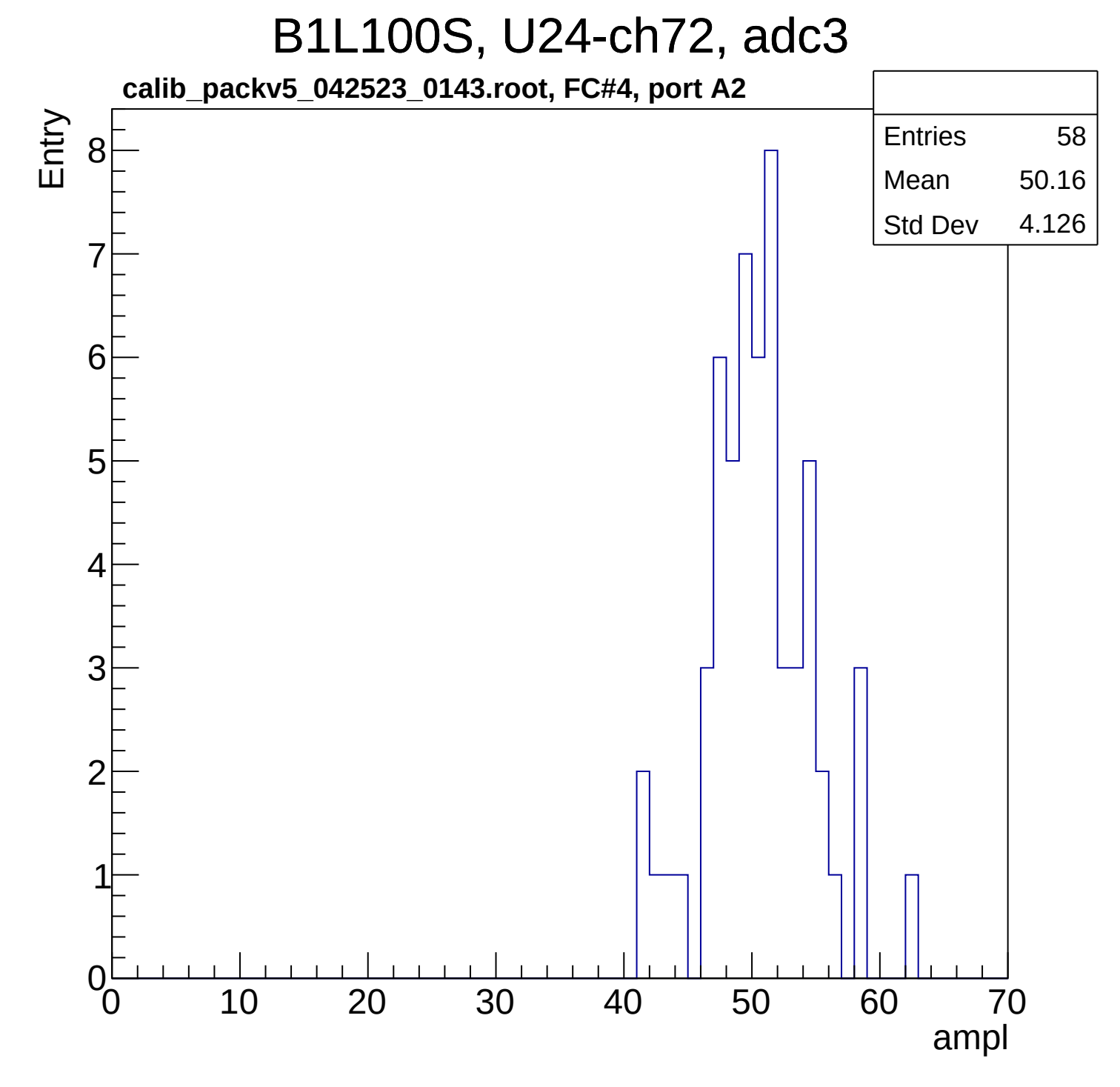
B1L100S, U24-ch72, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 50.16
Std Dev 4.126
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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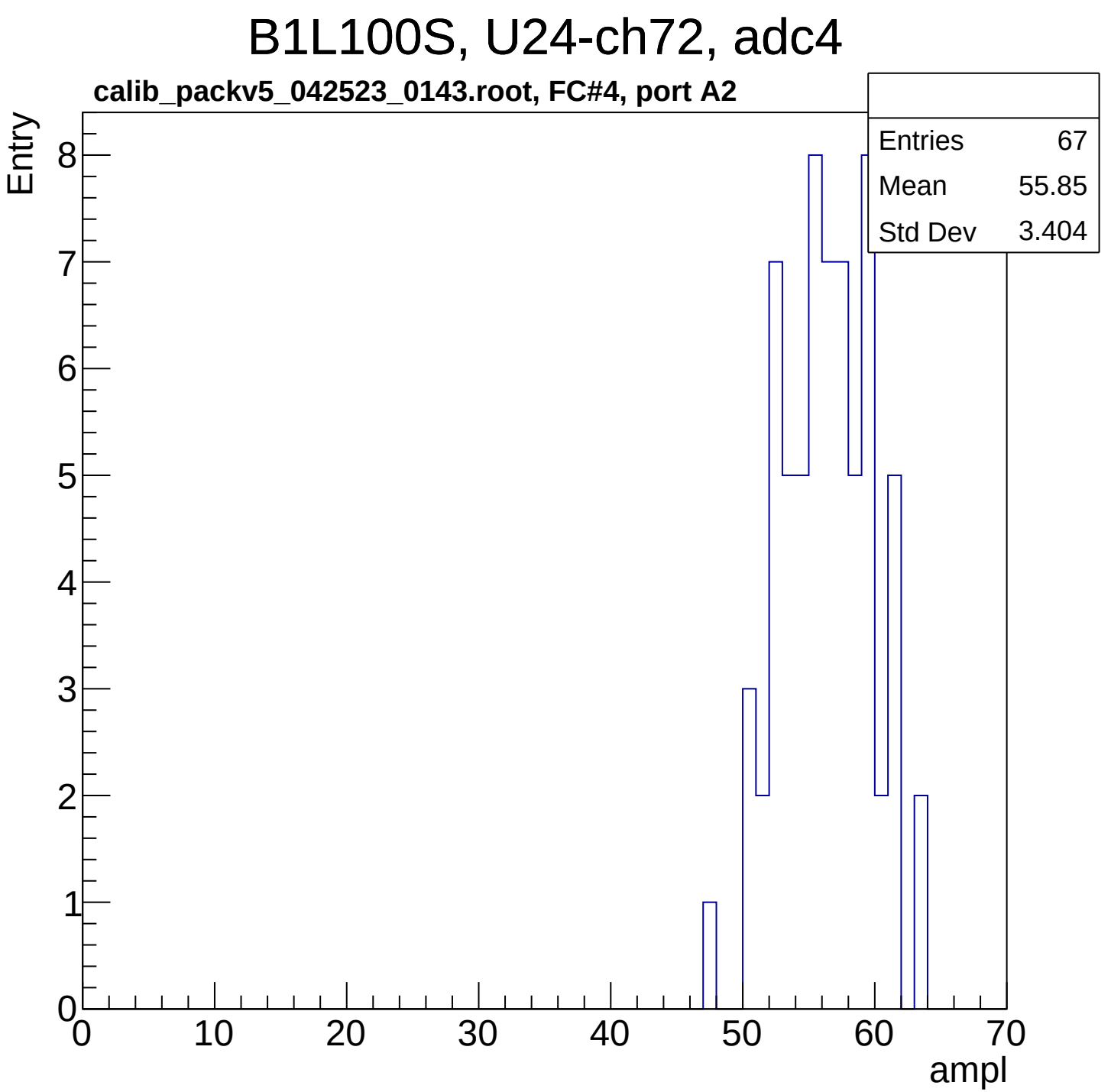
B1L100S, U24-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.85
Std Dev 3.404
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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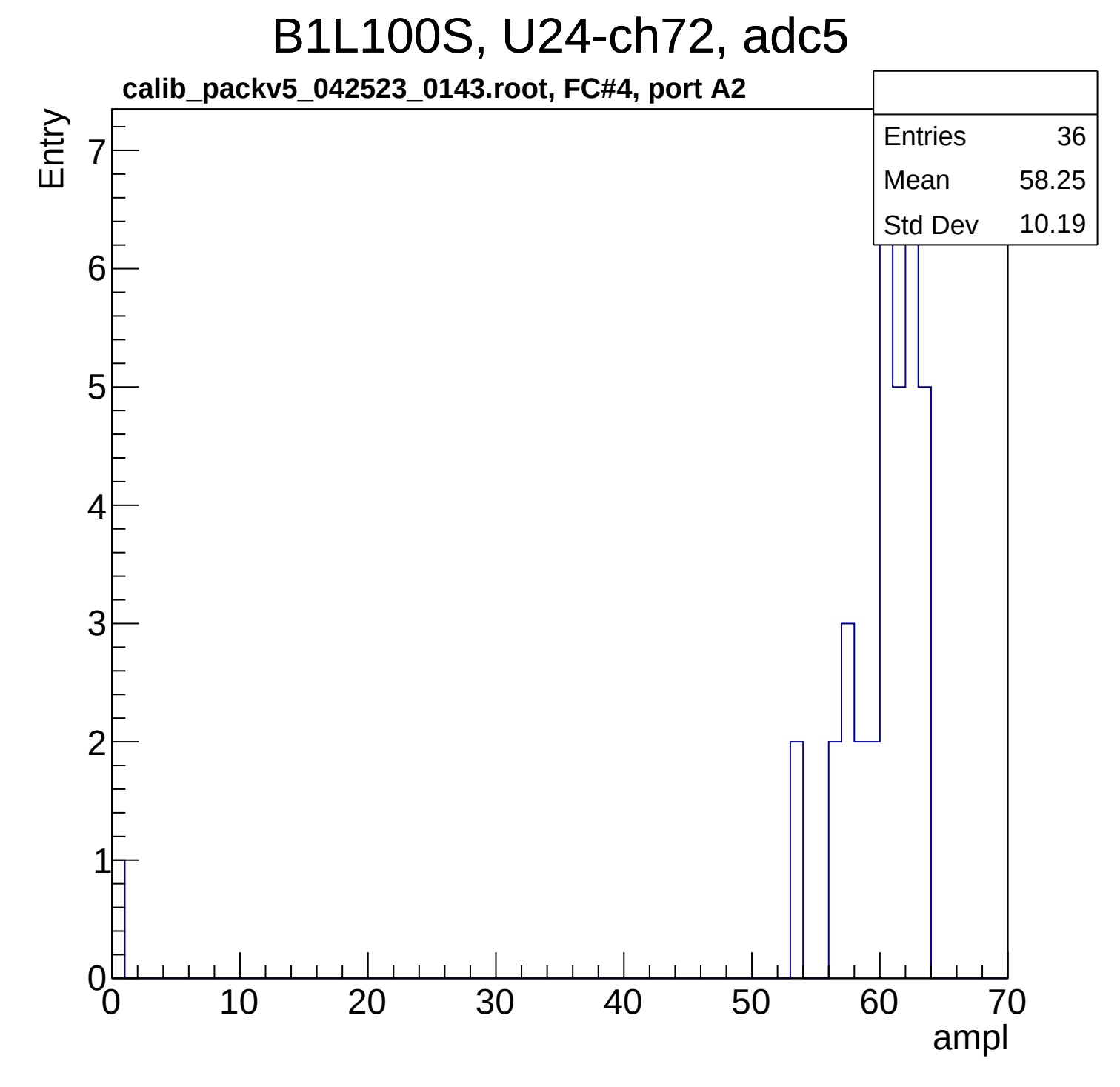
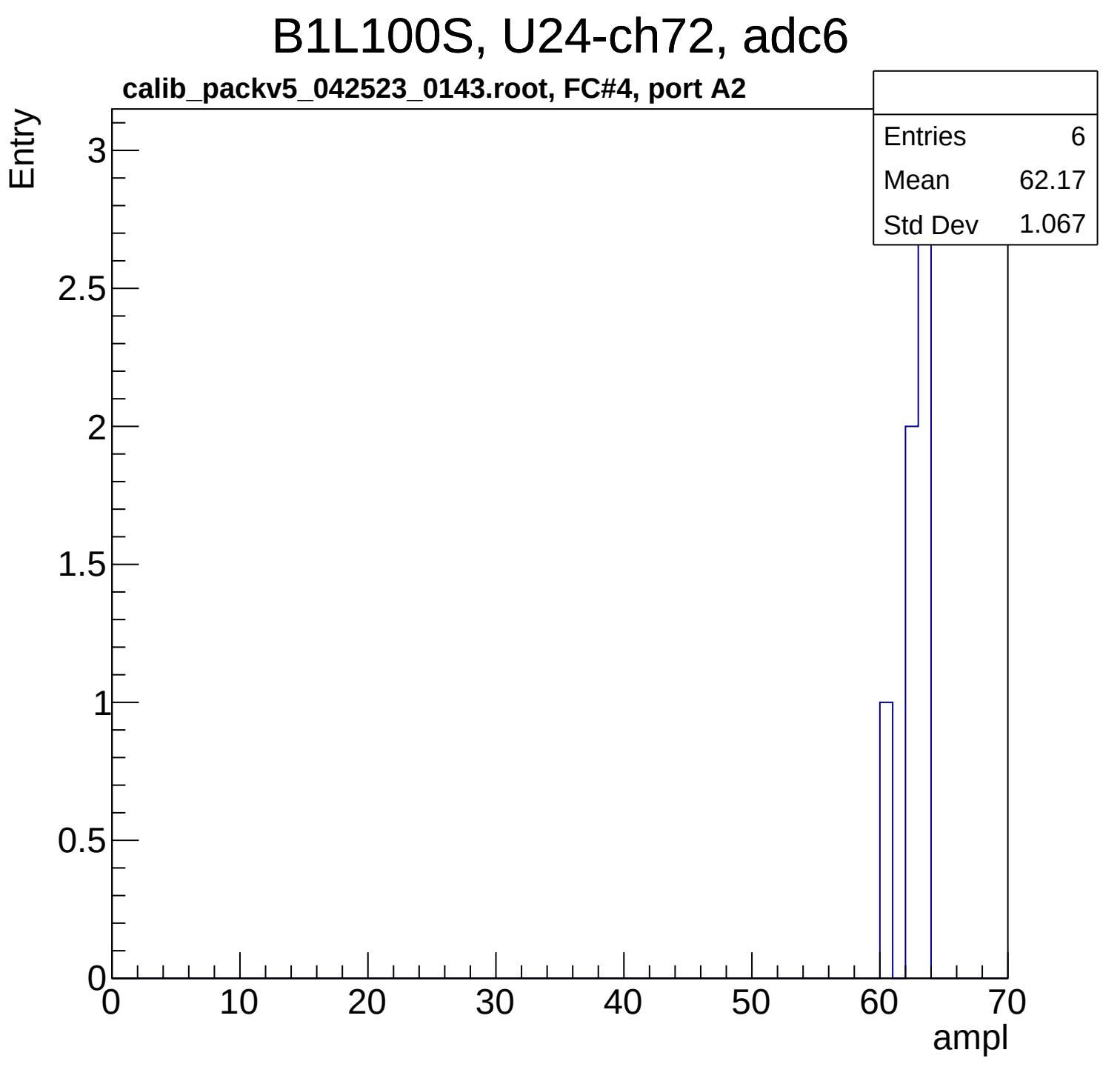

B1L100S, U24-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.25
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch72, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
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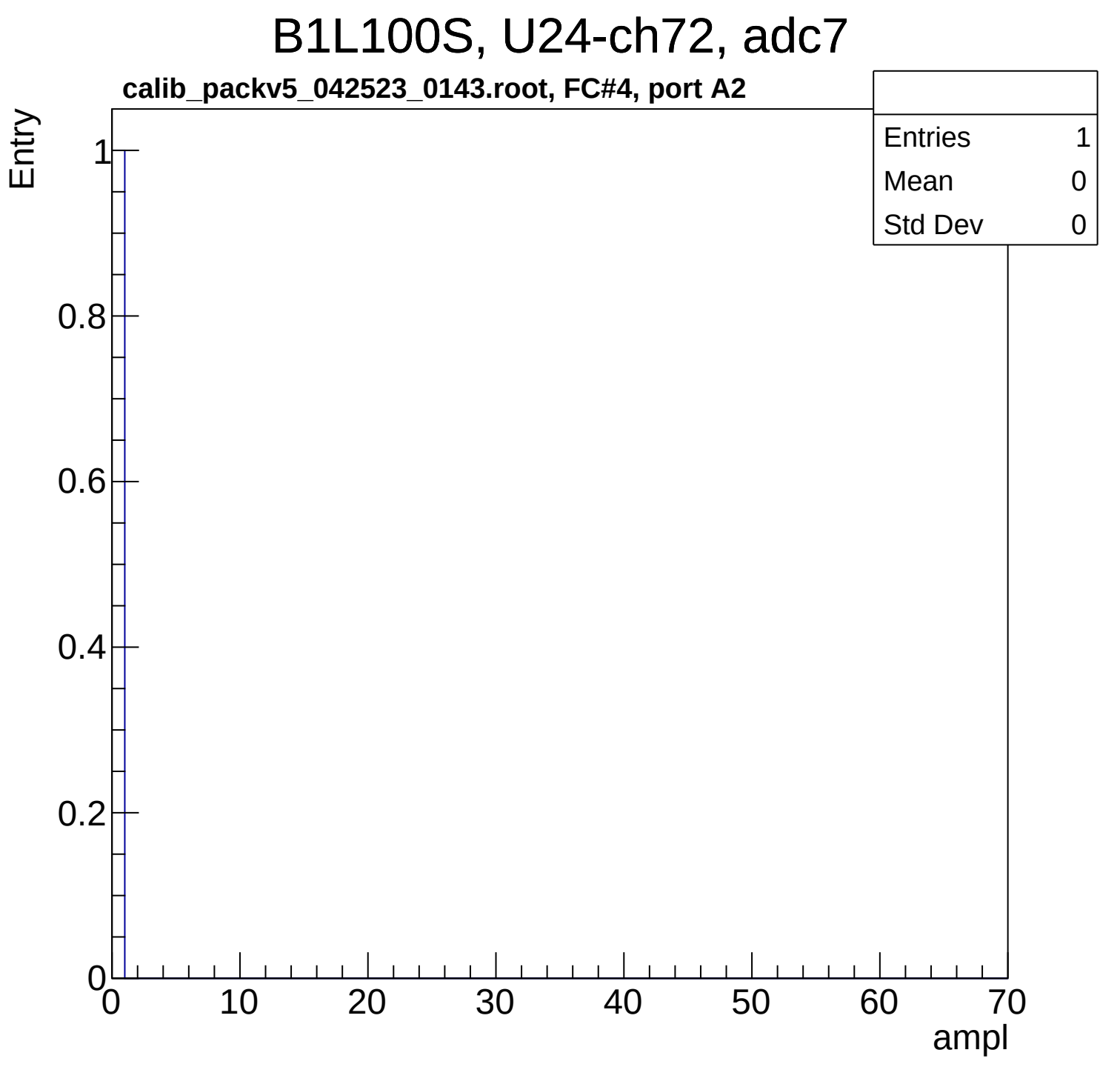
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch72, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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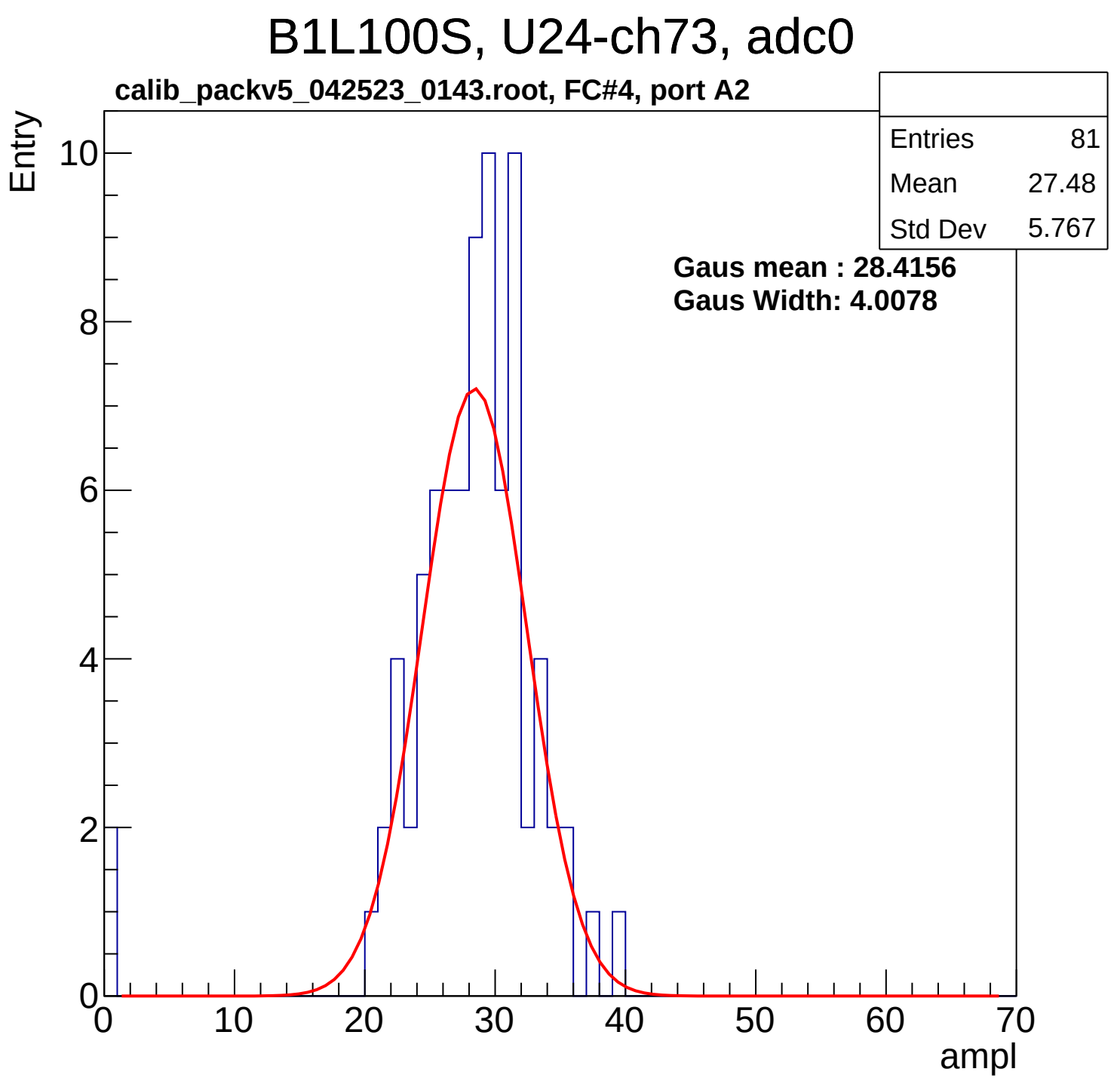
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 27.48
Std Dev 5.767
Gaus mean : 28.4156
Gaus Width: 4.0078
Entry
ampl
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6
8
10
0
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20
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40
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60
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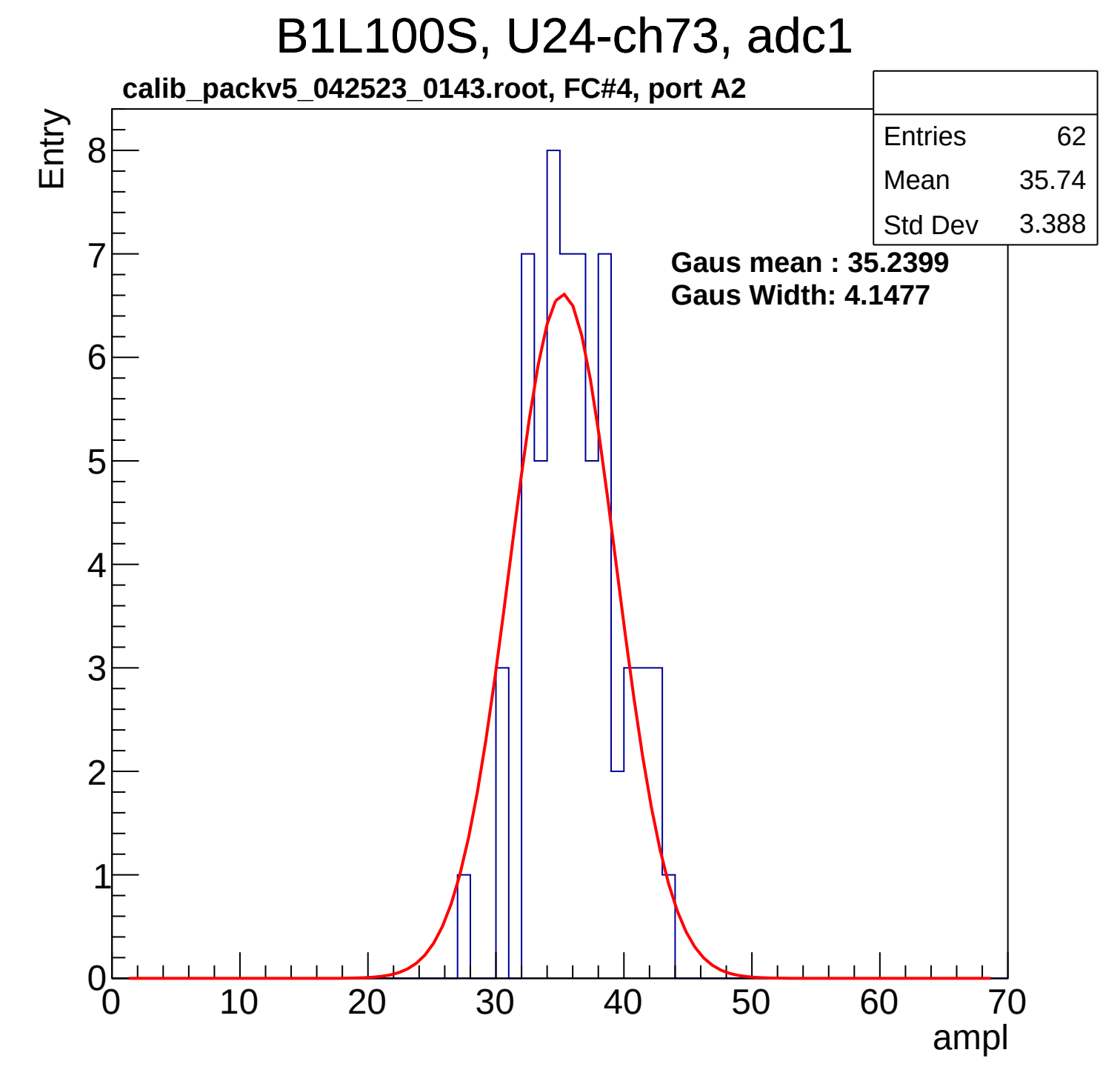
B1L100S, U24-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 35.74
Std Dev 3.388
Gaus mean : 35.2399
Gaus Width: 4.1477
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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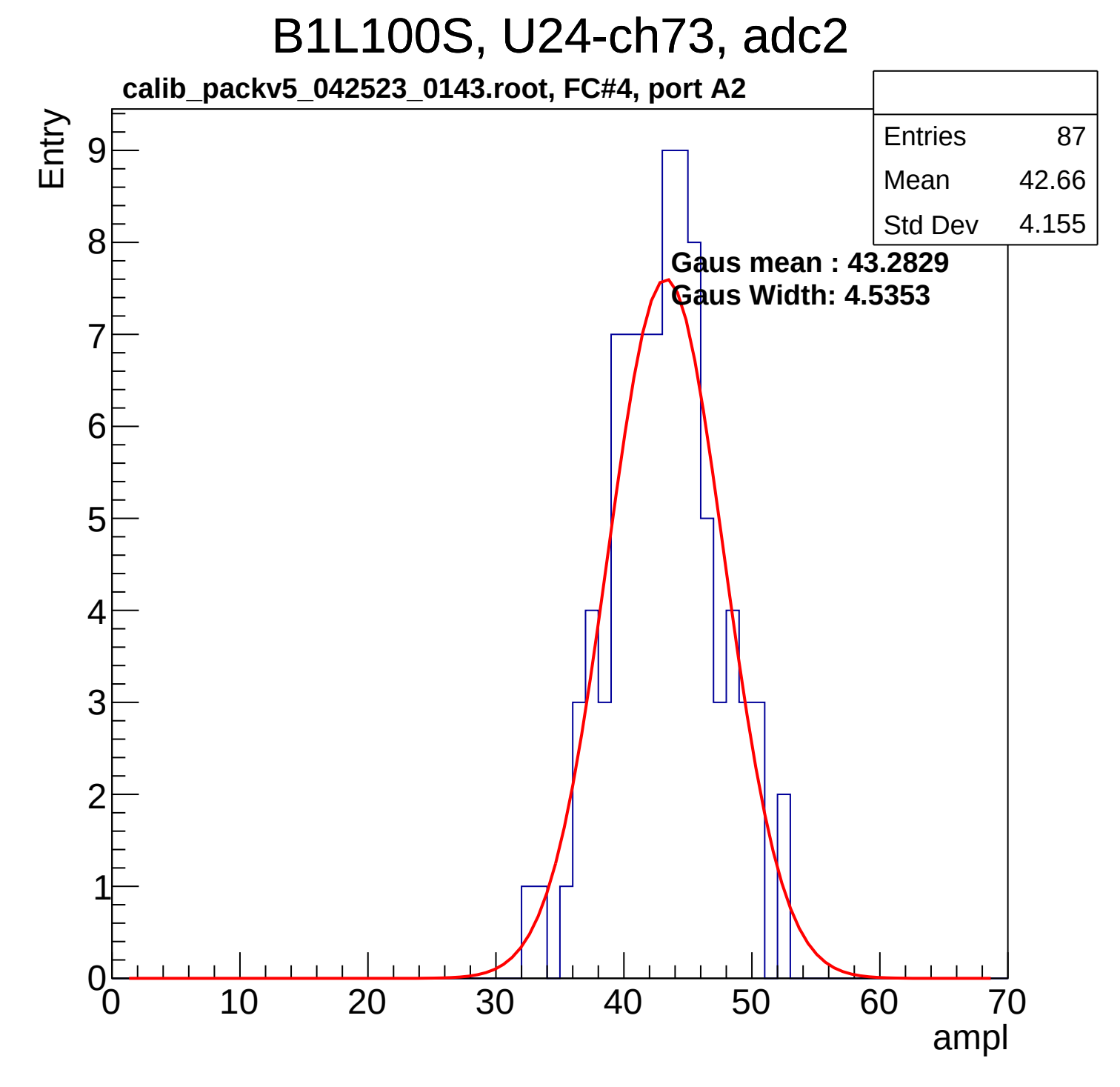
B1L100S, U24-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 42.66
Std Dev 4.155
Gaus mean : 43.2829
Gaus Width: 4.5353
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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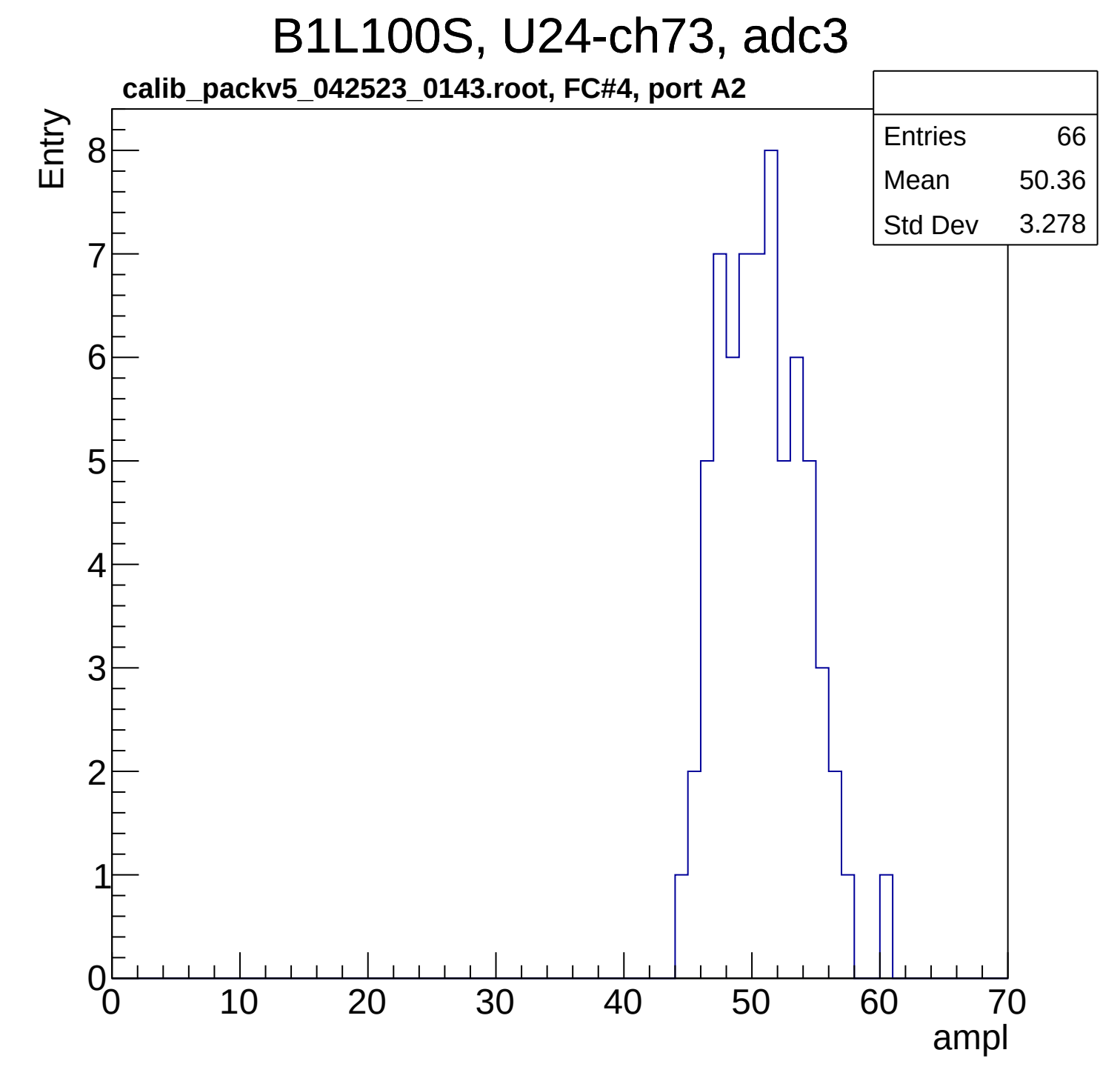
B1L100S, U24-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.36
Std Dev 3.278
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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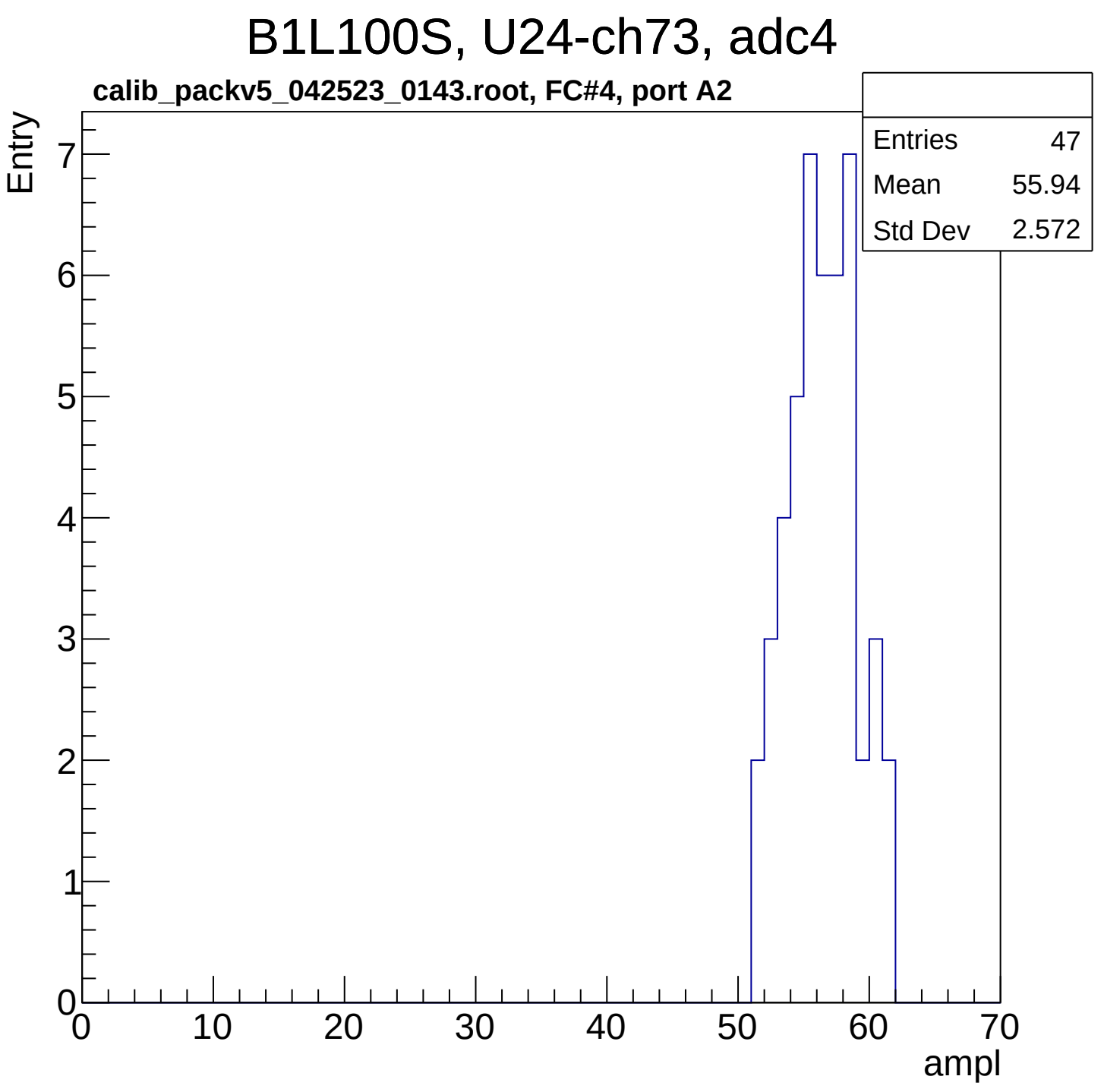
B1L100S, U24-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.94
Std Dev 2.572
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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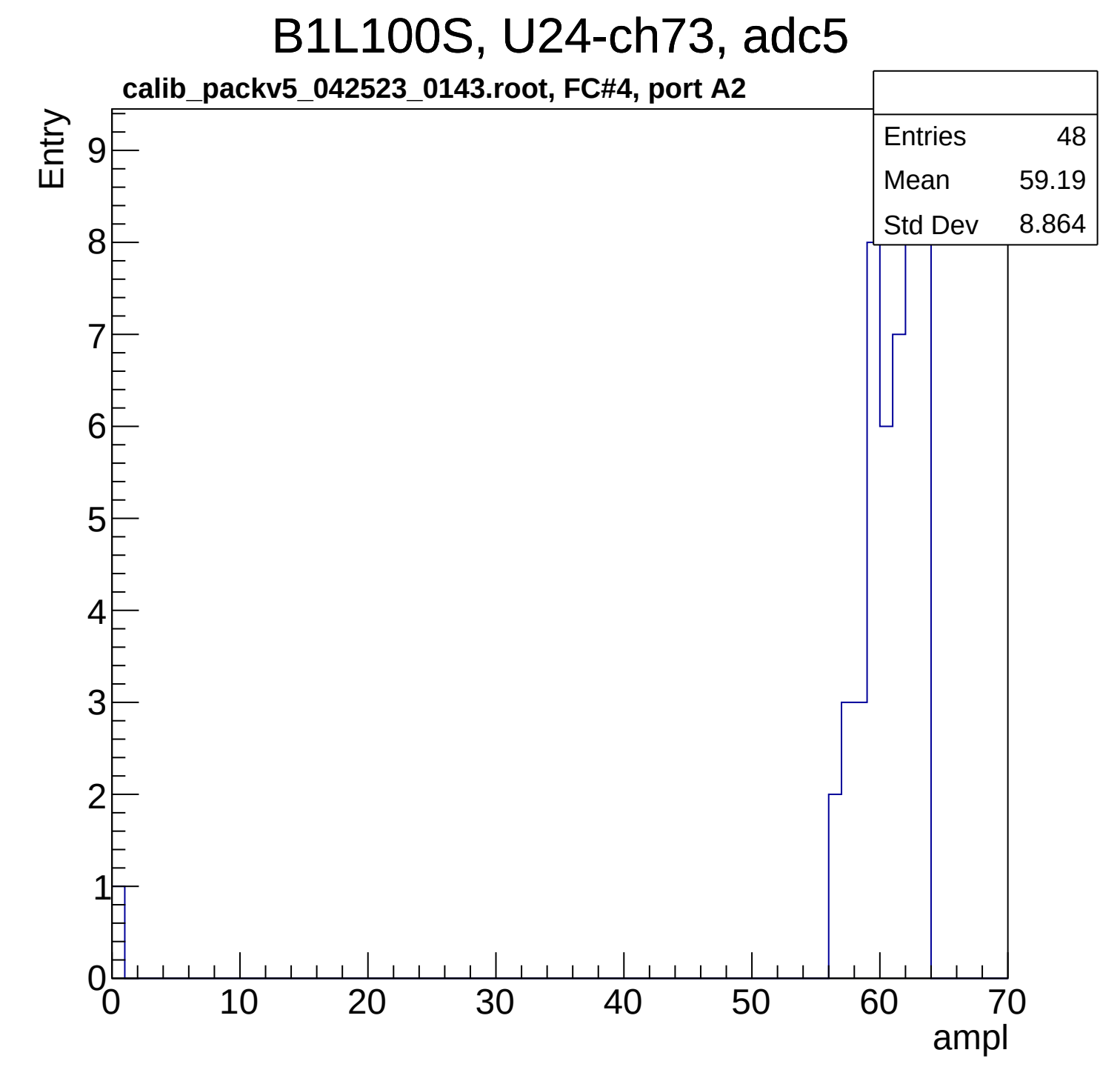
B1L100S, U24-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.19
Std Dev 8.864
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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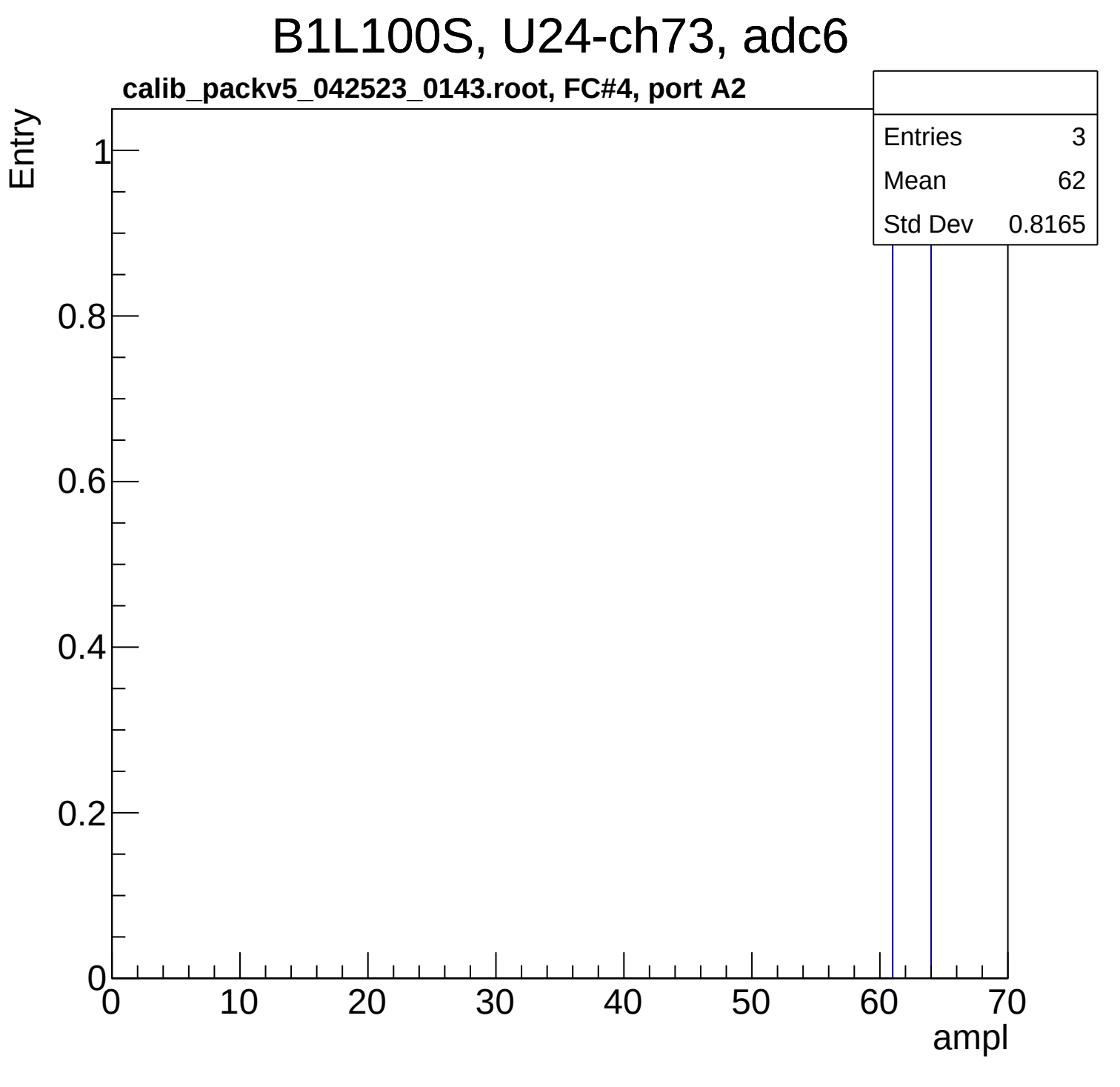
B1L100S, U24-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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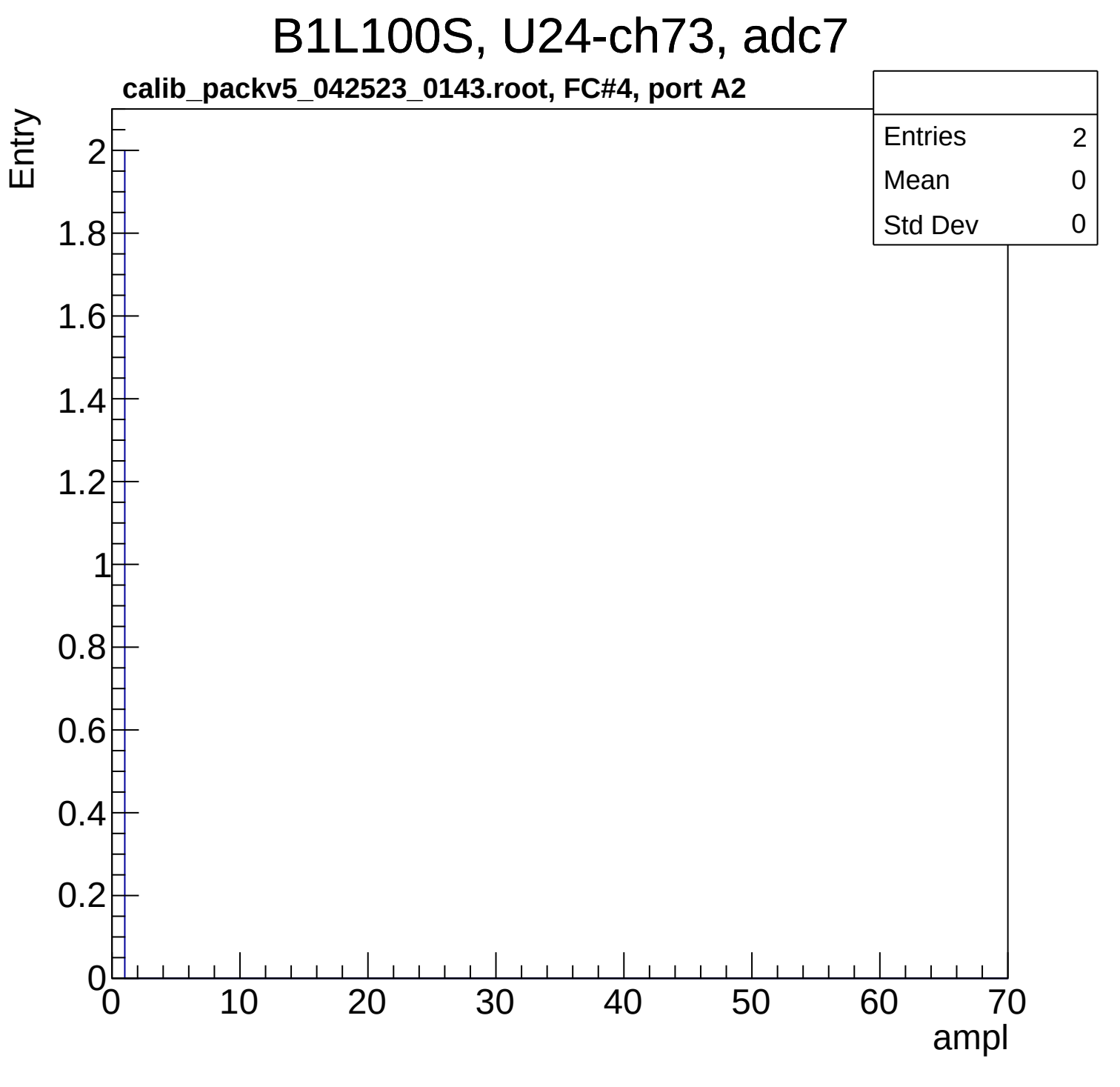
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch73, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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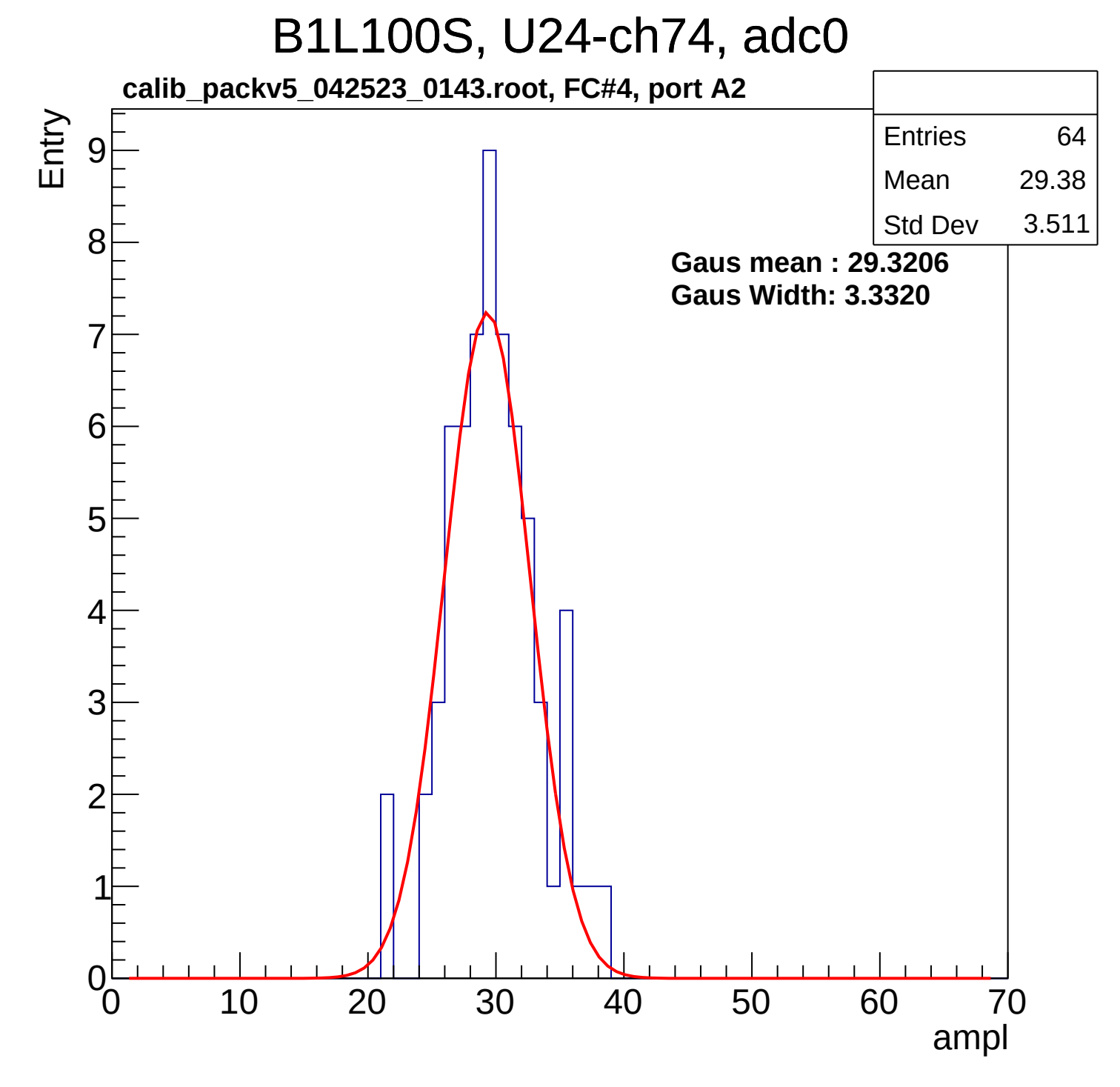
50
60
70
ampl


B1L100S, U24-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.38
Std Dev 3.511
Gaus mean : 29.3206
Gaus Width: 3.3320
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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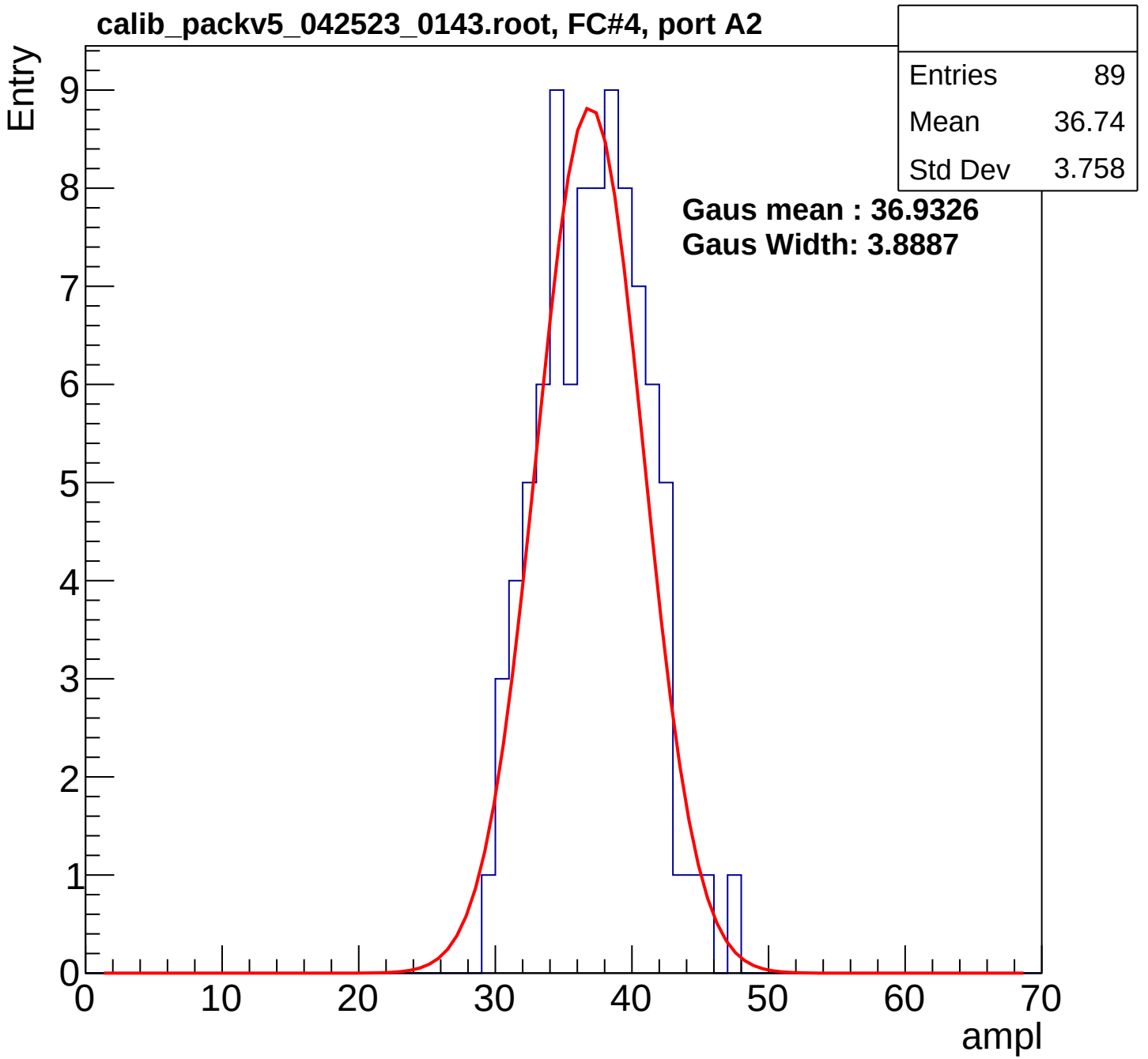
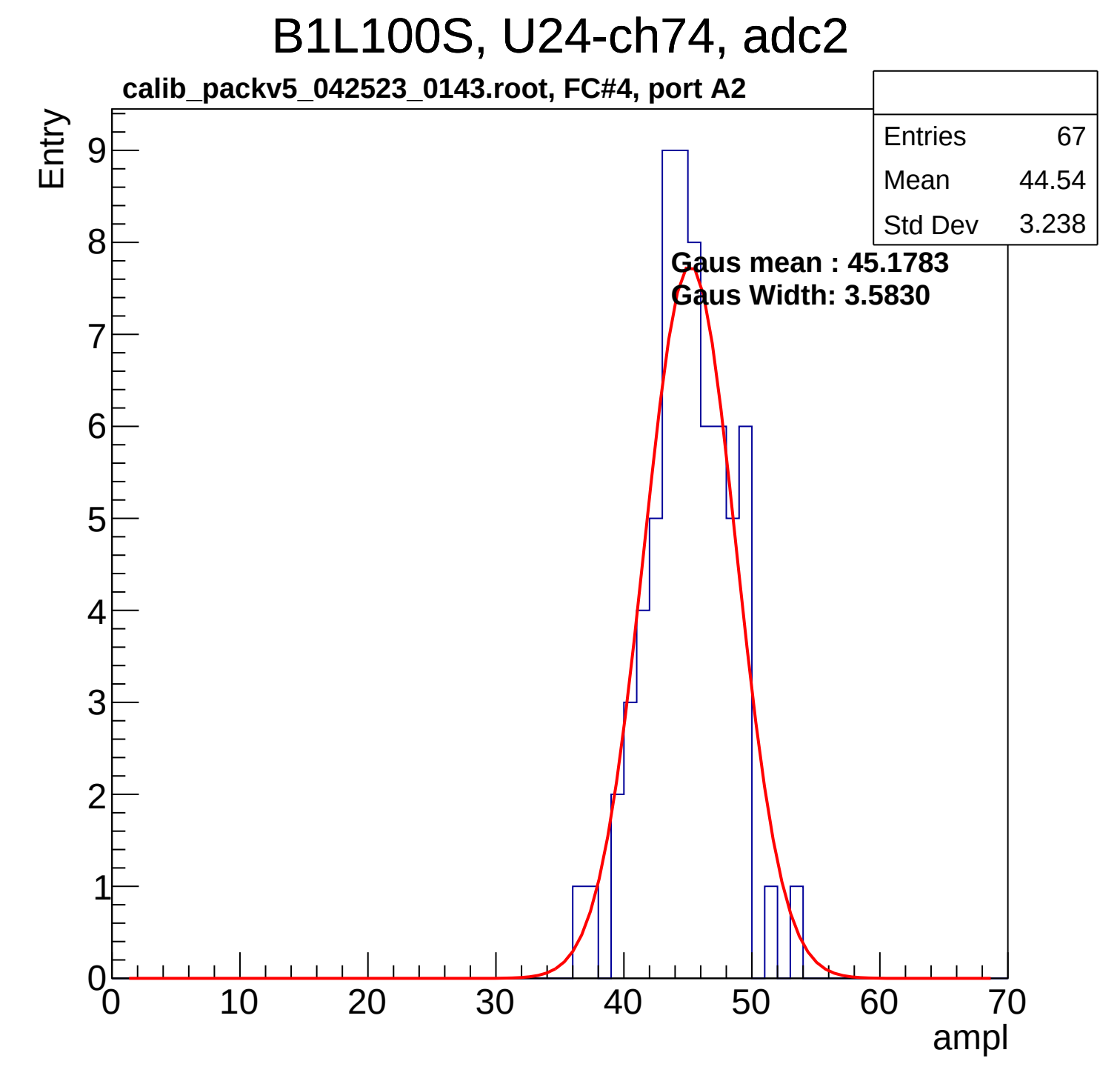
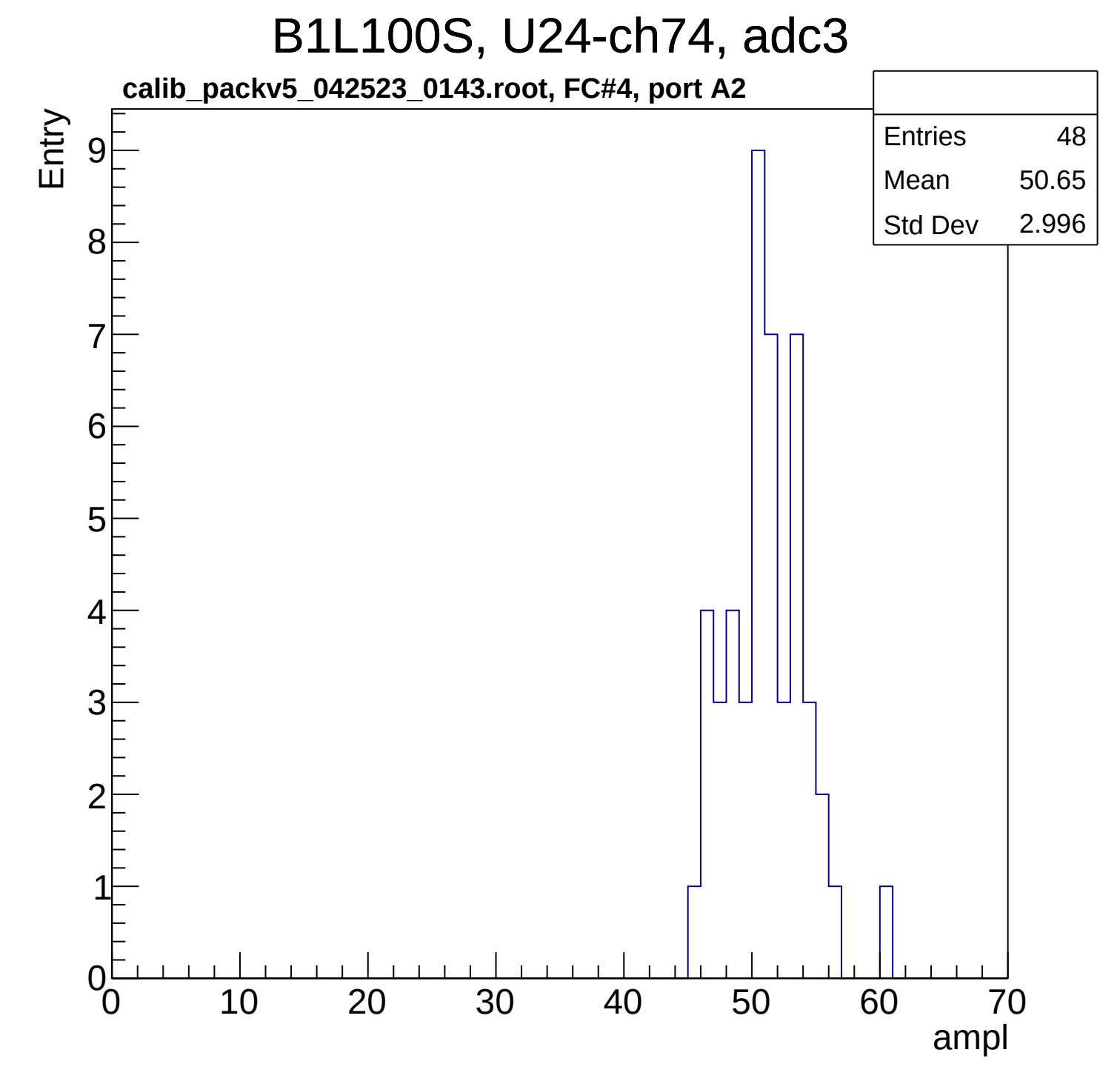
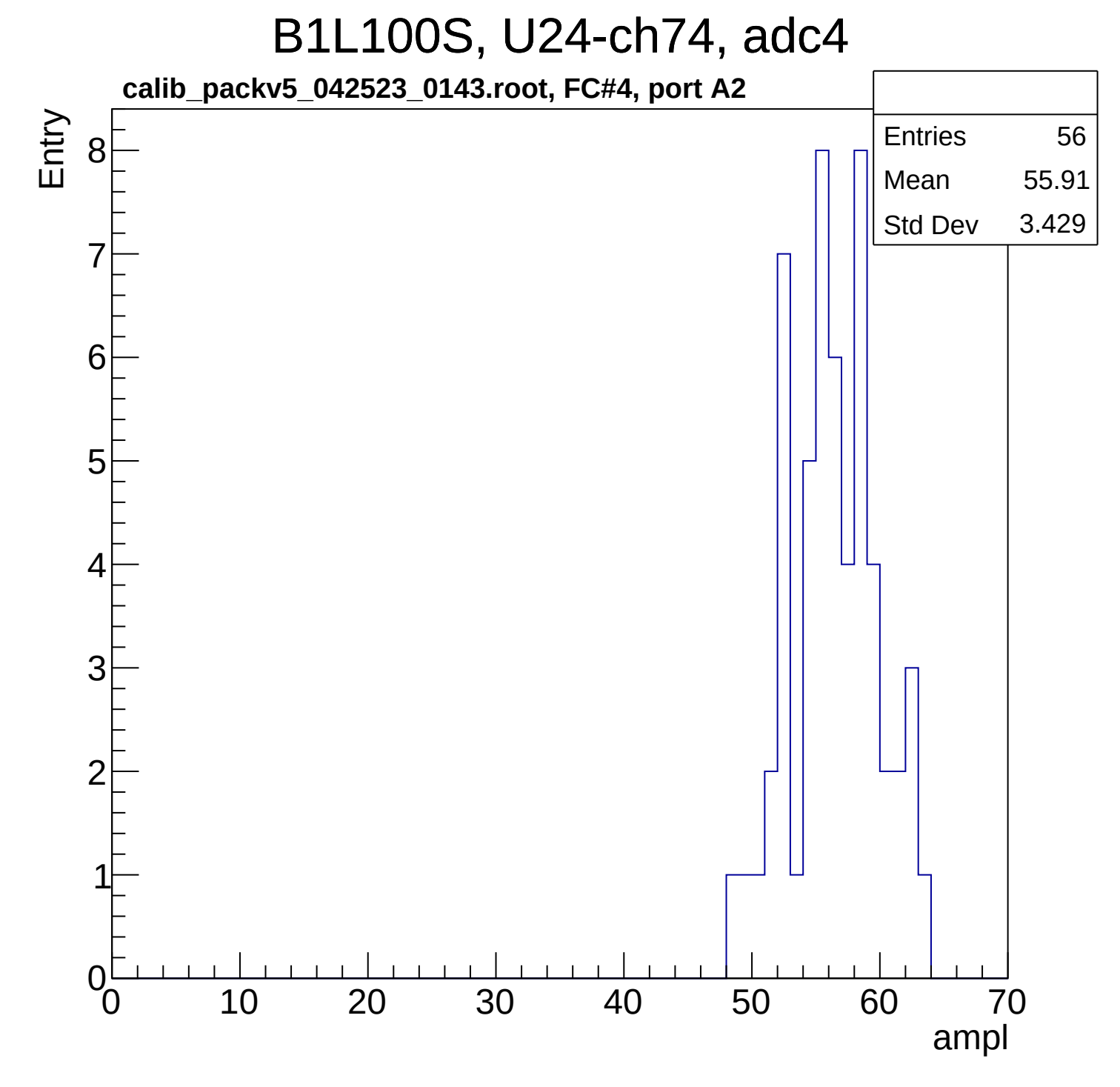
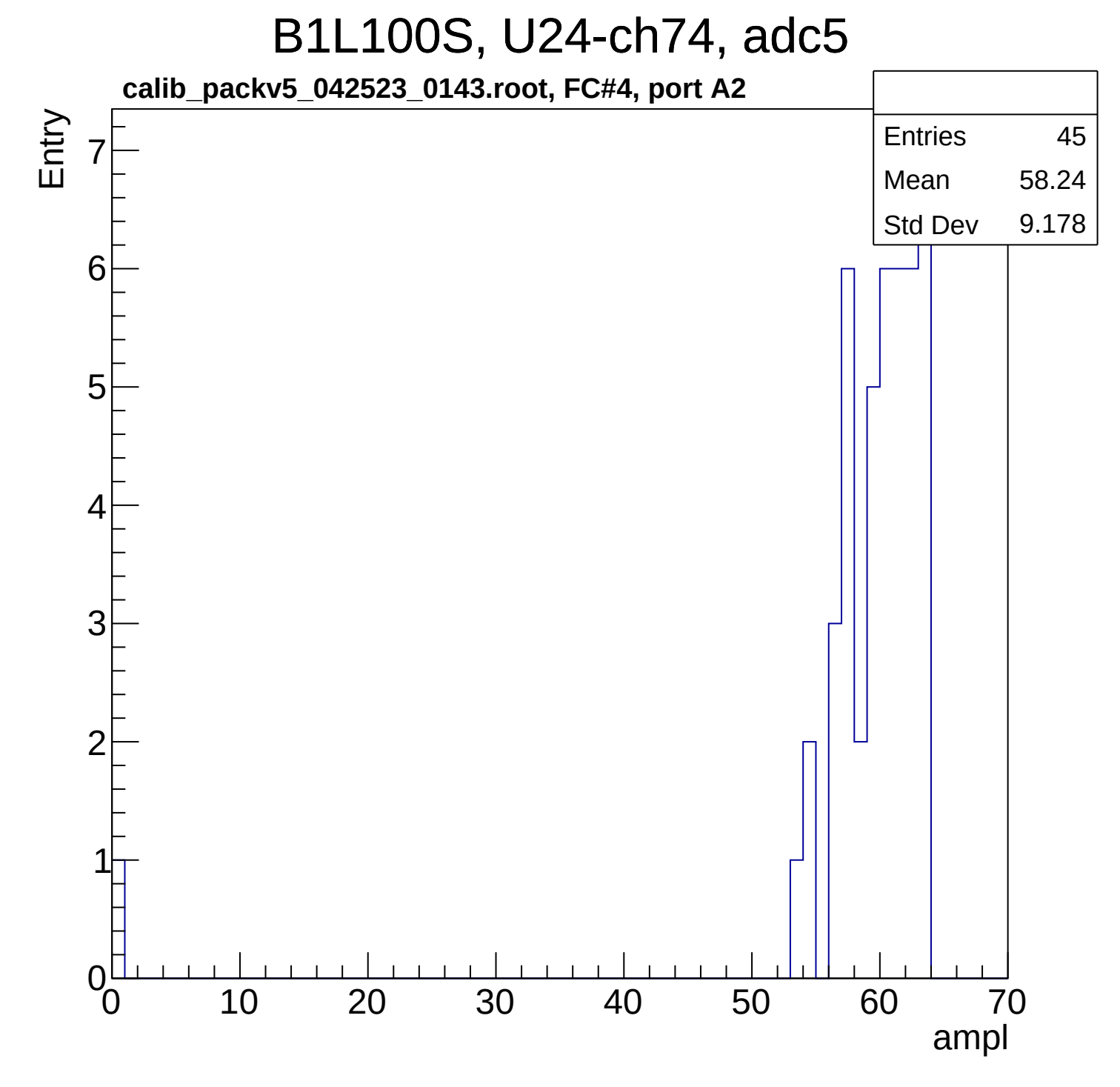

B1L100S, U24-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 36.74
Std Dev 3.758
Gaus mean : 36.9326
Gaus Width: 3.8887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.54
Std Dev 3.238
Gaus mean : 45.1783
Gaus Width: 3.5830
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch74, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 50.65
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.91
Std Dev 3.429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.24
Std Dev 9.178
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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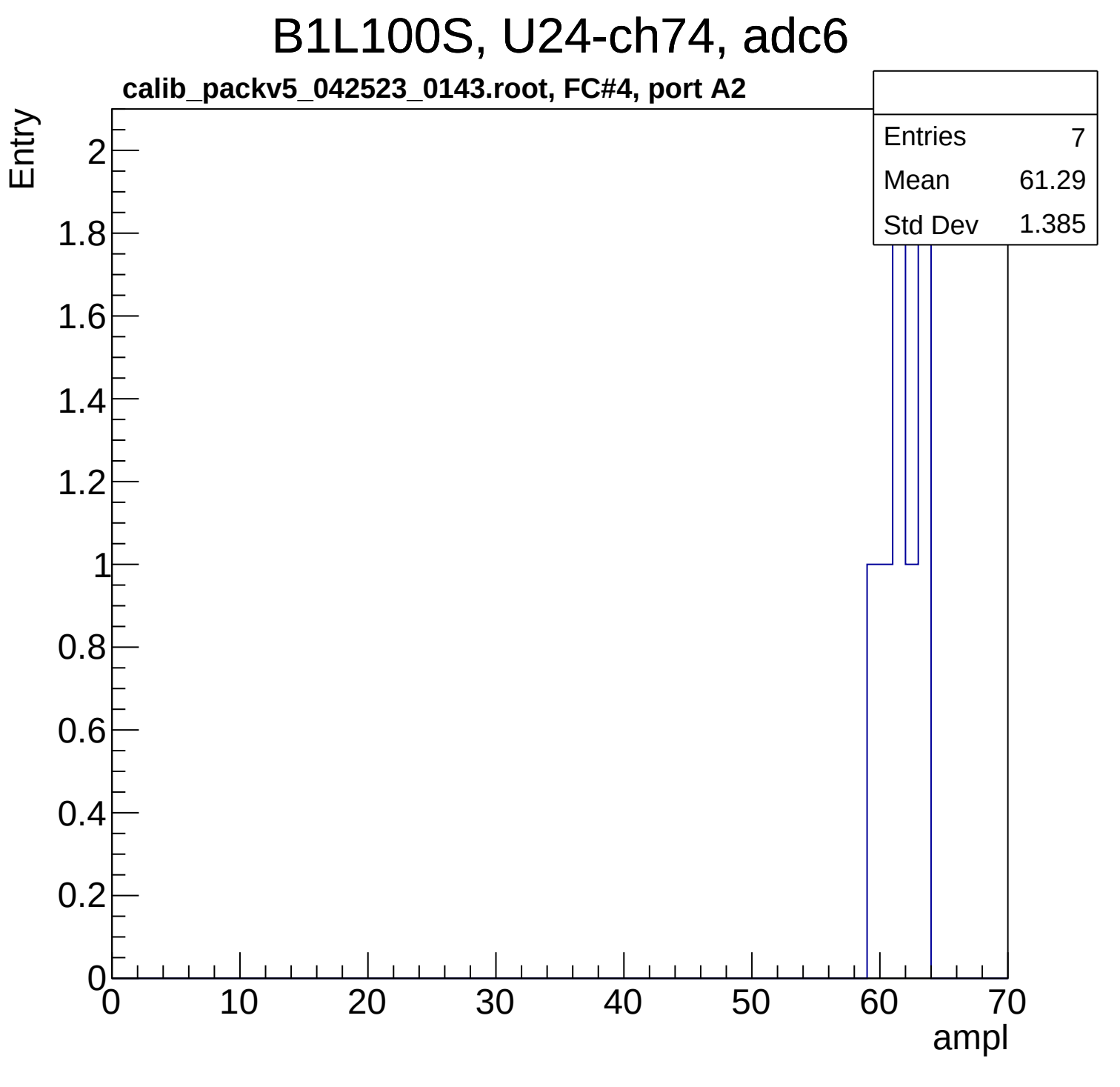
B1L100S, U24-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.29
Std Dev 1.385
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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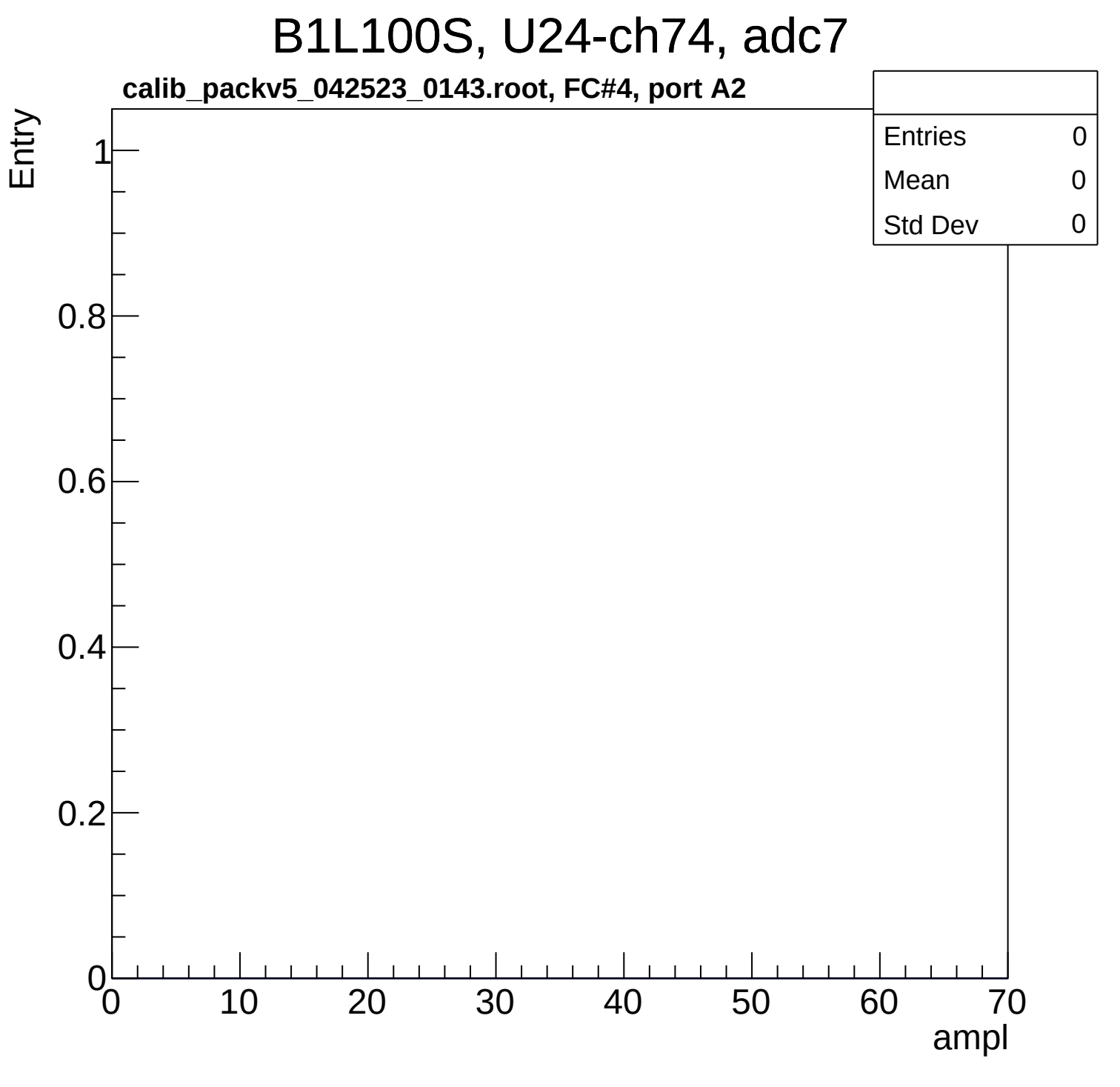
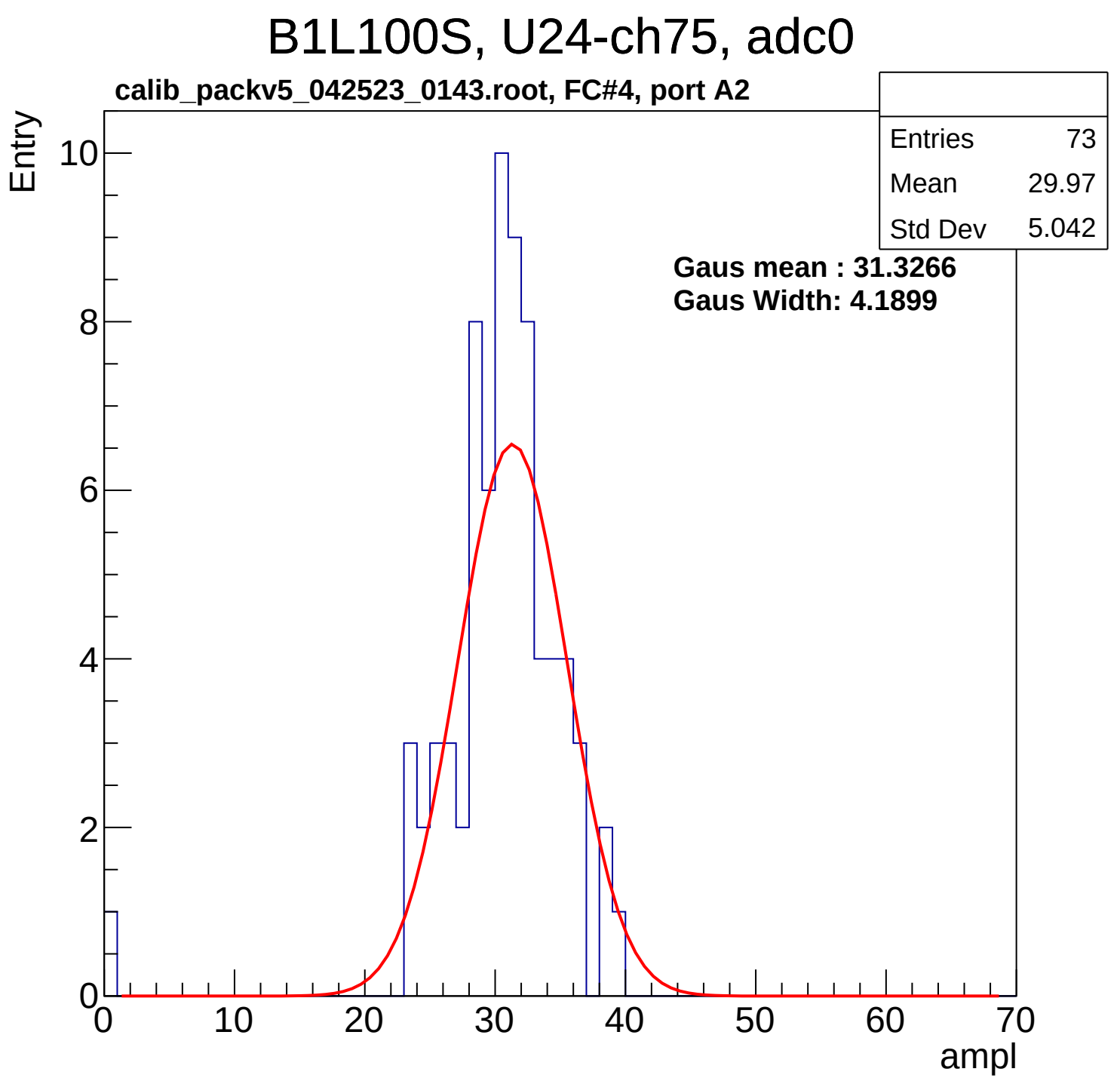
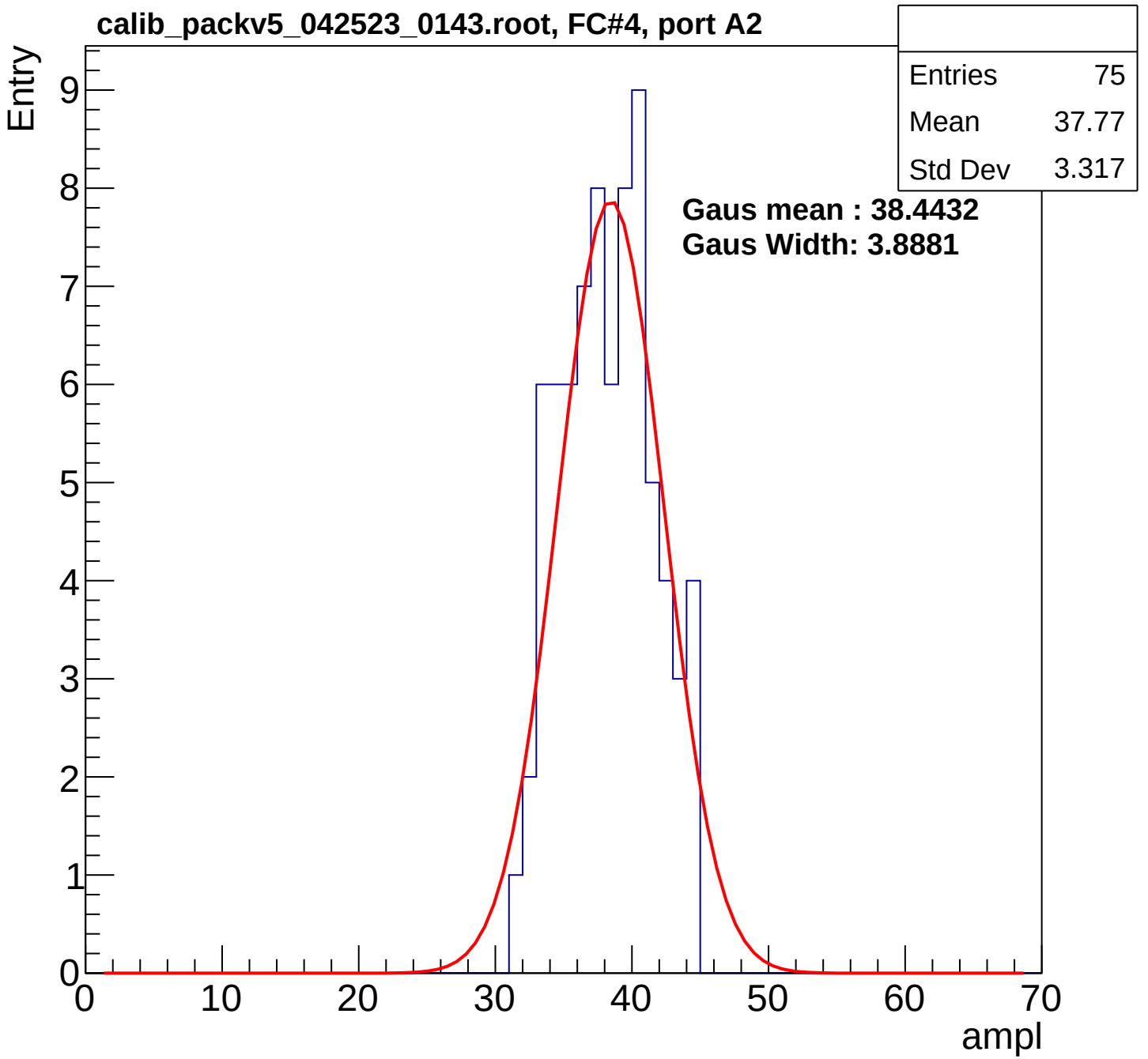
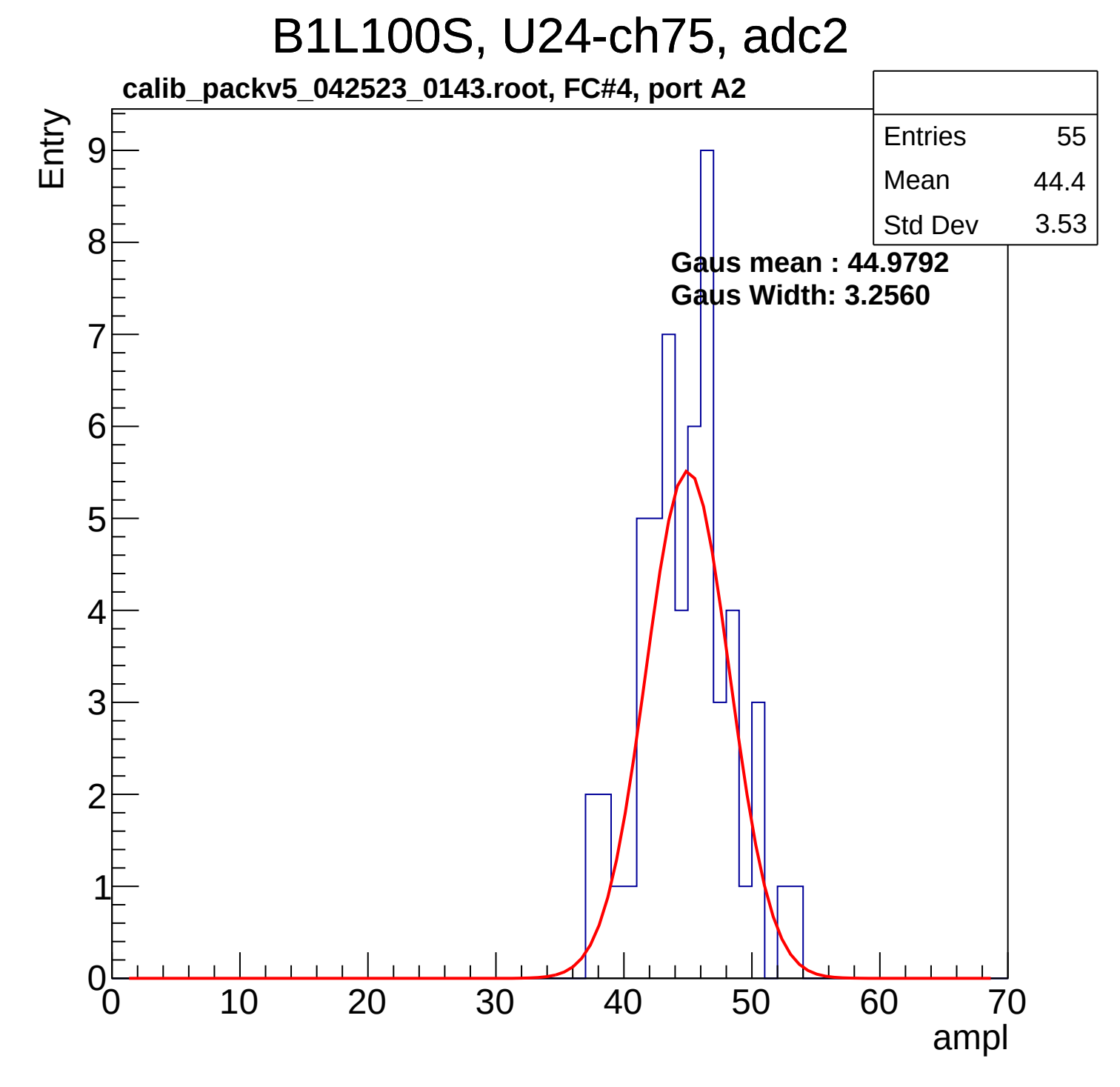
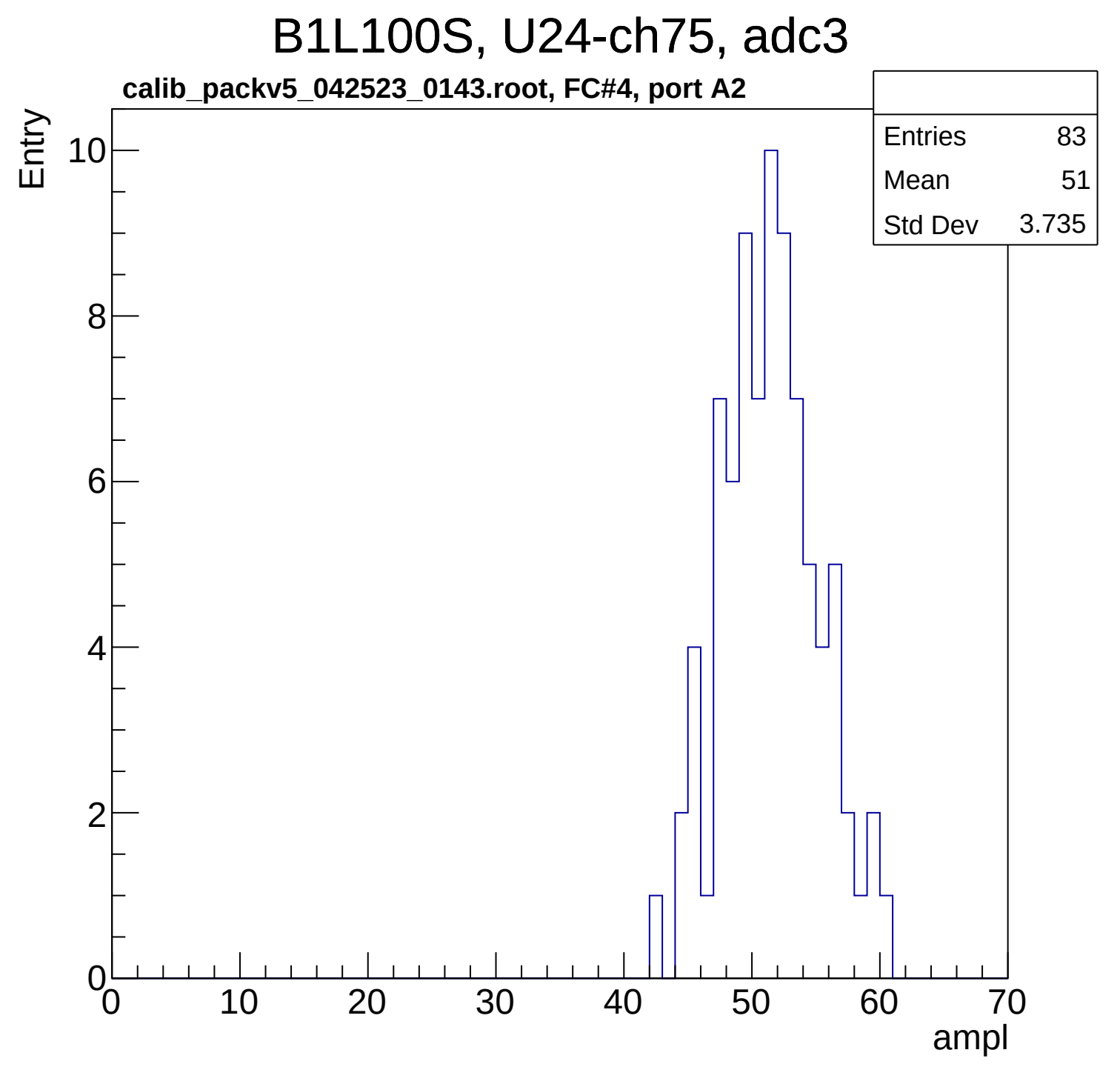
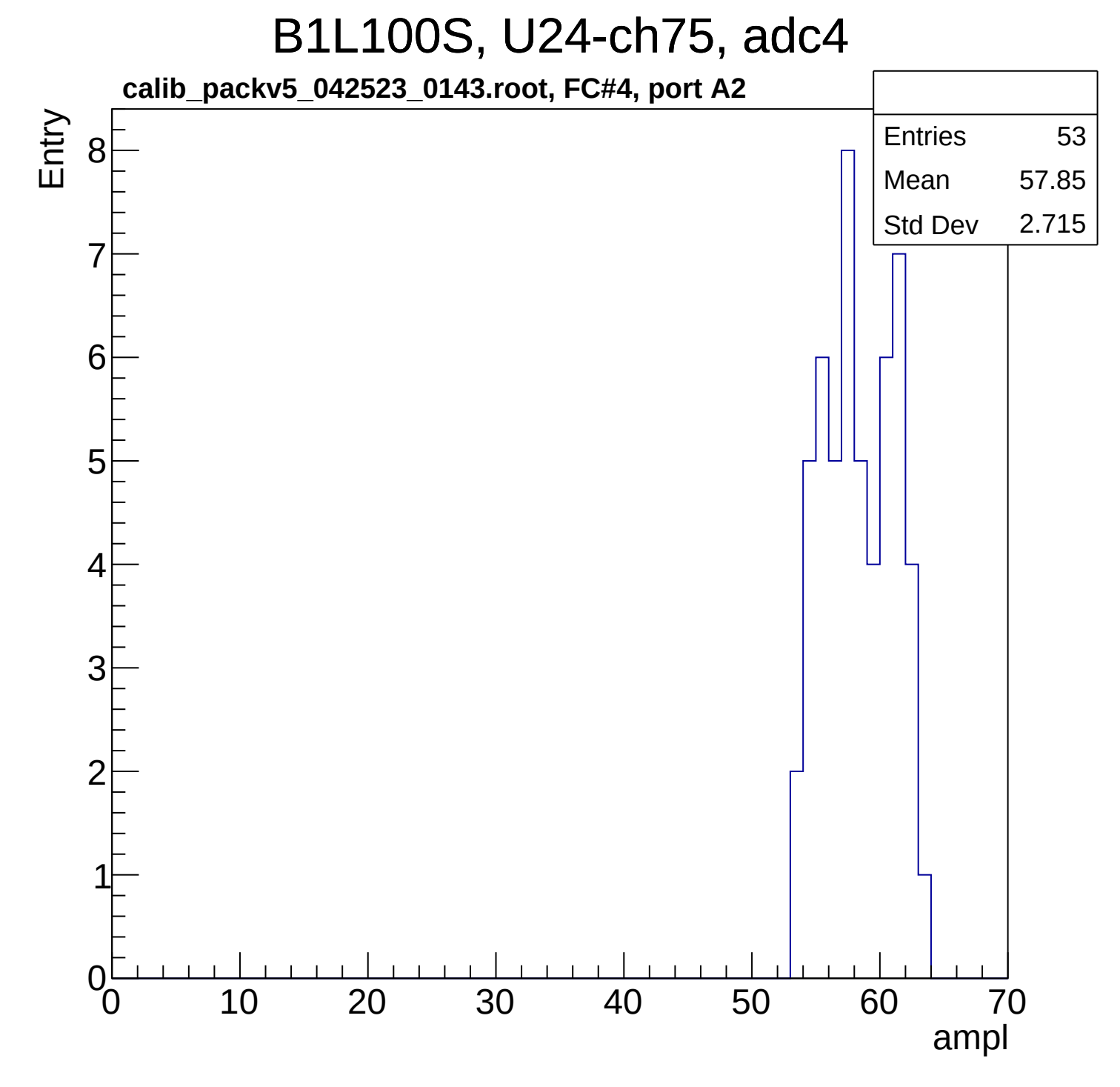

B1L100S, U24-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 29.97
Std Dev 5.042
Gaus mean : 31.3266
Gaus Width: 4.1899
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.77
Std Dev 3.317
Gaus mean : 38.4432
Gaus Width: 3.8881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 44.4
Std Dev 3.53
Gaus mean : 44.9792
Gaus Width: 3.2560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch75, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 51
Std Dev 3.735
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 57.85
Std Dev 2.715
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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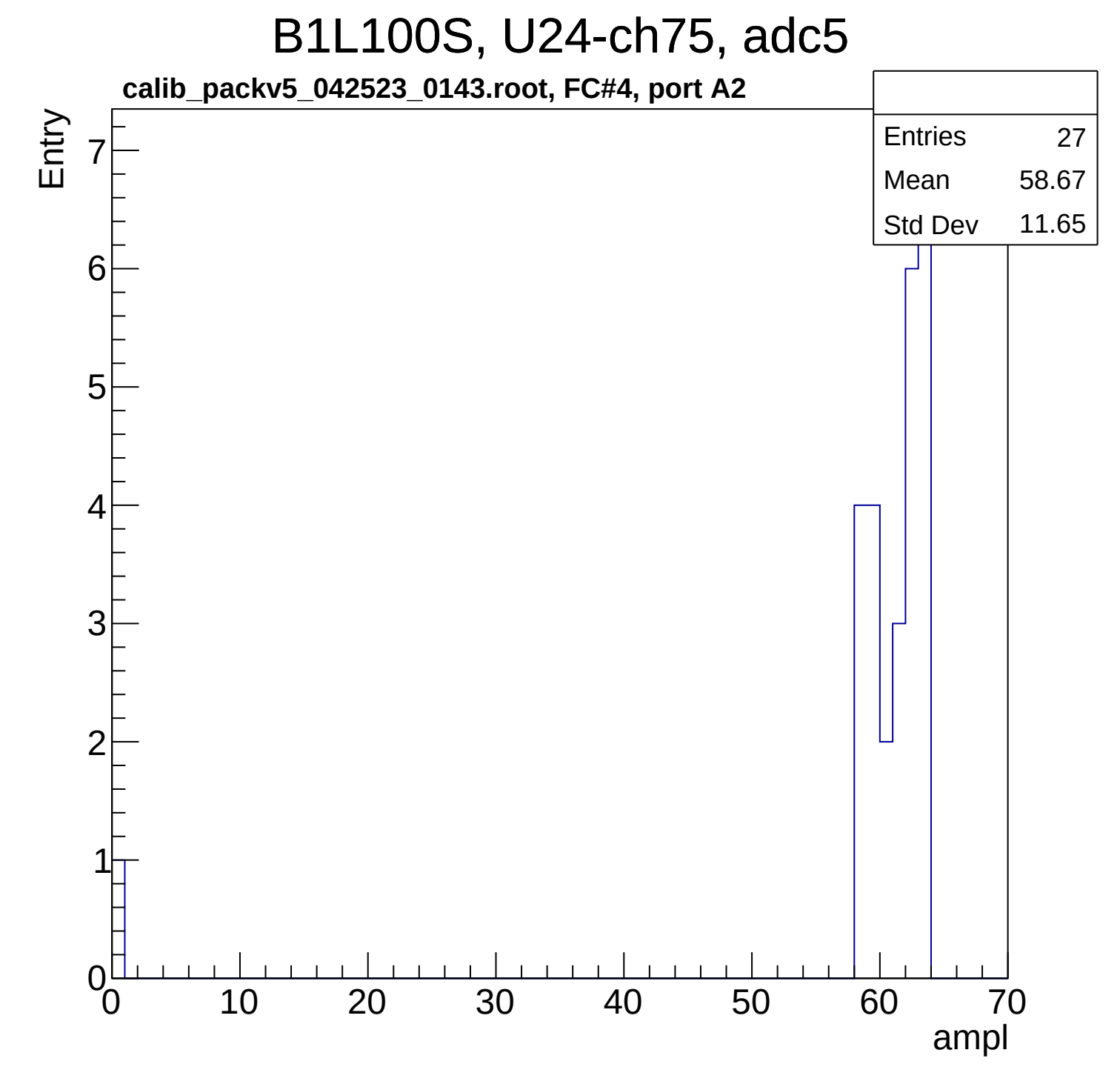
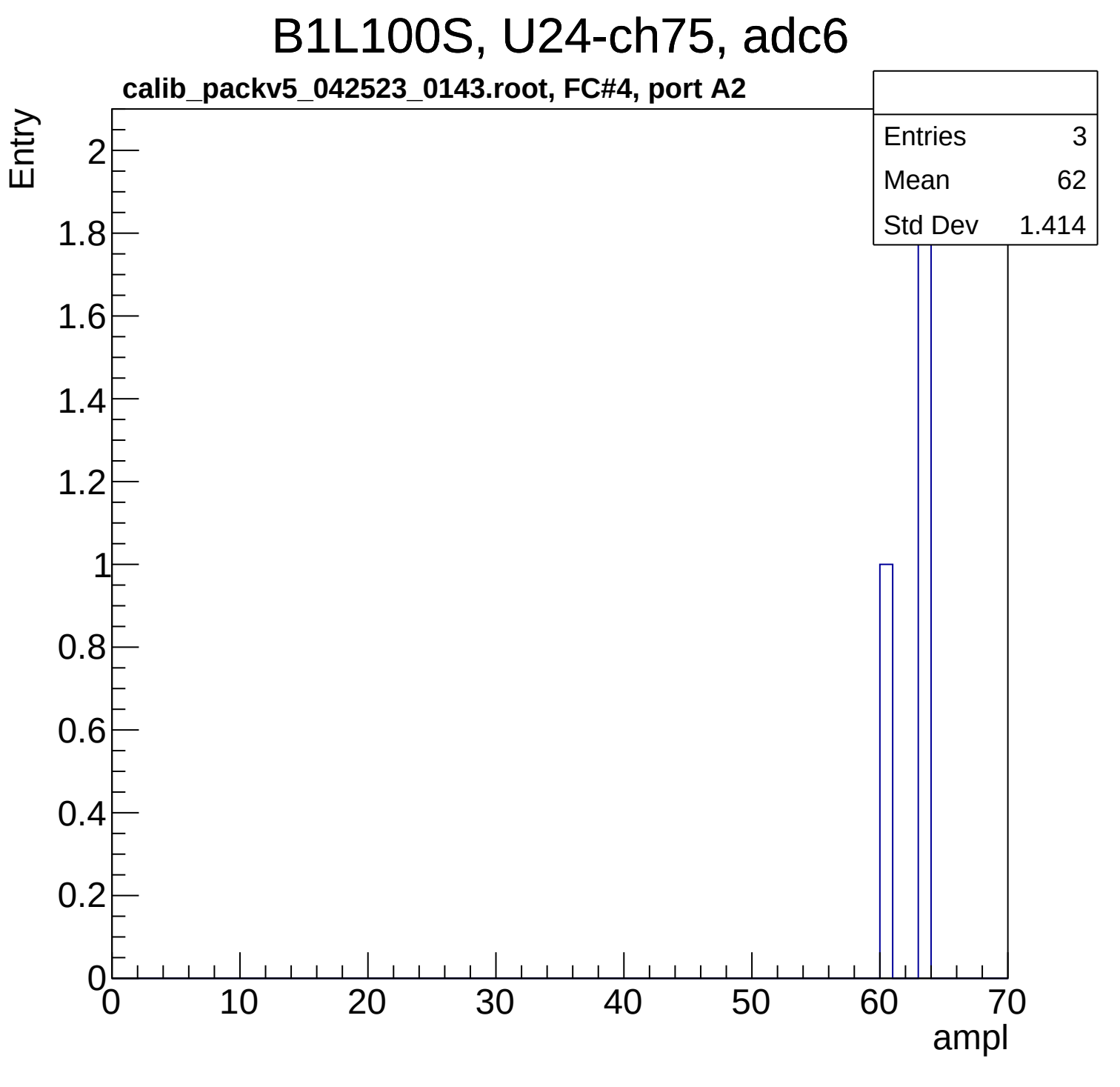

B1L100S, U24-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.67
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch75, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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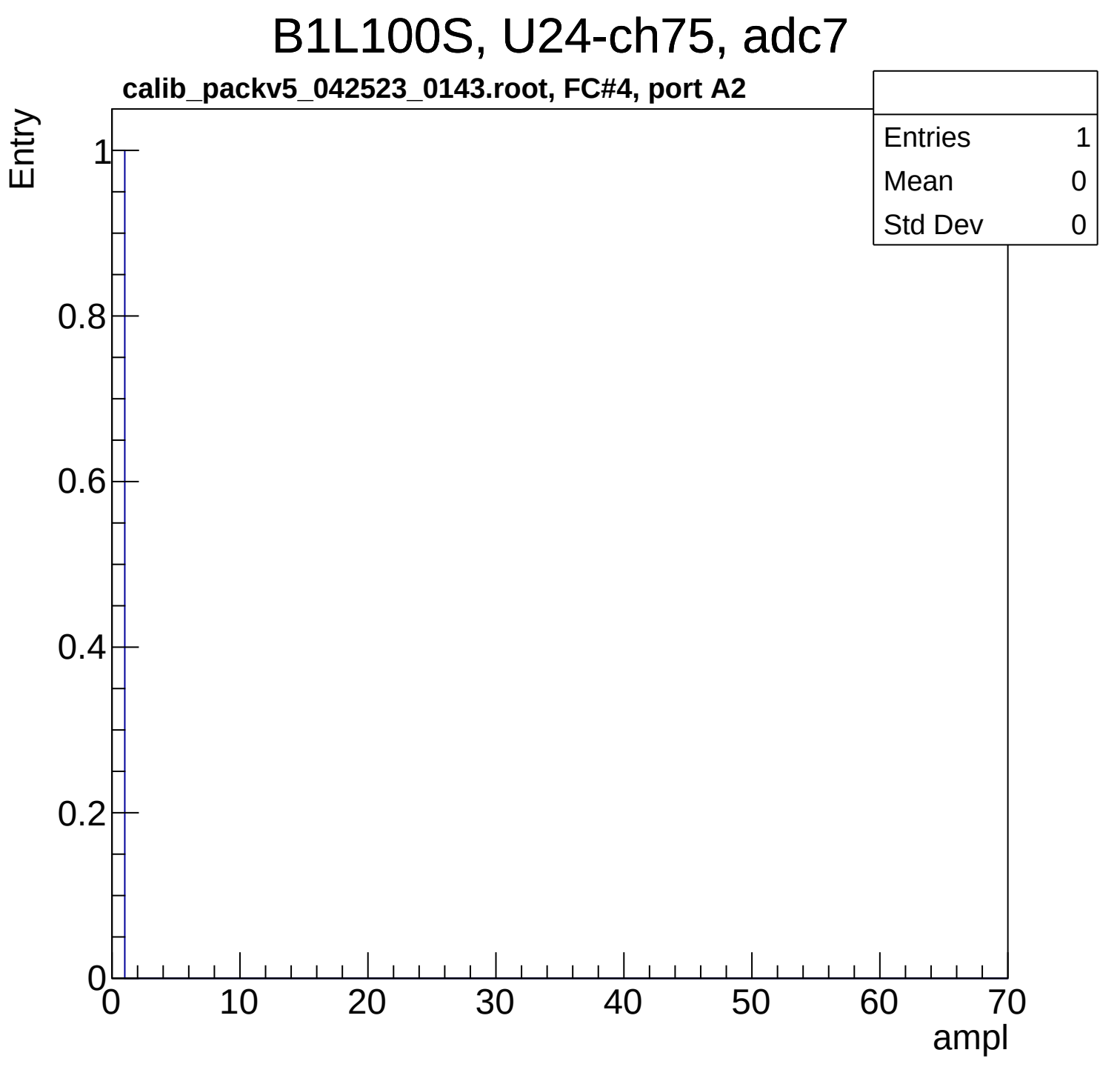
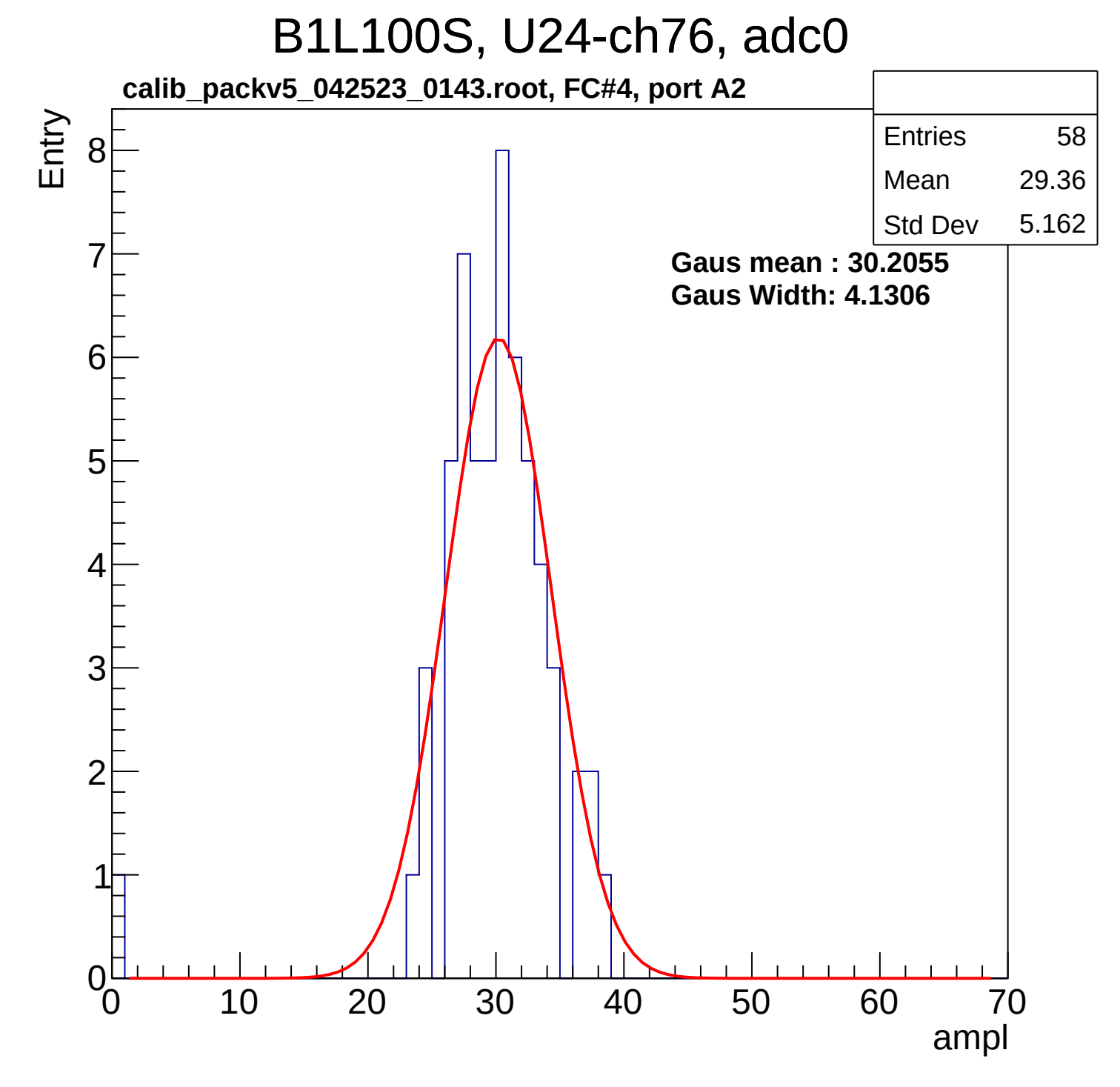

B1L100S, U24-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 29.36
Std Dev 5.162
Gaus mean : 30.2055
Gaus Width: 4.1306
Entry
ampl
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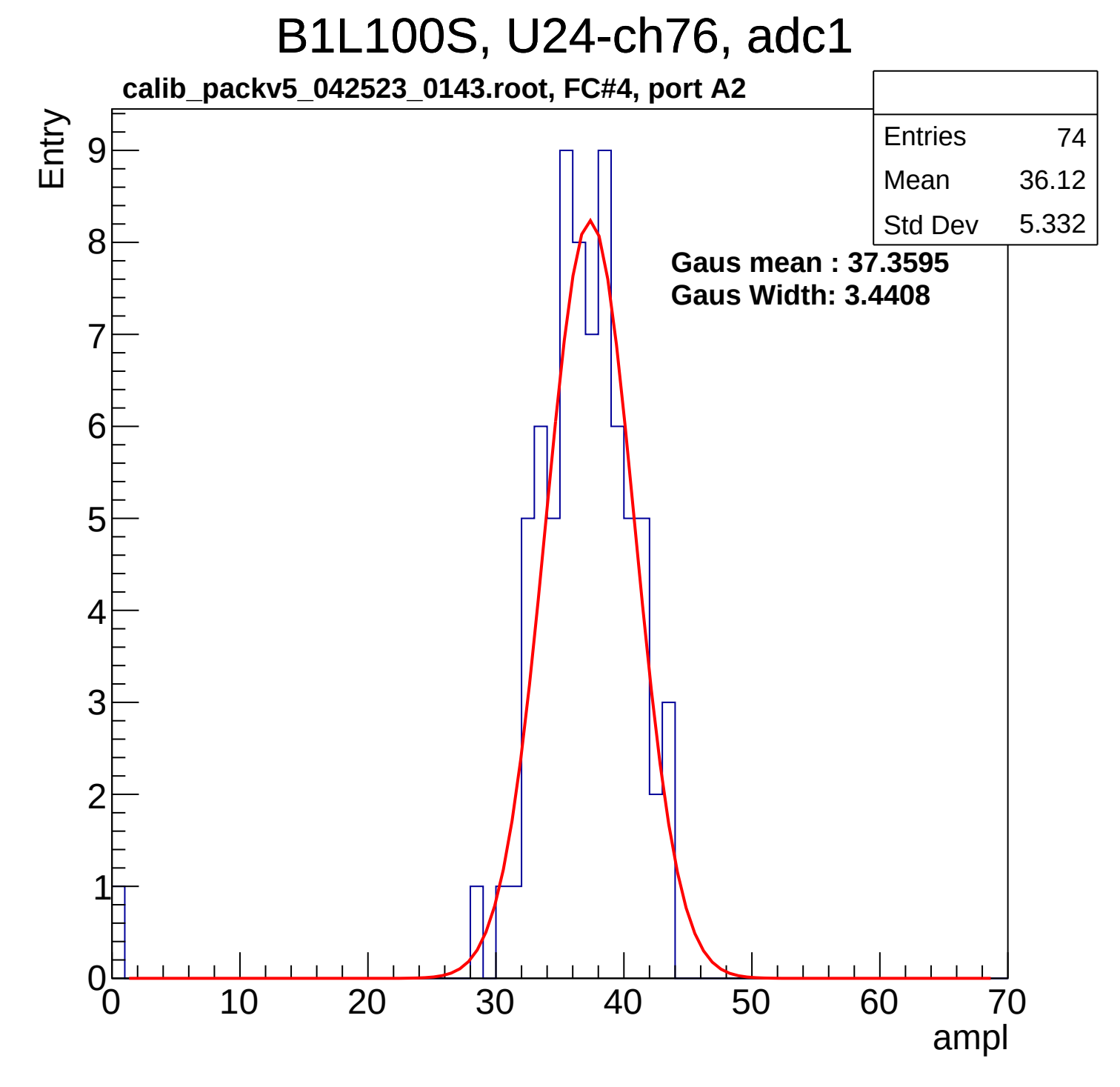
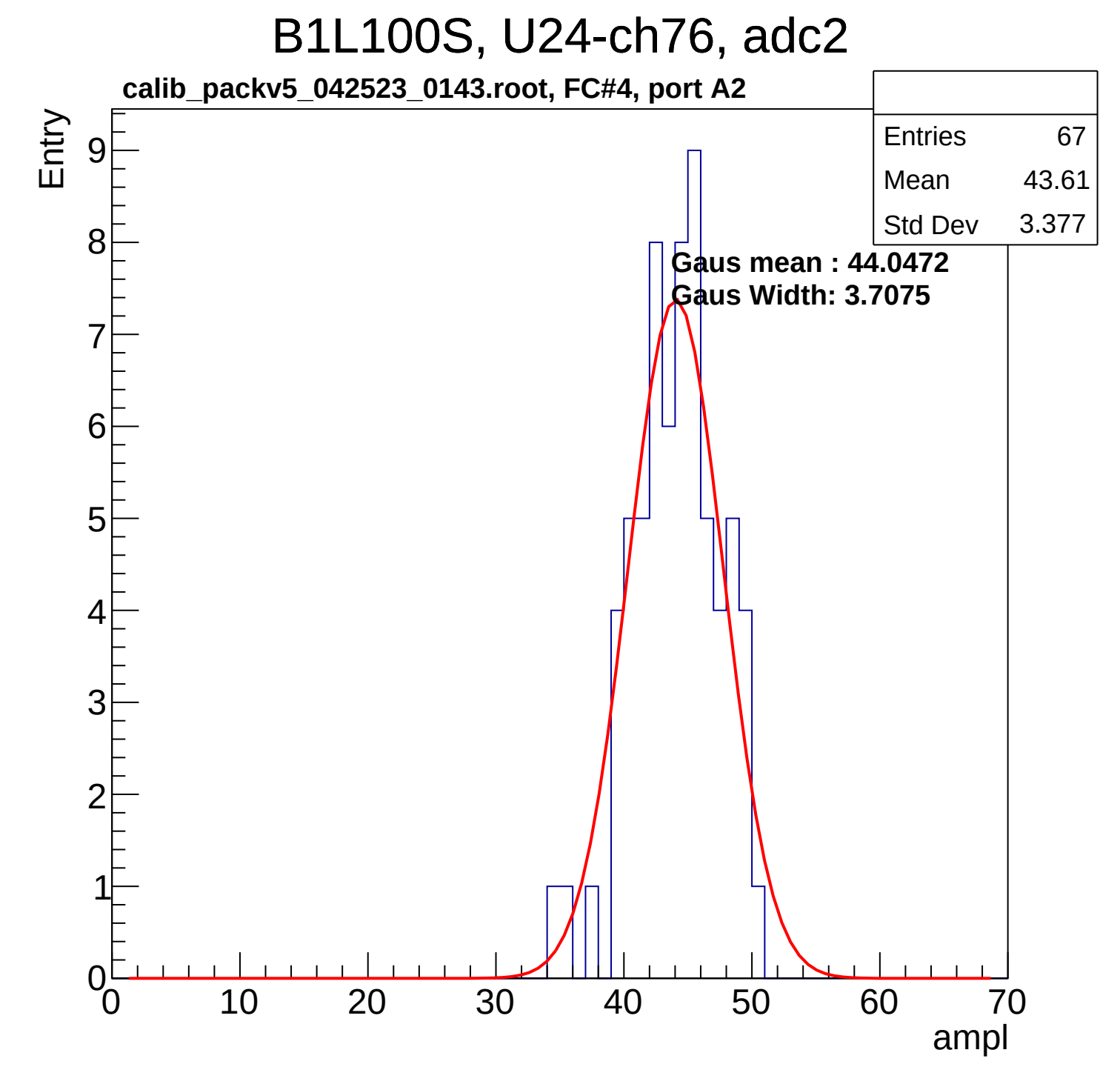
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 36.12
Std Dev 5.332
Gaus mean : 37.3595
Gaus Width: 3.4408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43.61
Std Dev 3.377
Gaus mean : 44.0472
Gaus Width: 3.7075
Entry
ampl
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2
3
4
5
6
7
8
9
0
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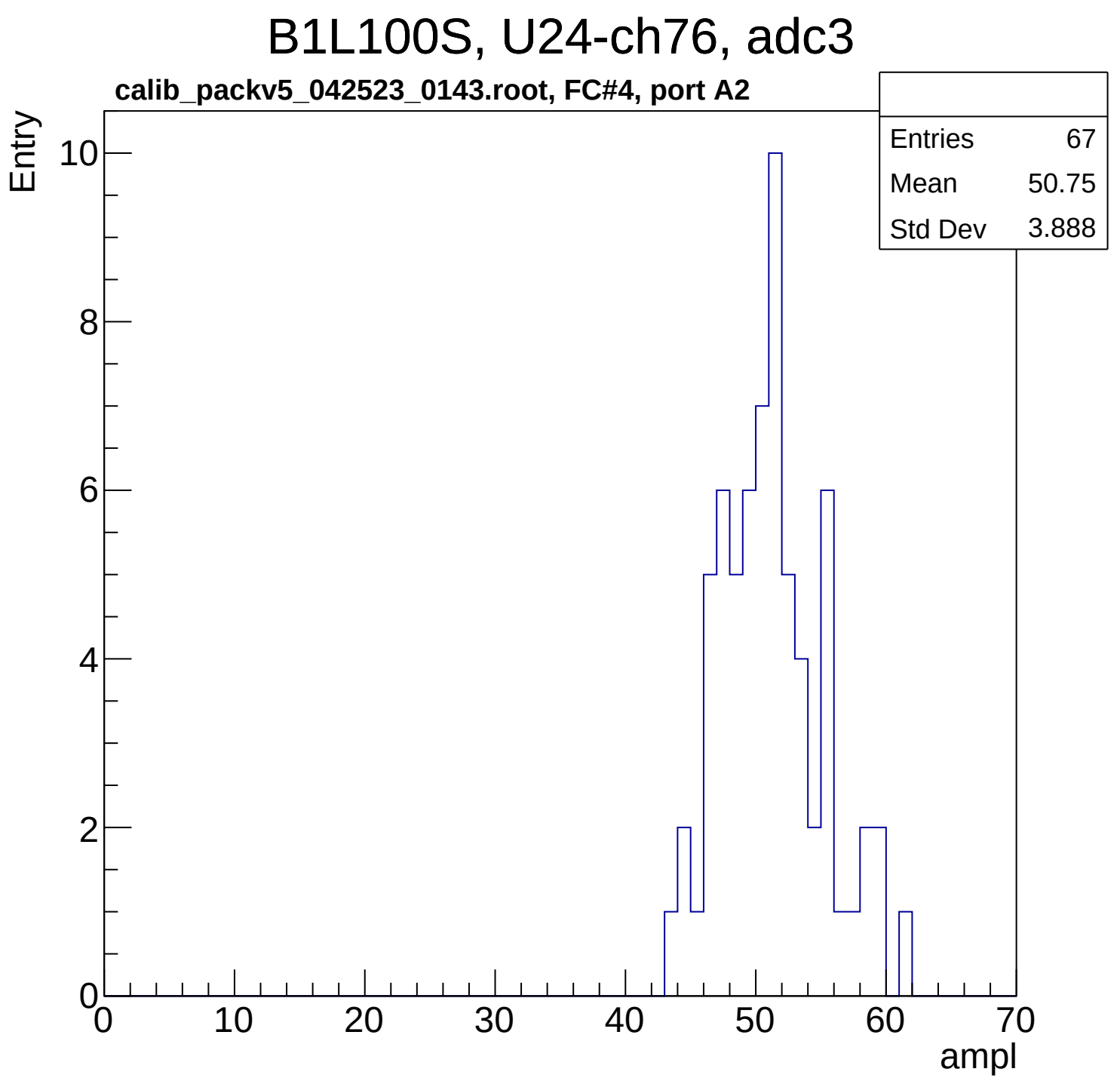
B1L100S, U24-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.75
Std Dev 3.888
Entry
ampl
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0
10
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50
60
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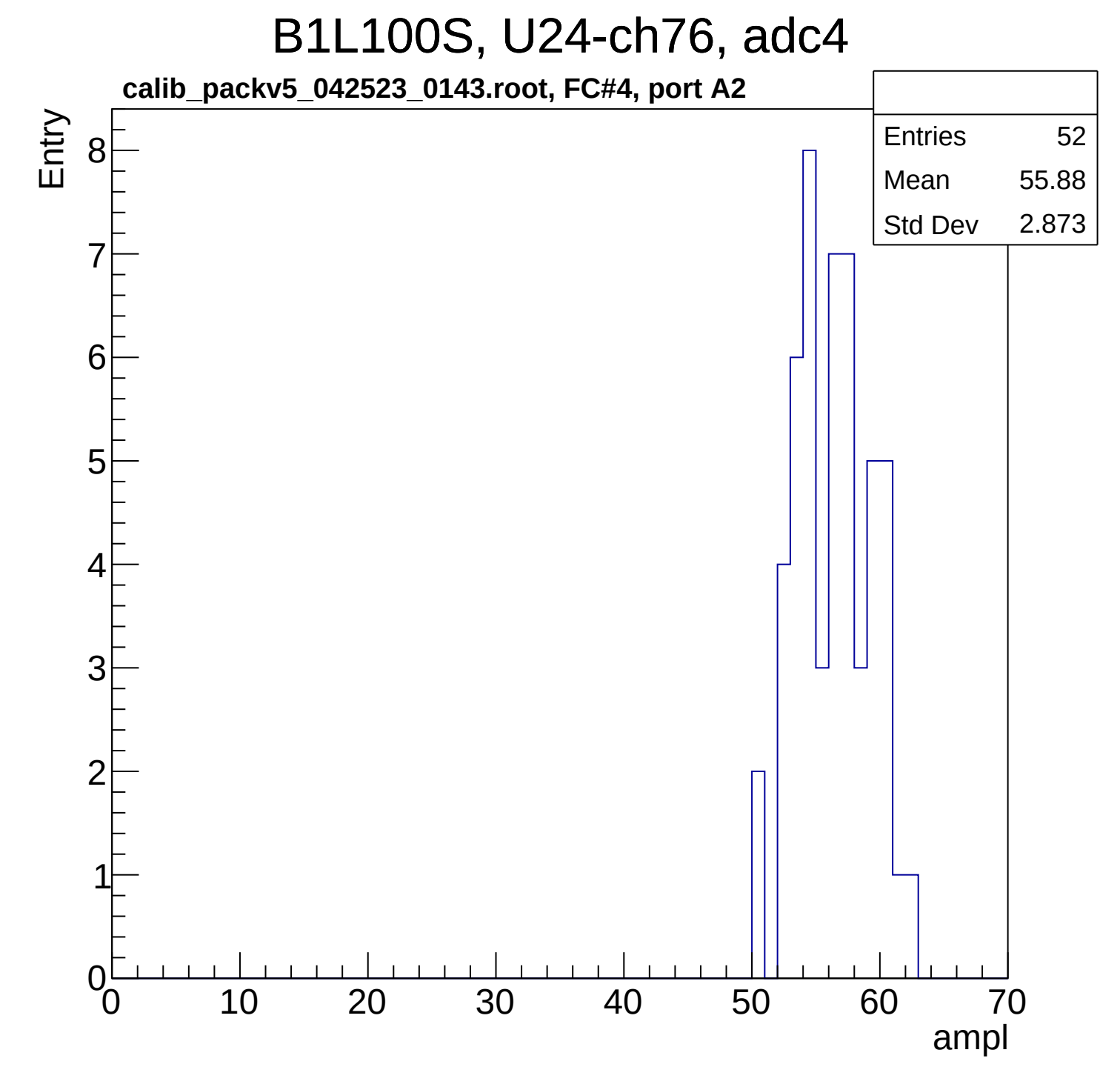
B1L100S, U24-ch76, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.88
Std Dev 2.873
Entry
ampl
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2
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4
5
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7
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0
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40
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60
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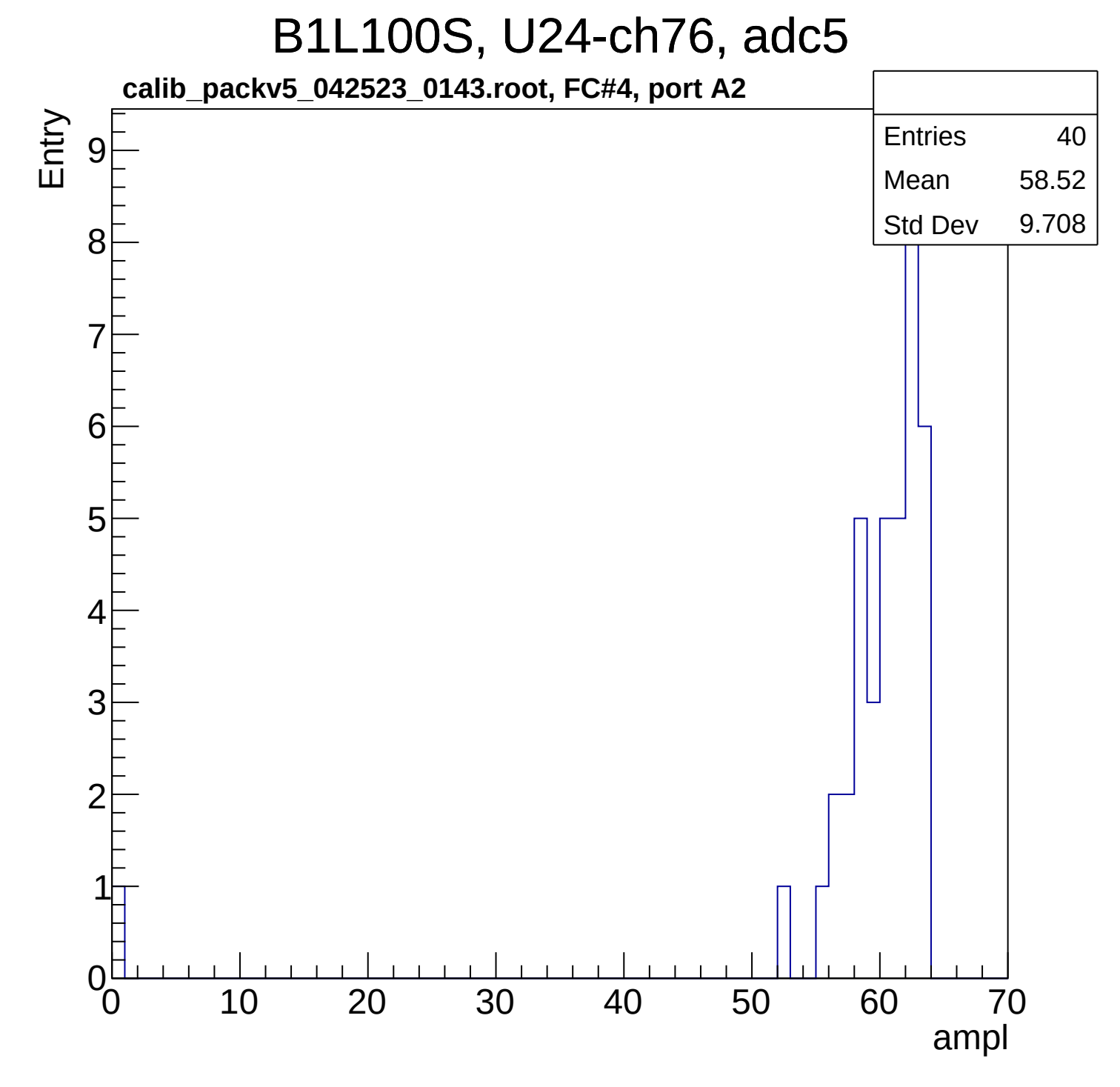
B1L100S, U24-ch76, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.52
Std Dev 9.708
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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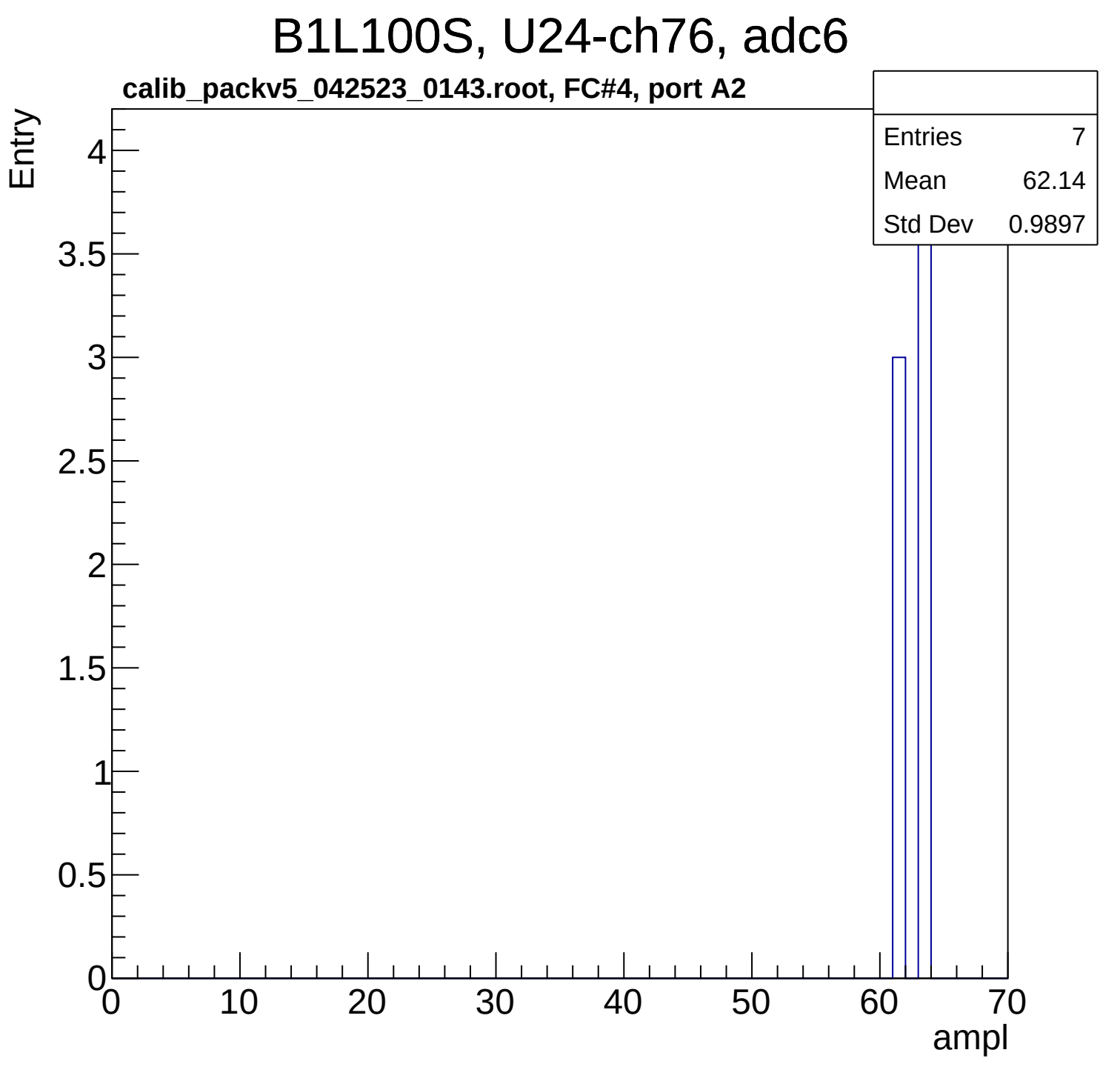
B1L100S, U24-ch76, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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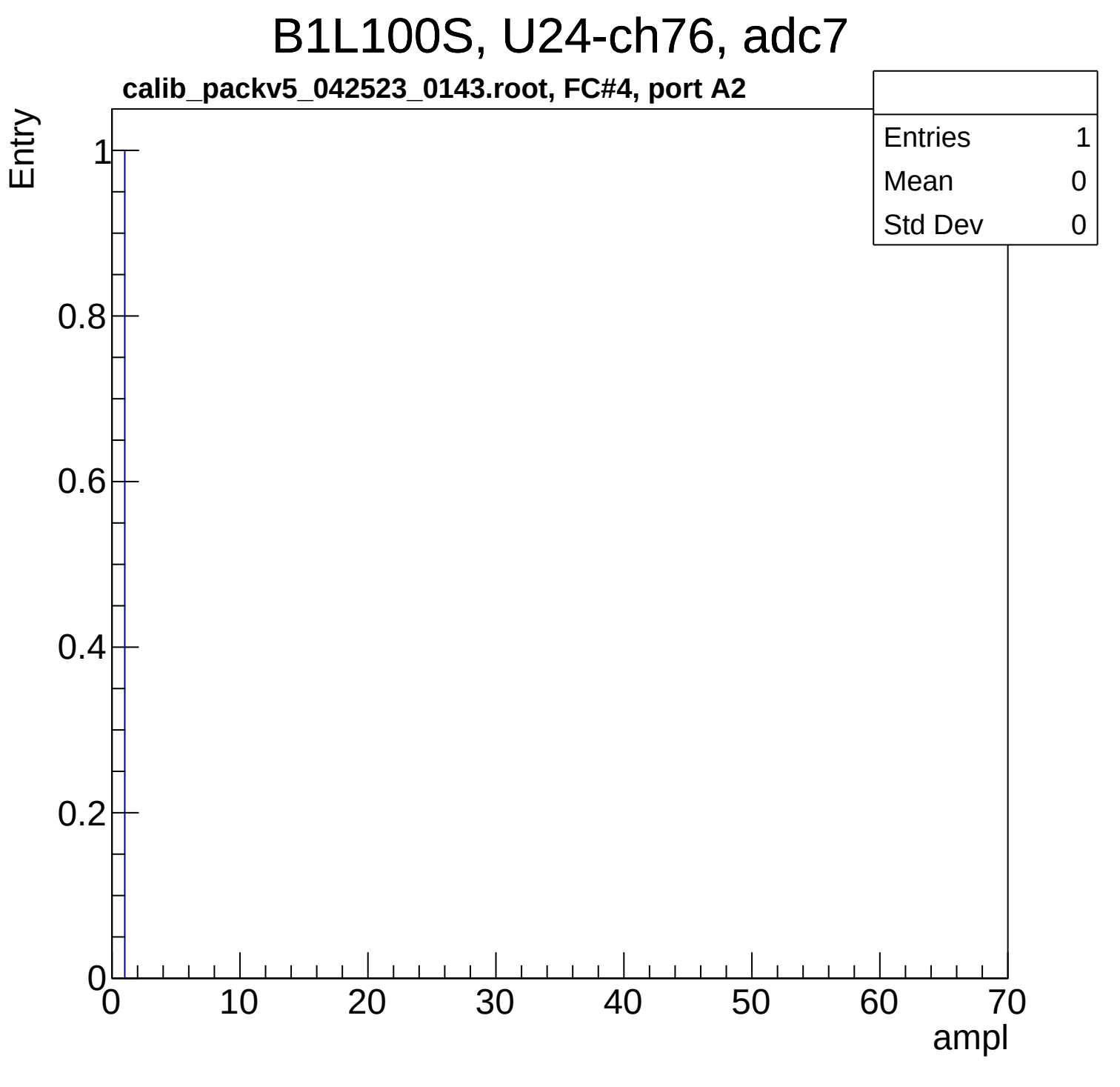
40
50
60
70


B1L100S, U24-ch76, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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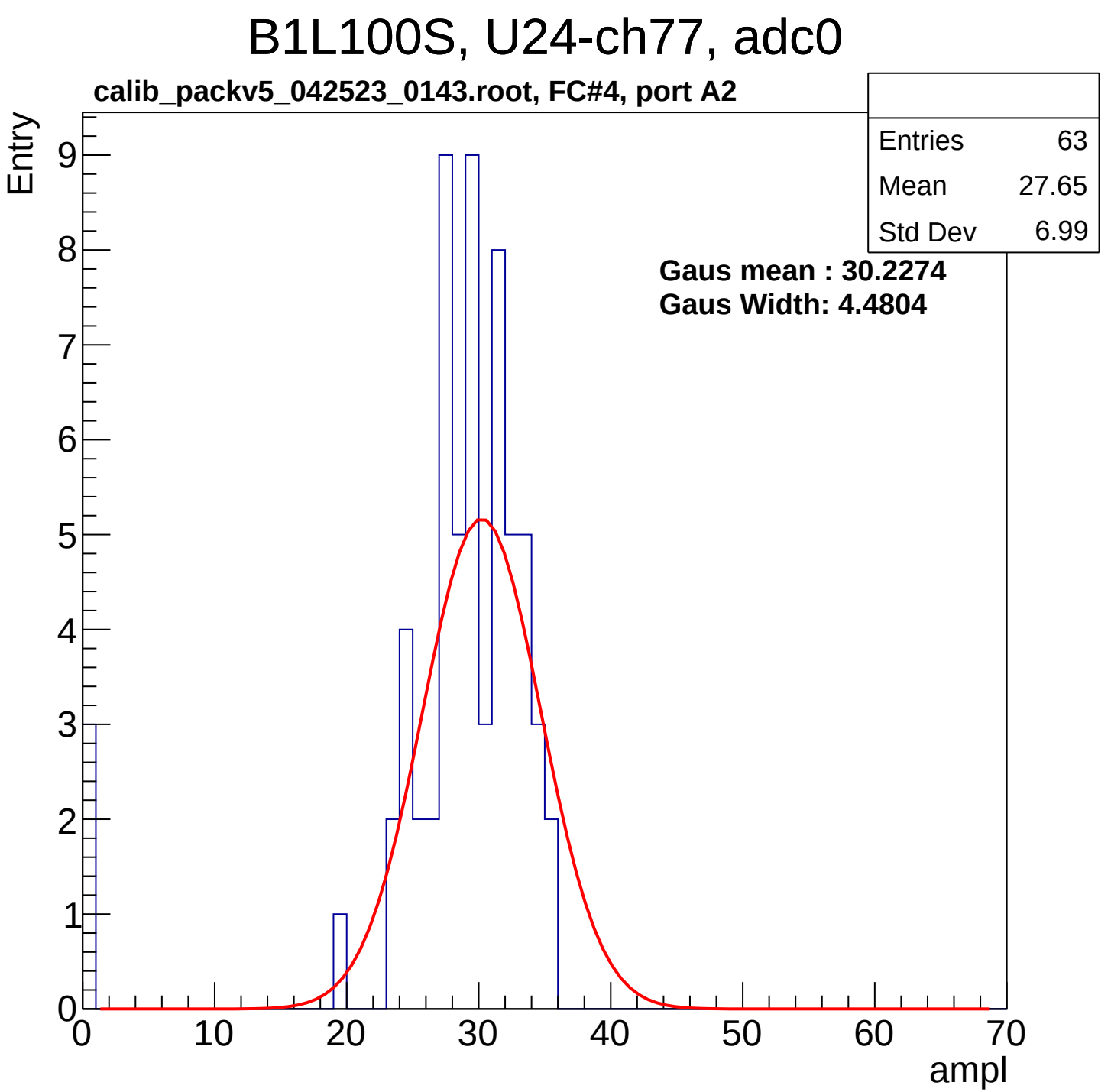
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 27.65
Std Dev 6.99
Gaus mean : 30.2274
Gaus Width: 4.4804
Entry
ampl
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1
2
3
4
5
6
7
8
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0
10
20
30
40
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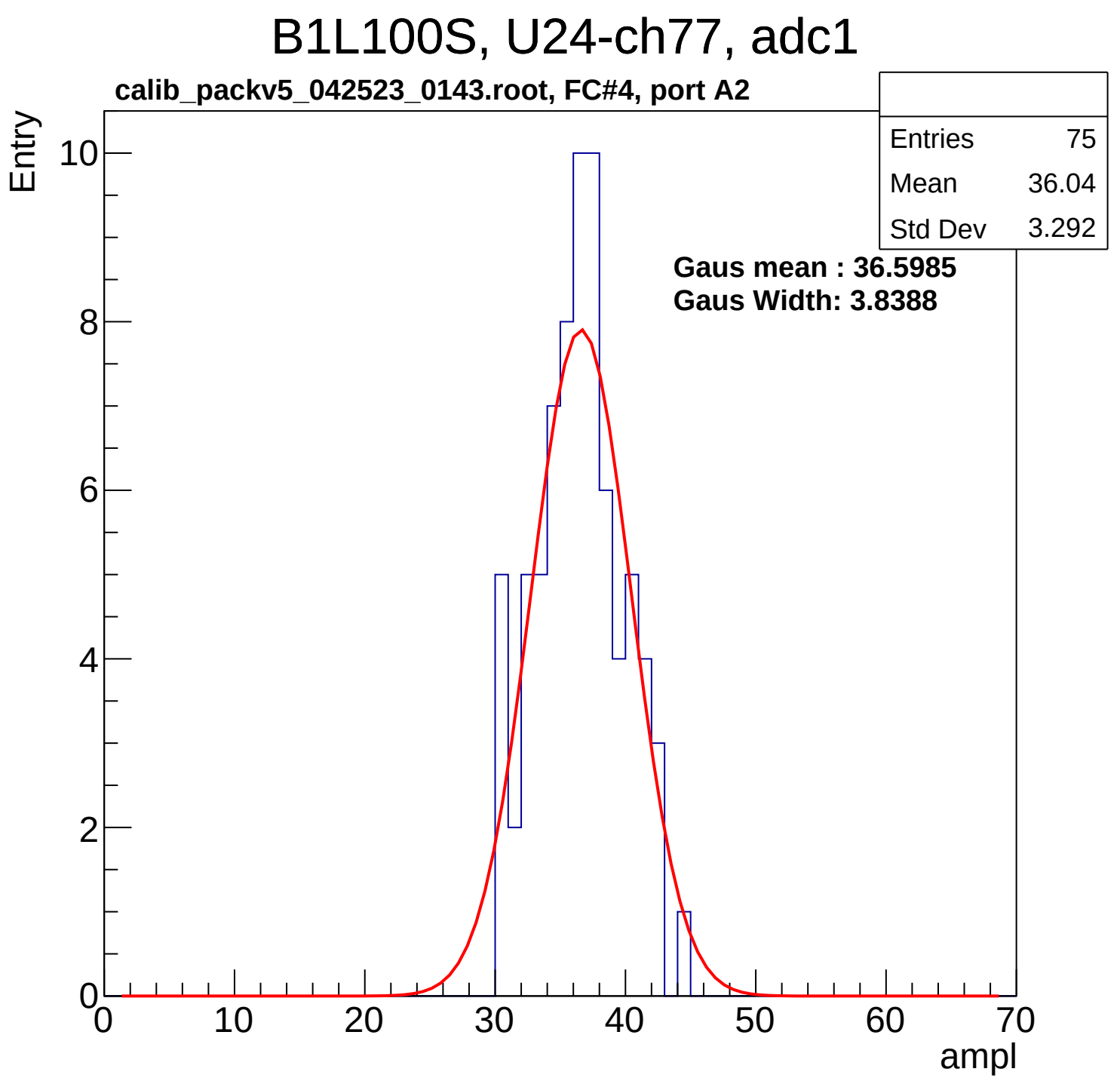
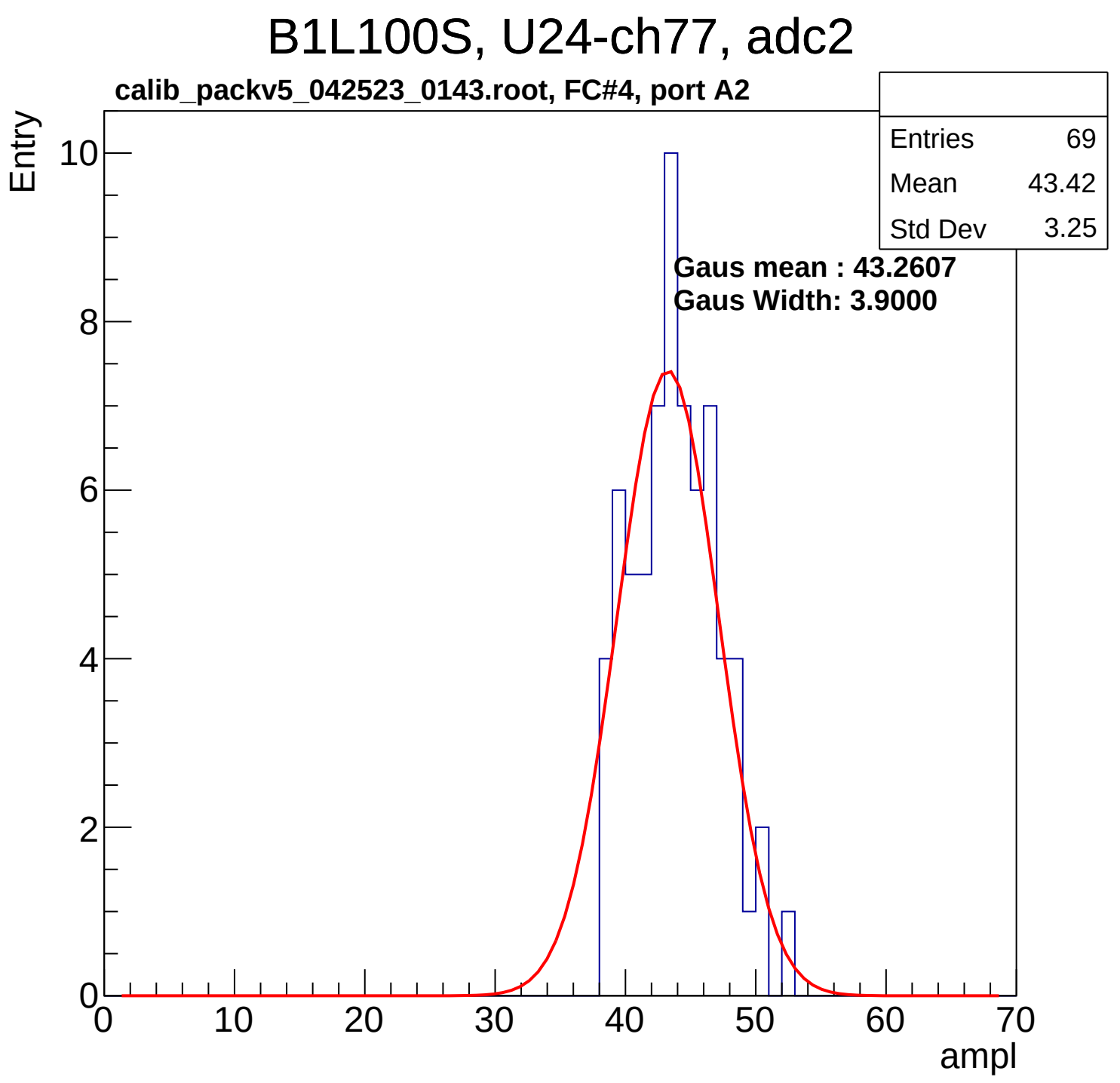
60
70


B1L100S, U24-ch77, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.04
Std Dev 3.292
Gaus mean : 36.5985
Gaus Width: 3.8388
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.42
Std Dev 3.25
Gaus mean : 43.2607
Gaus Width: 3.9000
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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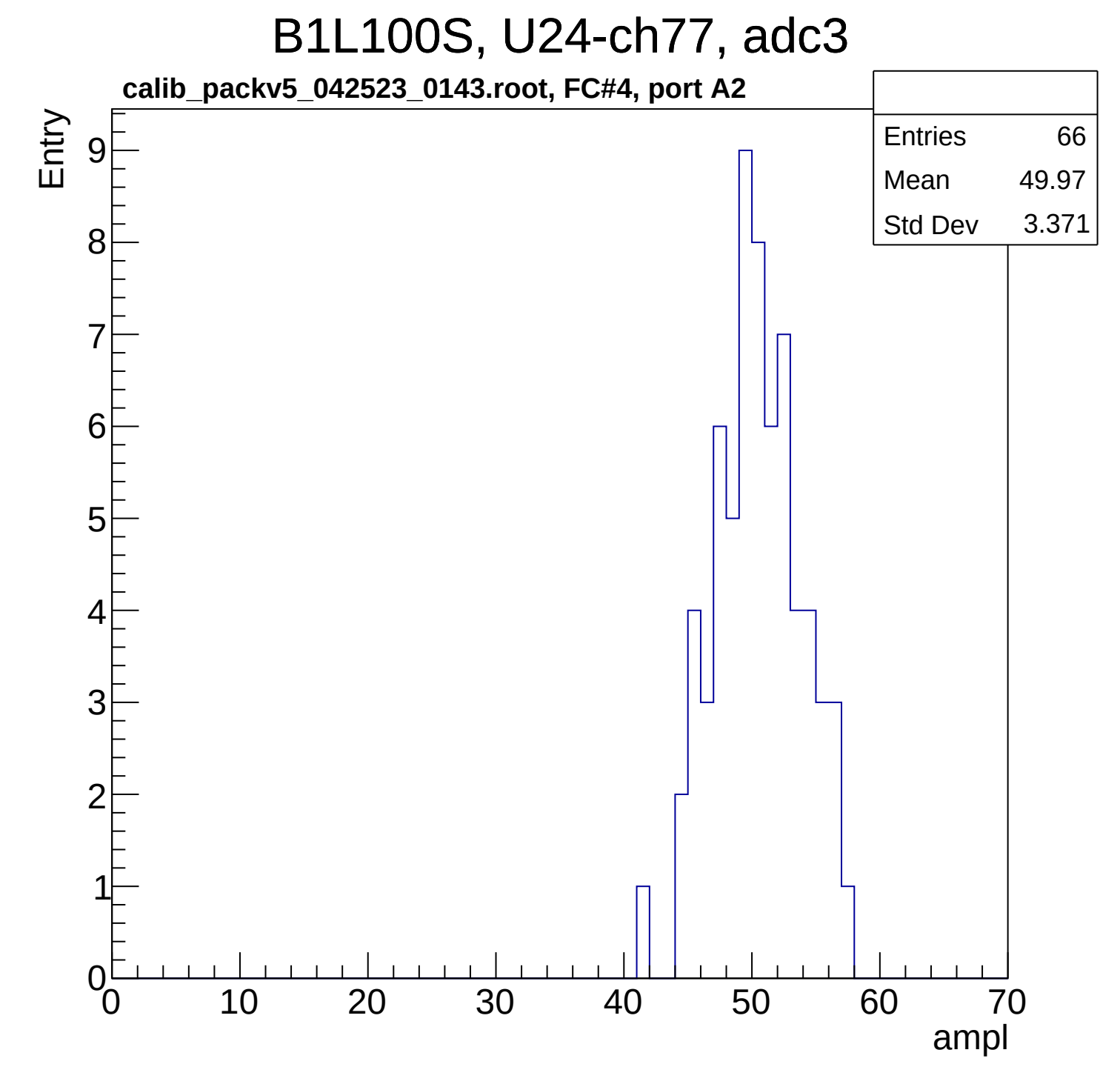
B1L100S, U24-ch77, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.97
Std Dev 3.371
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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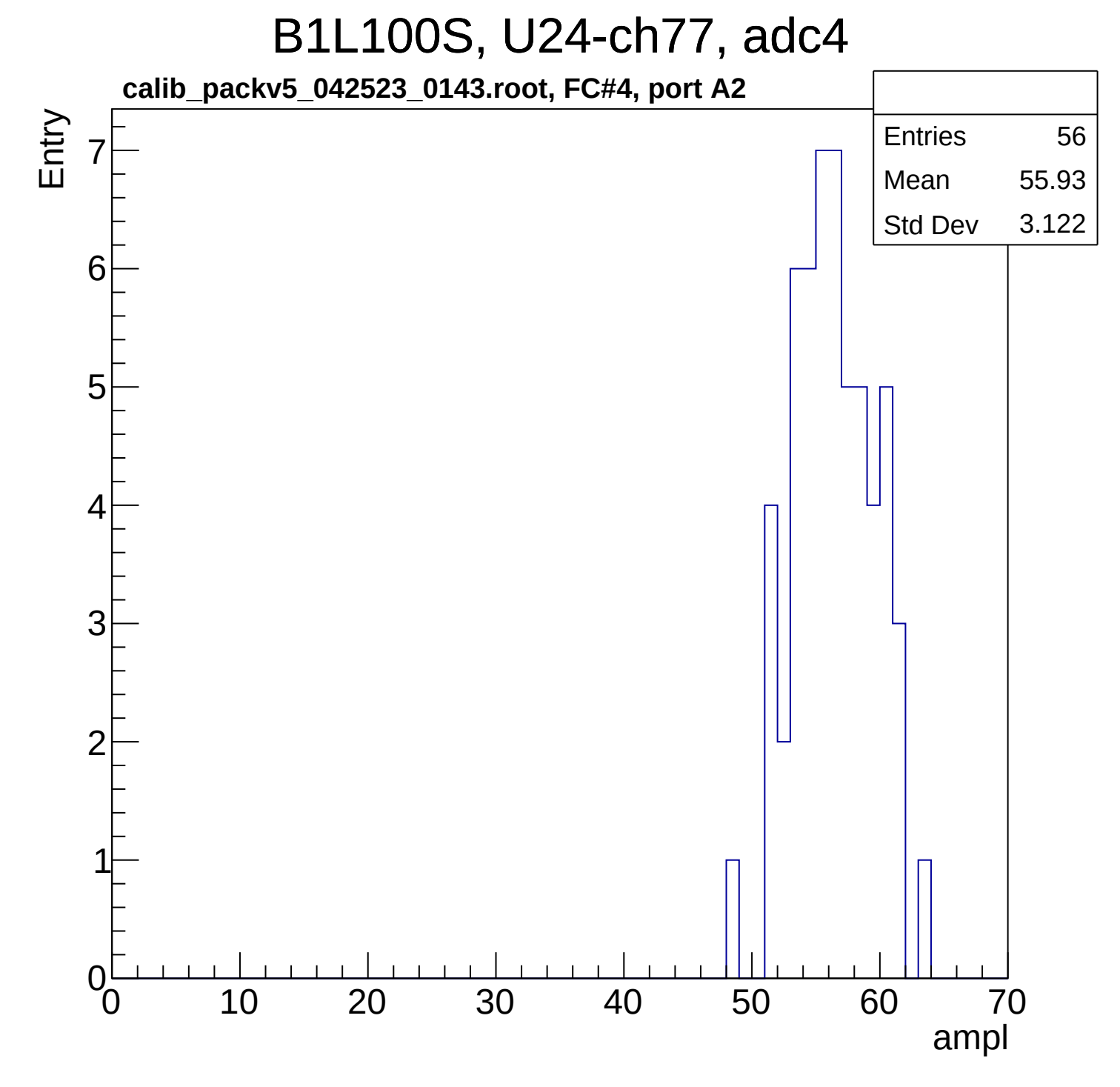
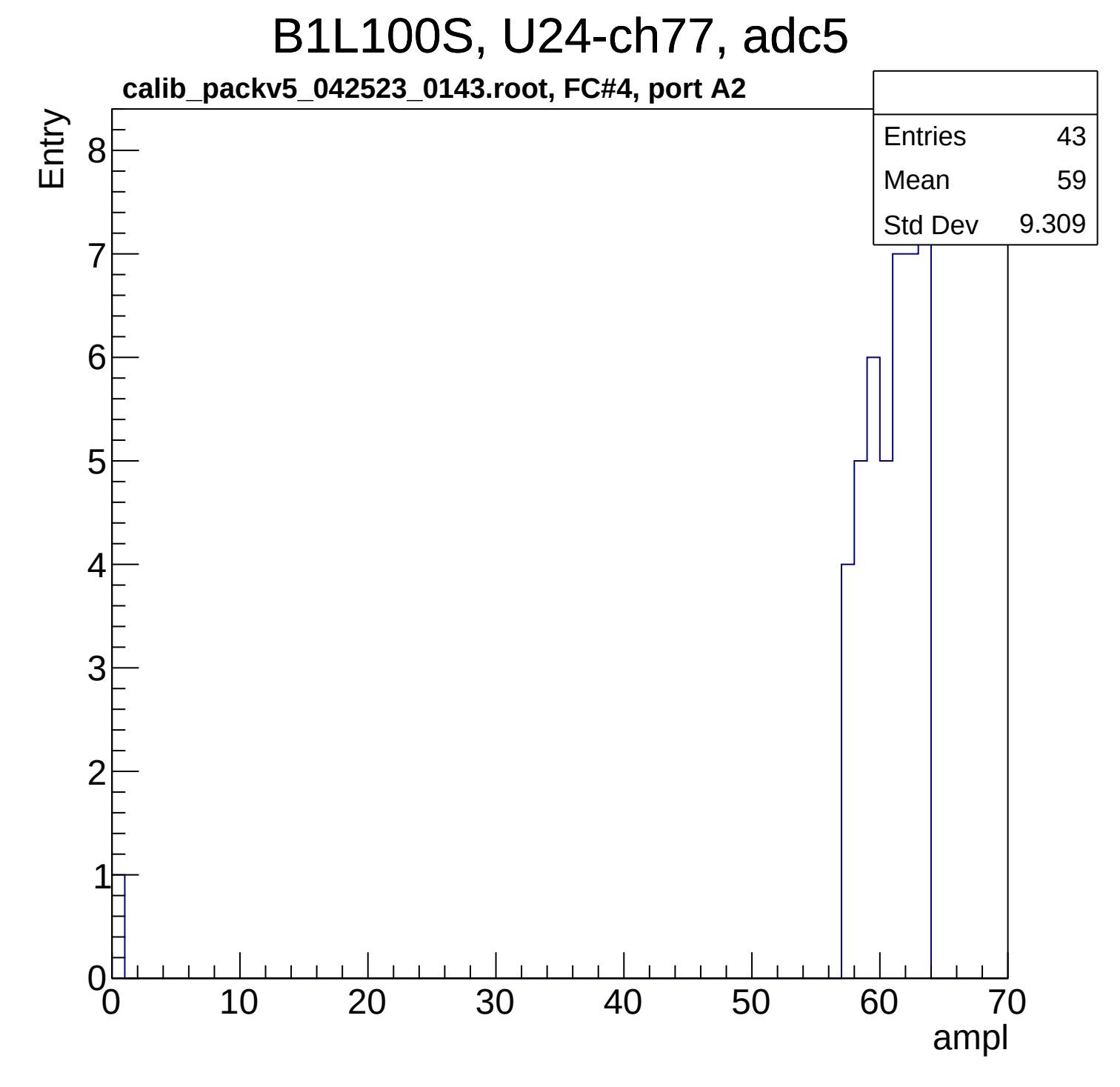

B1L100S, U24-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.93
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch77, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59
Std Dev 9.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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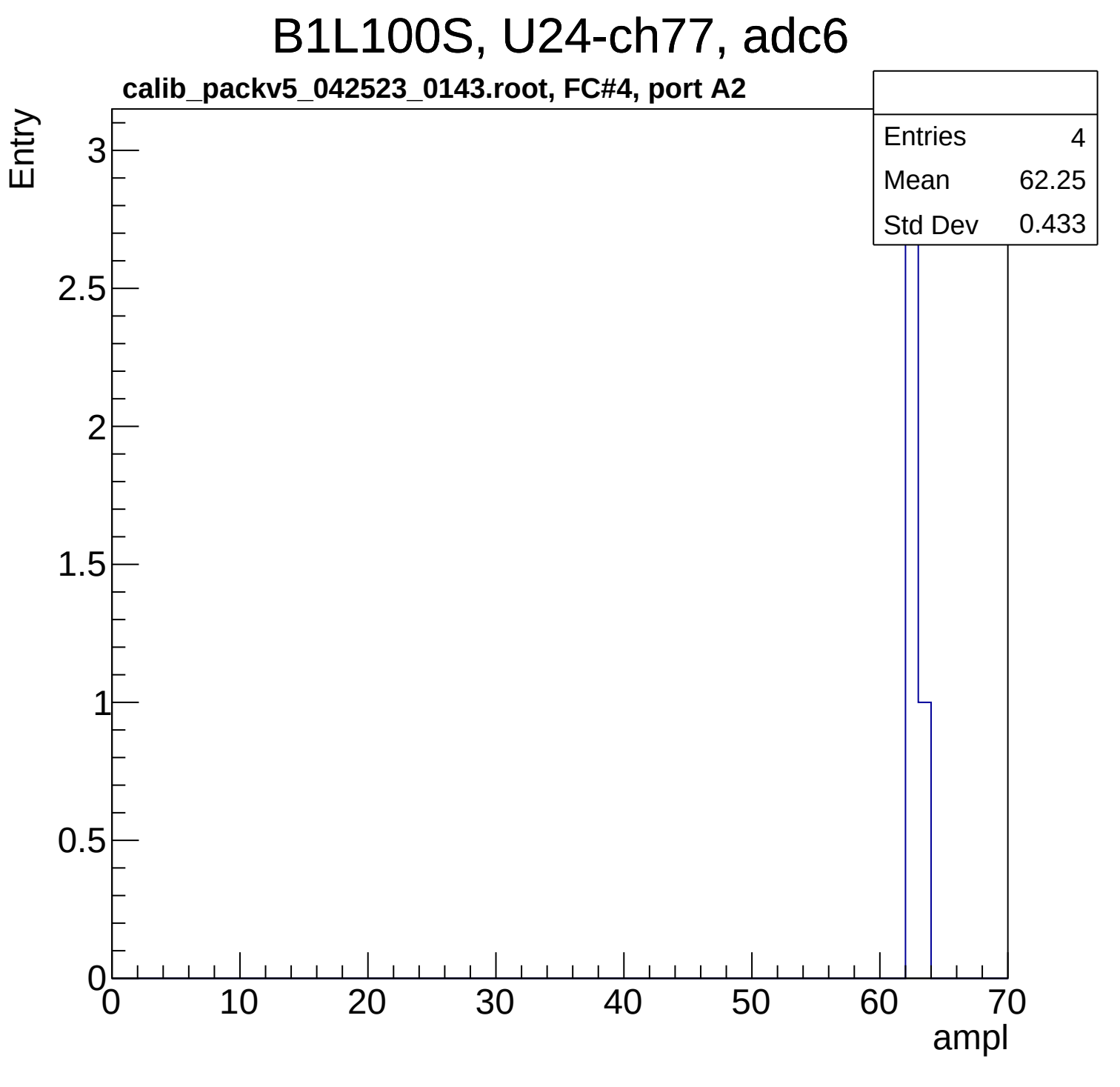
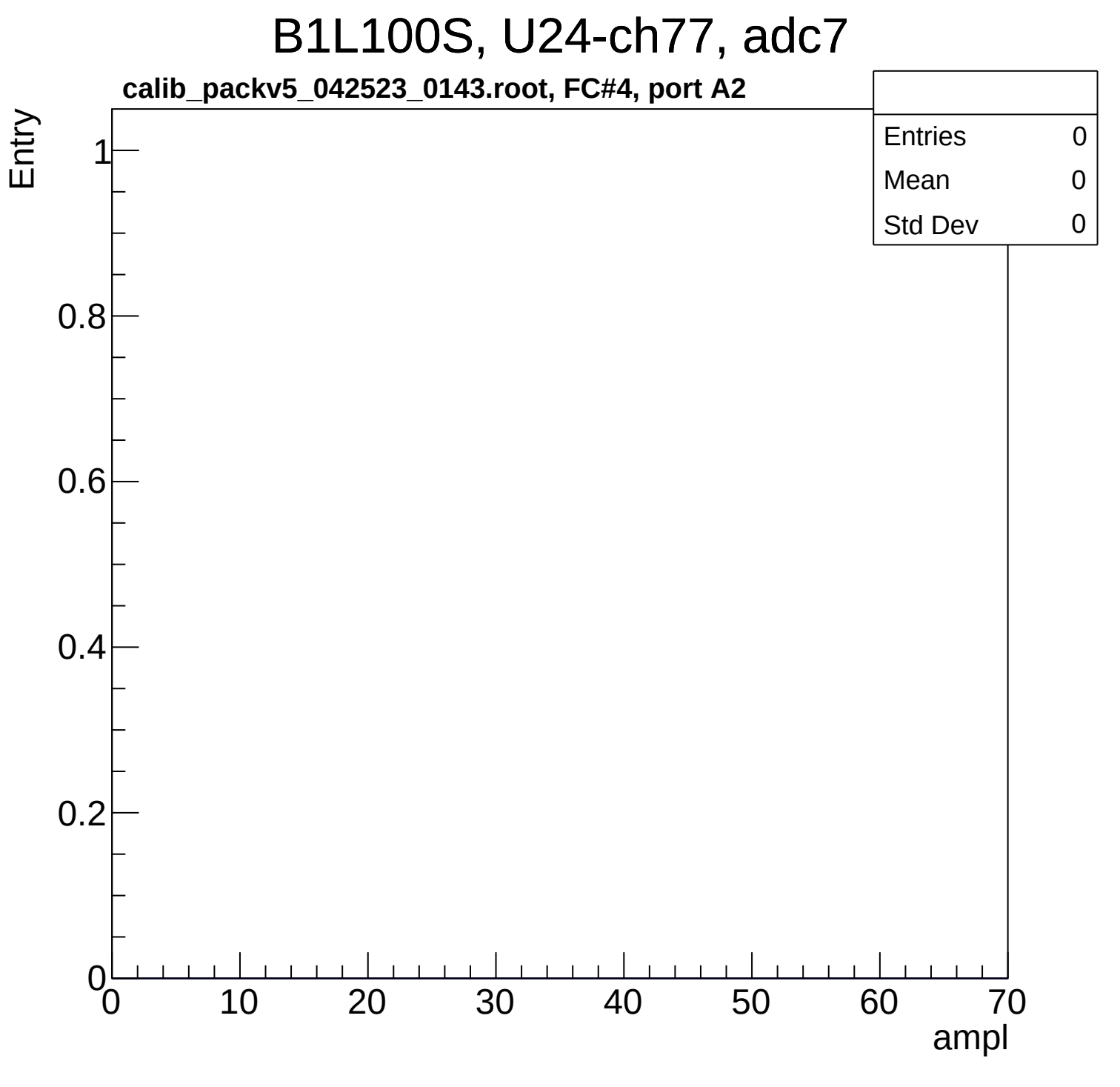
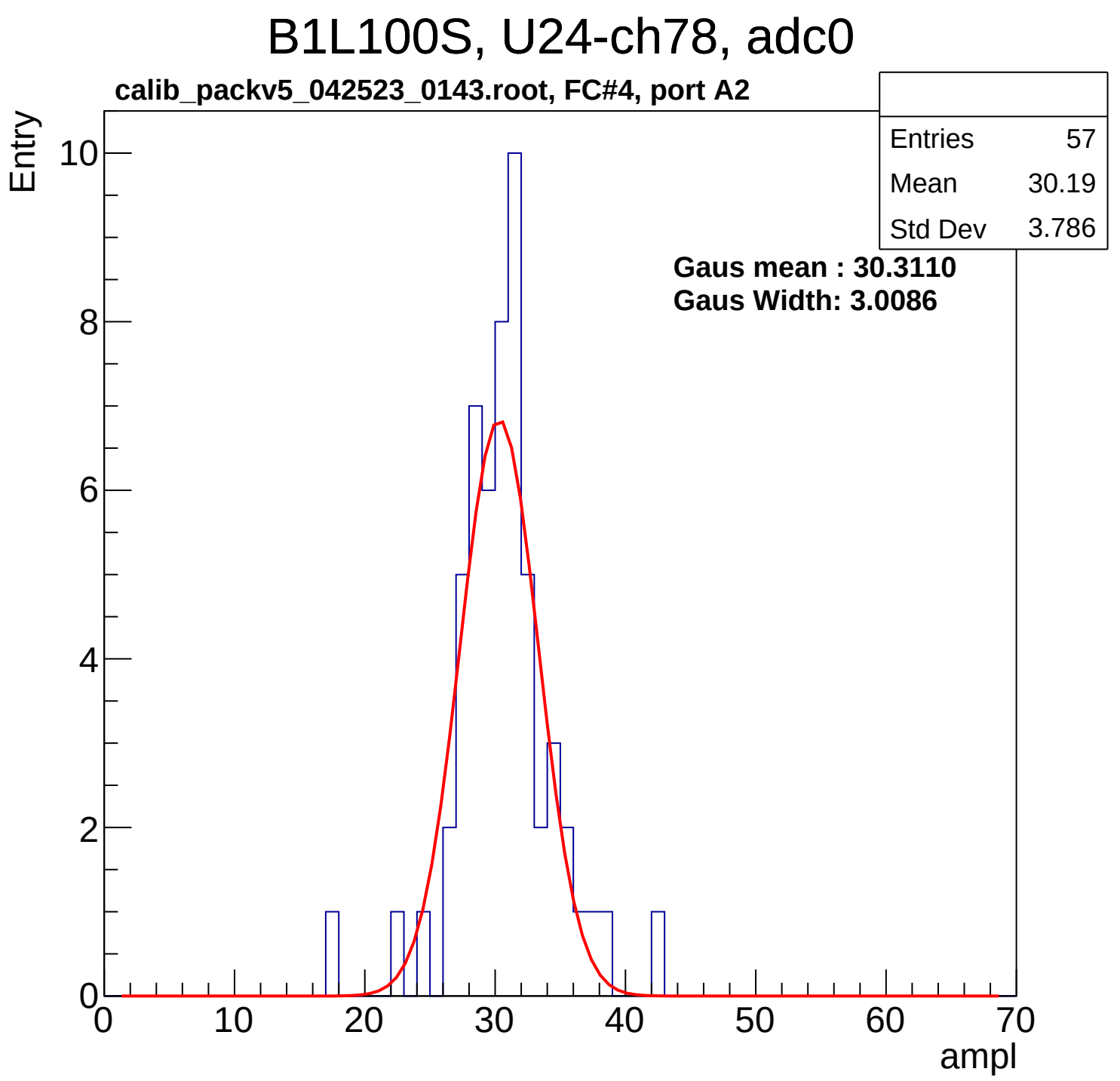
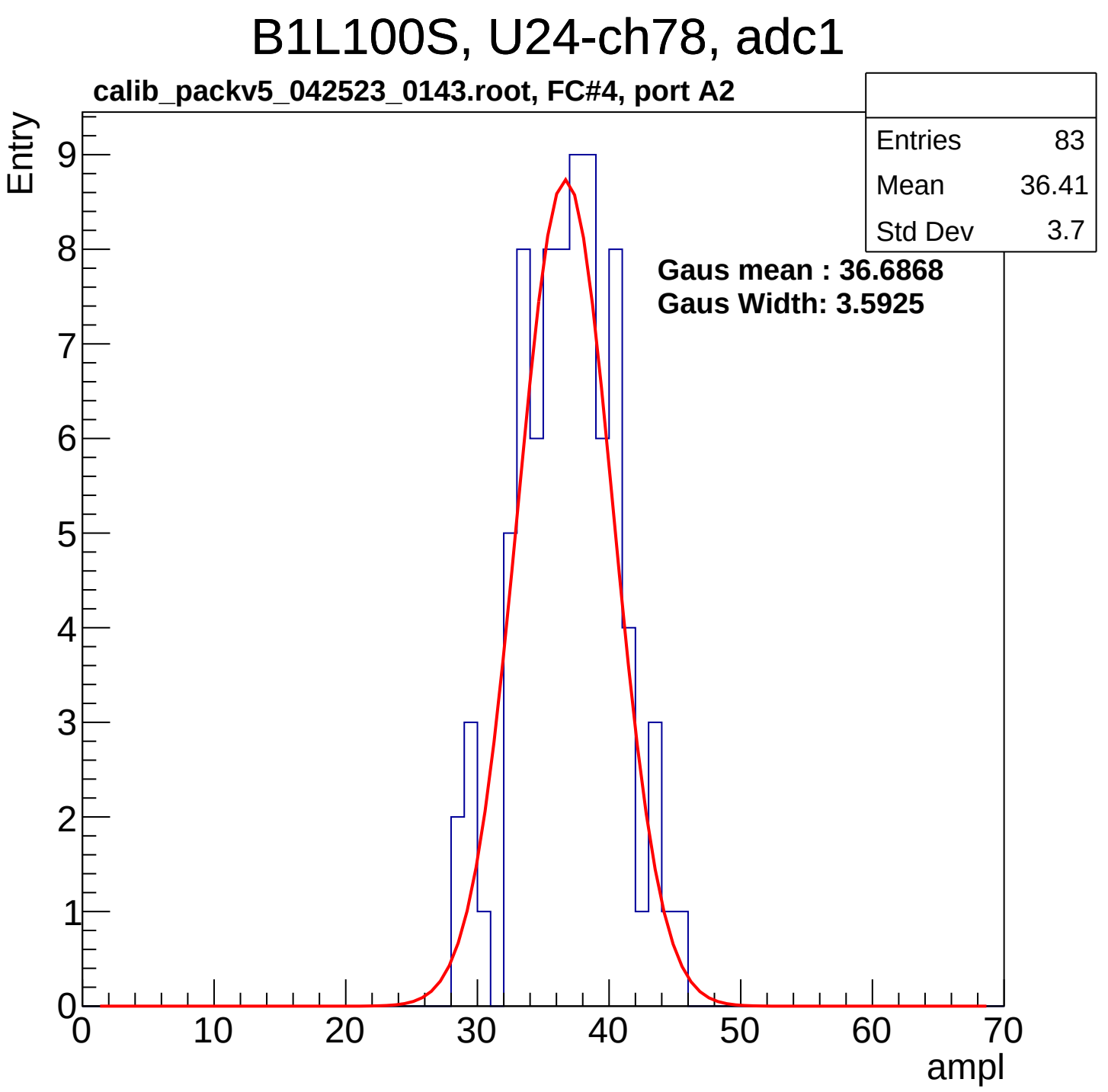

B1L100S, U24-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 30.19
Std Dev 3.786
Gaus mean : 30.3110
Gaus Width: 3.0086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.41
Std Dev 3.7
Gaus mean : 36.6868
Gaus Width: 3.5925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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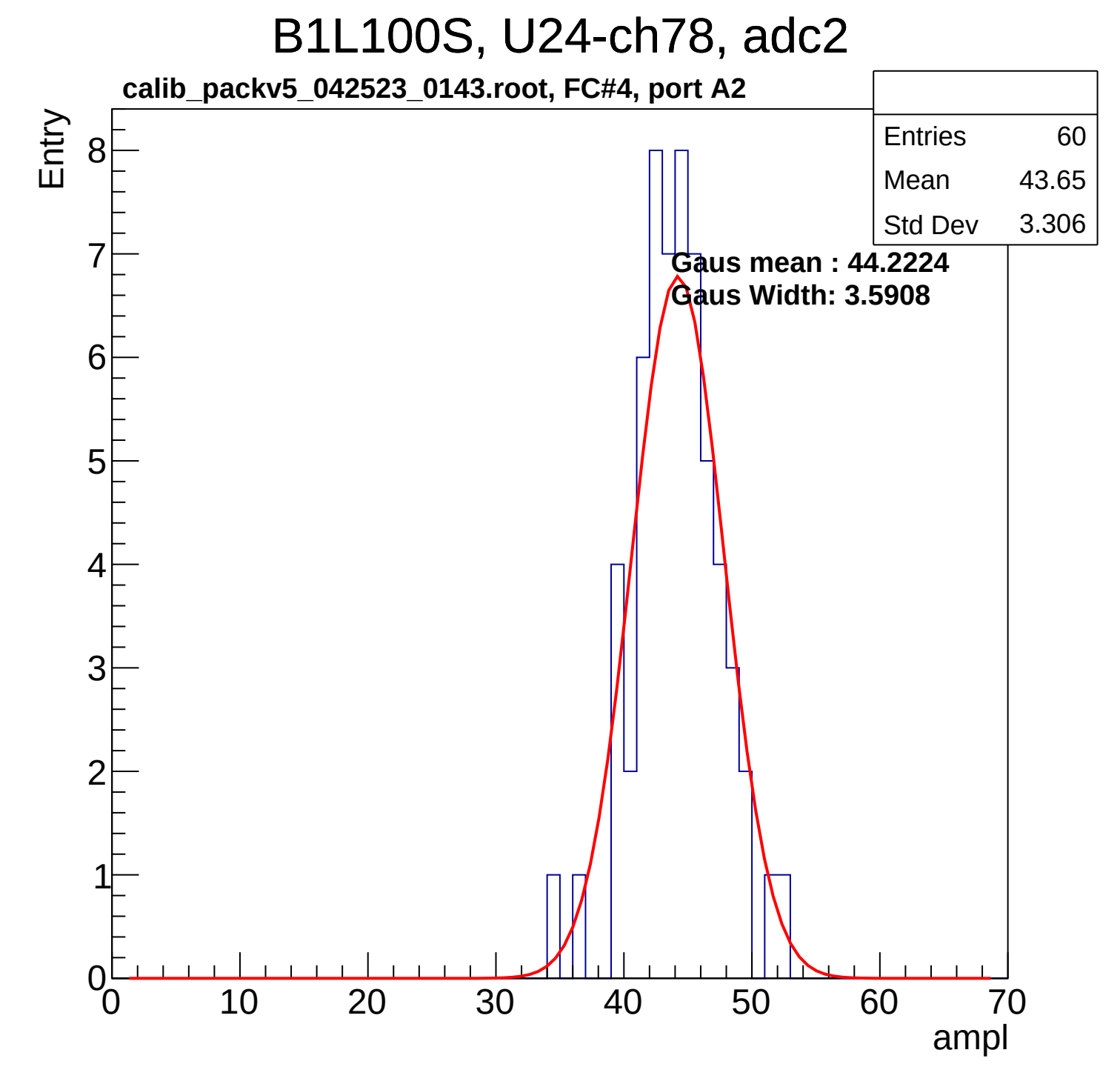
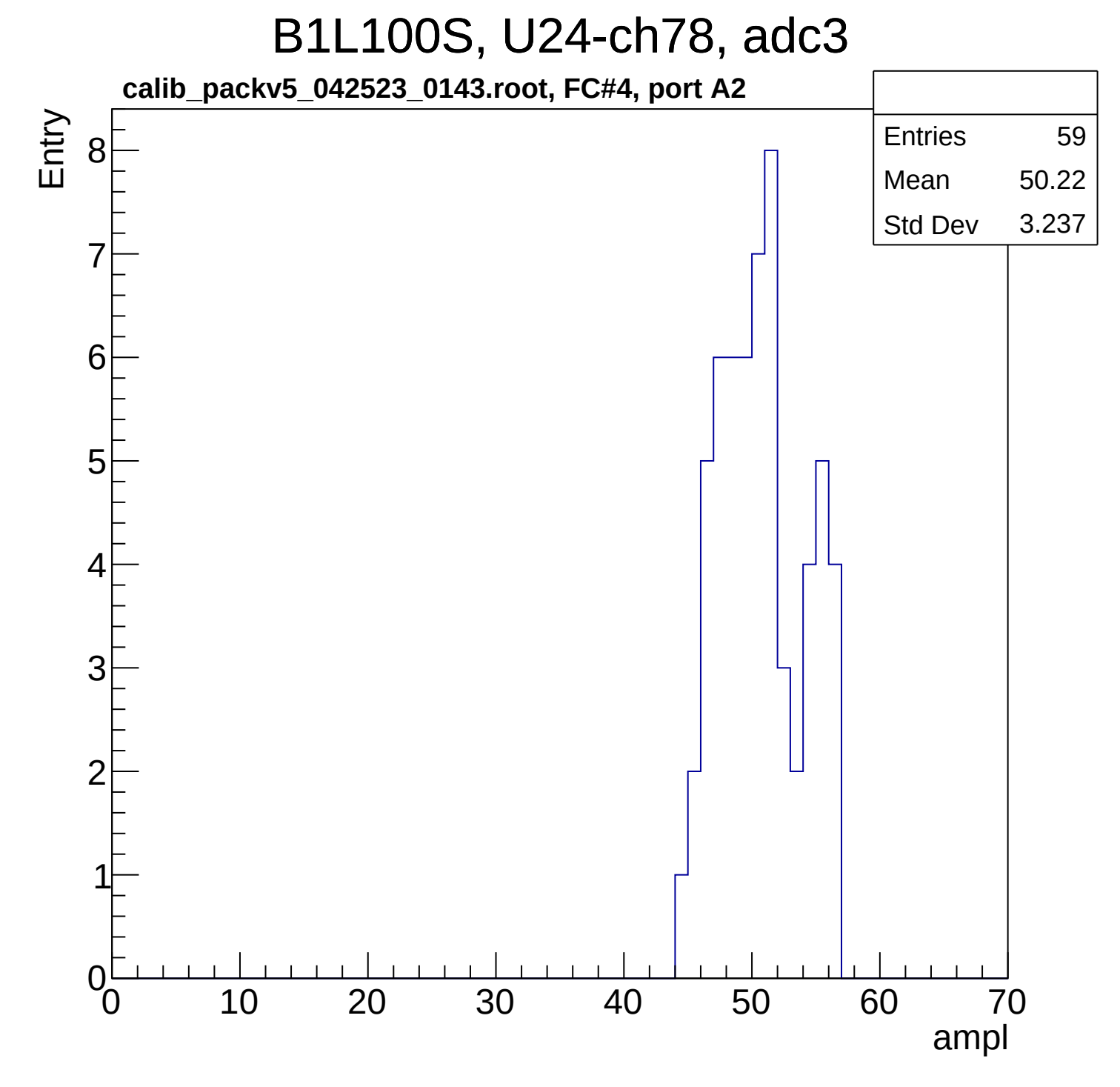
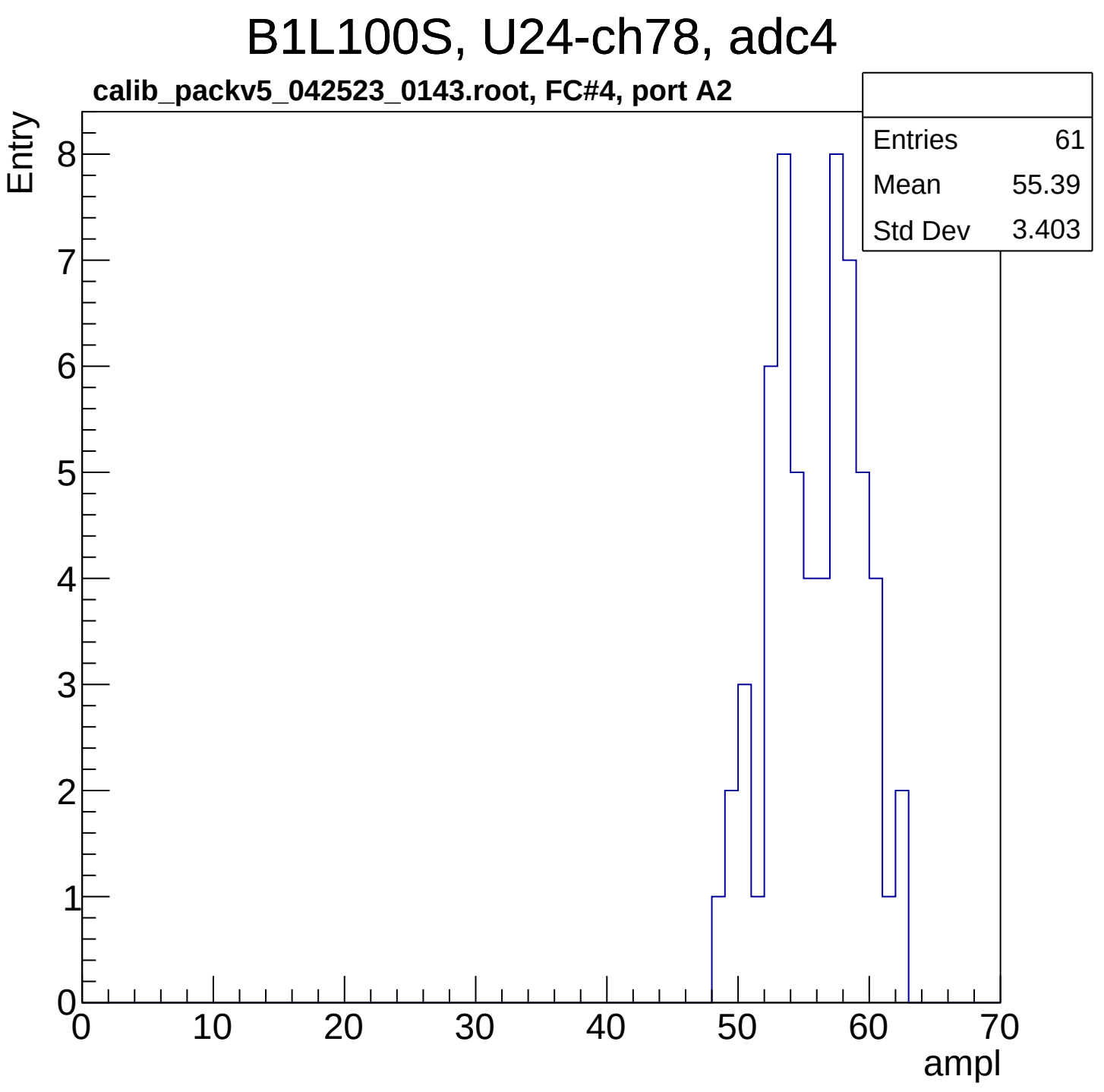
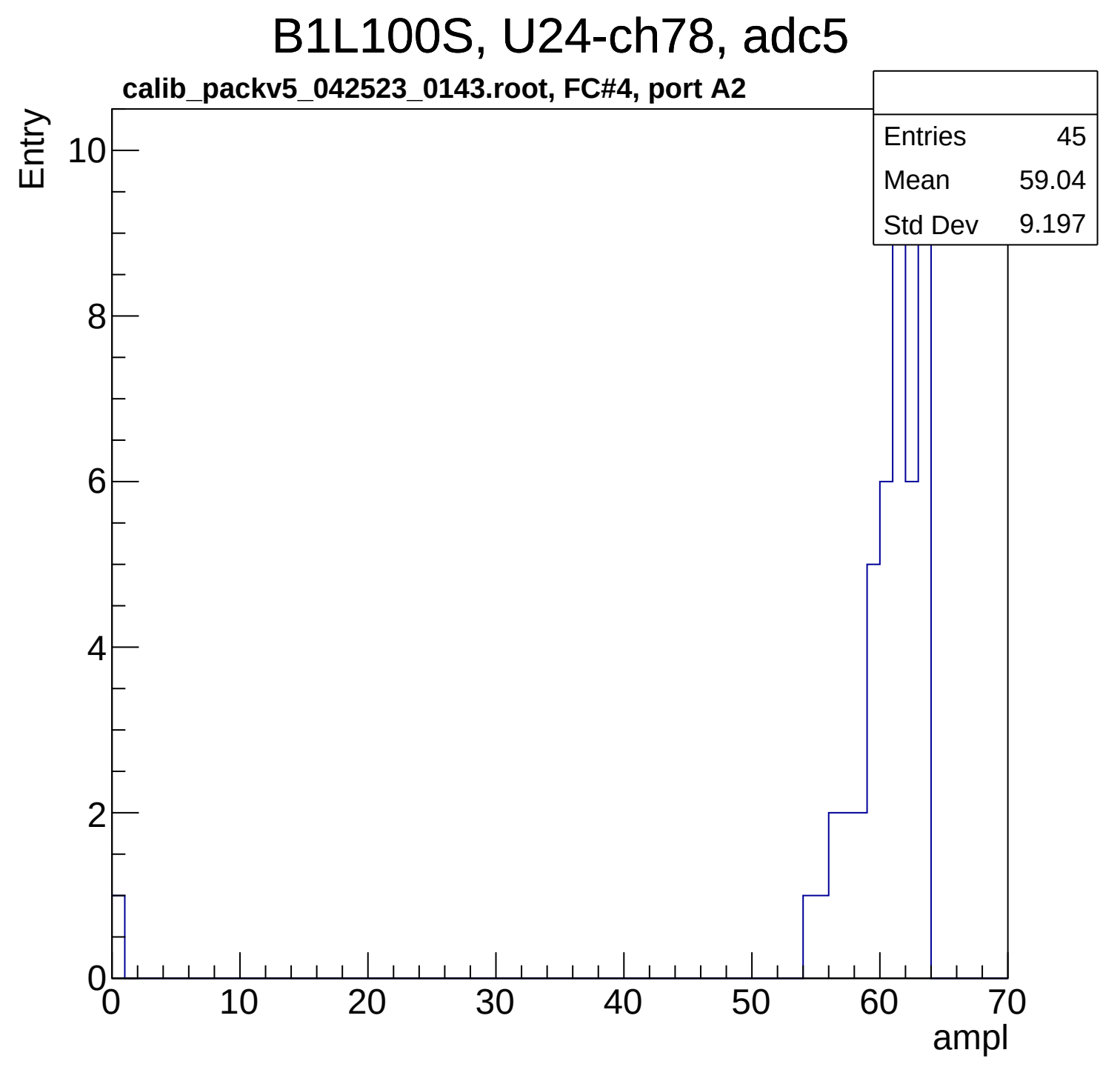

B1L100S, U24-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.65
Std Dev 3.306
Gaus mean : 44.2224
Gaus Width: 3.5908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch78, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 50.22
Std Dev 3.237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.39
Std Dev 3.403
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.04
Std Dev 9.197
Entry
ampl
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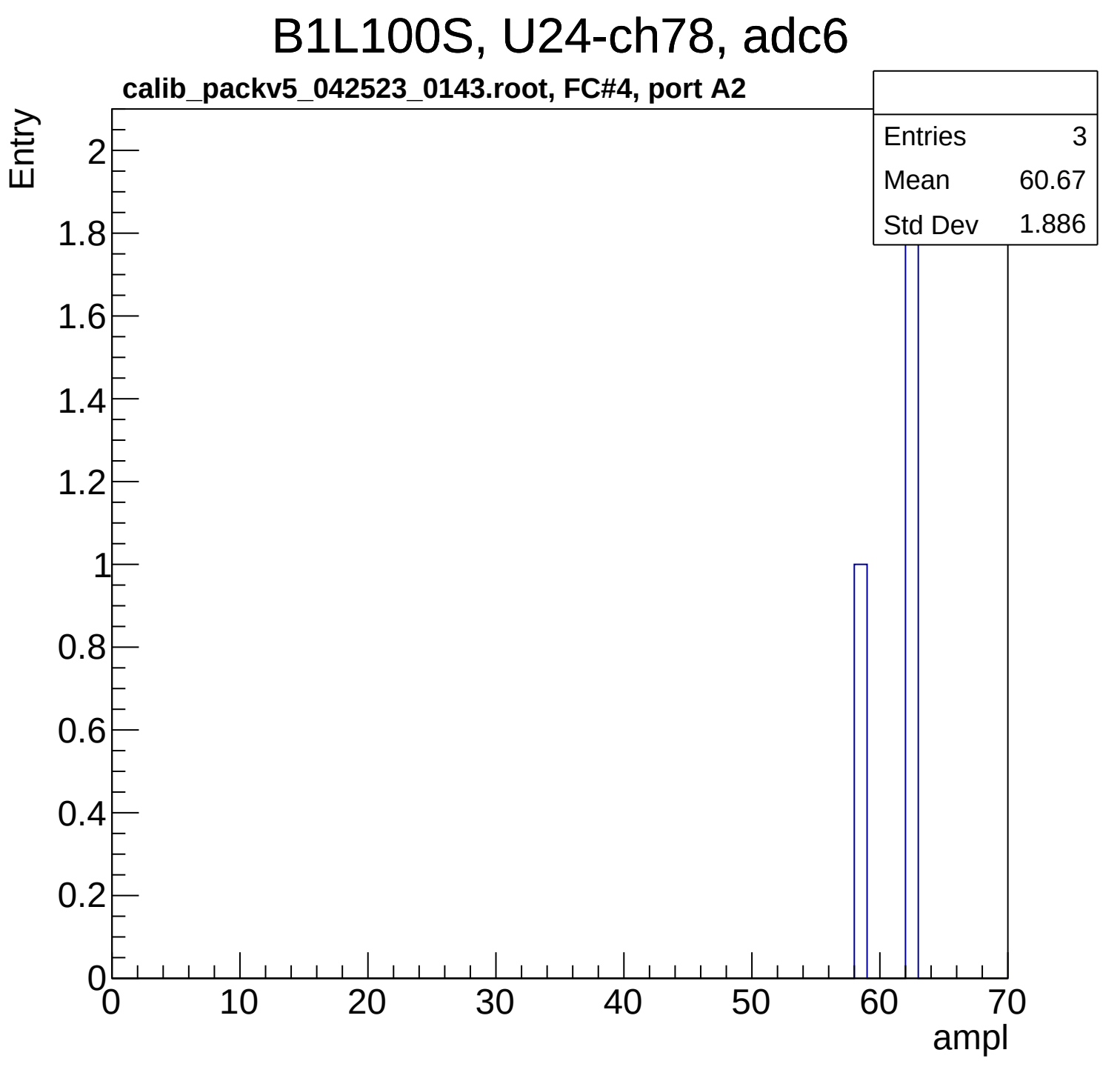
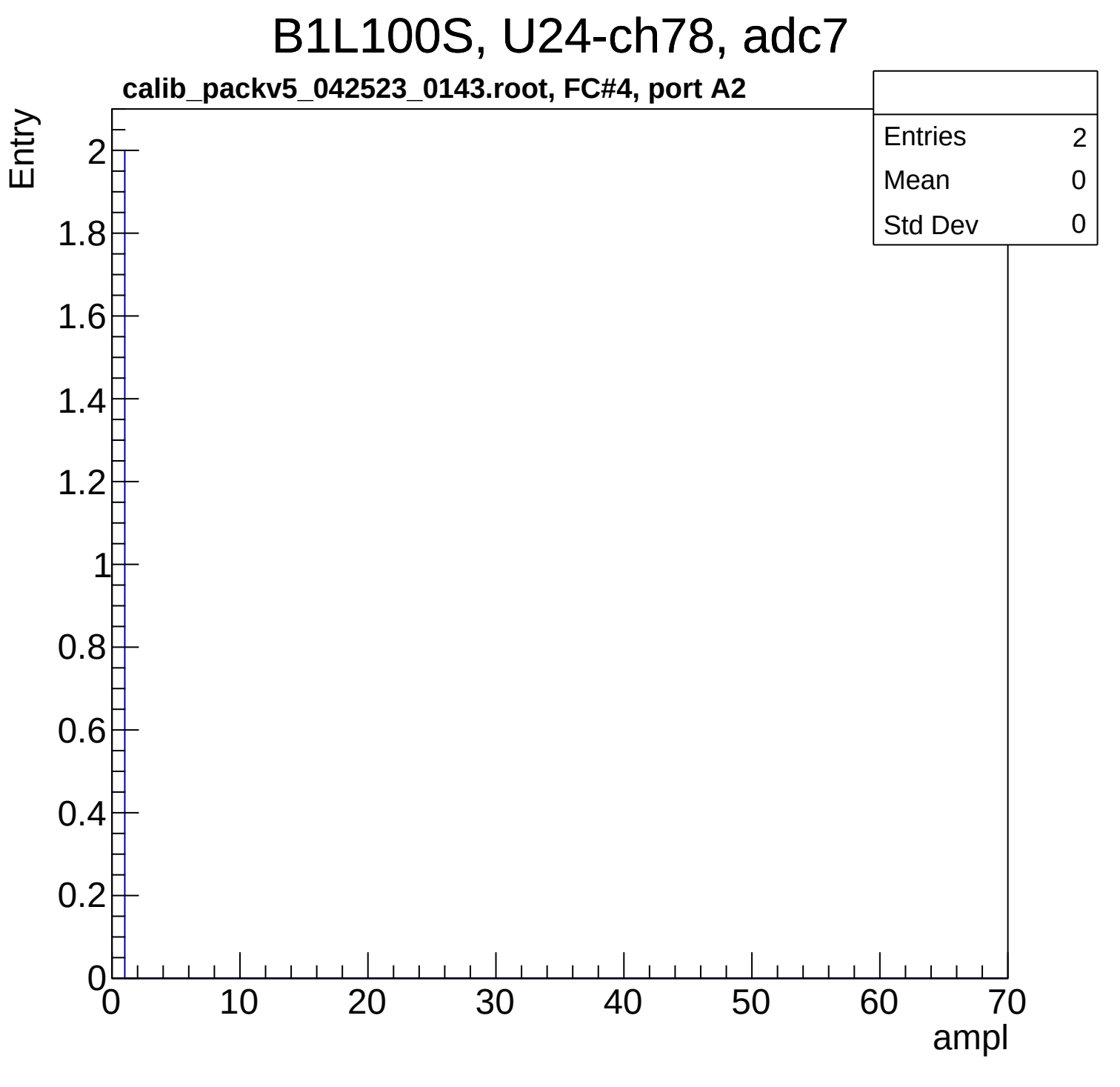
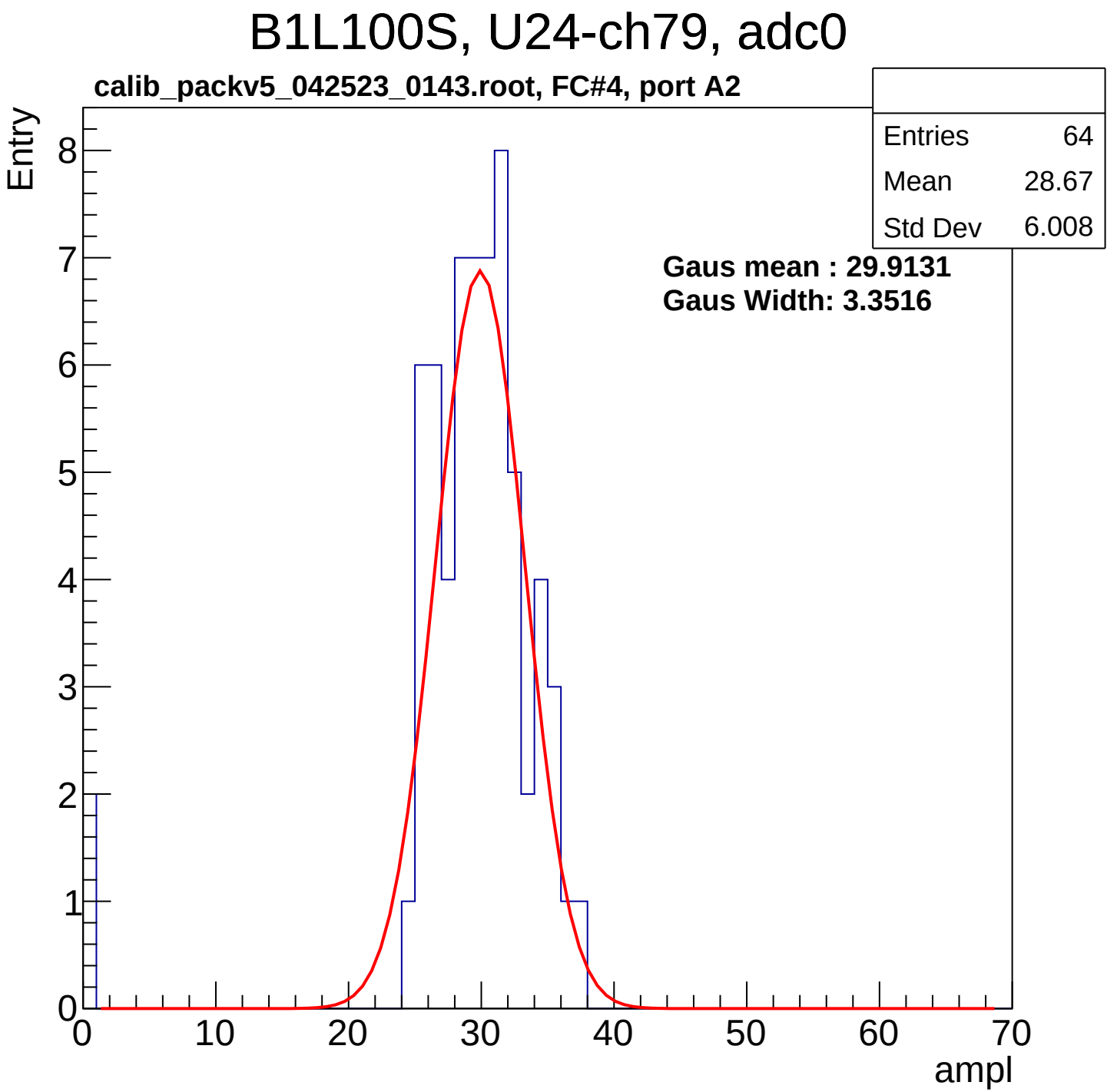
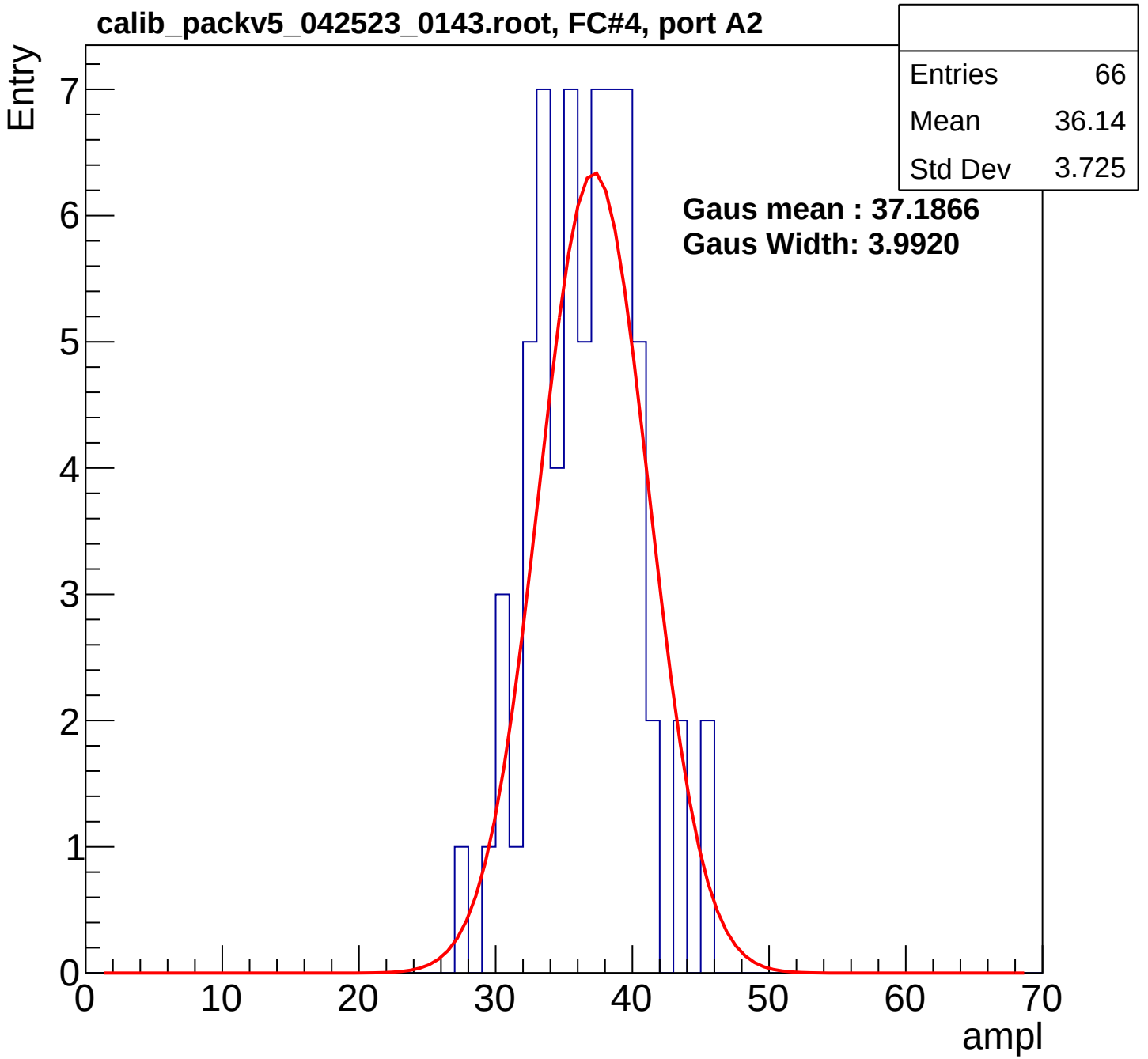
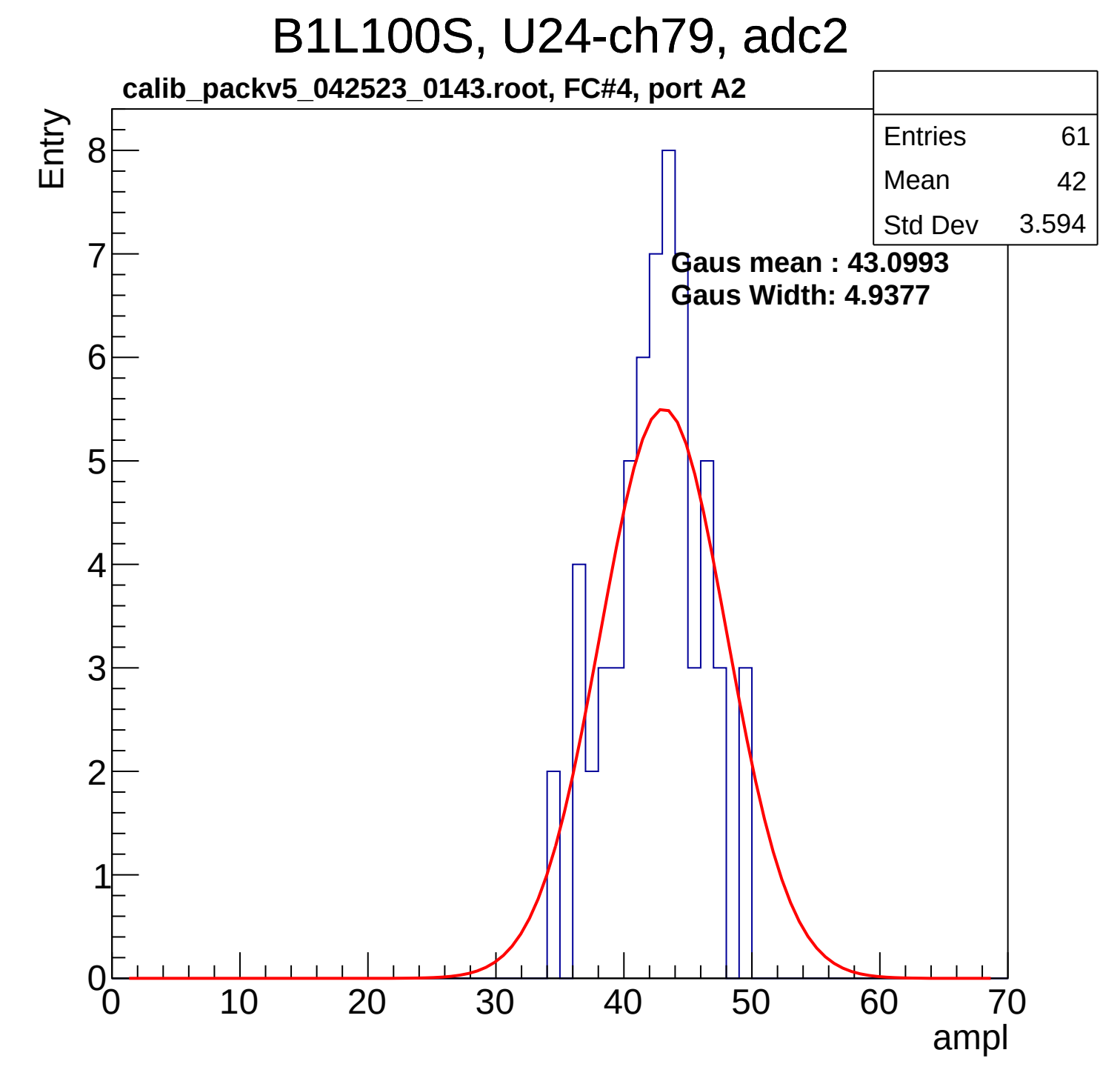
2
4
6
8
10
0
10
20
30
40
50
60
70

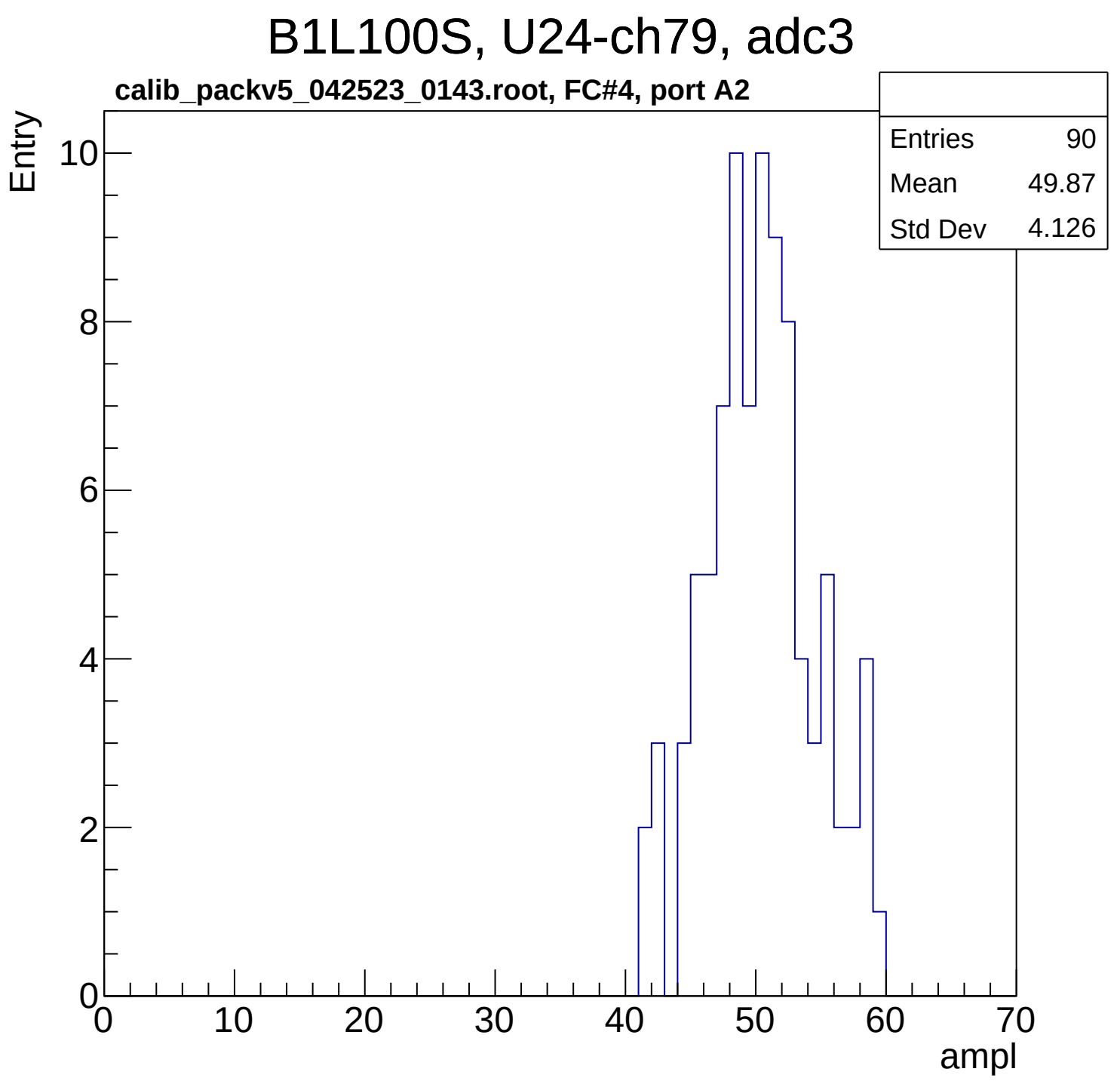

B1L100S, U24-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

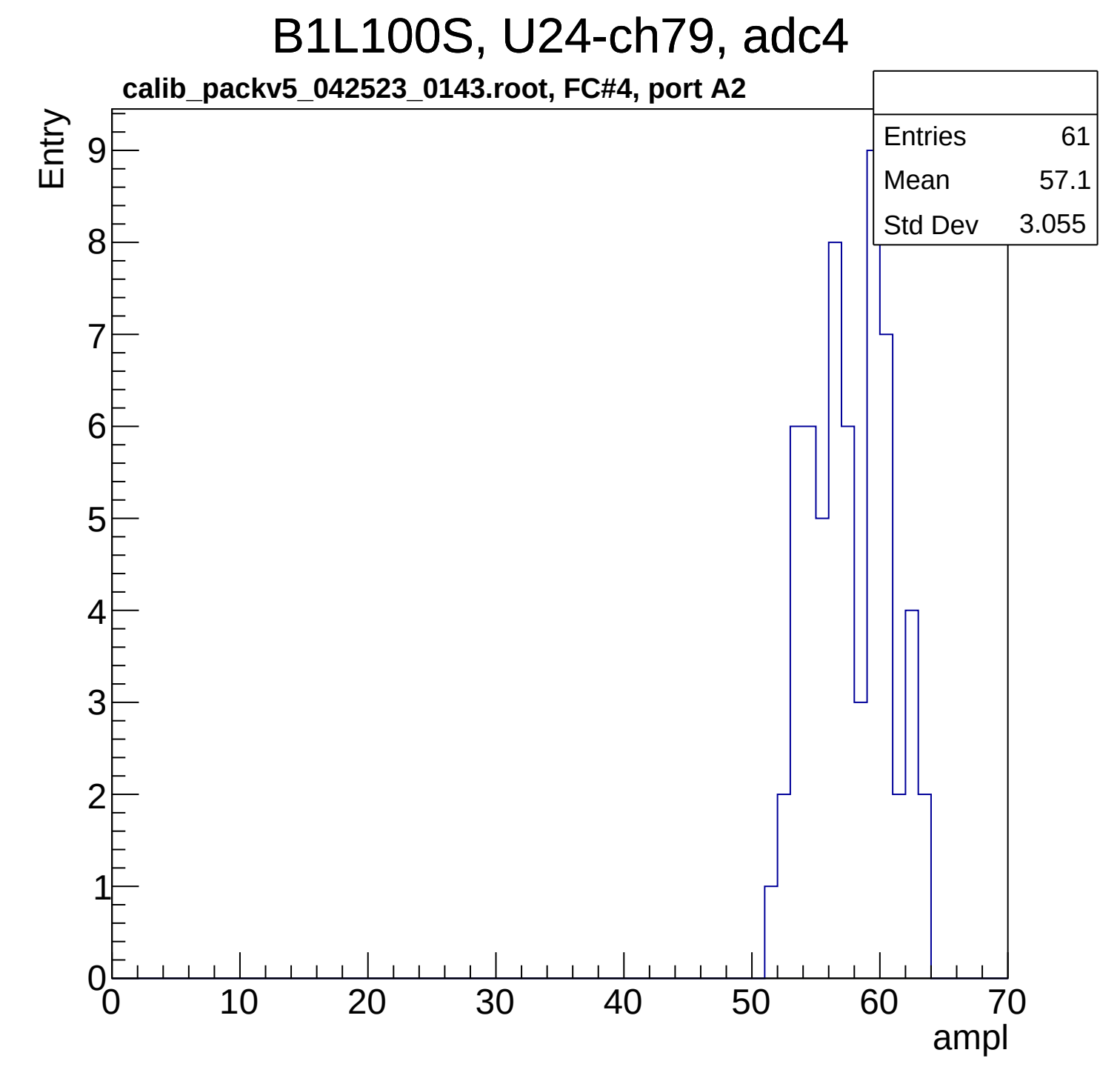

B1L100S, U24-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 28.67
Std Dev 6.008
Gaus mean : 29.9131
Gaus Width: 3.3516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.14
Std Dev 3.725
Gaus mean : 37.1866
Gaus Width: 3.9920
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42
Std Dev 3.594
Gaus mean : 43.0993
Gaus Width: 4.9377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 49.87
Std Dev 4.126
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.1
Std Dev 3.055
Entry
ampl
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2
3
4
5
6
7
8
9
0
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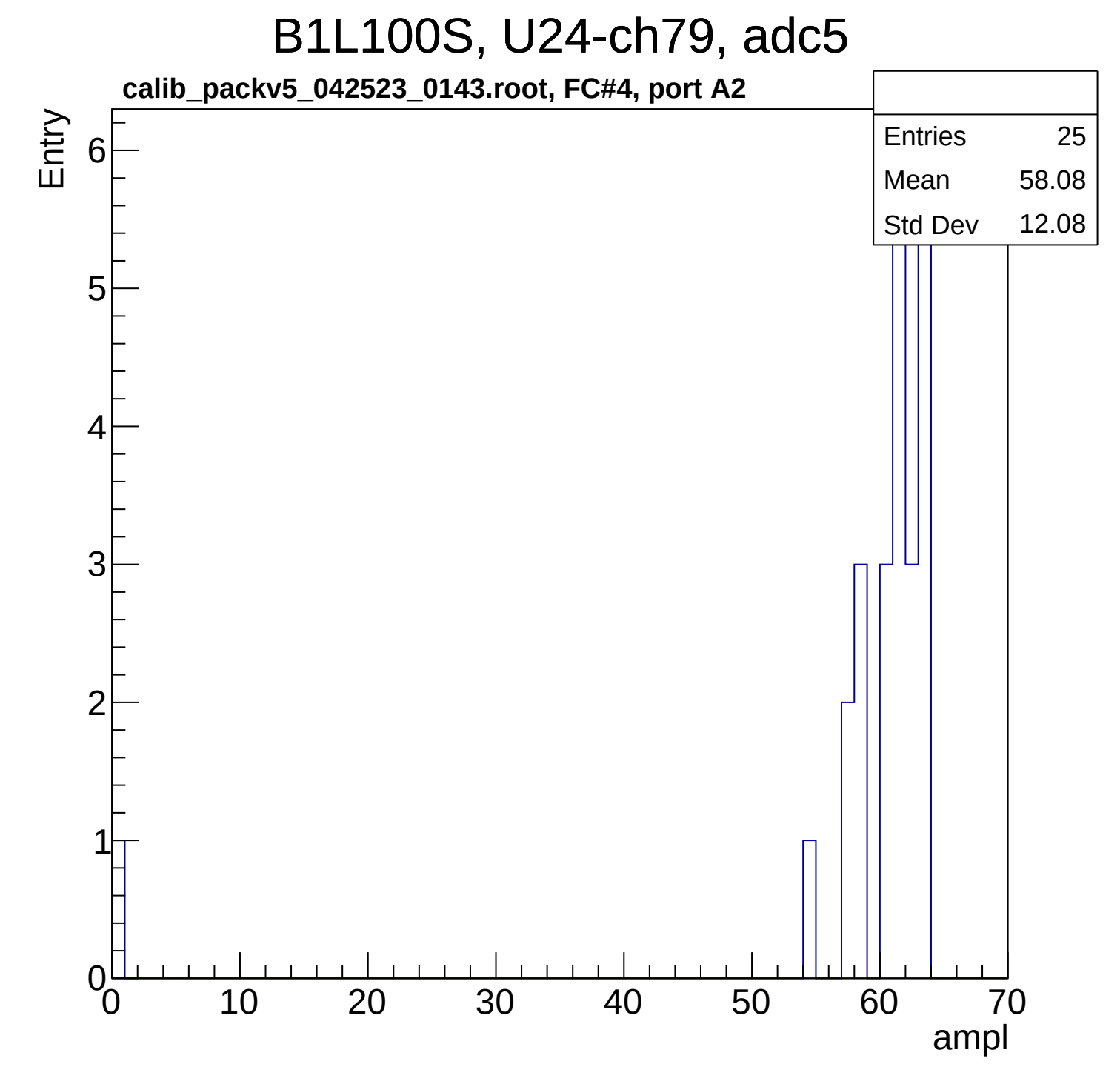
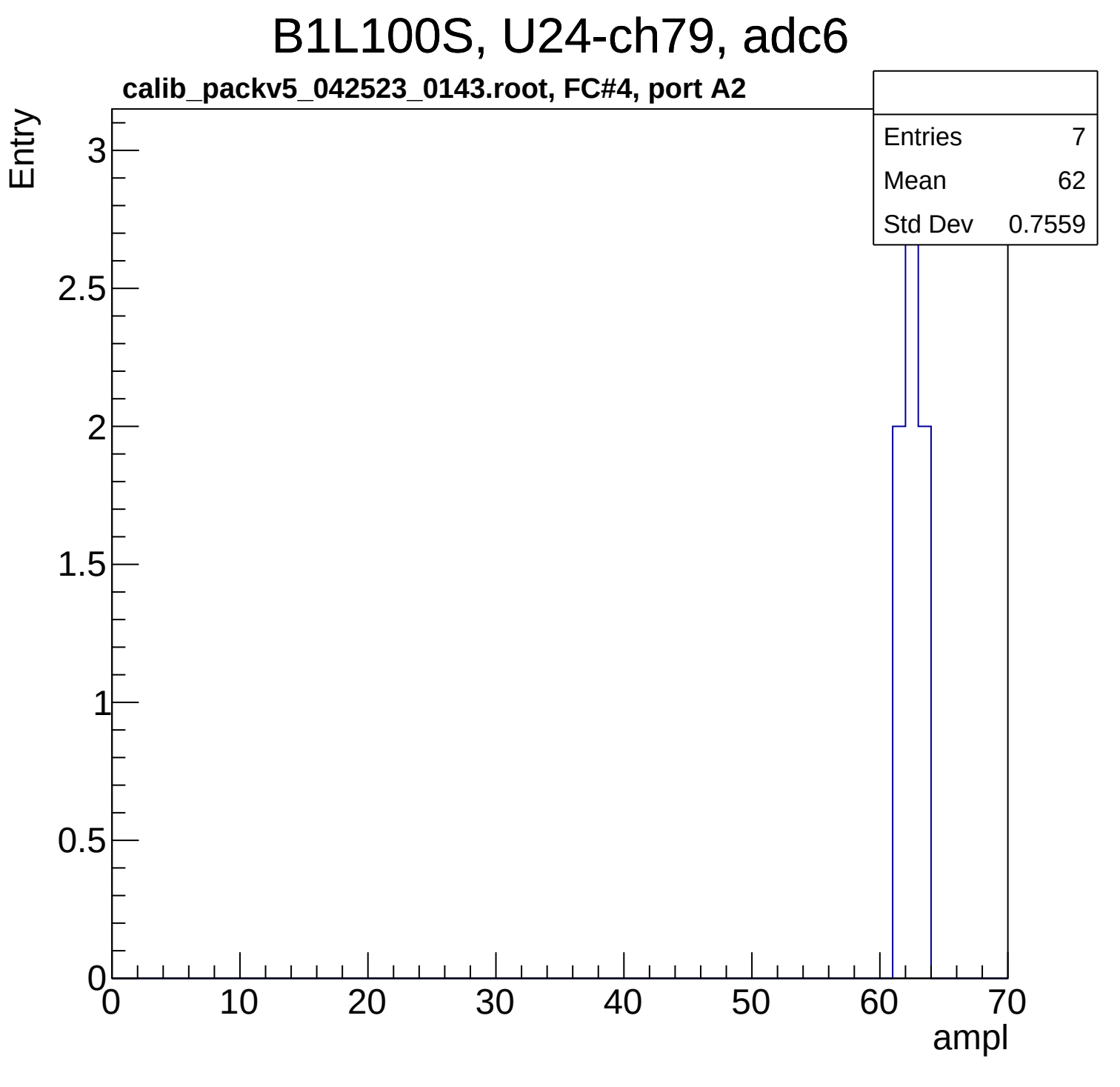
20
30
40
50
60
70


B1L100S, U24-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.08
Std Dev 12.08
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch79, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62
Std Dev 0.7559
Entry
ampl
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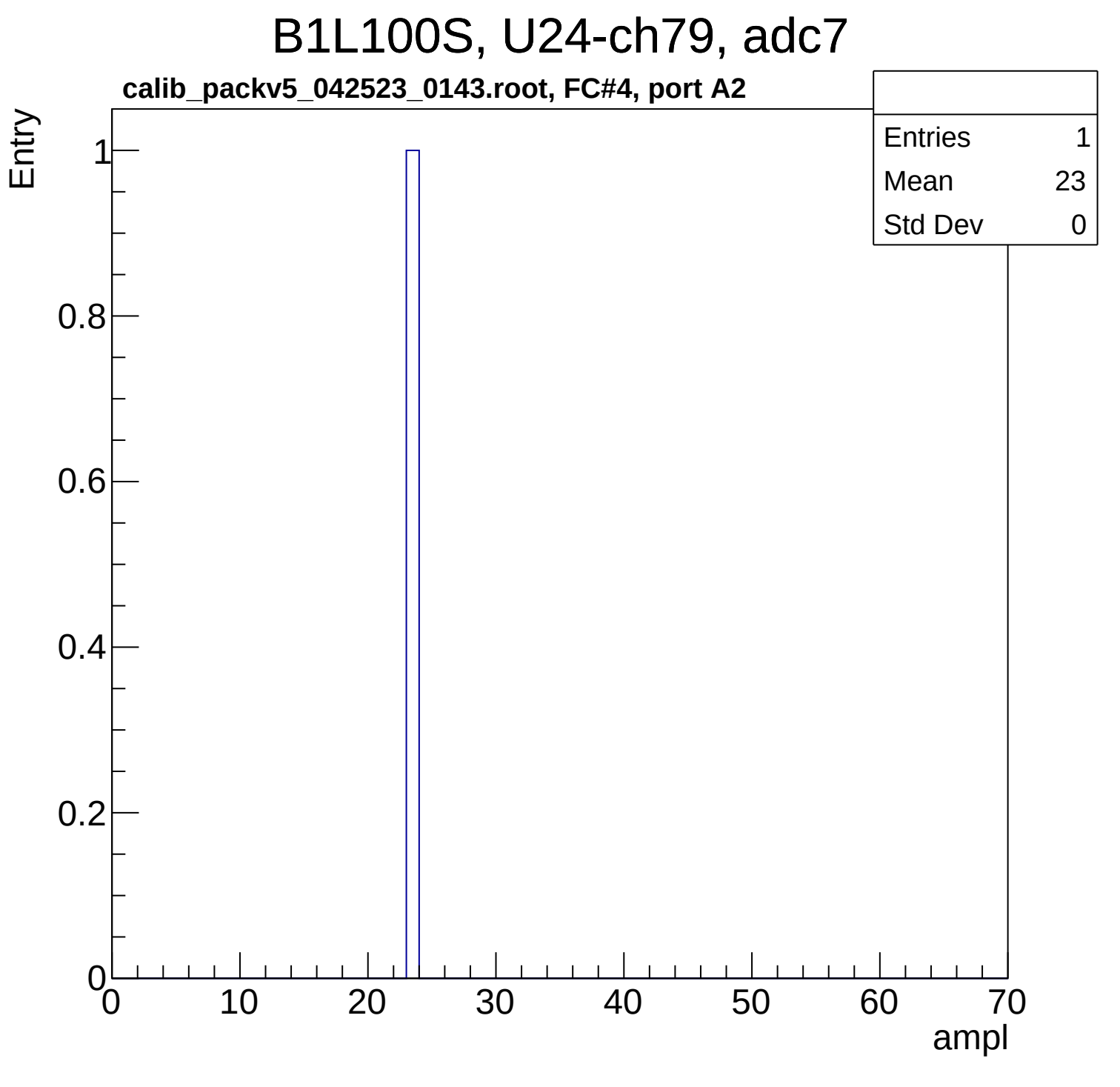
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
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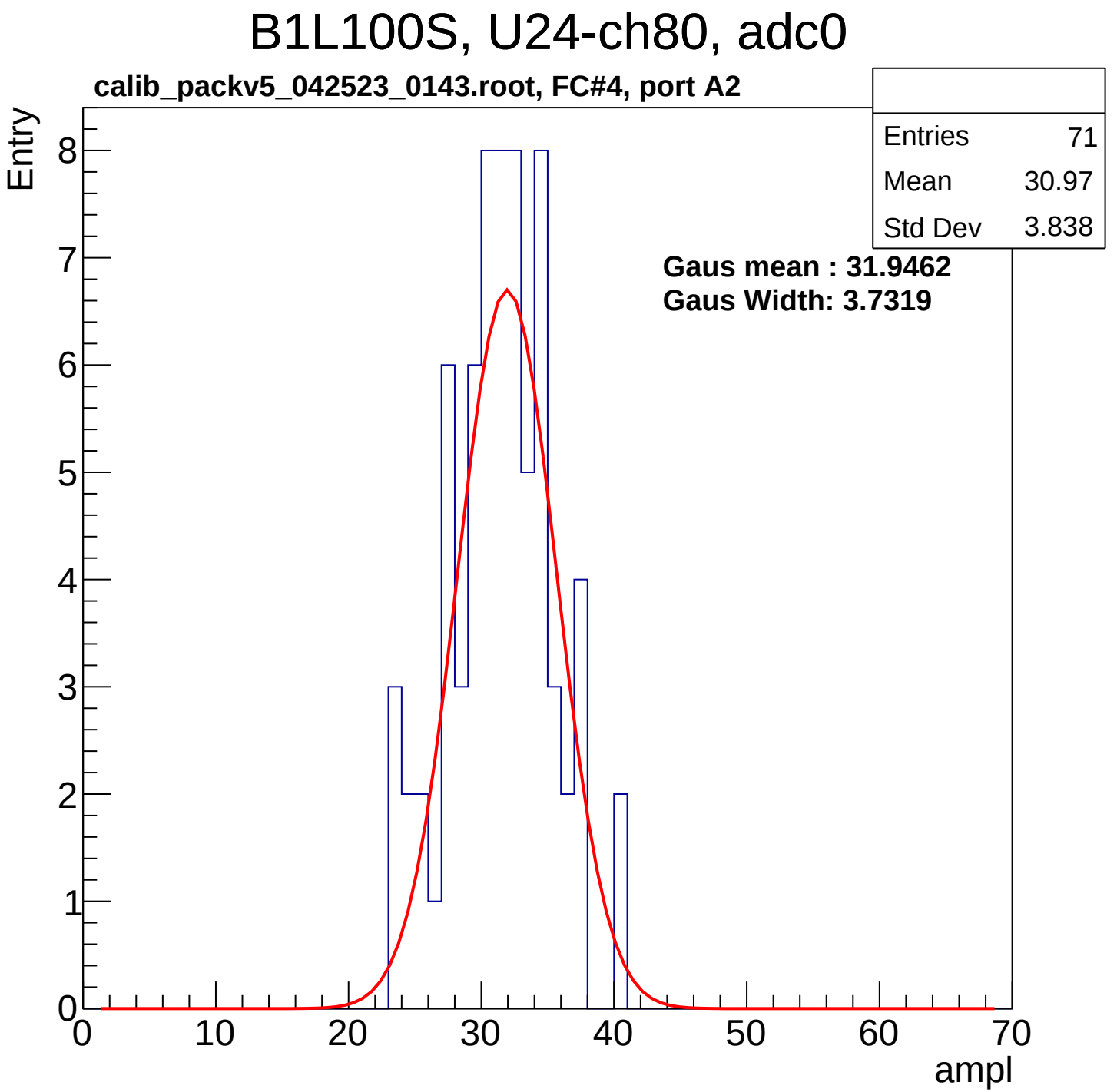
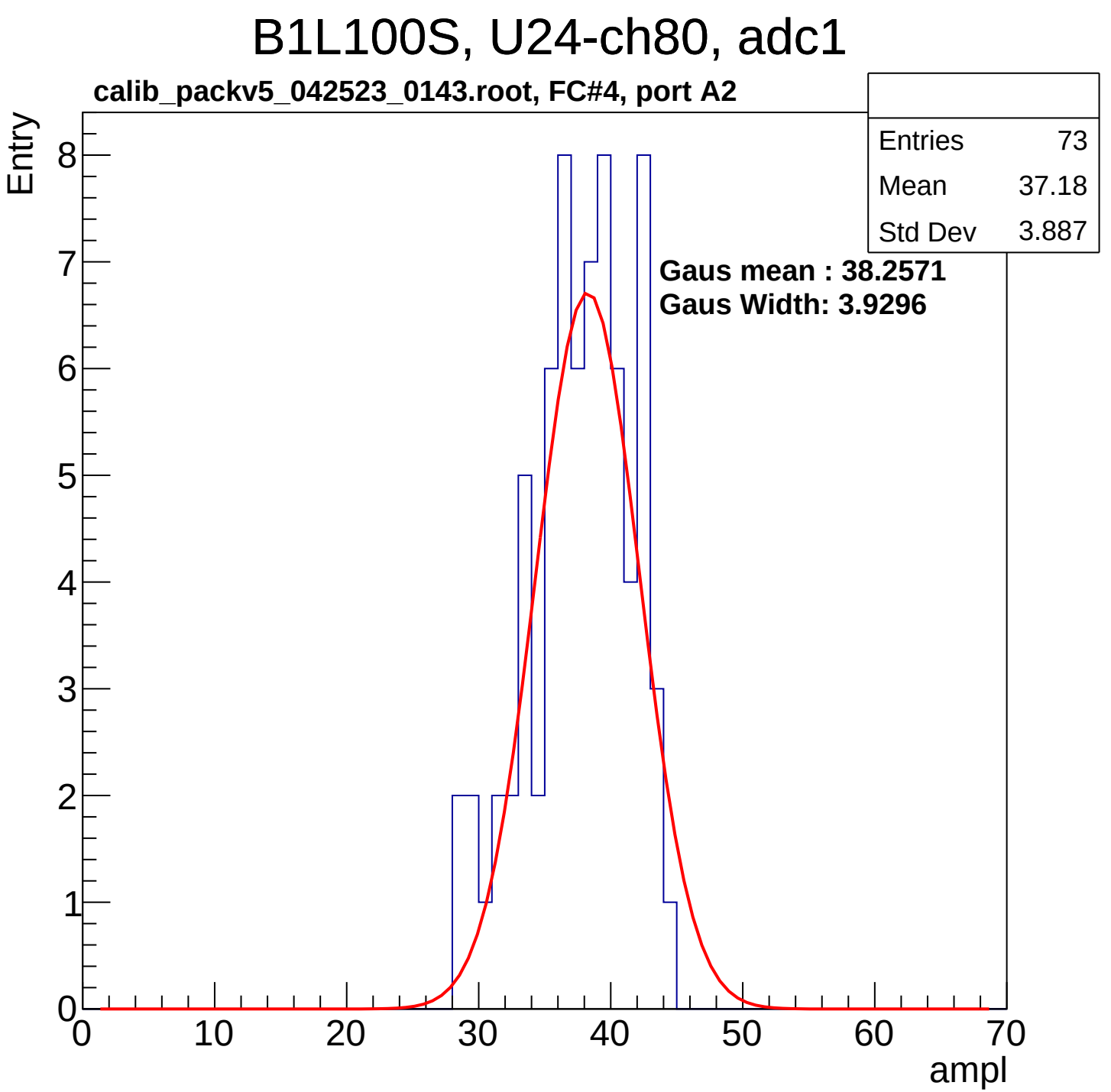
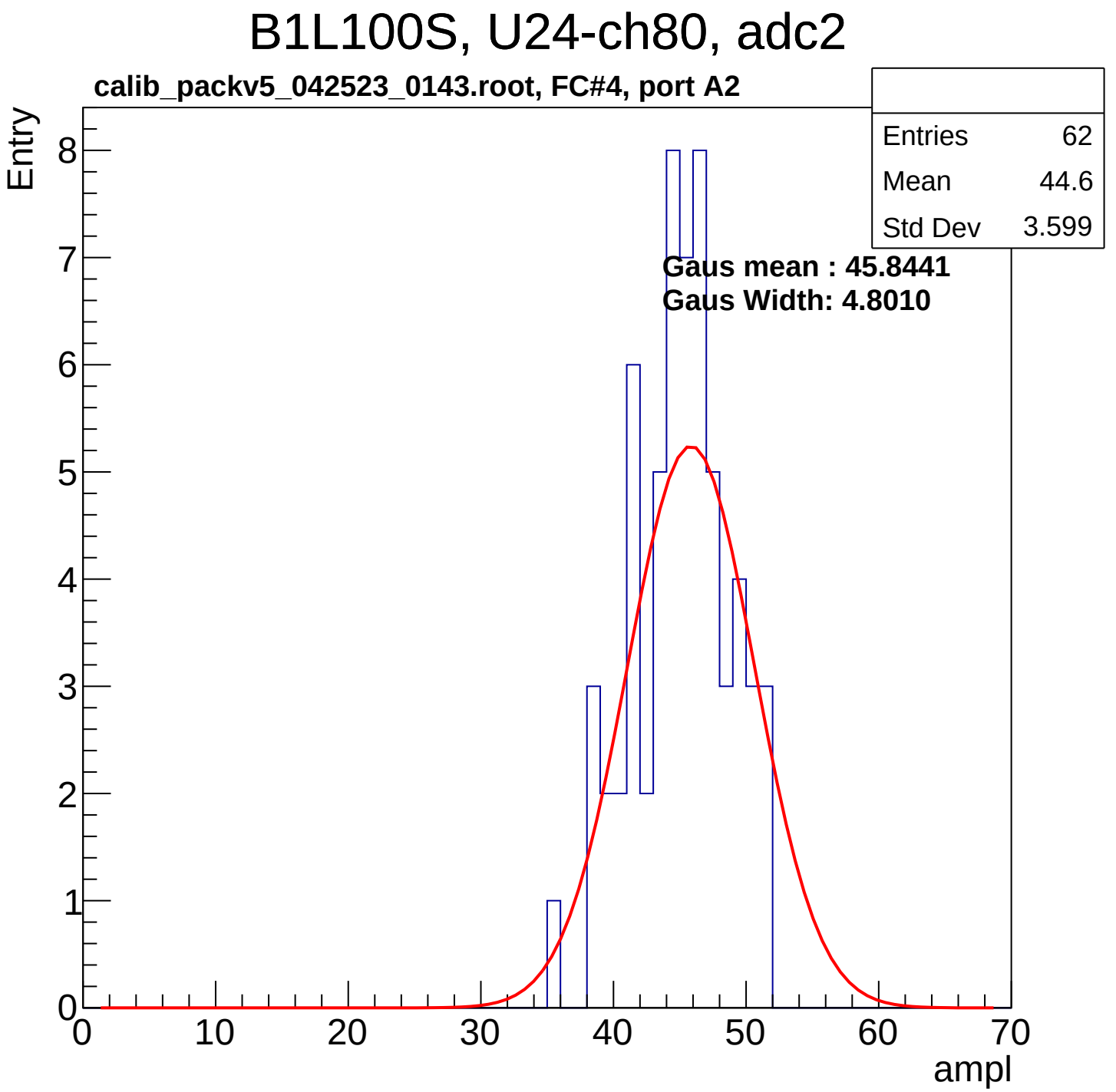
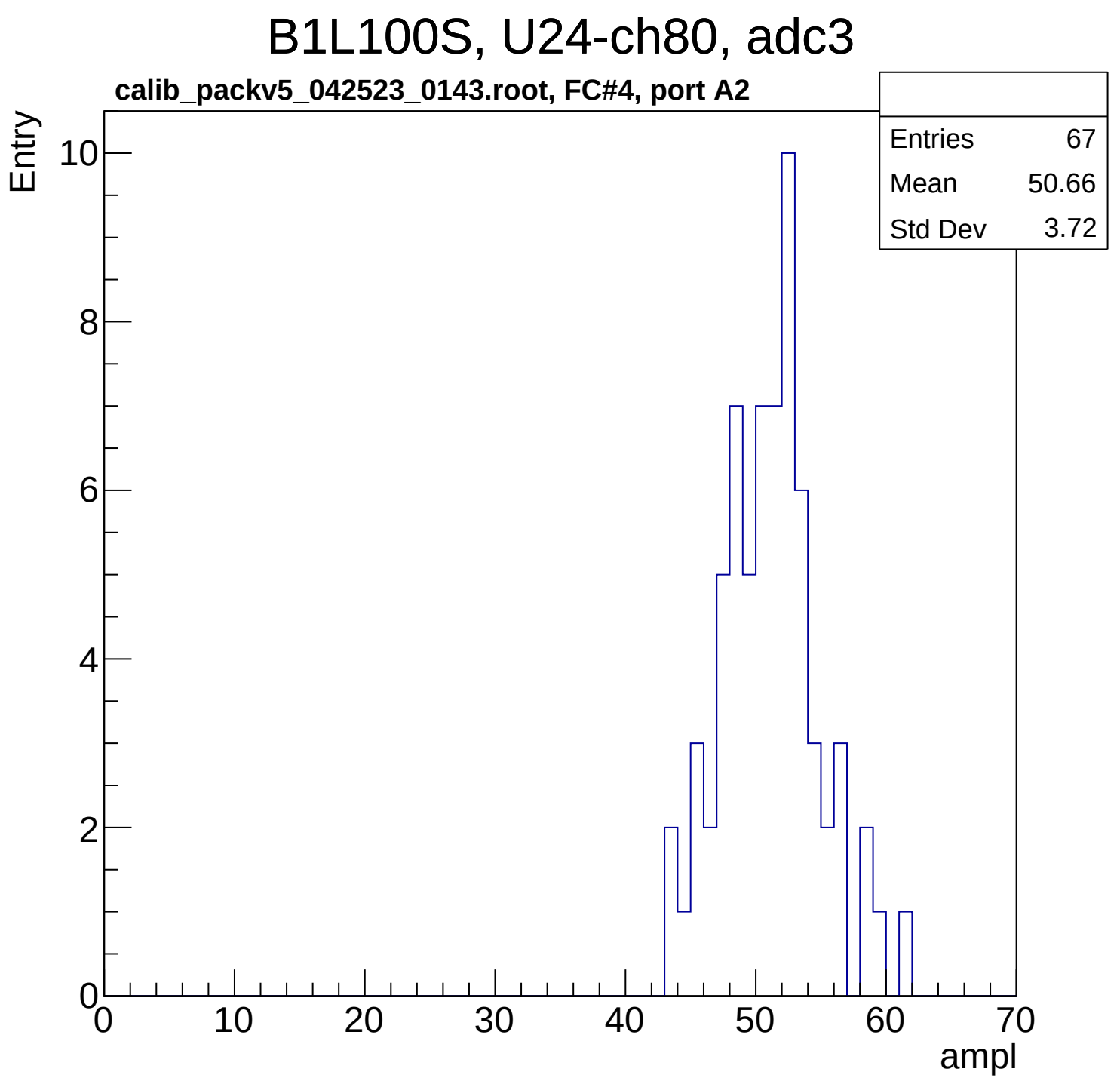
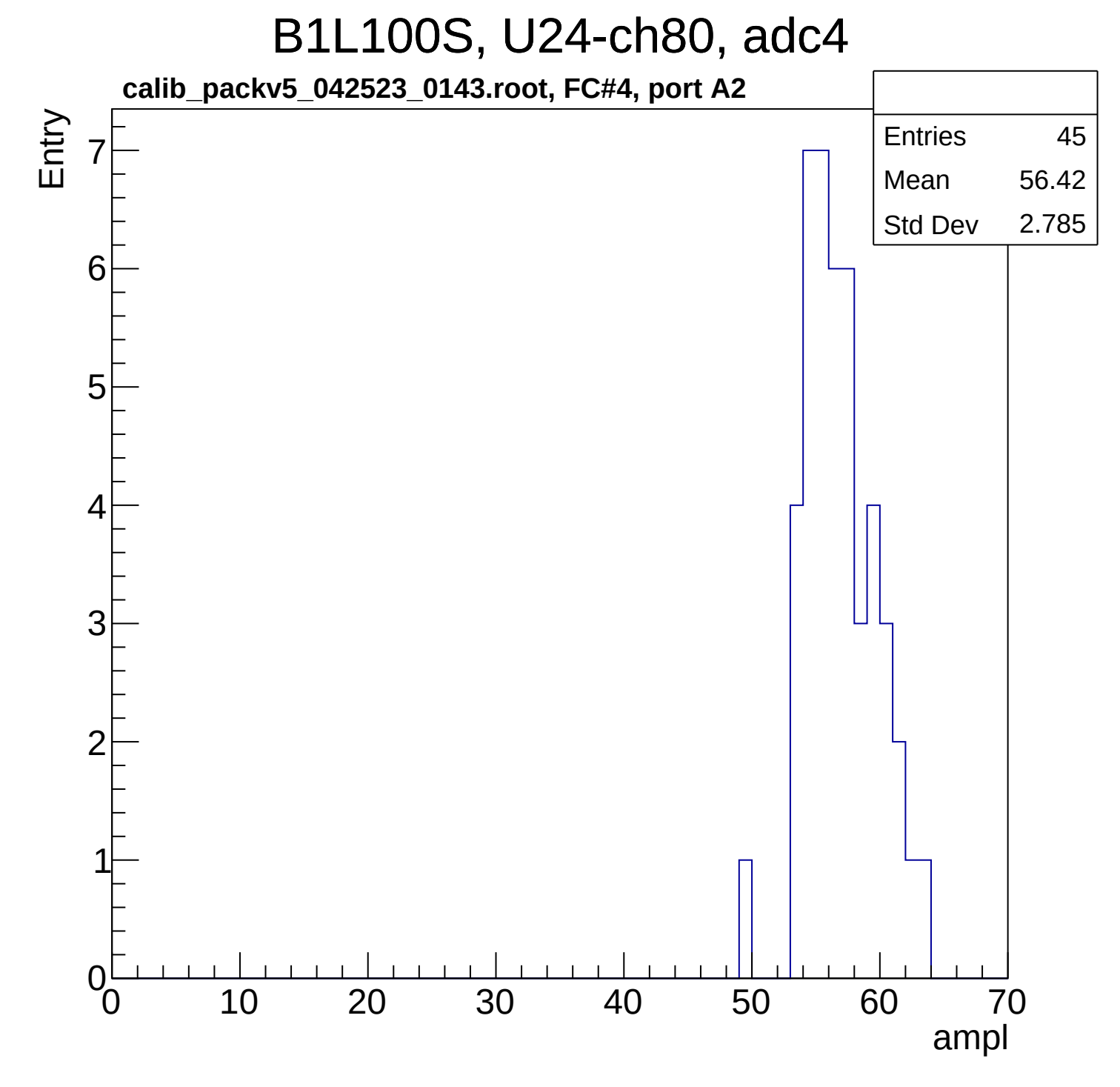
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

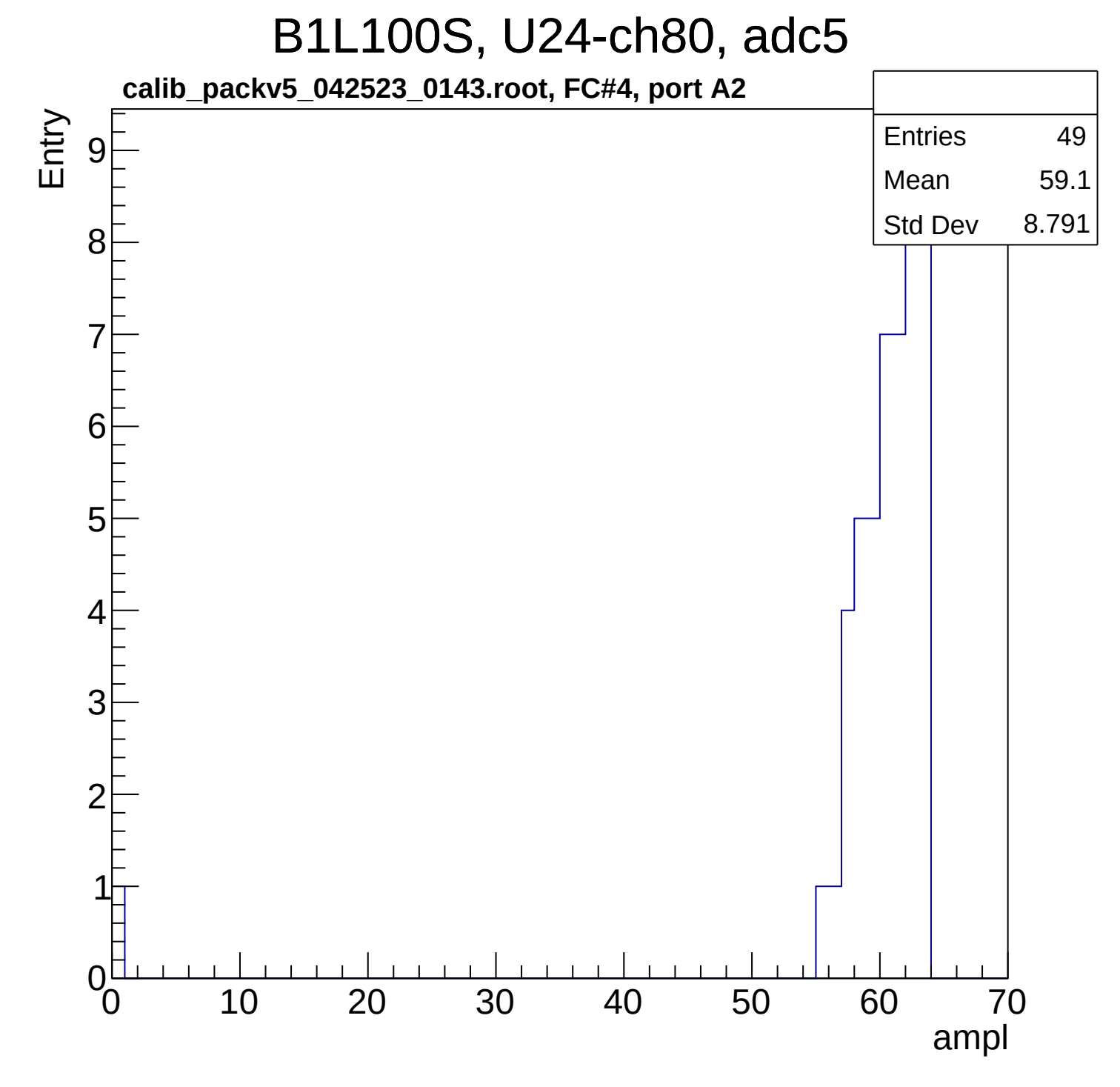

B1L100S, U24-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.97
Std Dev 3.838
Gaus mean : 31.9462
Gaus Width: 3.7319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.18
Std Dev 3.887
Gaus mean : 38.2571
Gaus Width: 3.9296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44.6
Std Dev 3.599
Gaus mean : 45.8441
Gaus Width: 4.8010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.66
Std Dev 3.72
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 56.42
Std Dev 2.785
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 59.1
Std Dev 8.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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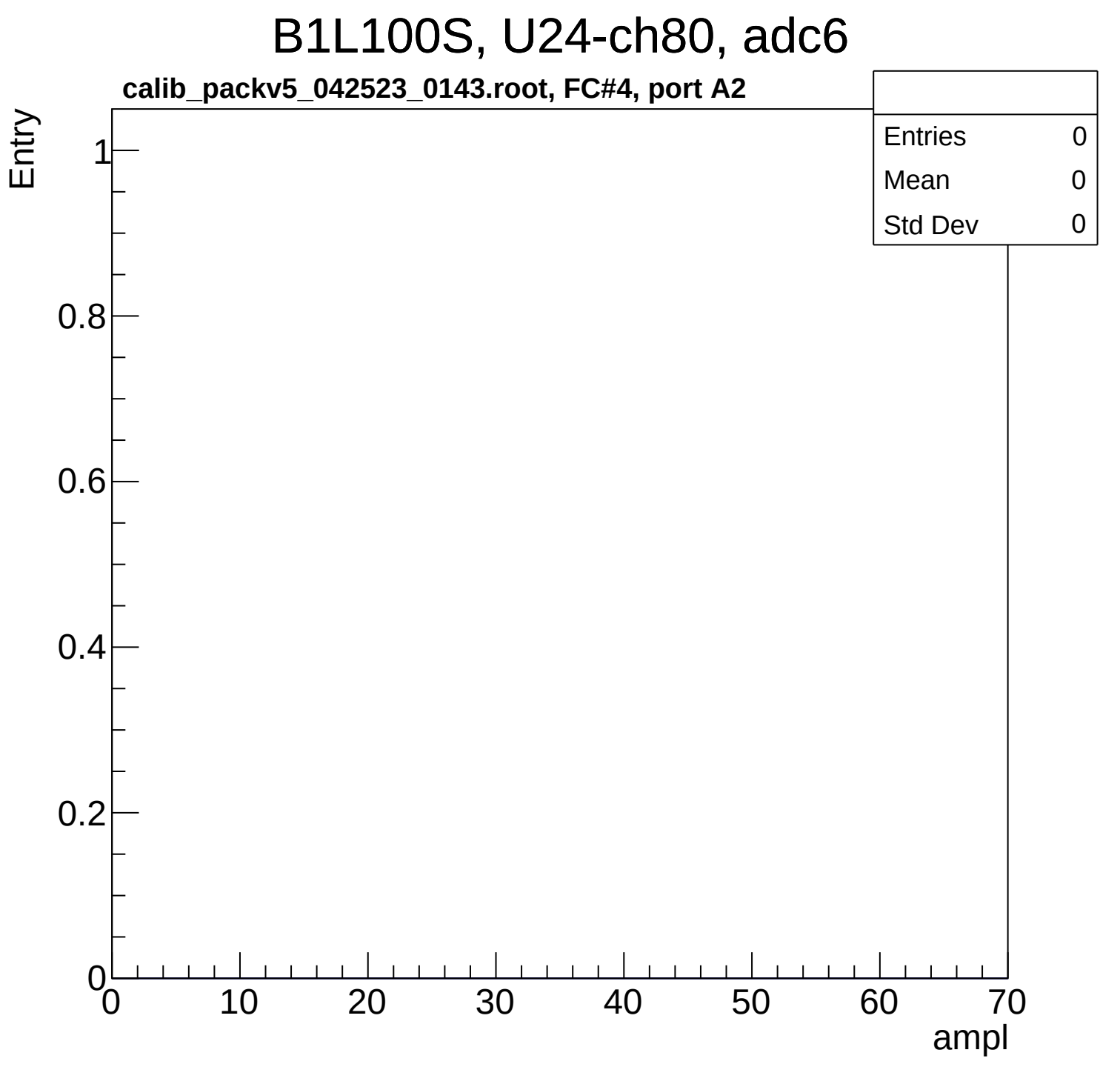
B1L100S, U24-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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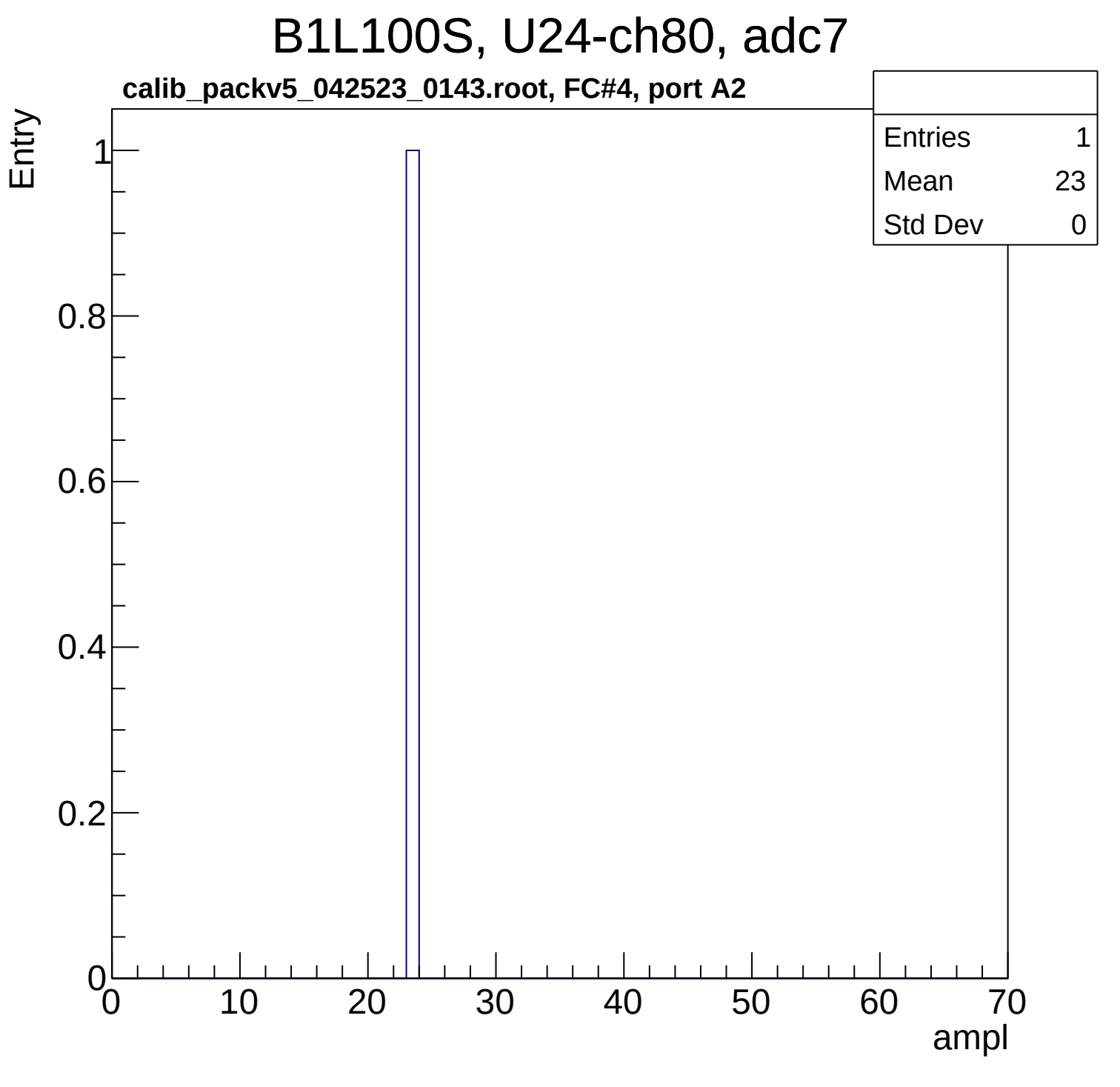
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch80, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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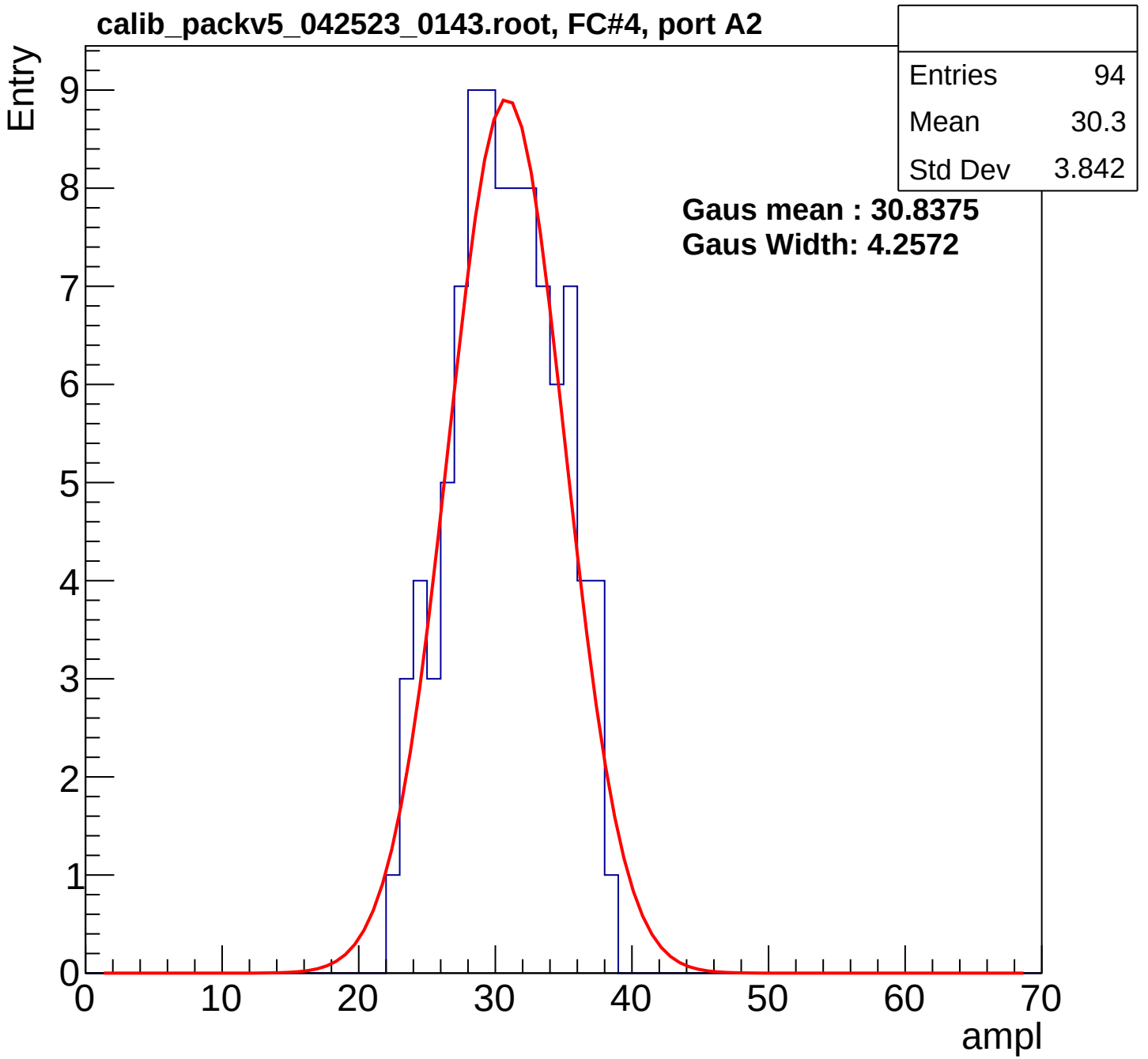
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 30.3
Std Dev 3.842
Gaus mean : 30.8375
Gaus Width: 4.2572
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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60
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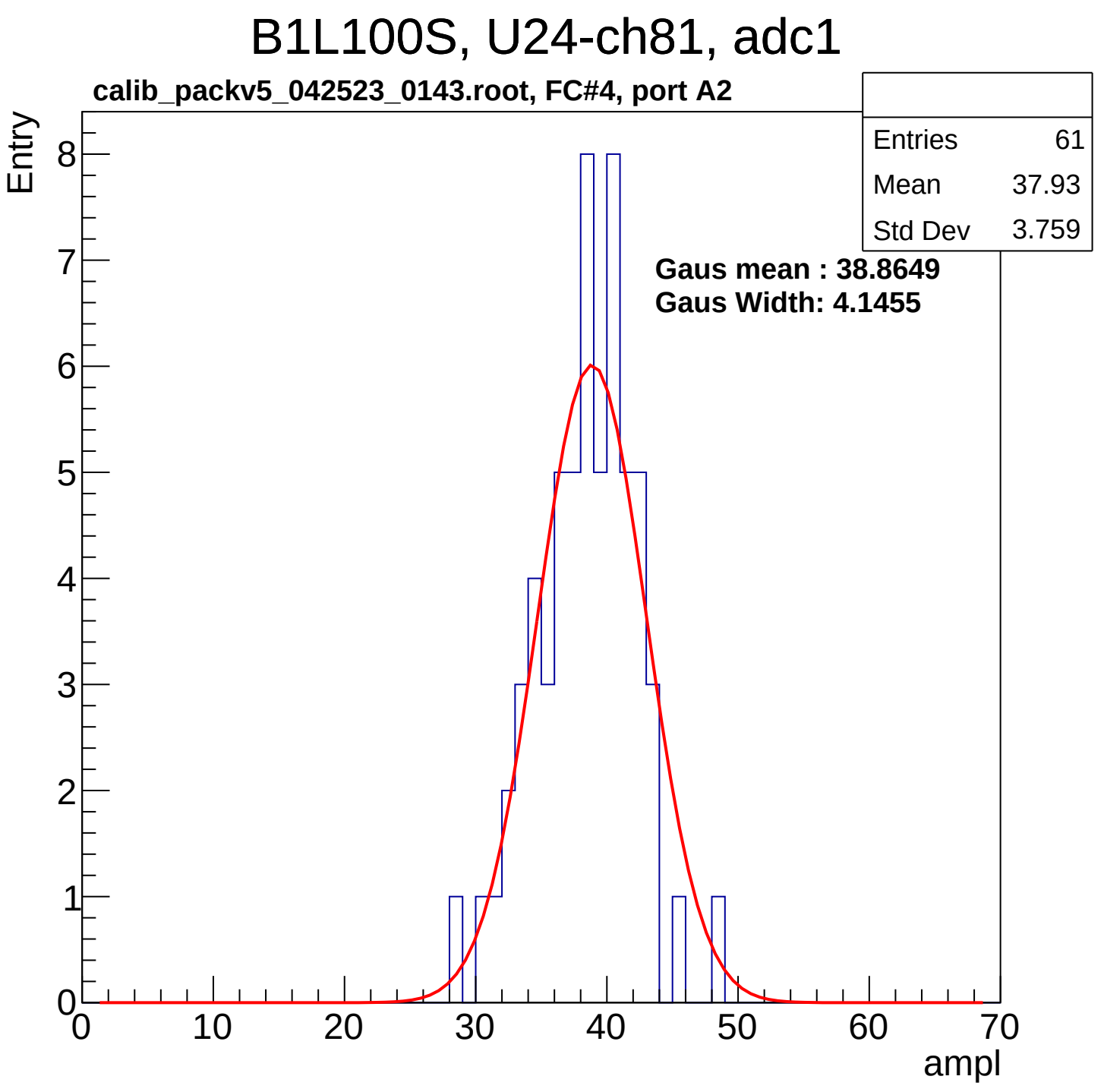
B1L100S, U24-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.93
Std Dev 3.759
Gaus mean : 38.8649
Gaus Width: 4.1455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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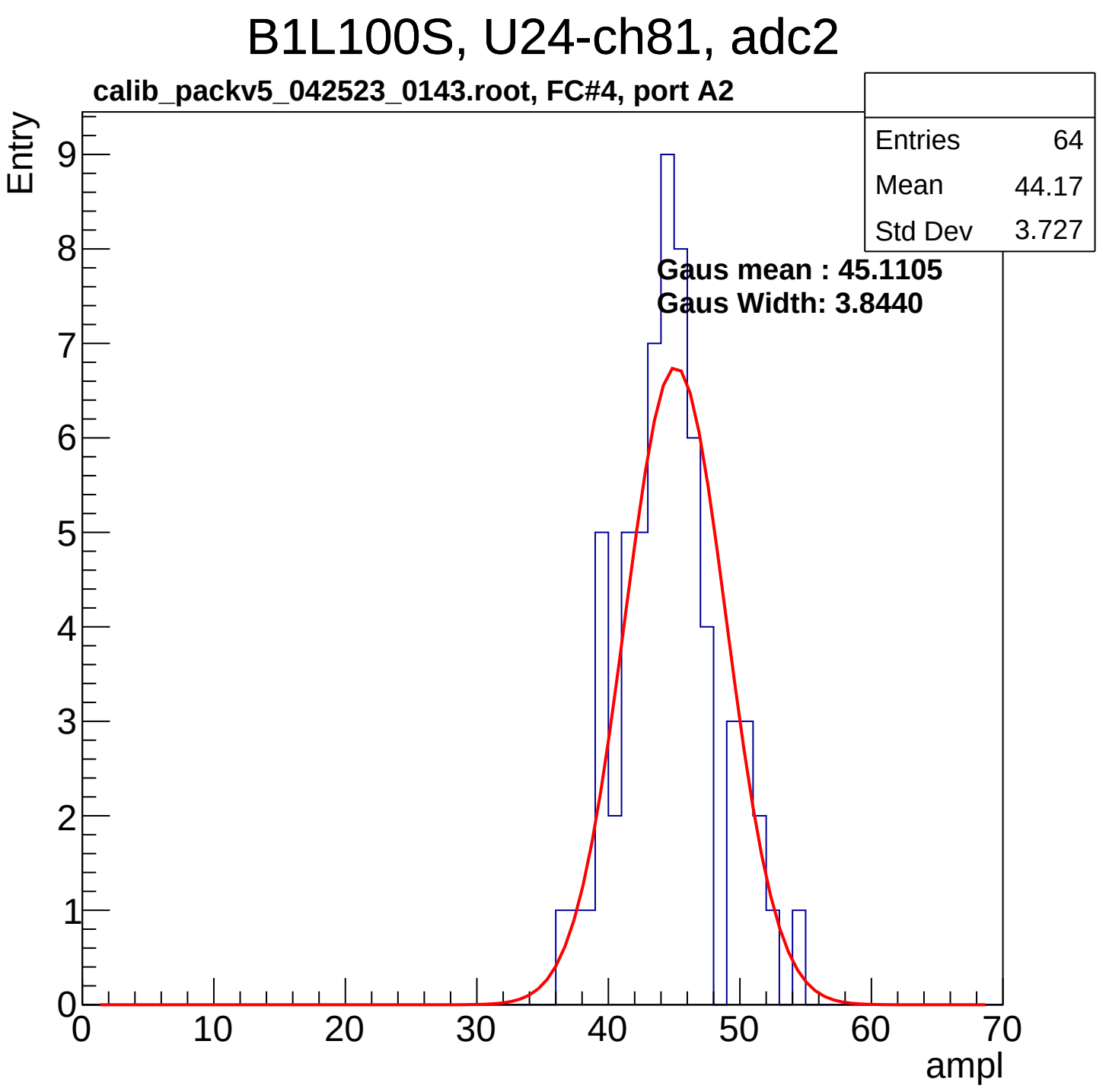
B1L100S, U24-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 44.17
Std Dev 3.727
Gaus mean : 45.1105
Gaus Width: 3.8440
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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40
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60
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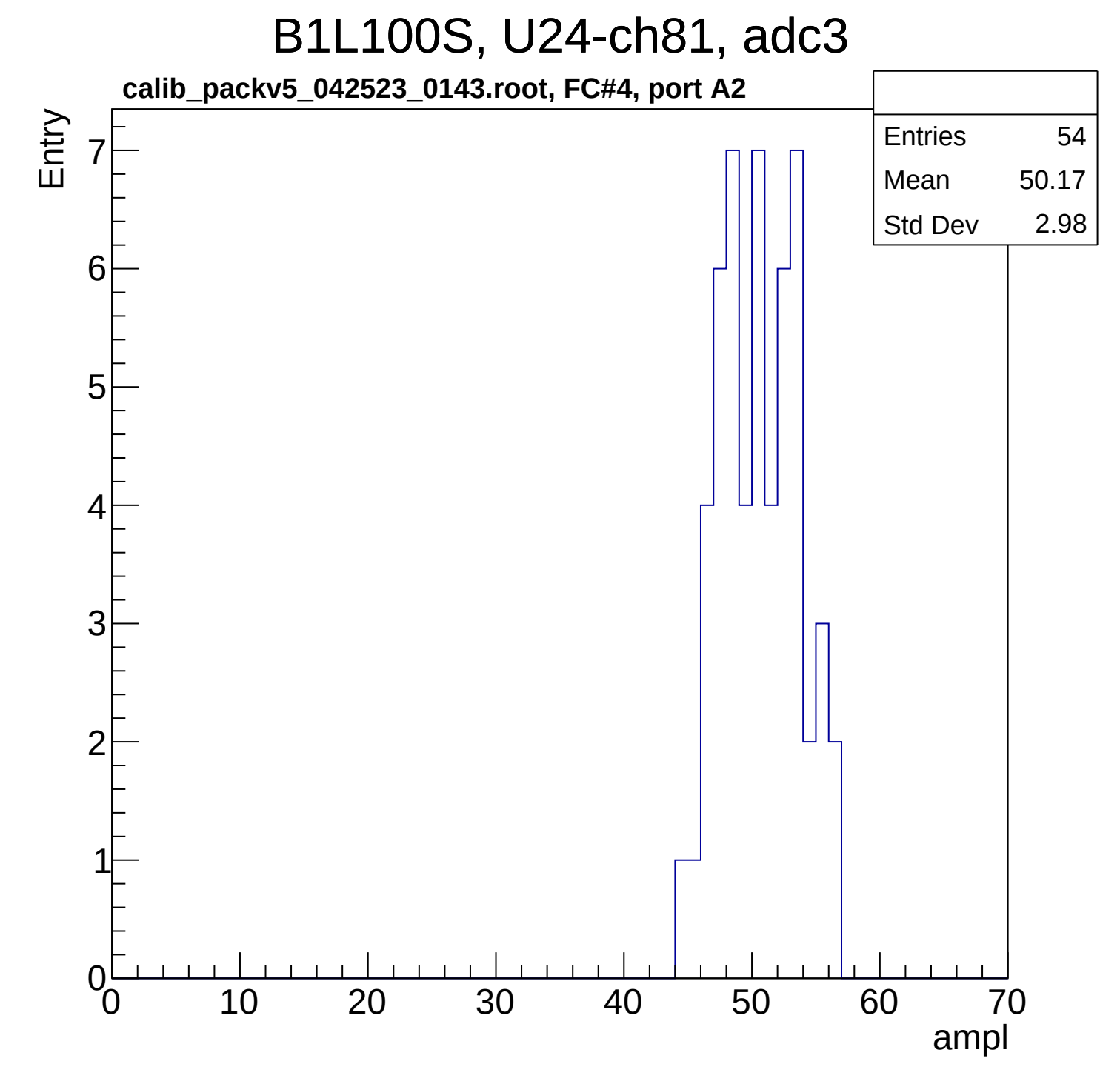
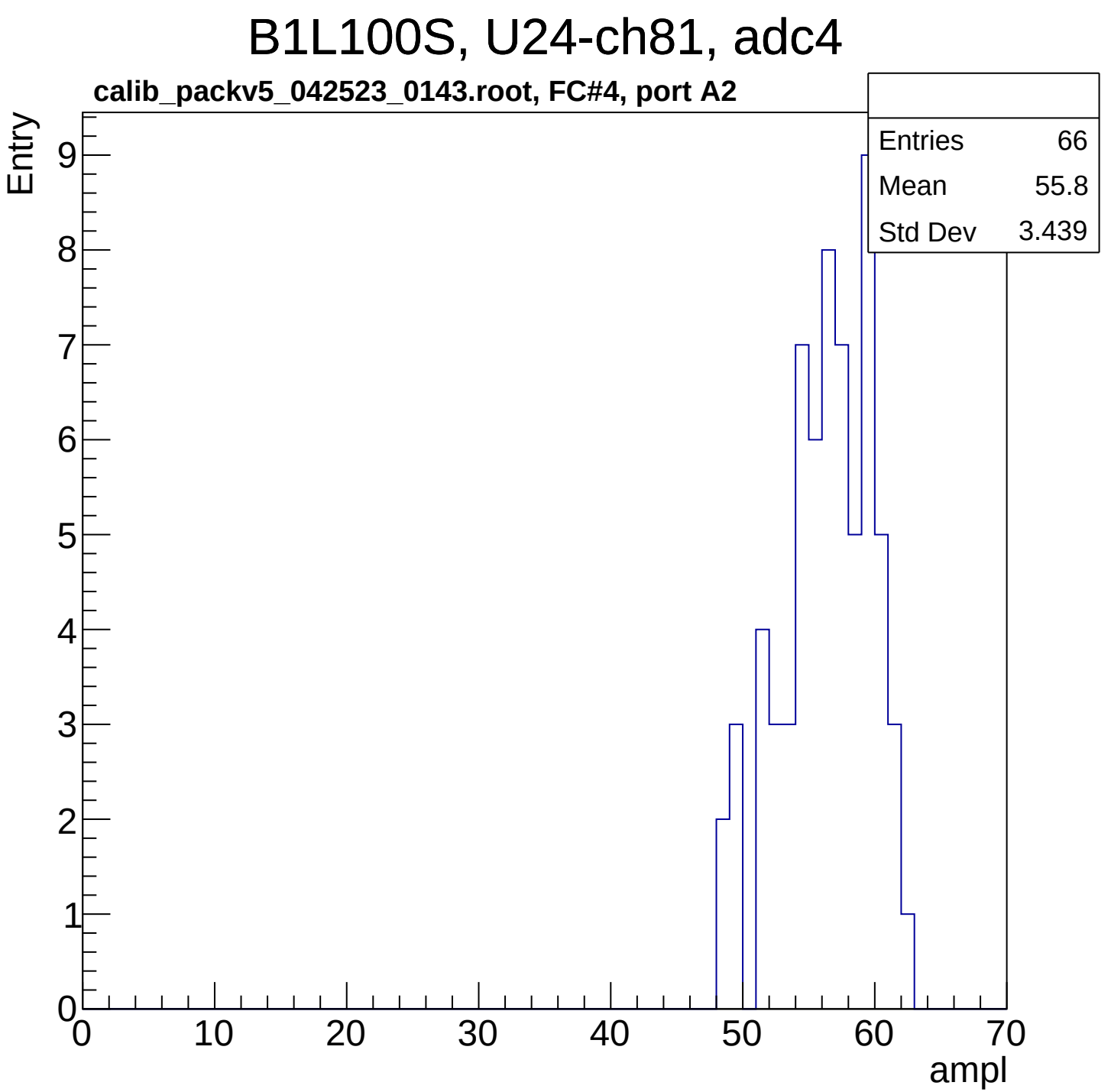

B1L100S, U24-ch81, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 50.17
Std Dev 2.98
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch81, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.8
Std Dev 3.439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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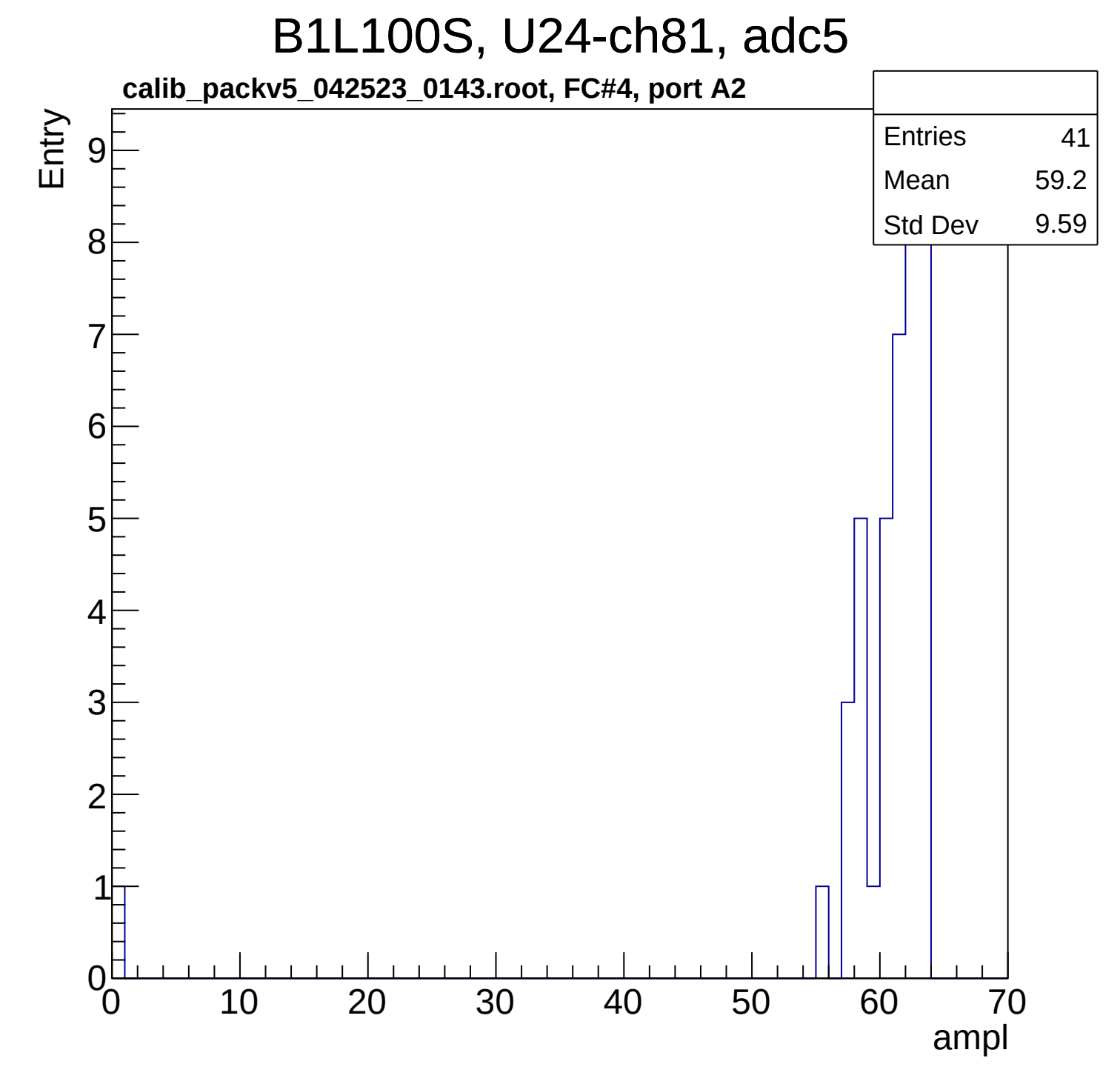
B1L100S, U24-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.2
Std Dev 9.59
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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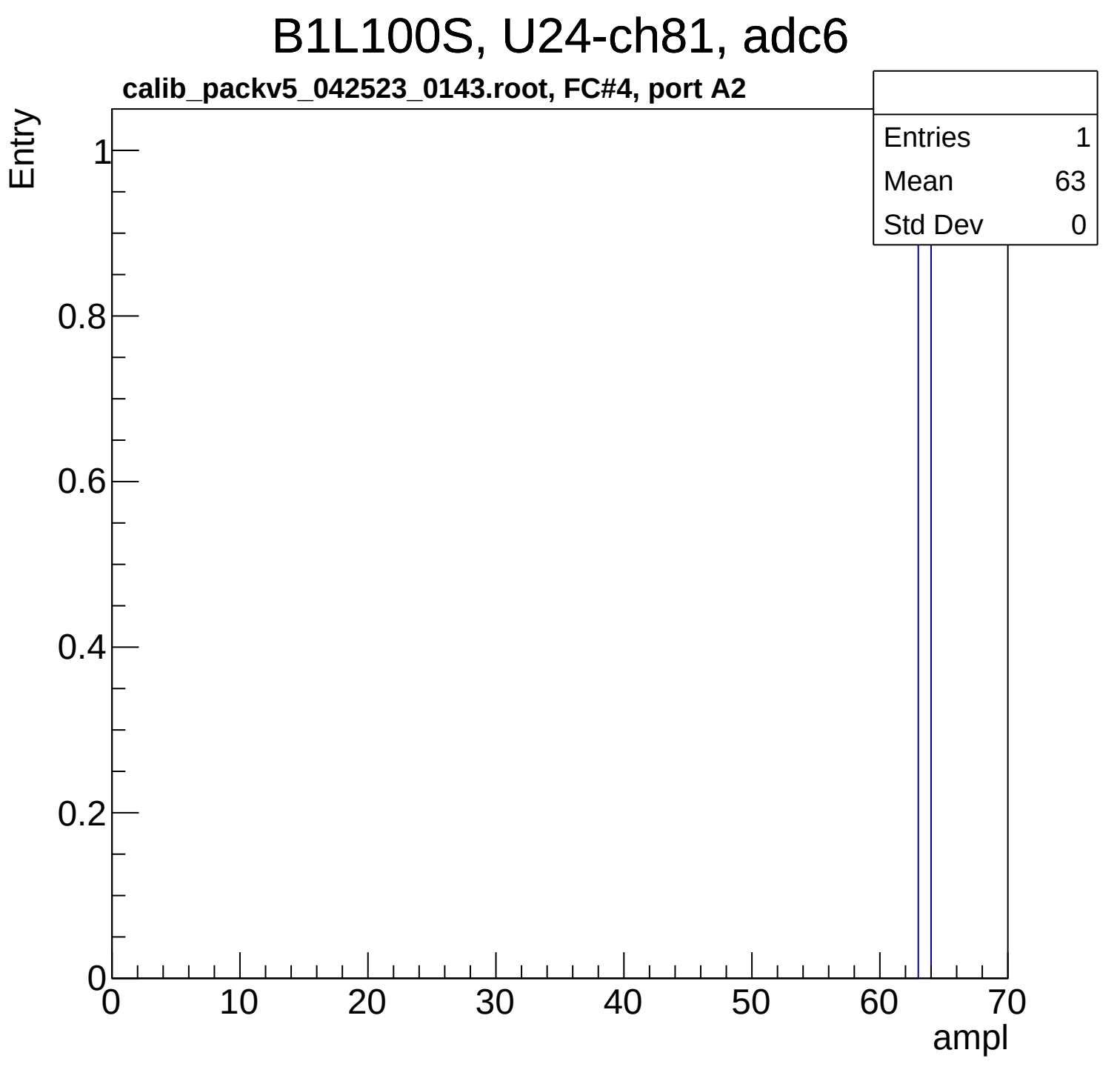
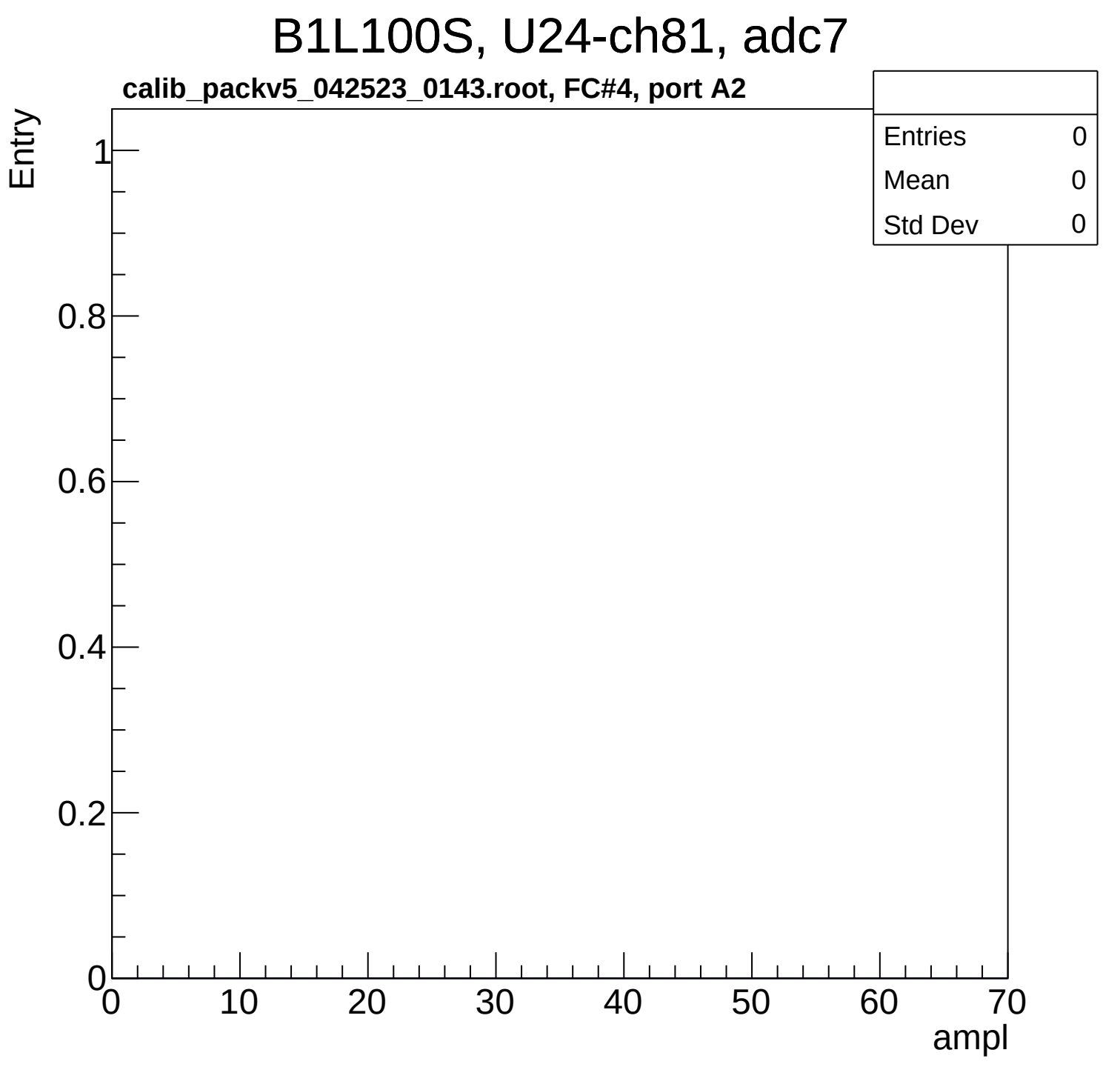
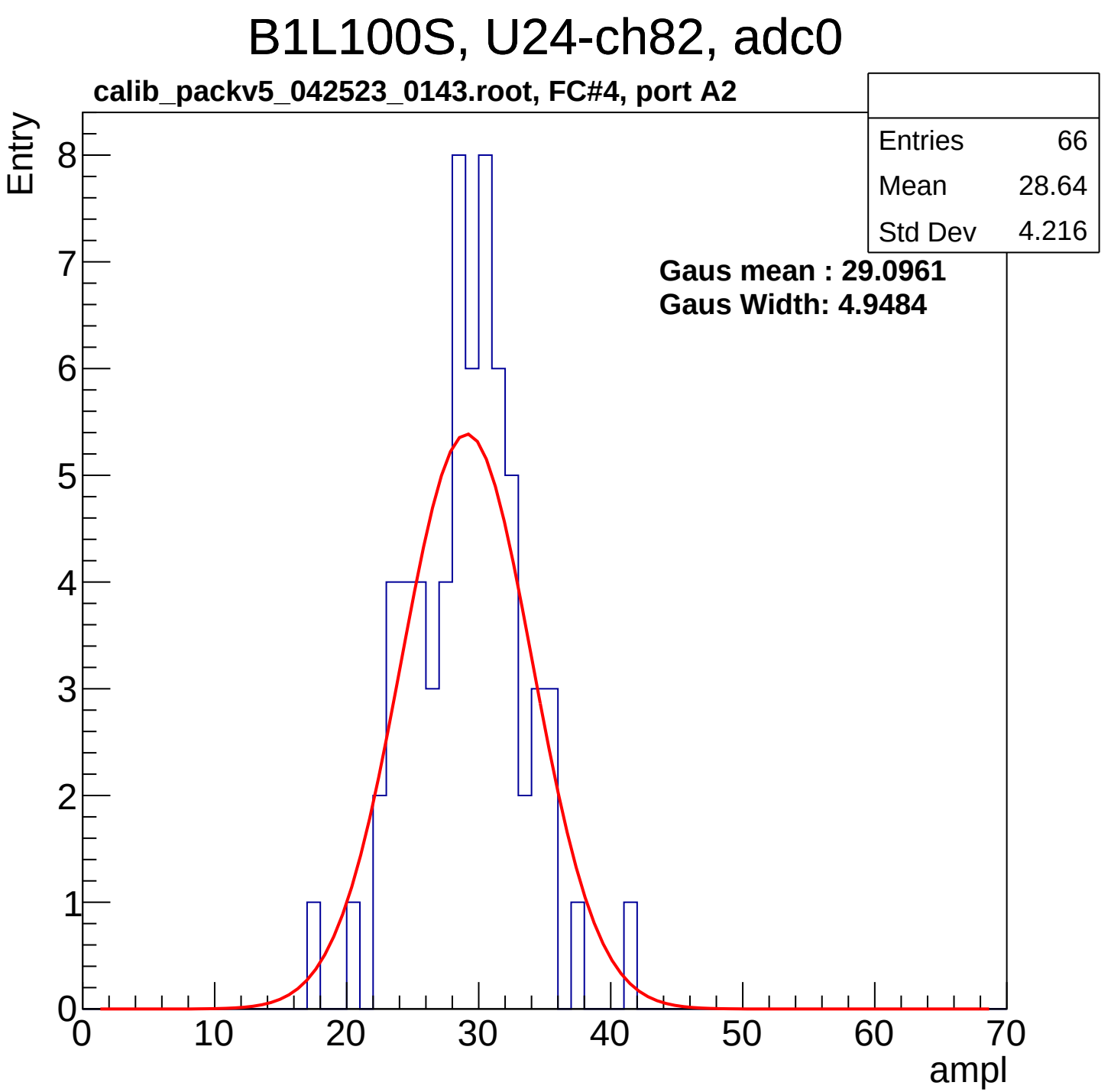

B1L100S, U24-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.64
Std Dev 4.216
Gaus mean : 29.0961
Gaus Width: 4.9484
Entry
ampl
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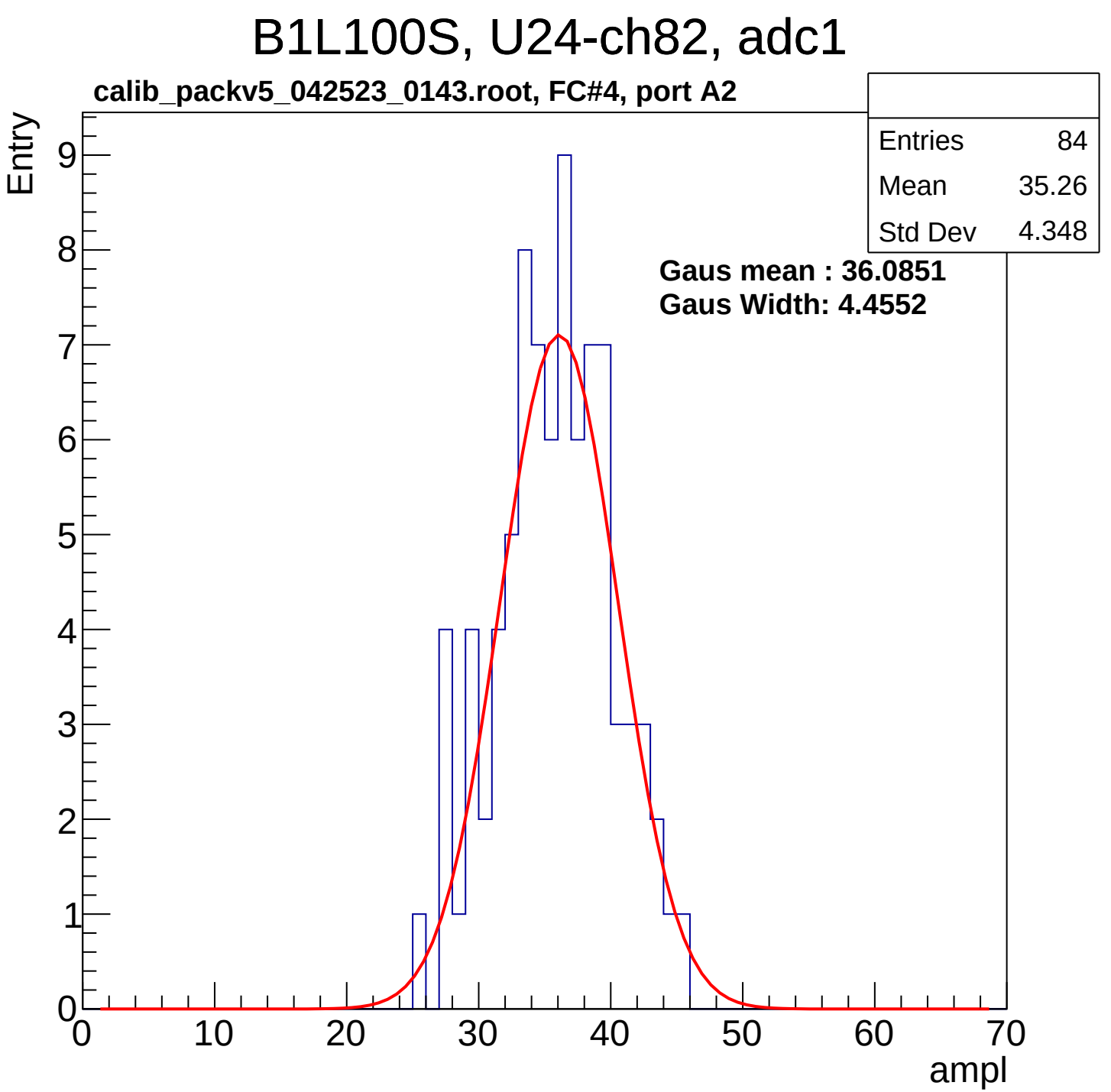
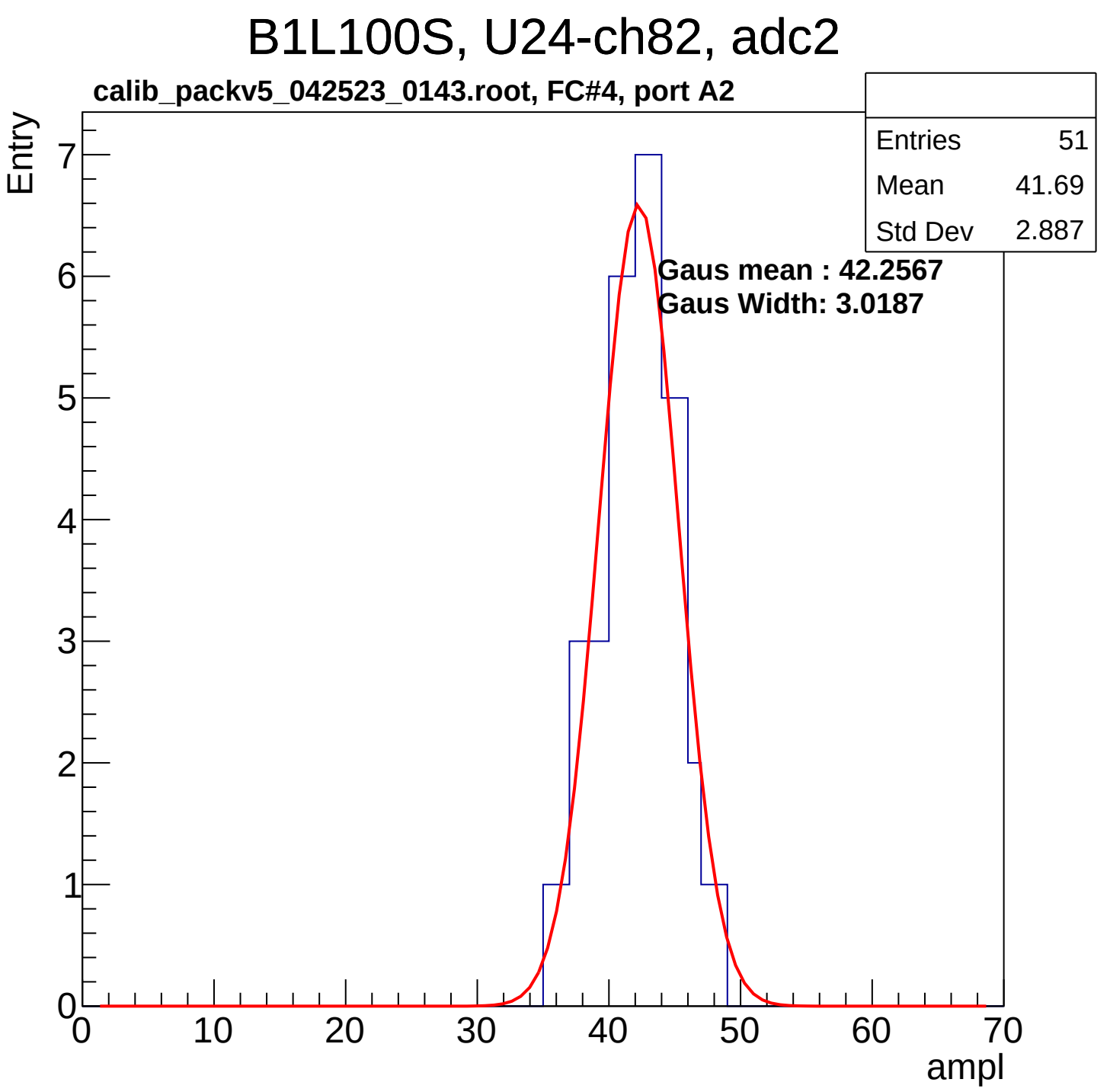
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 35.26
Std Dev 4.348
Gaus mean : 36.0851
Gaus Width: 4.4552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 41.69
Std Dev 2.887
Gaus mean : 42.2567
Gaus Width: 3.0187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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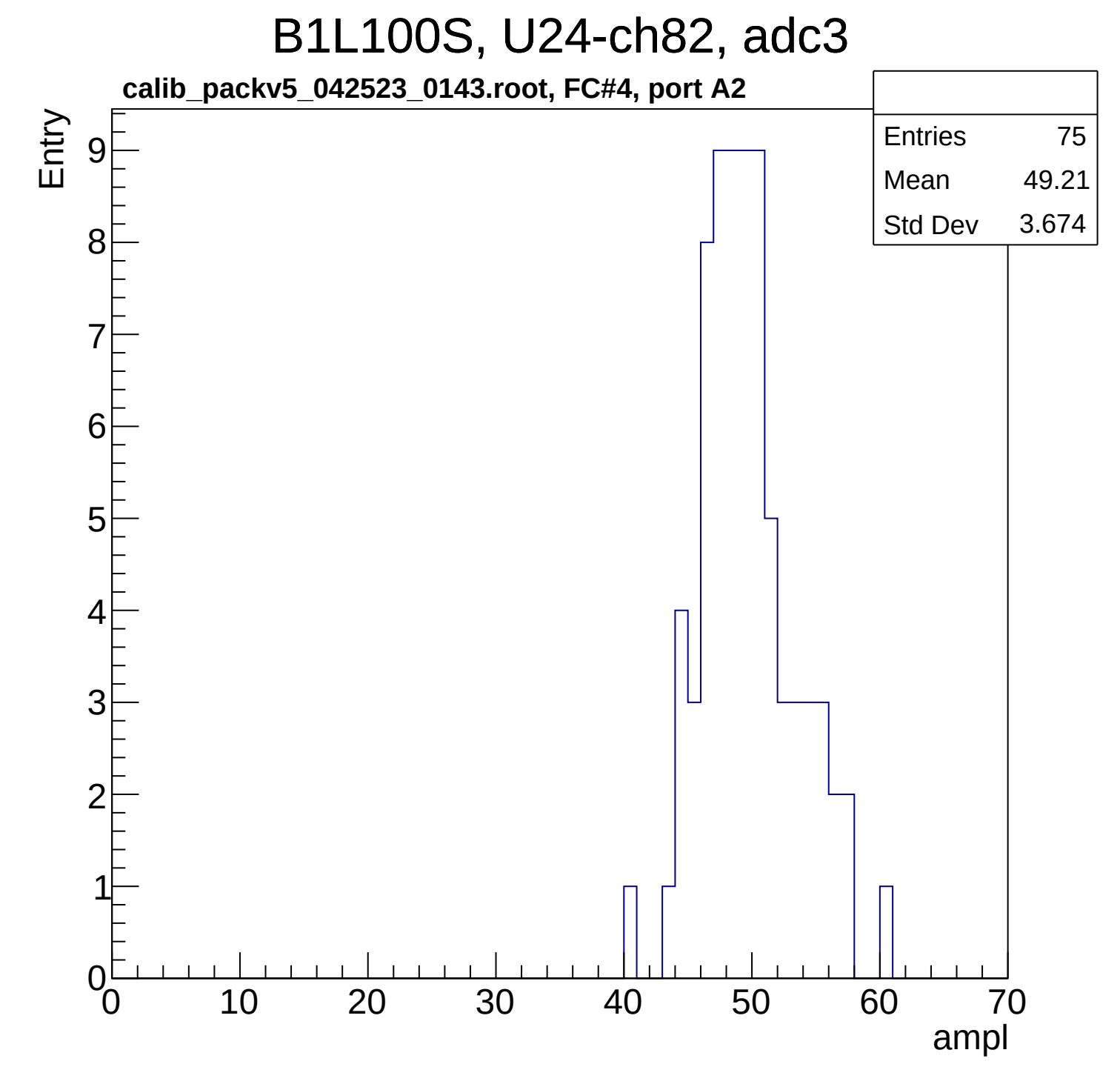
B1L100S, U24-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 49.21
Std Dev 3.674
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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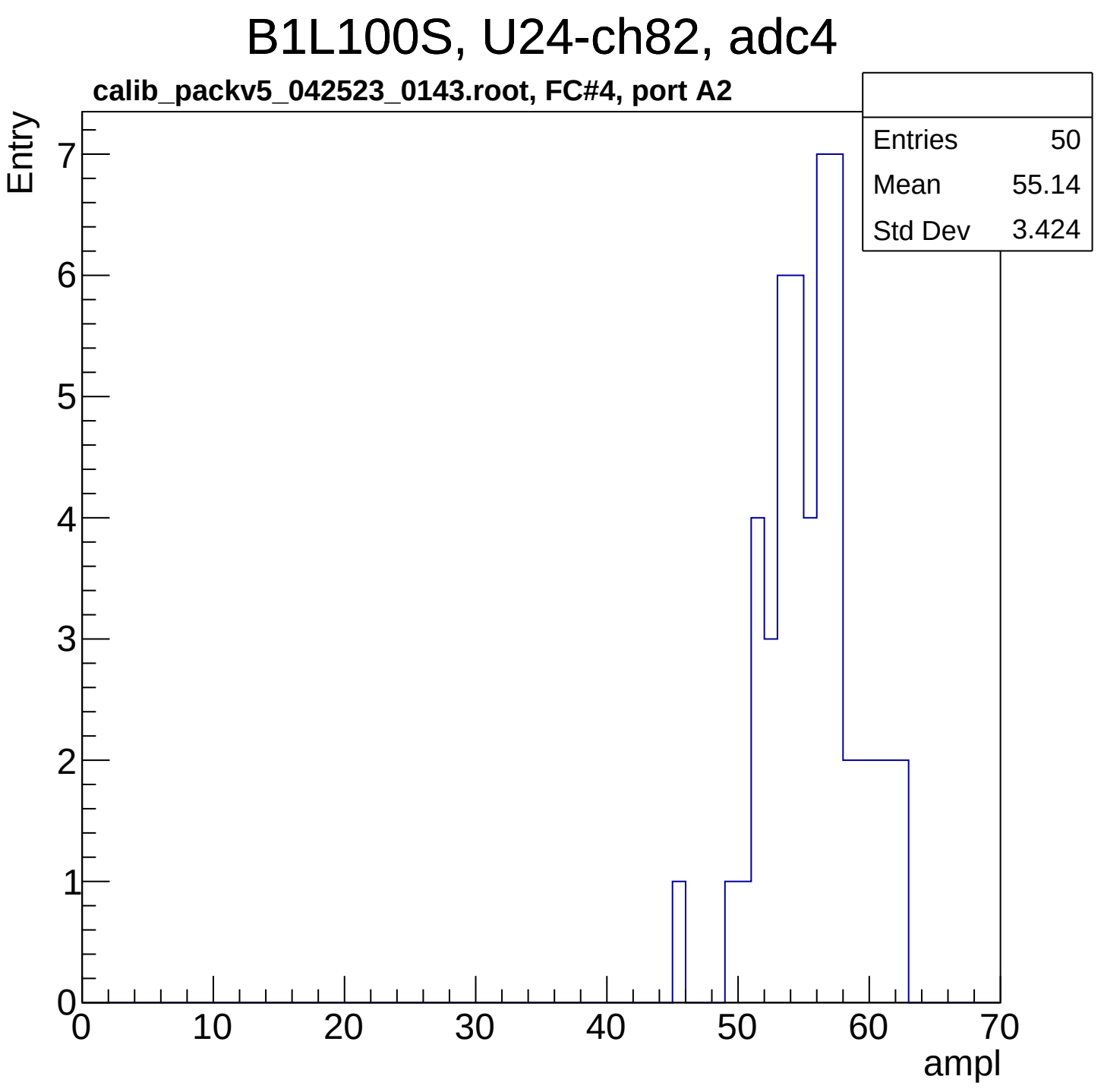
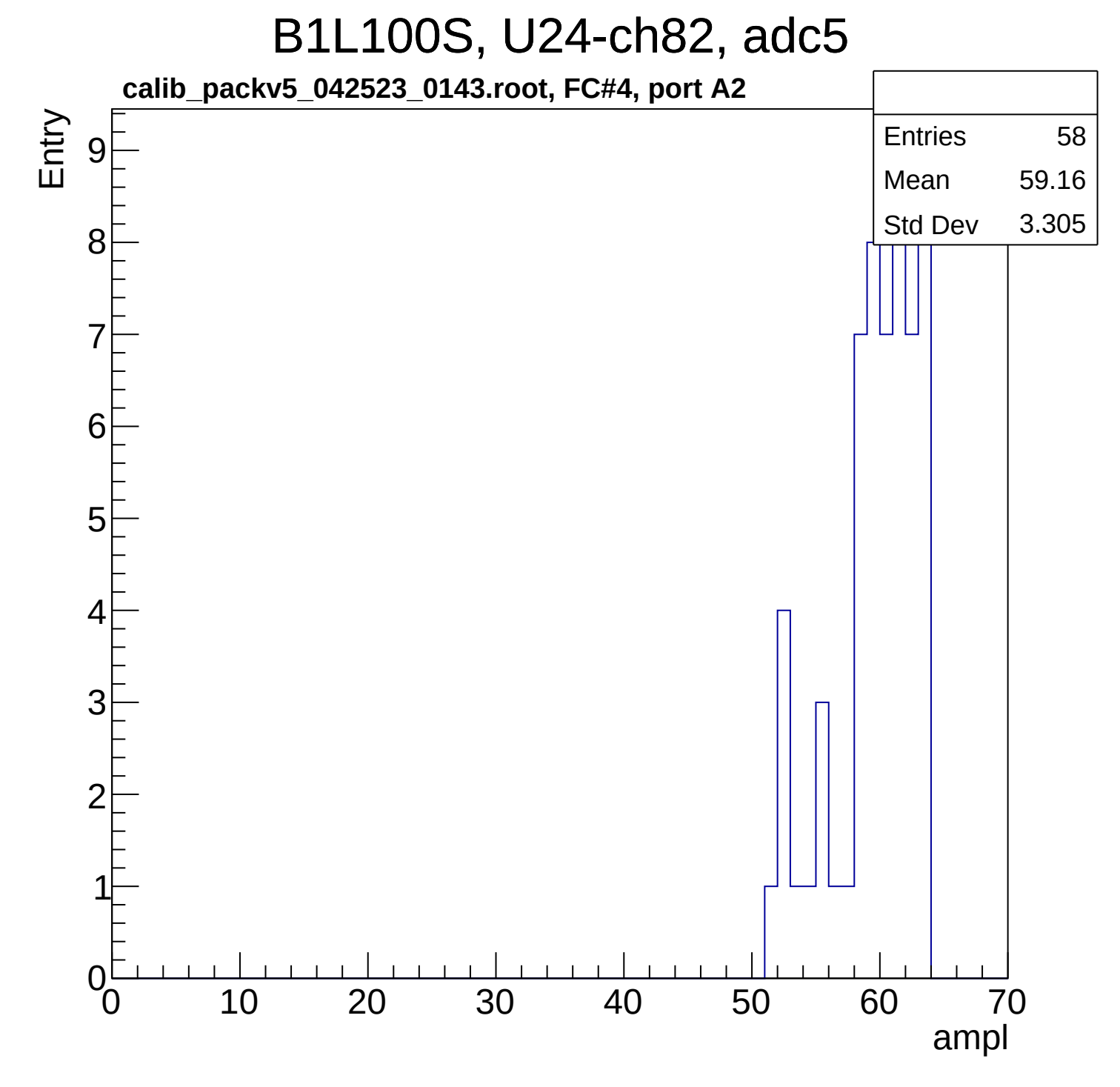
40
50
60
70


B1L100S, U24-ch82, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.14
Std Dev 3.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 59.16
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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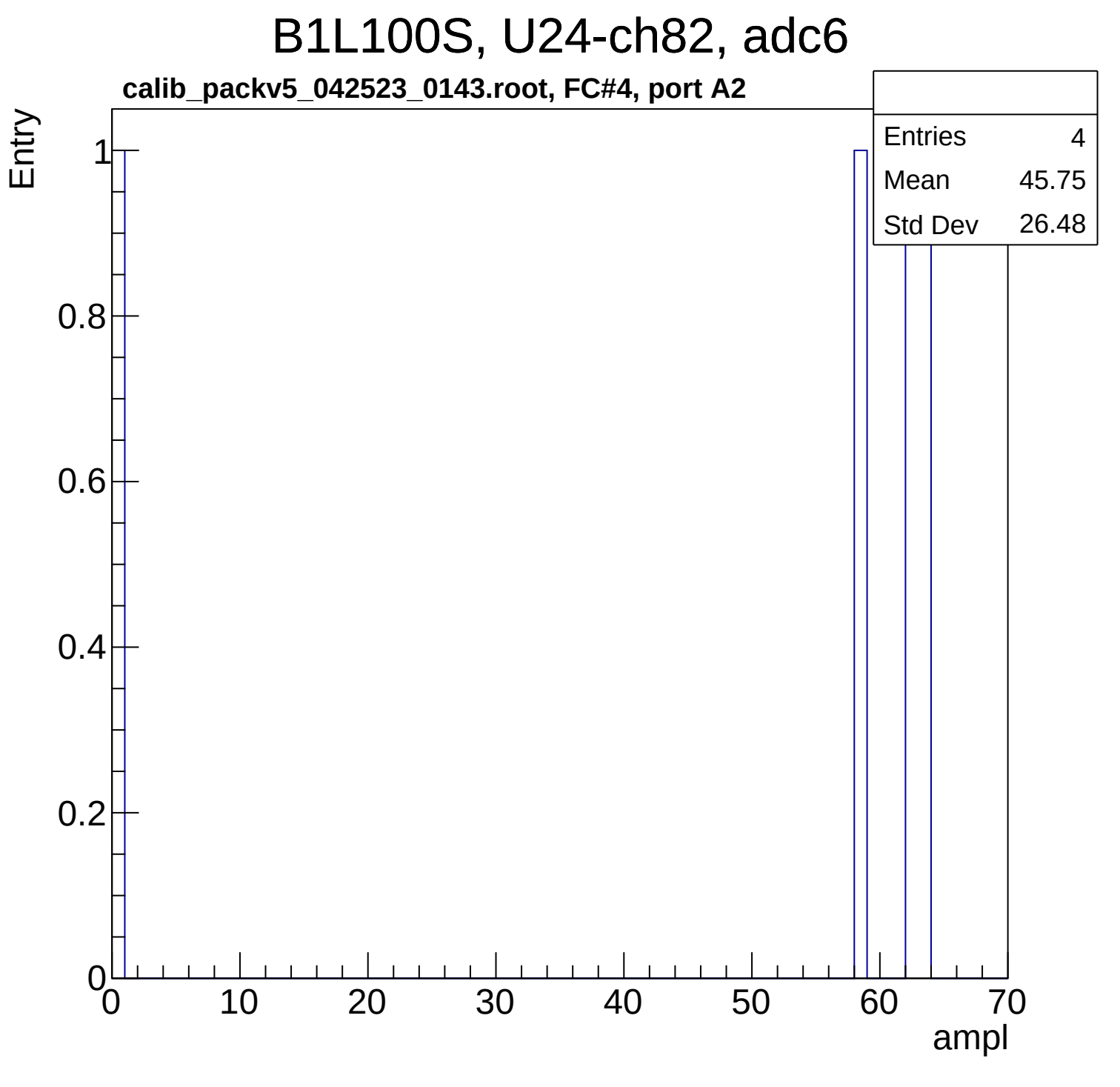
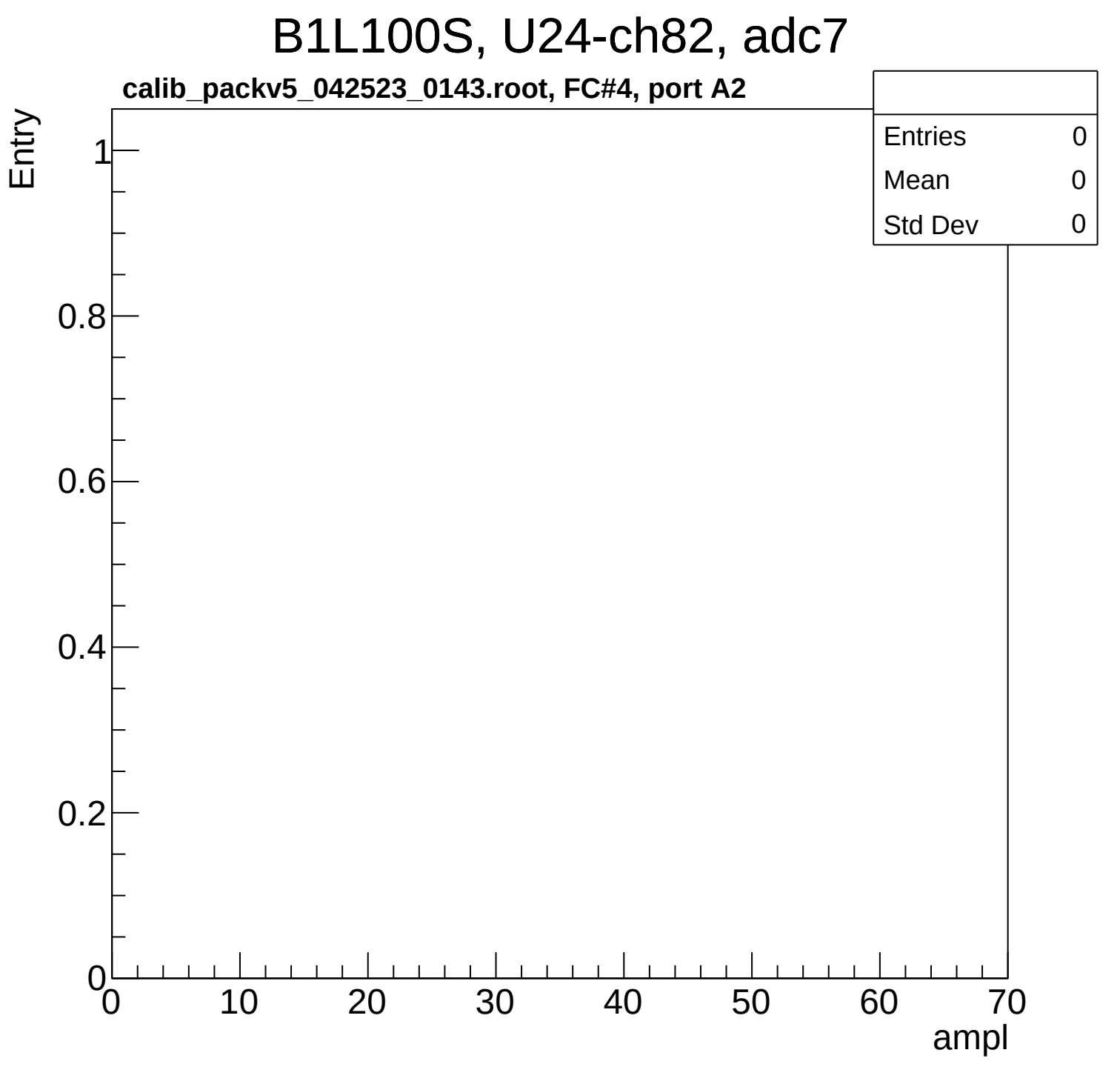
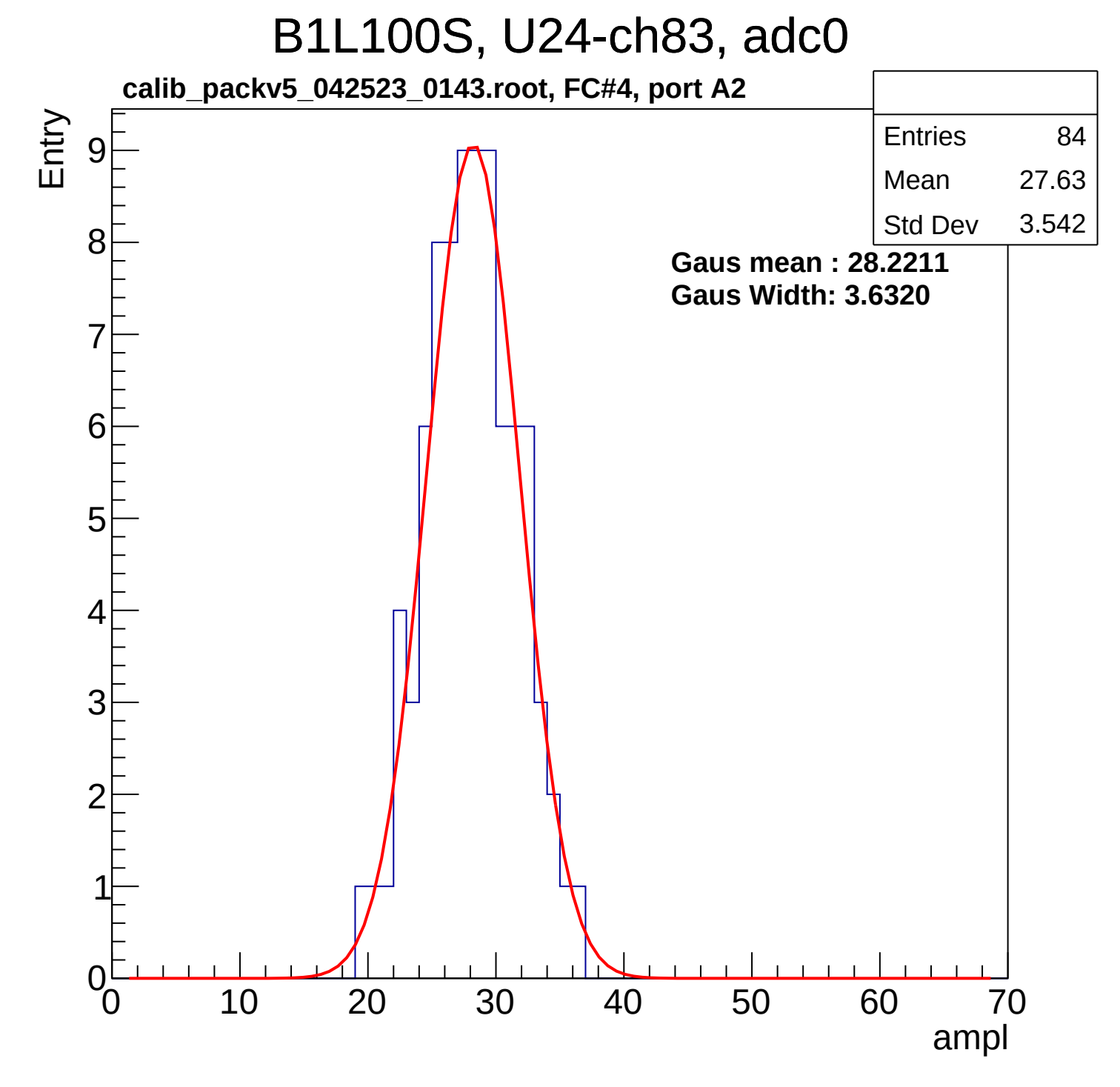
60
70


B1L100S, U24-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 45.75
Std Dev 26.48
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch82, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 27.63
Std Dev 3.542
Gaus mean : 28.2211
Gaus Width: 3.6320
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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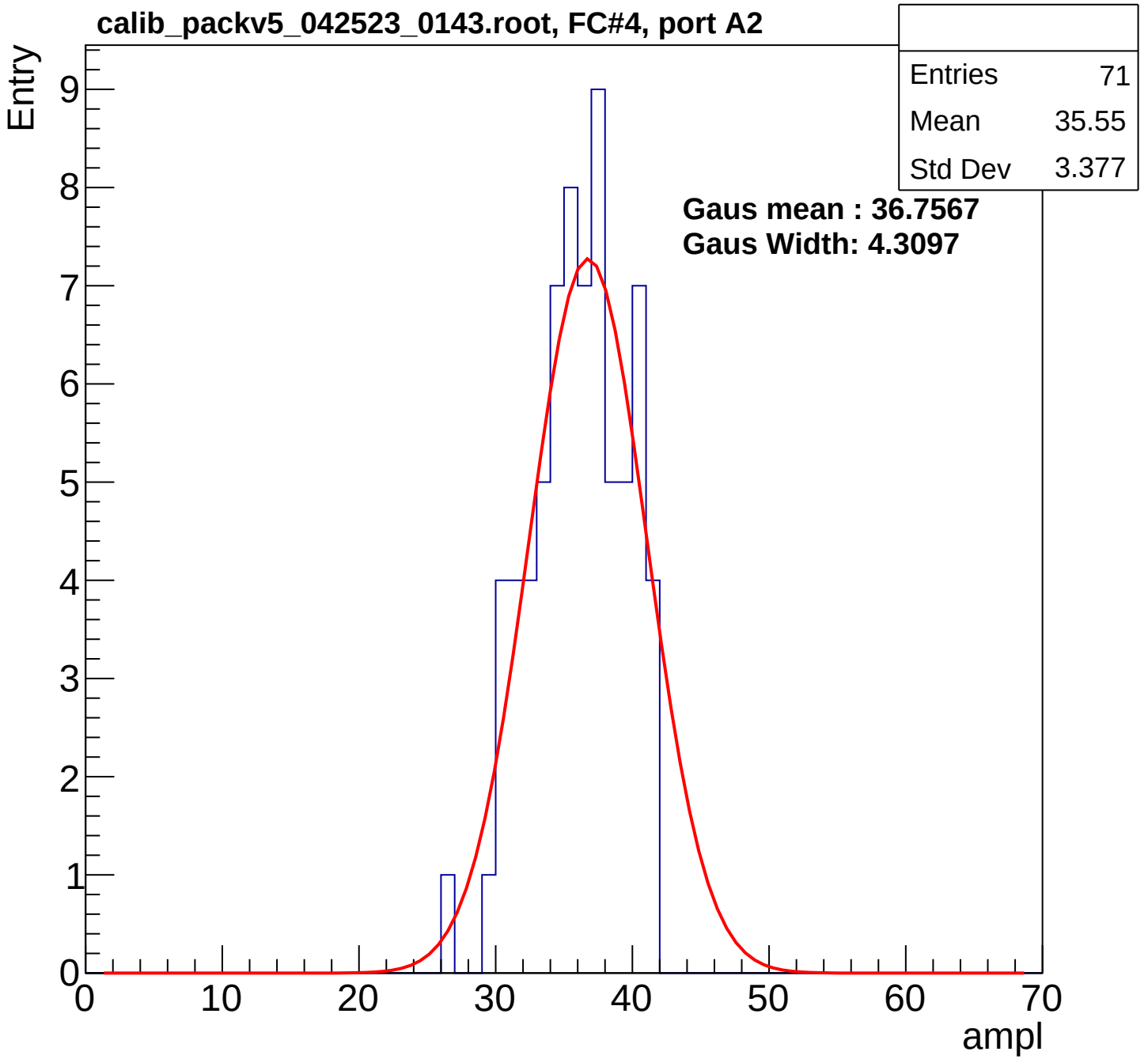
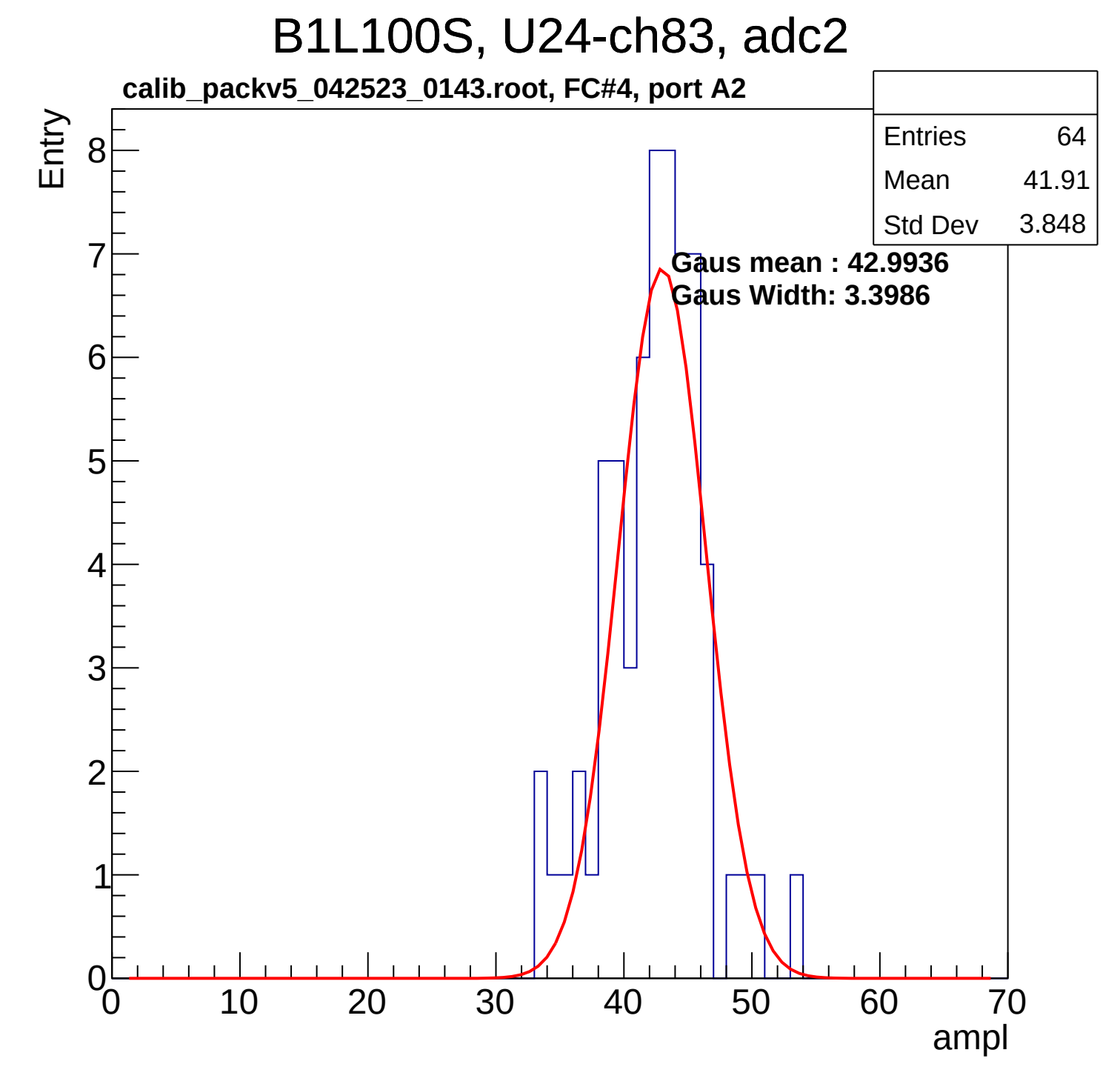

B1L100S, U24-ch83, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 35.55
Std Dev 3.377
Gaus mean : 36.7567
Gaus Width: 4.3097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 41.91
Std Dev 3.848
Gaus mean : 42.9936
Gaus Width: 3.3986
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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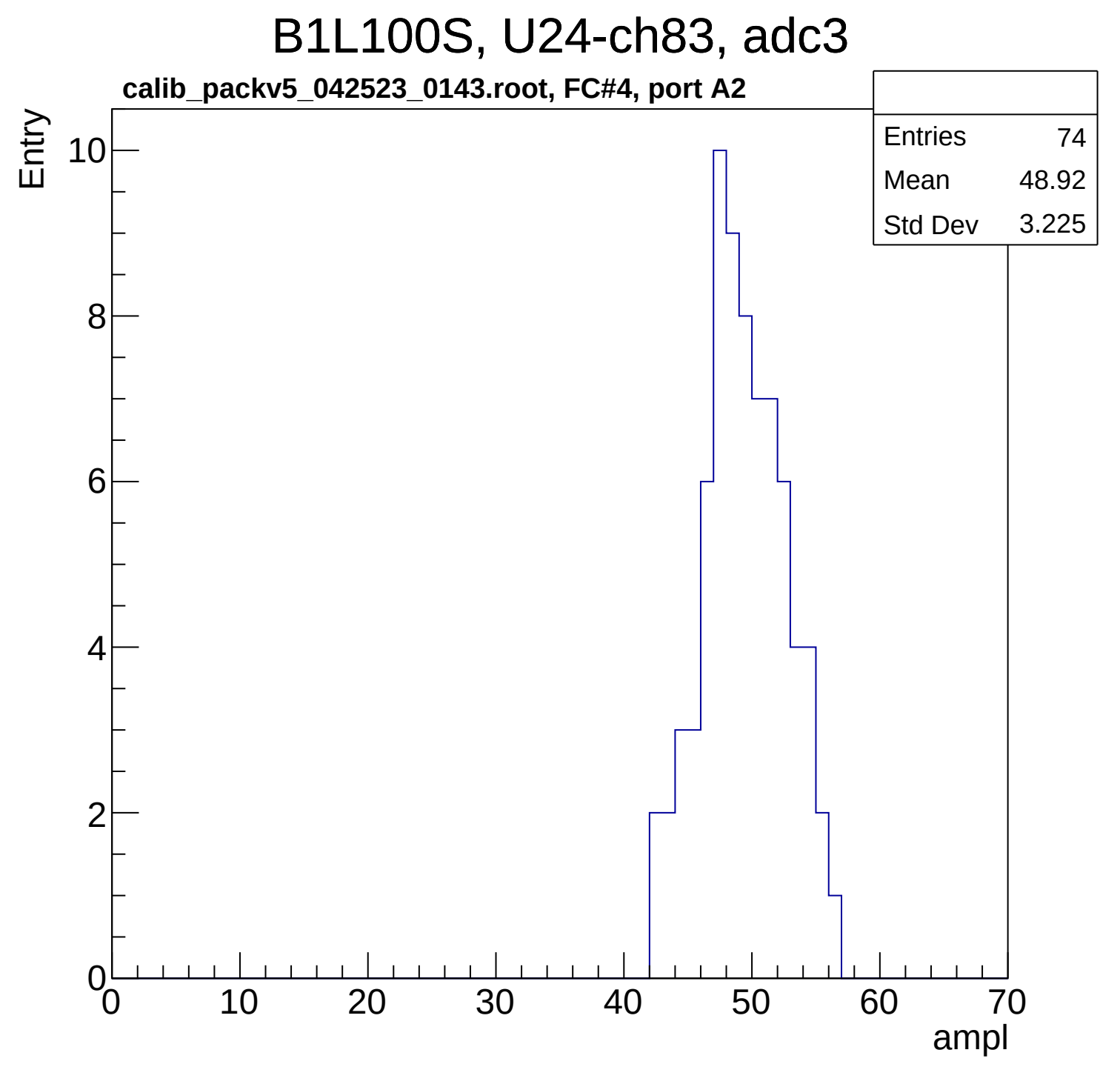
B1L100S, U24-ch83, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 48.92
Std Dev 3.225
Entry
0
2
4
6
8
10
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10
20
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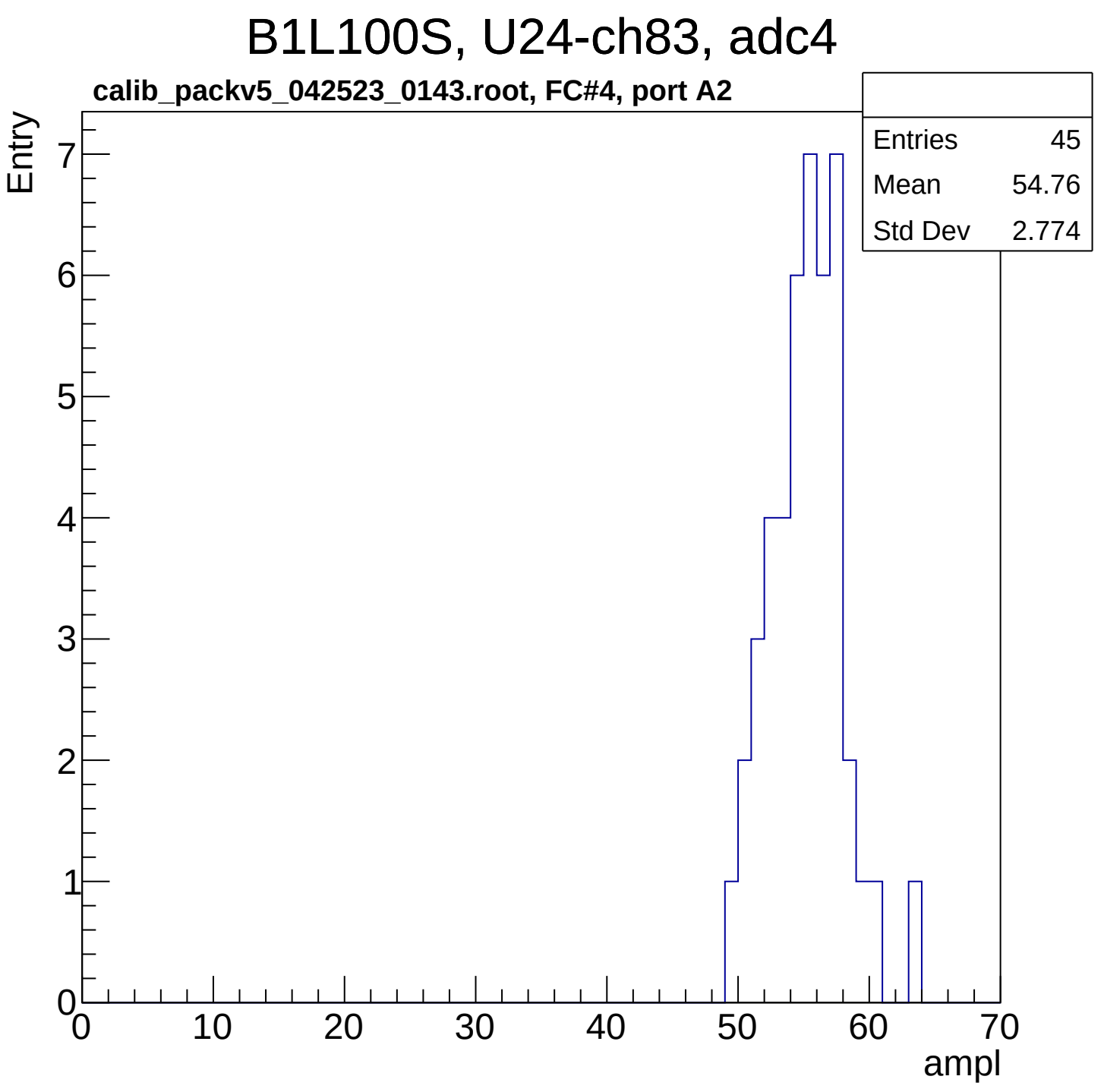
50
60
70
ampl


B1L100S, U24-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 54.76
Std Dev 2.774
Entry
ampl
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1
2
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4
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7
0
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60
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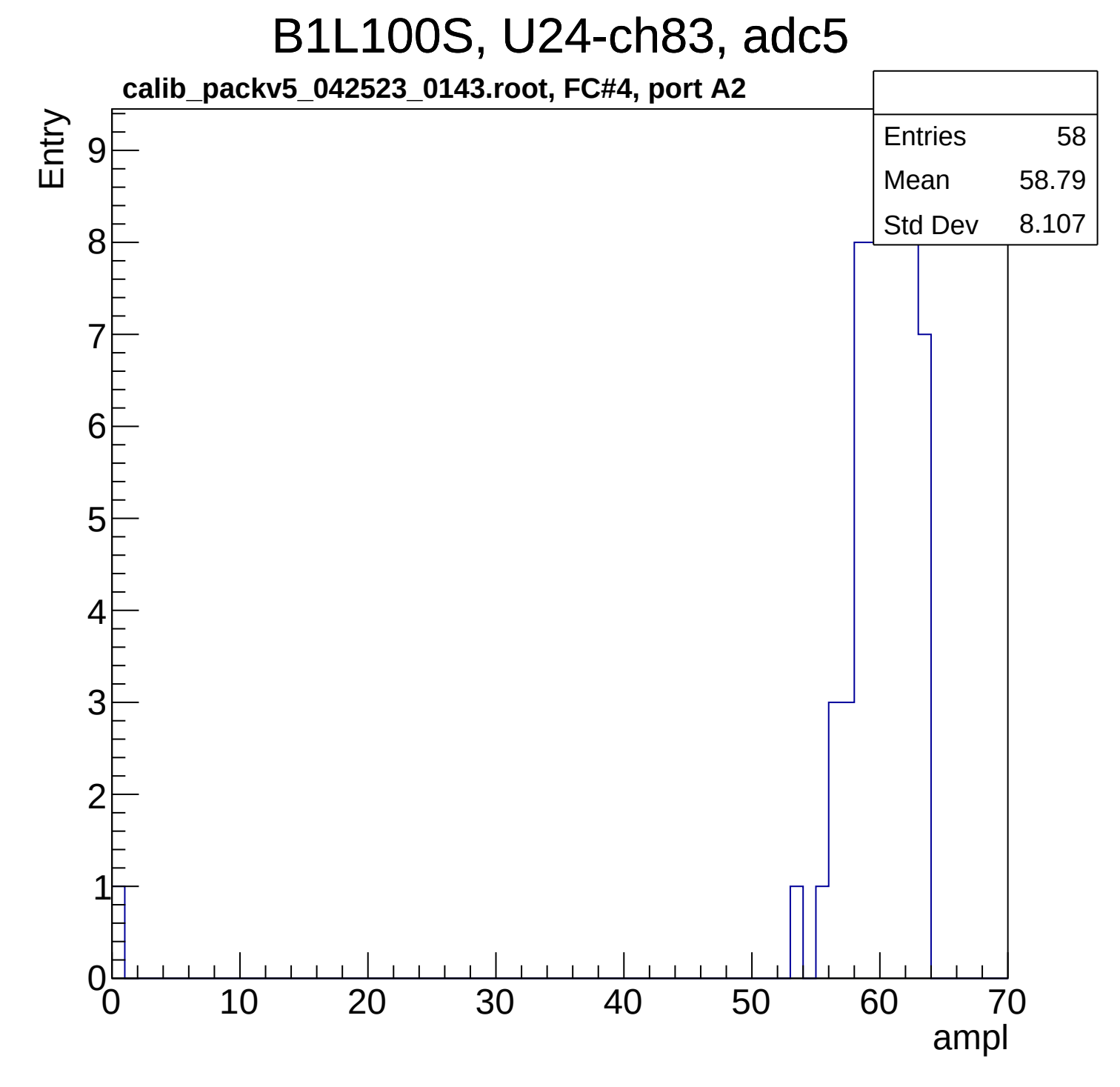
B1L100S, U24-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 58.79
Std Dev 8.107
Entry
ampl
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5
6
7
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0
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60
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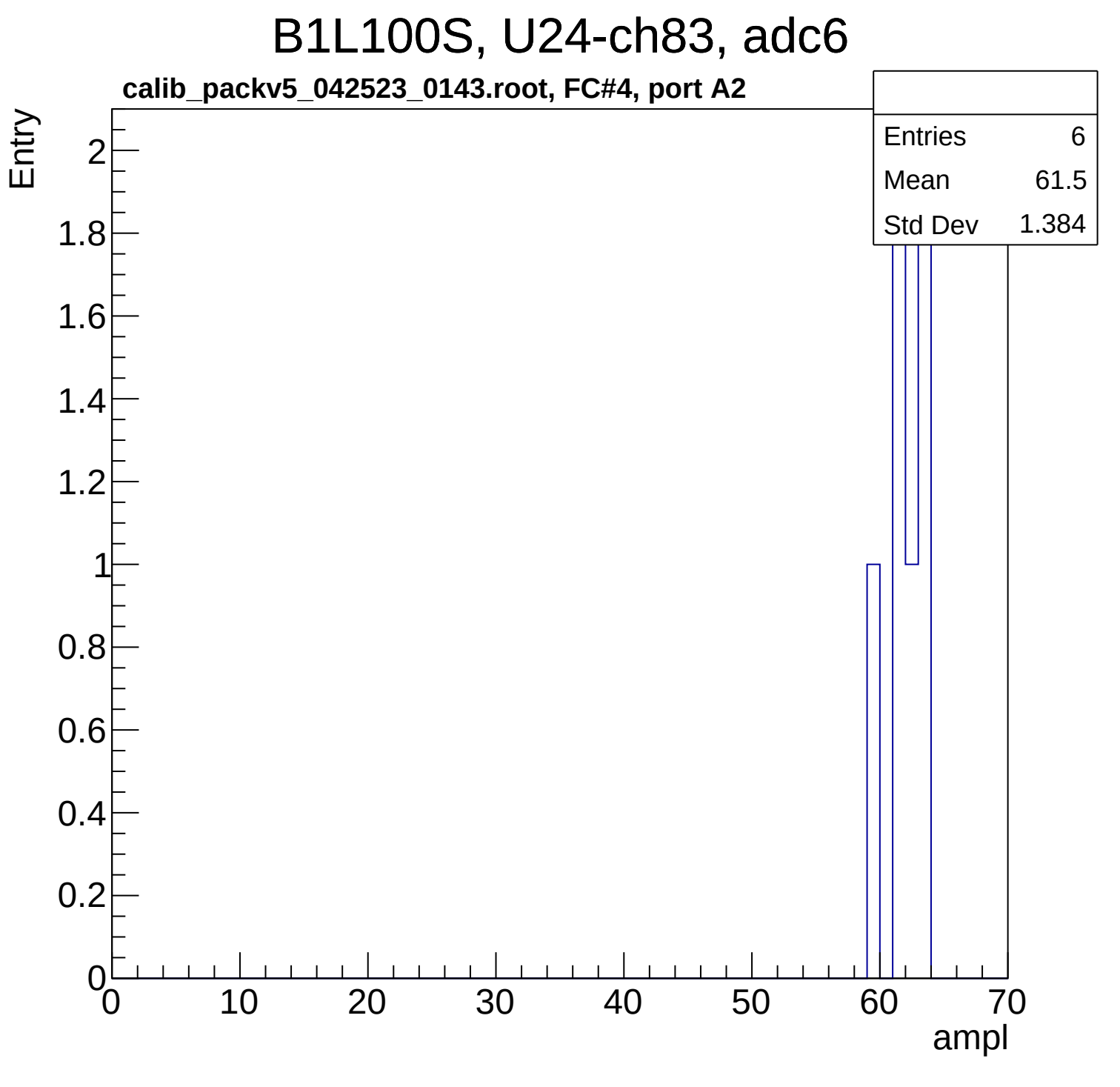
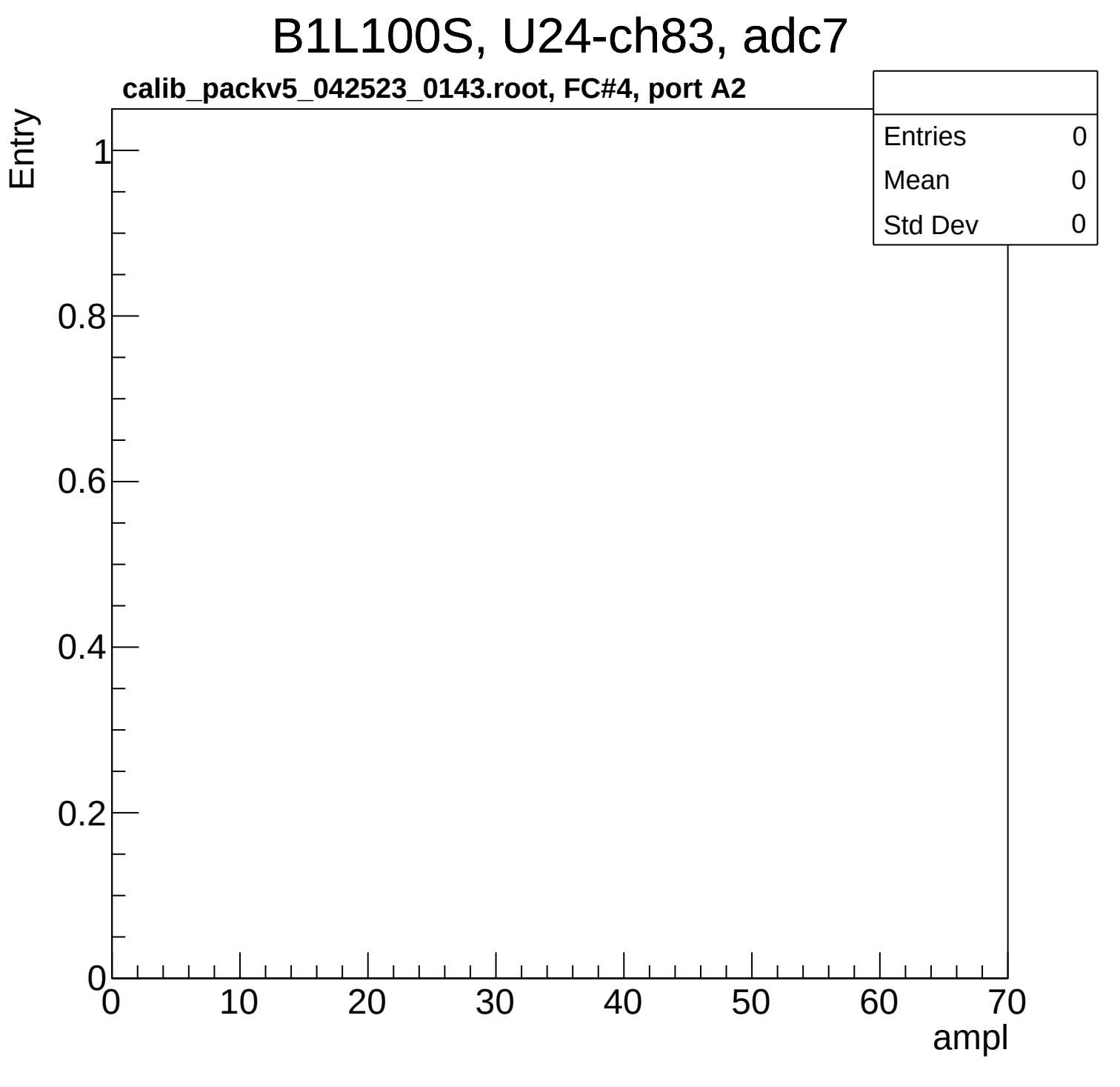

B1L100S, U24-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.5
Std Dev 1.384
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch83, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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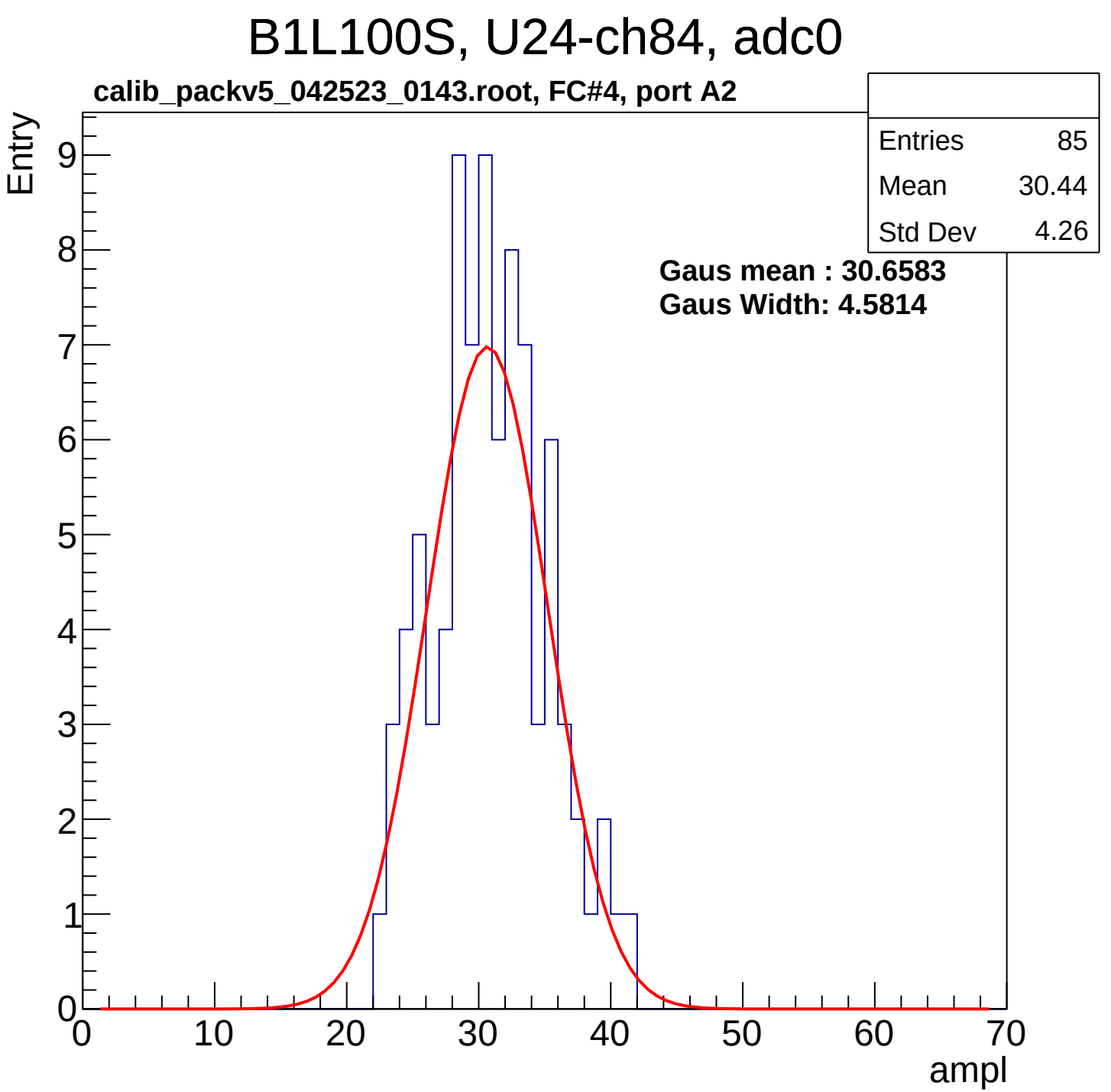
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 30.44
Std Dev 4.26
Gaus mean : 30.6583
Gaus Width: 4.5814
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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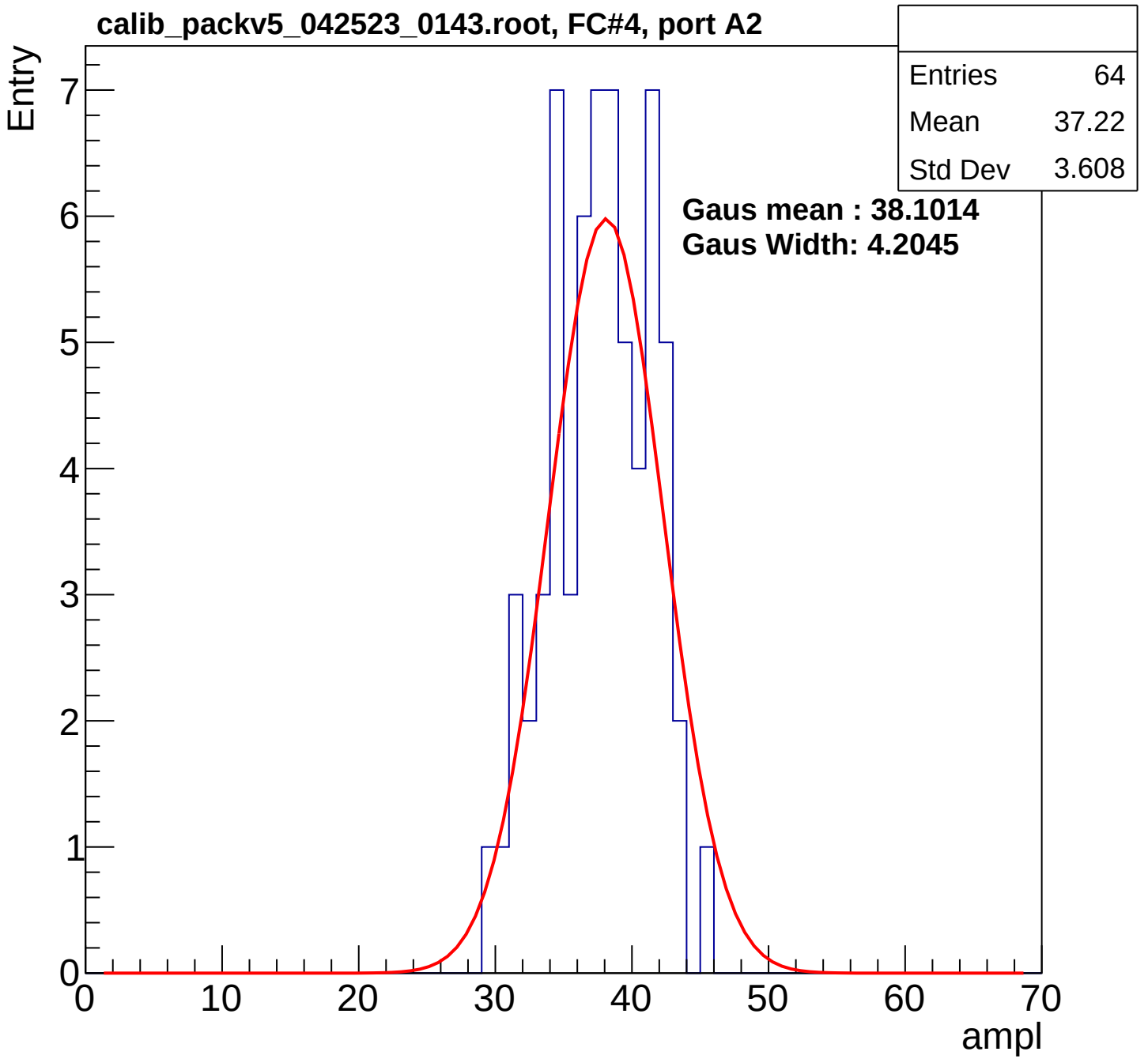
B1L100S, U24-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.22
Std Dev 3.608
Gaus mean : 38.1014
Gaus Width: 4.2045
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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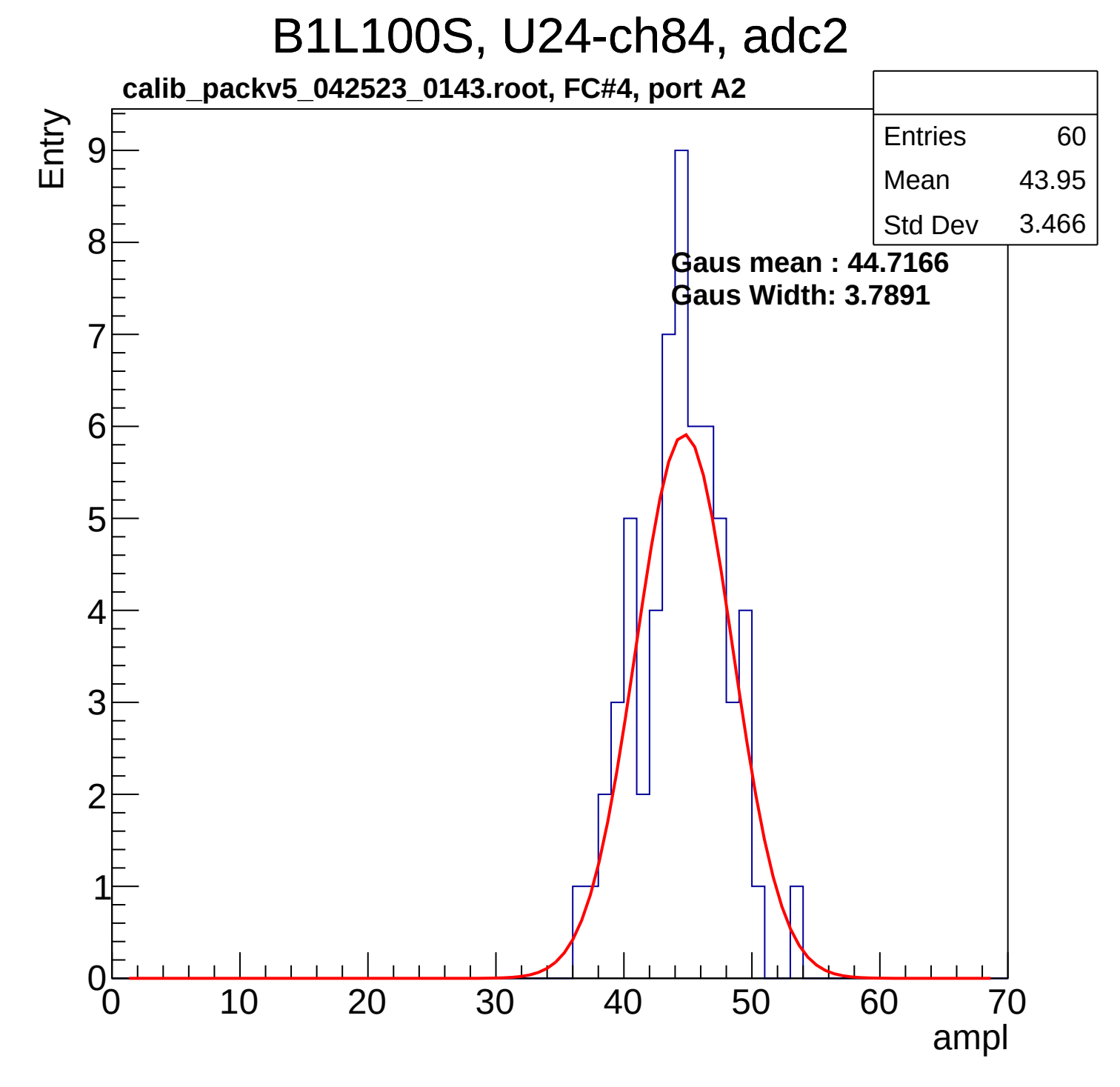
B1L100S, U24-ch84, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.95
Std Dev 3.466
Gaus mean : 44.7166
Gaus Width: 3.7891
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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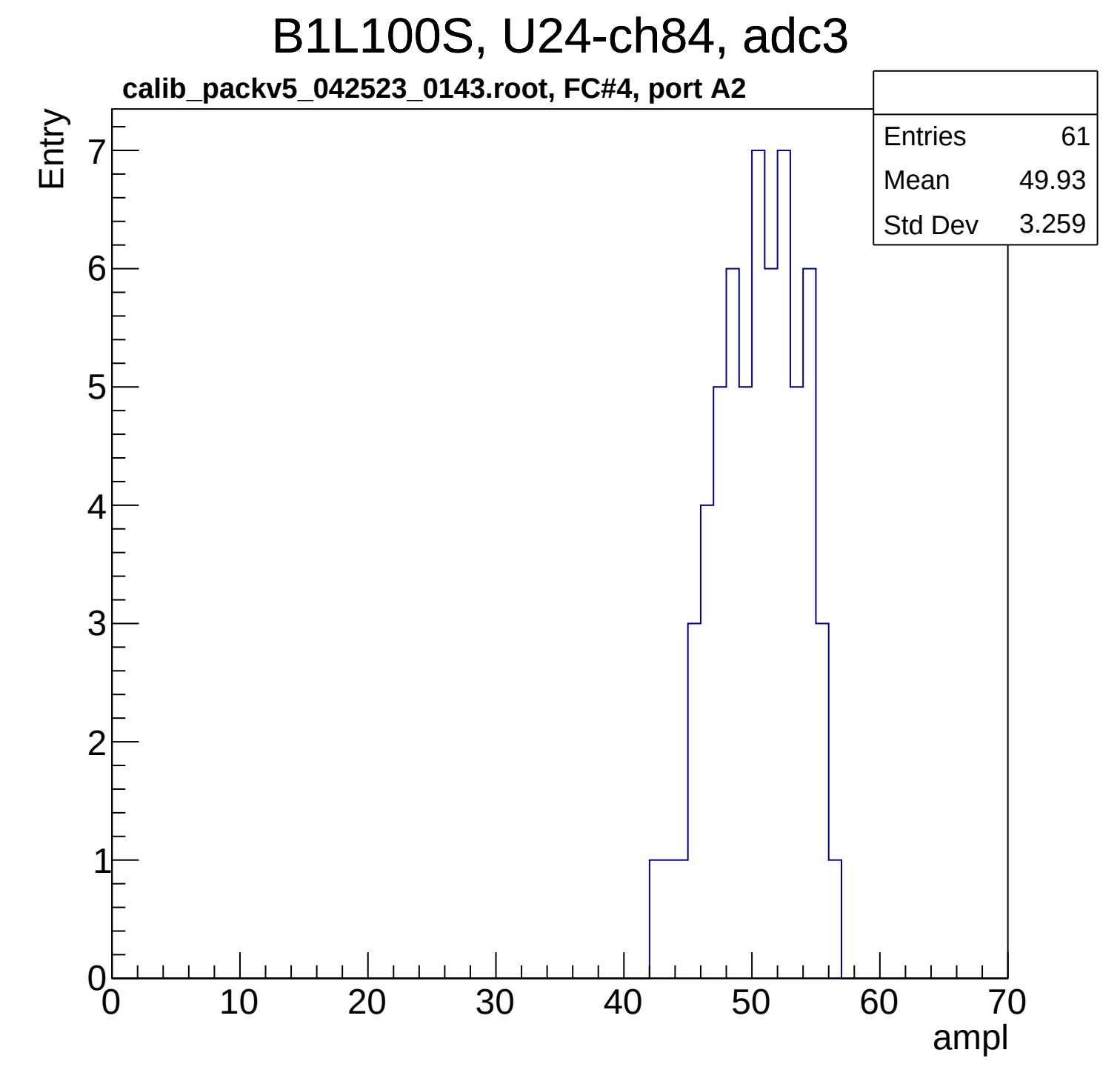
B1L100S, U24-ch84, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.93
Std Dev 3.259
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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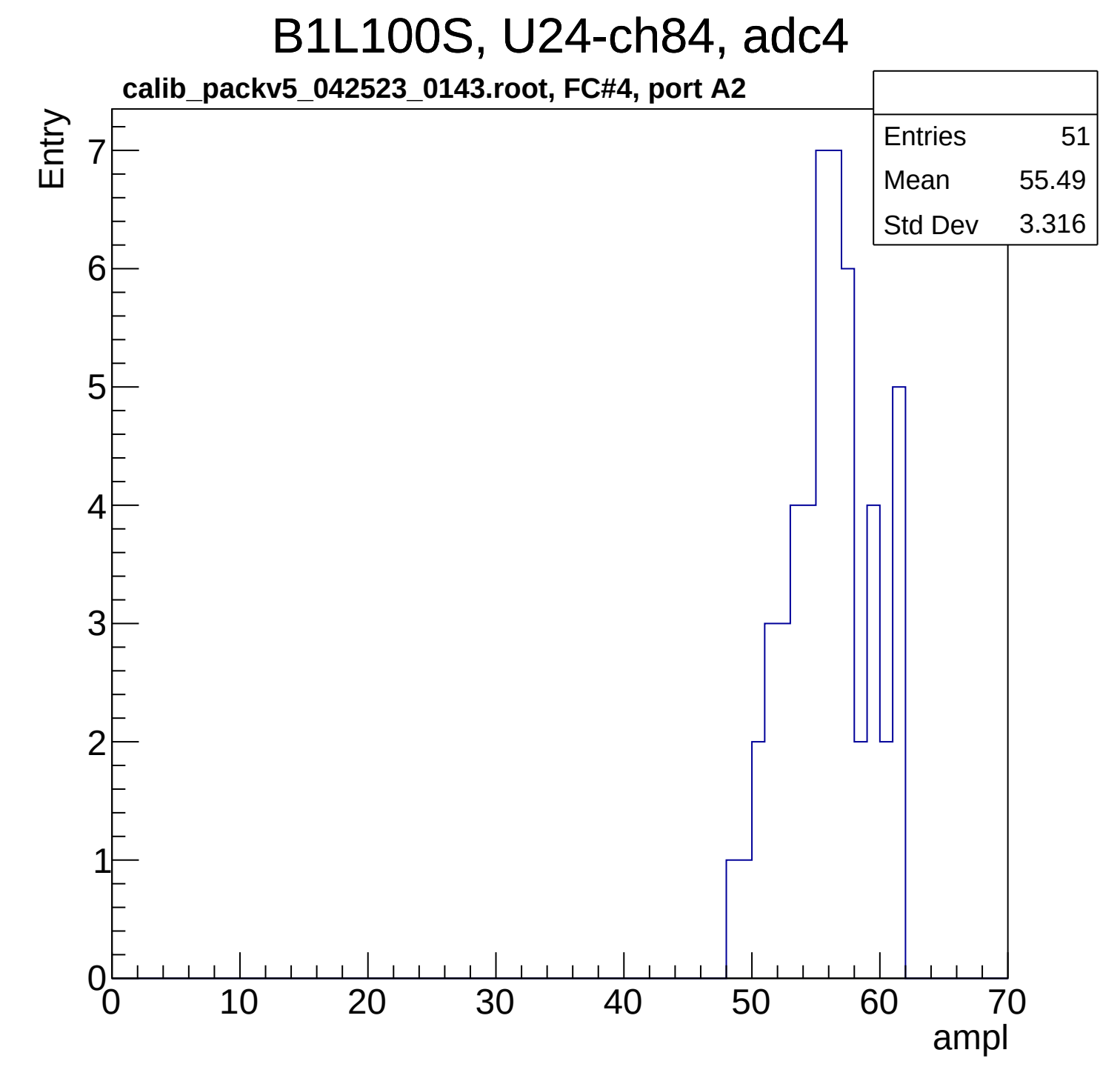
B1L100S, U24-ch84, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.49
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
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7
0
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60
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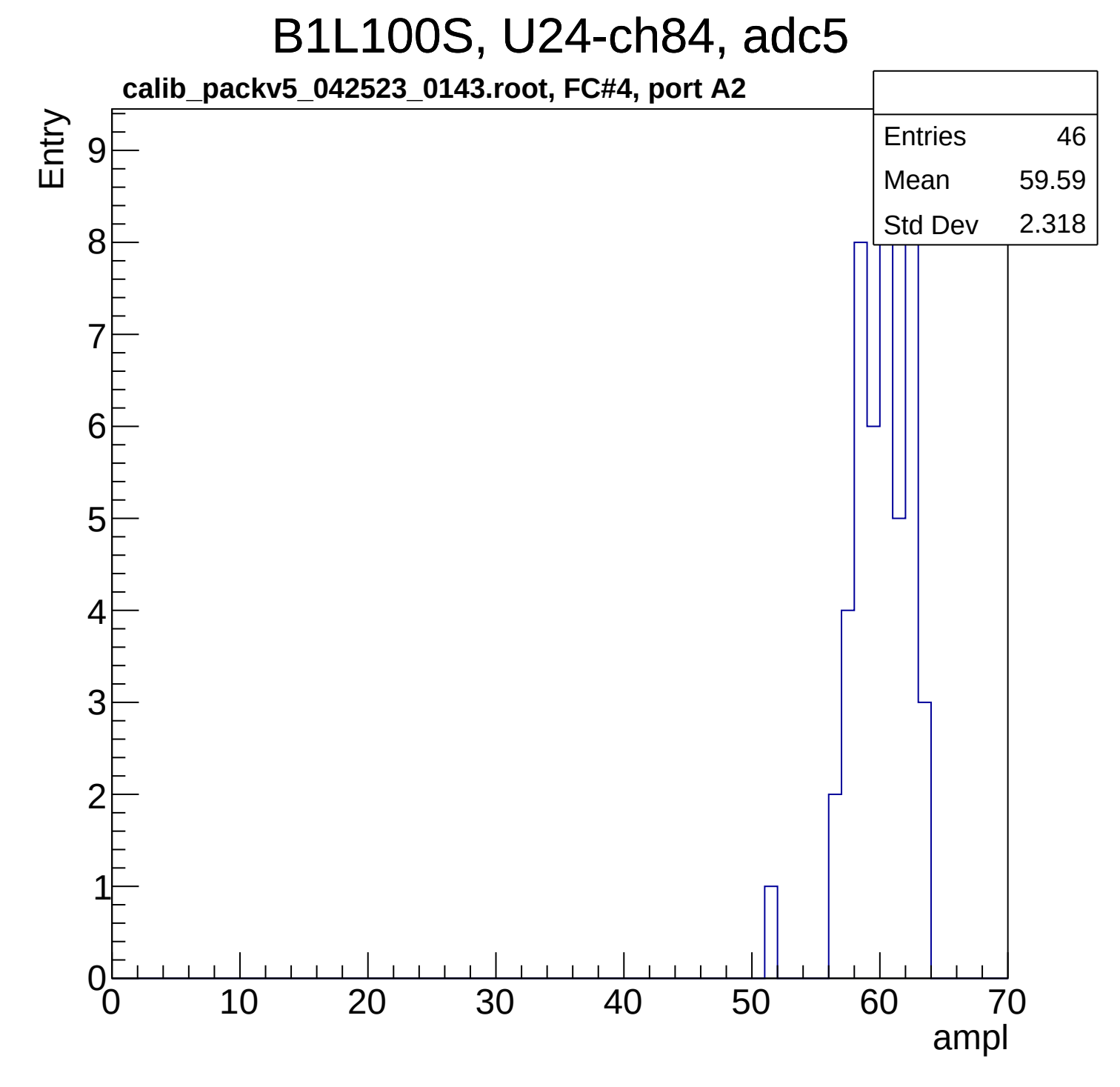
B1L100S, U24-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 59.59
Std Dev 2.318
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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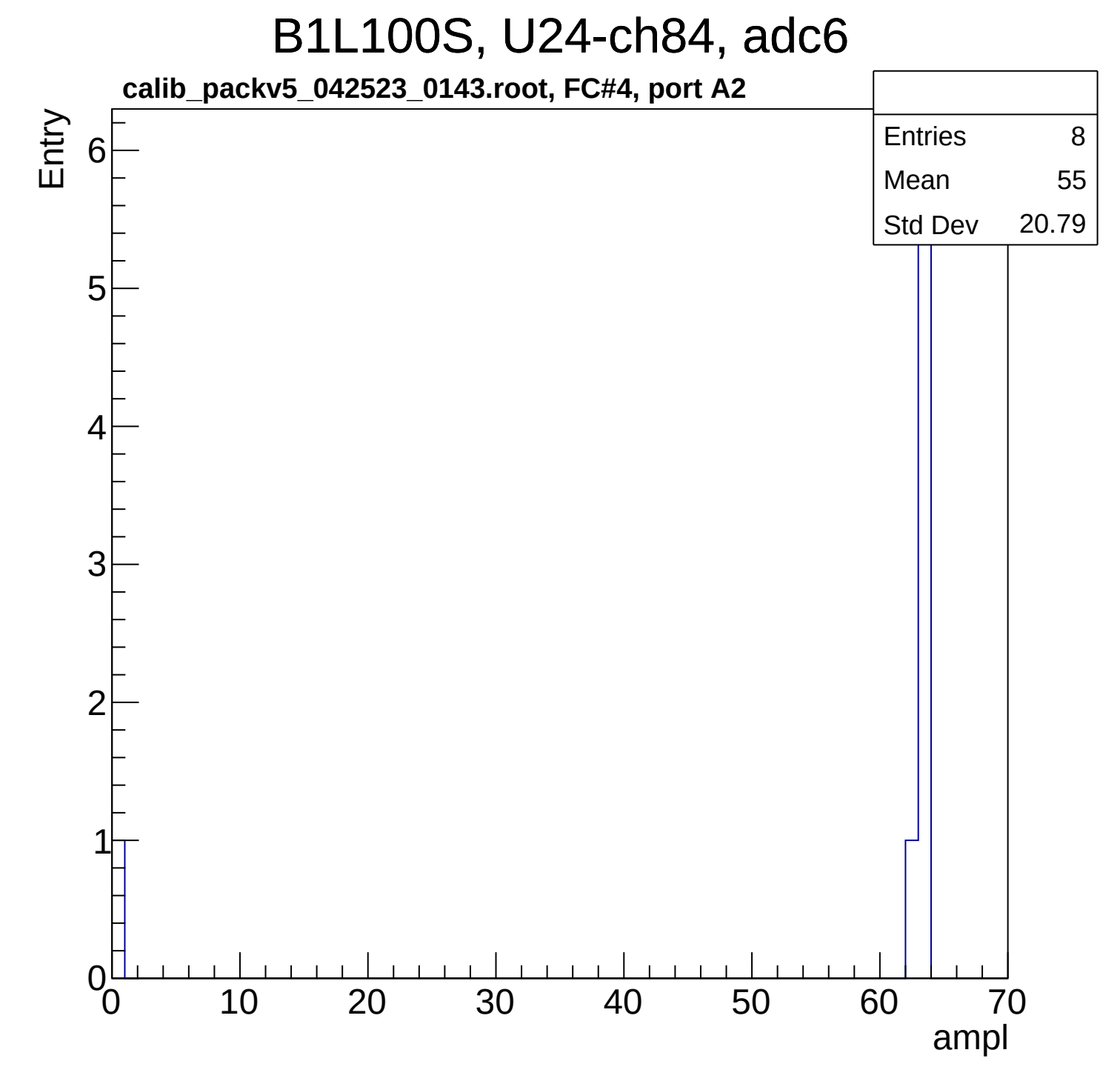
B1L100S, U24-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 55
Std Dev 20.79
Entry
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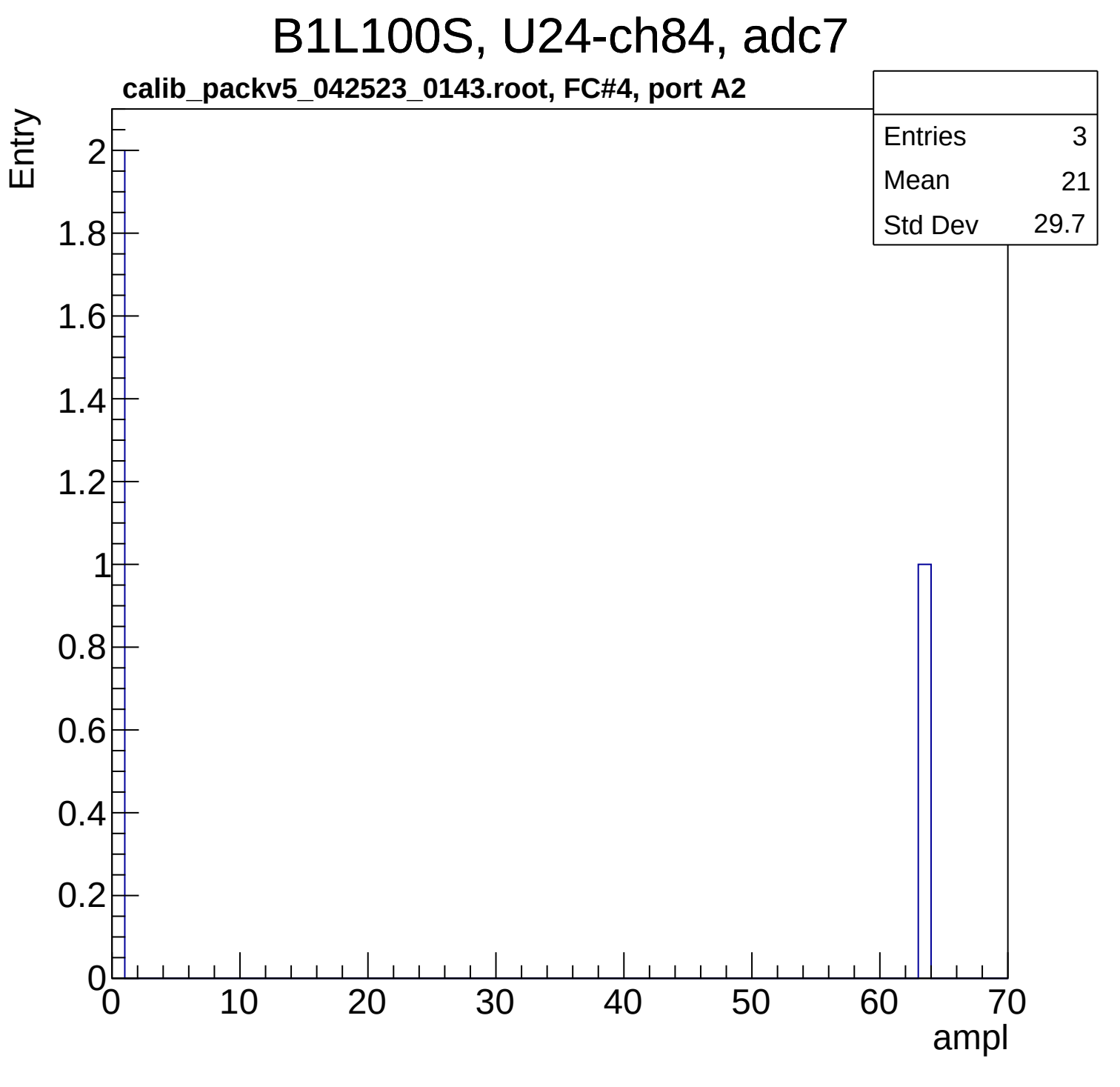
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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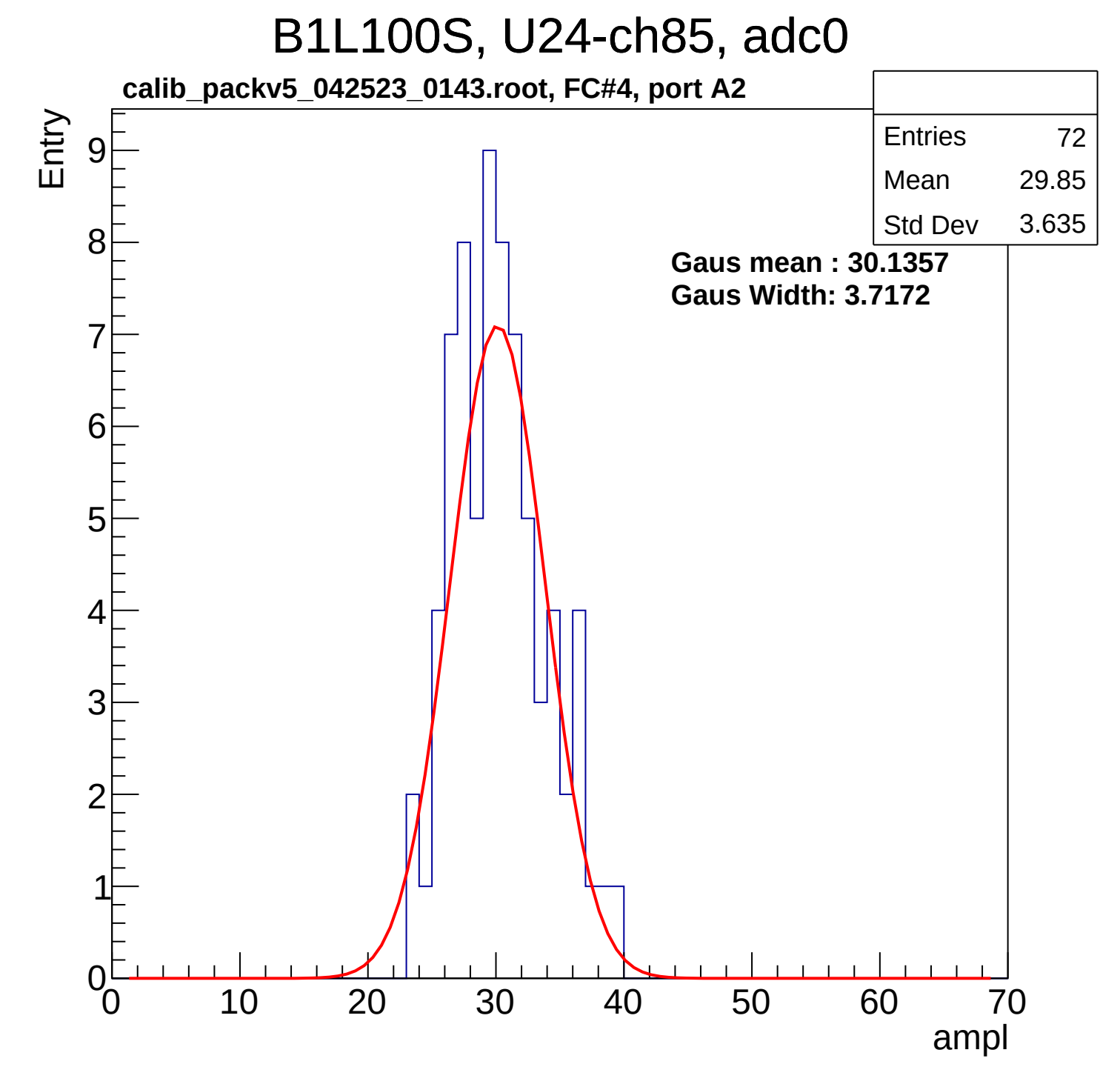
B1L100S, U24-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.85
Std Dev 3.635
Gaus mean : 30.1357
Gaus Width: 3.7172
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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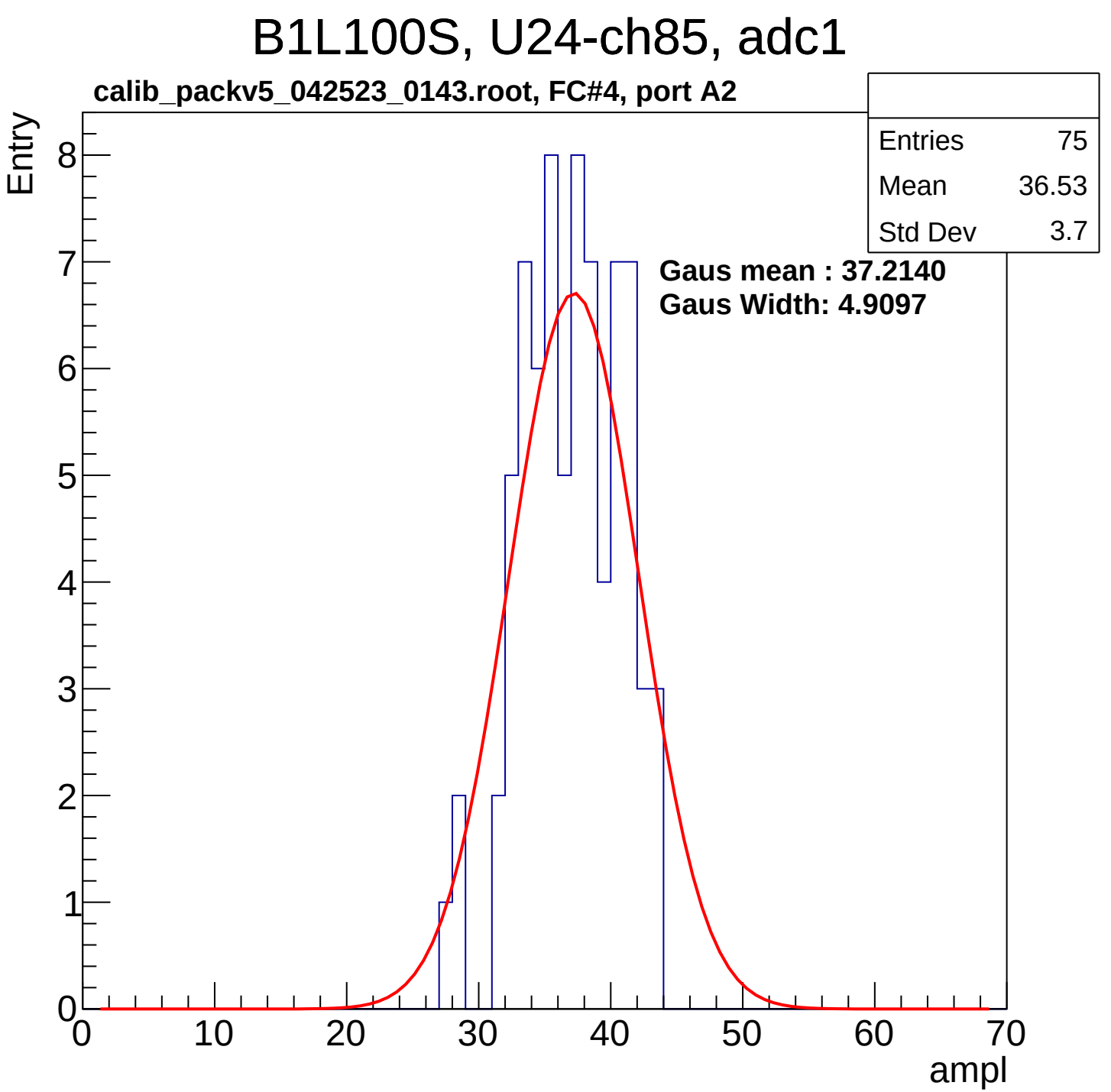
B1L100S, U24-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.53
Std Dev 3.7
Gaus mean : 37.2140
Gaus Width: 4.9097
Entry
ampl
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1
2
3
4
5
6
7
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0
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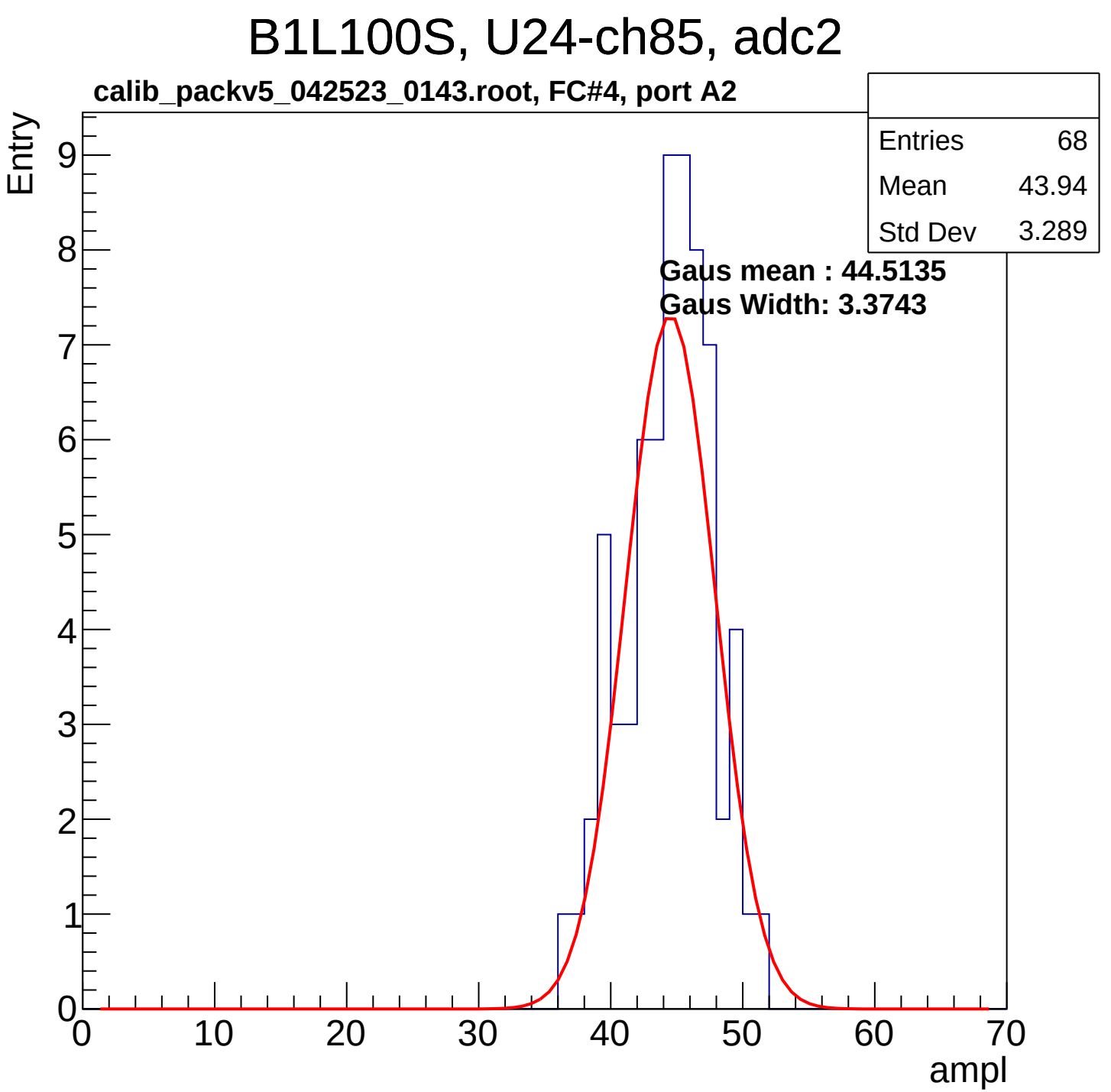
B1L100S, U24-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.94
Std Dev 3.289
Gaus mean : 44.5135
Gaus Width: 3.3743
Entry
ampl
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3
4
5
6
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0
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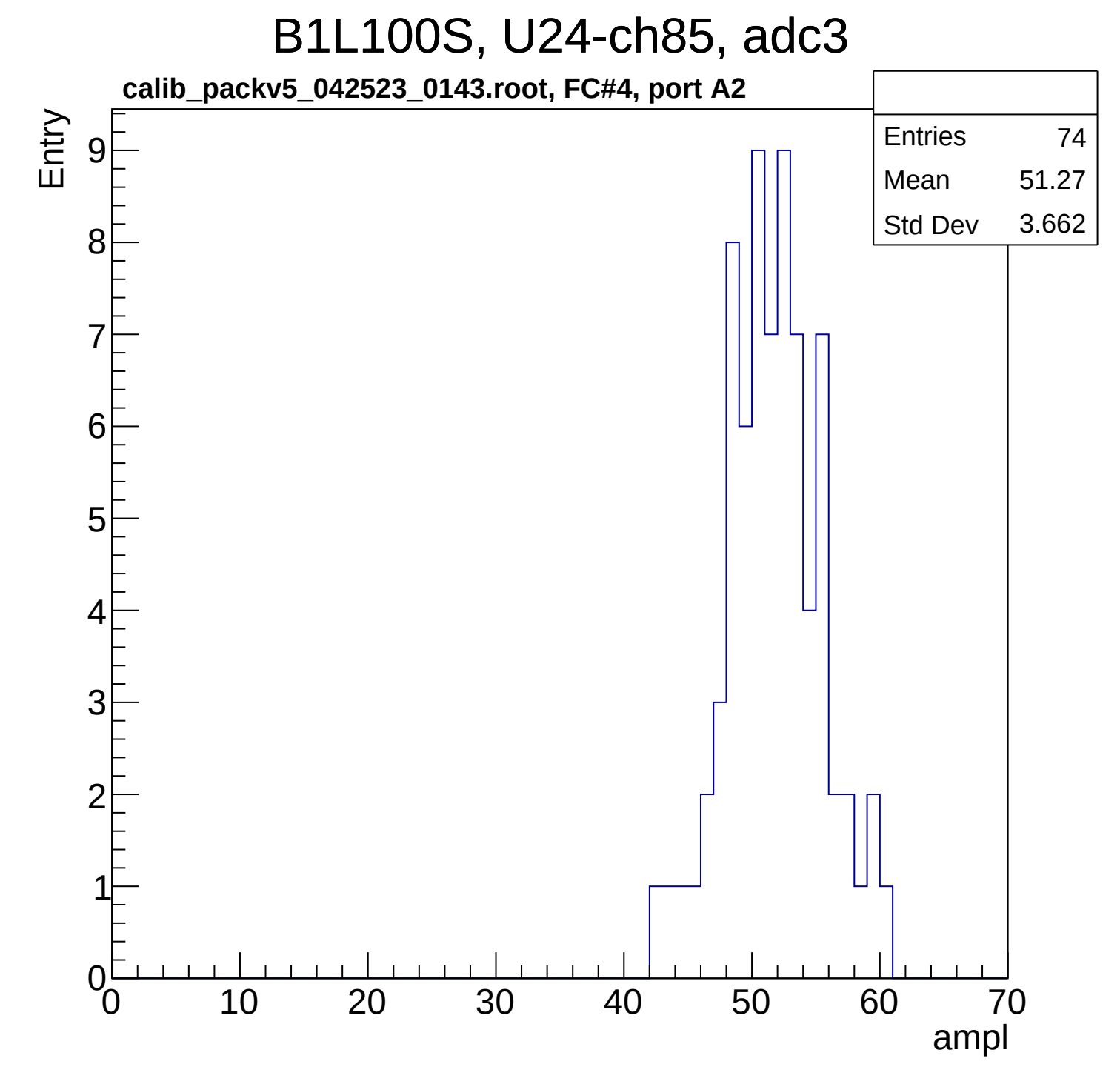
B1L100S, U24-ch85, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.27
Std Dev 3.662
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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60
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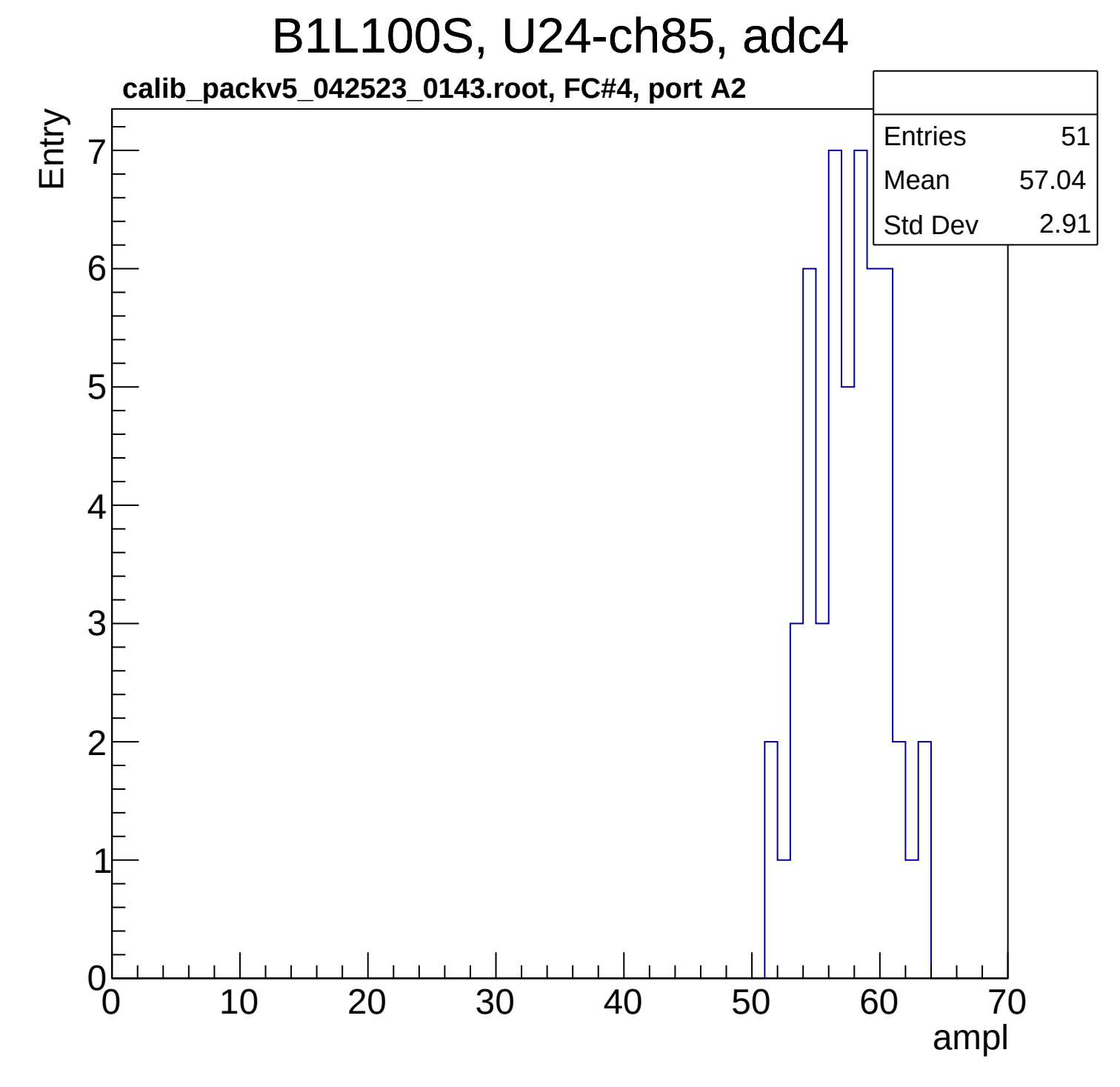
B1L100S, U24-ch85, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.04
Std Dev 2.91
Entry
ampl
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1
2
3
4
5
6
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0
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60
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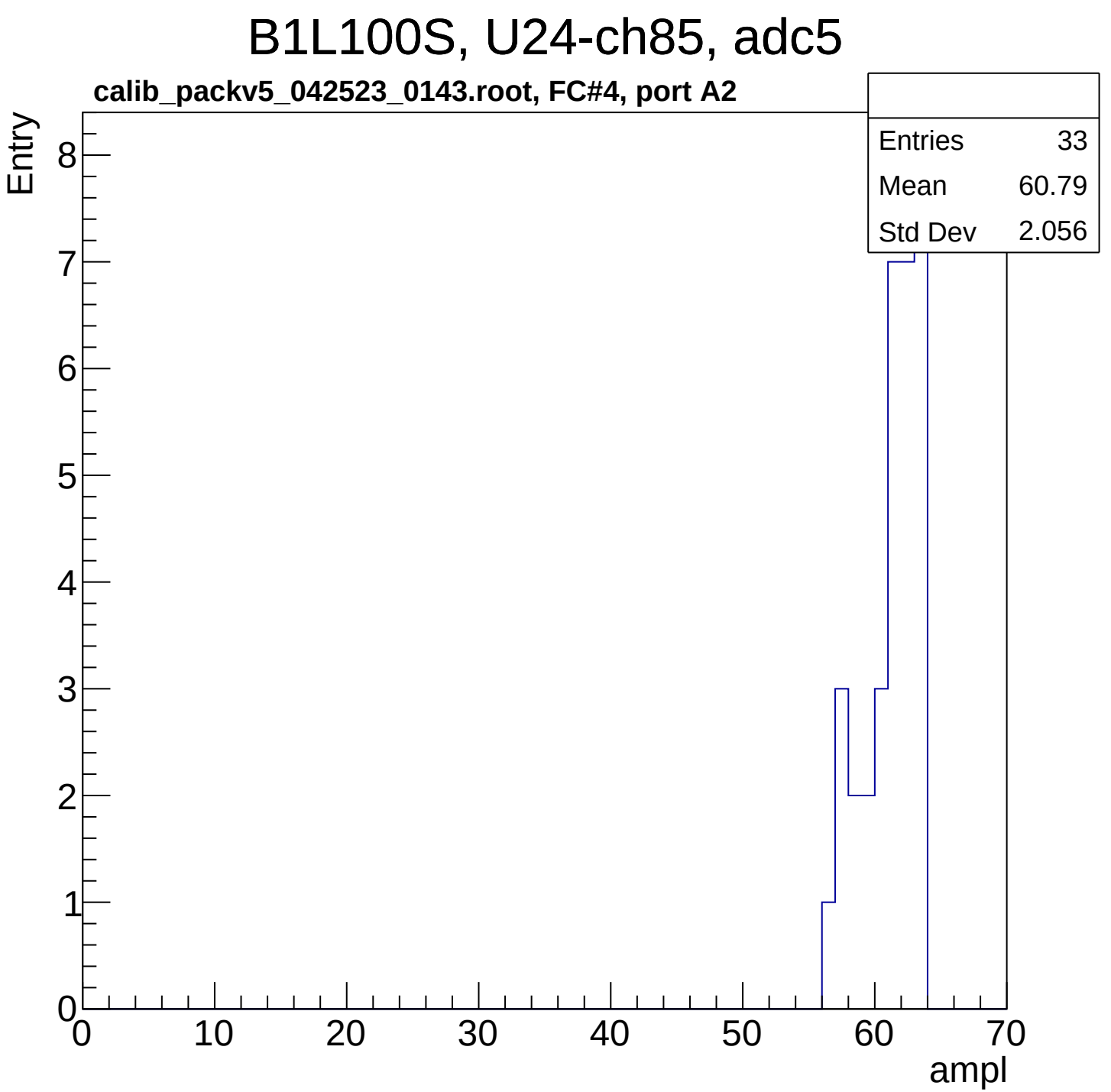
B1L100S, U24-ch85, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 60.79
Std Dev 2.056
Entry
ampl
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1
2
3
4
5
6
7
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0
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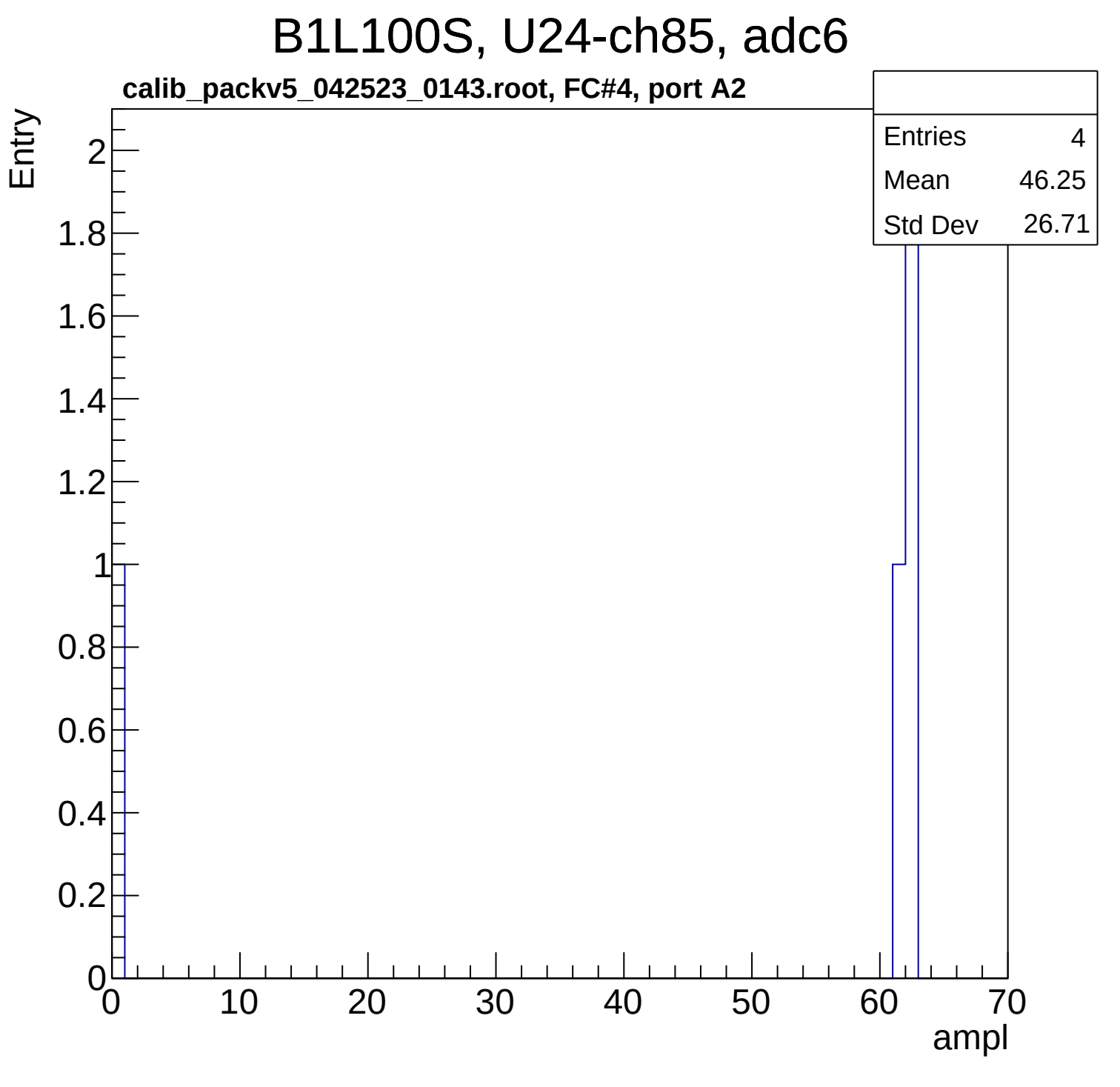
B1L100S, U24-ch85, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.25
Std Dev 26.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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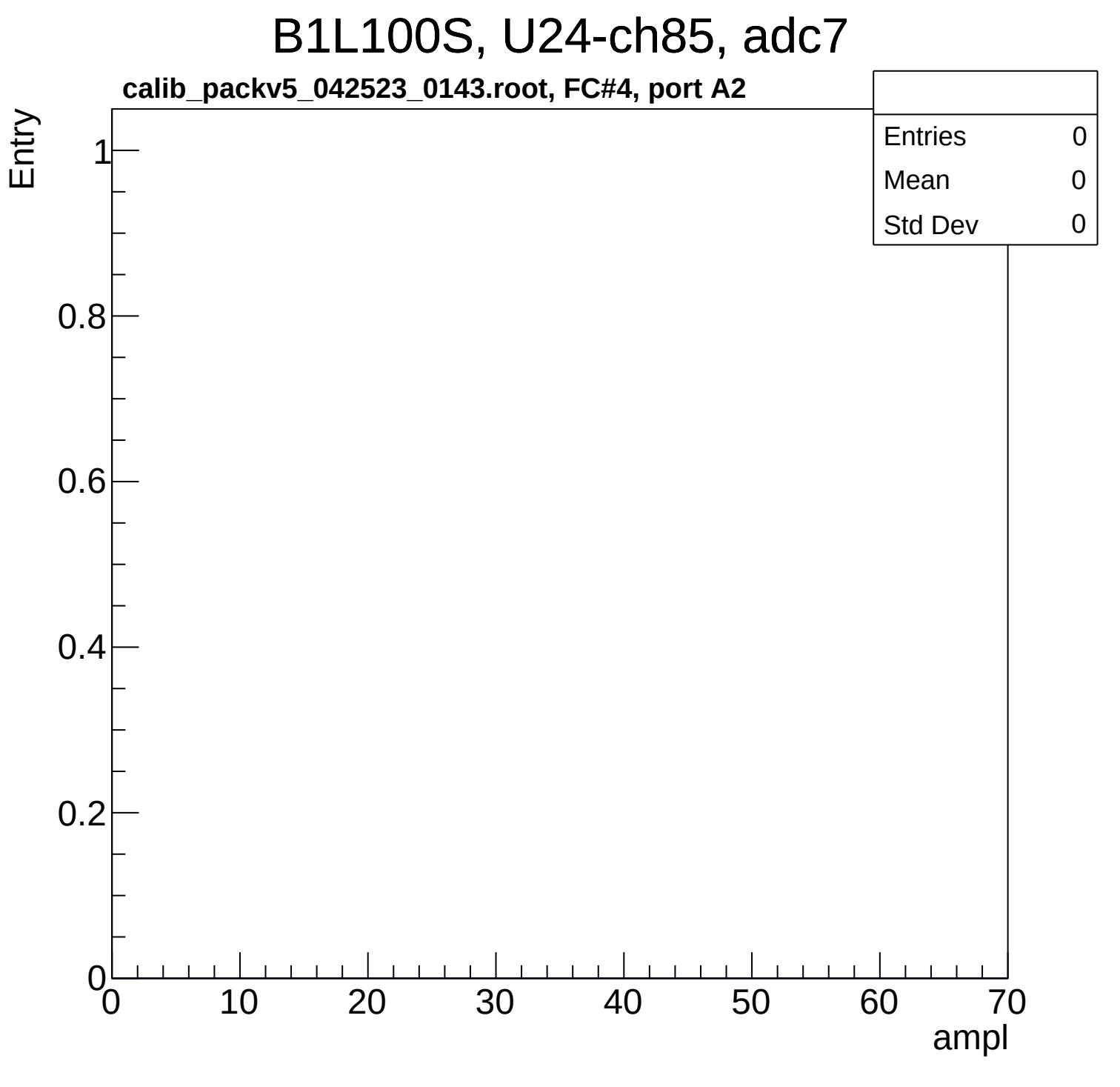
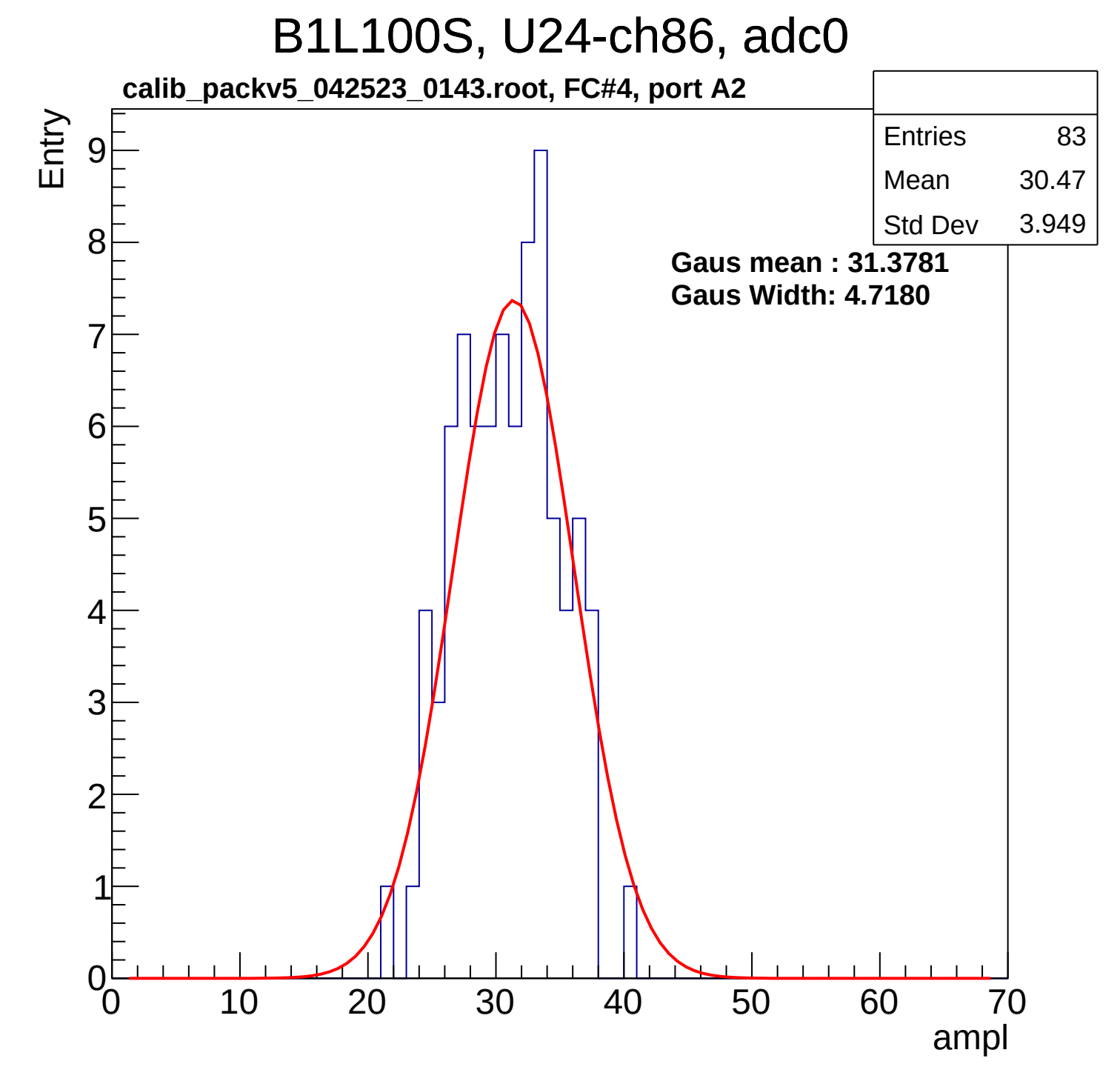

B1L100S, U24-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 30.47
Std Dev 3.949
Gaus mean : 31.3781
Gaus Width: 4.7180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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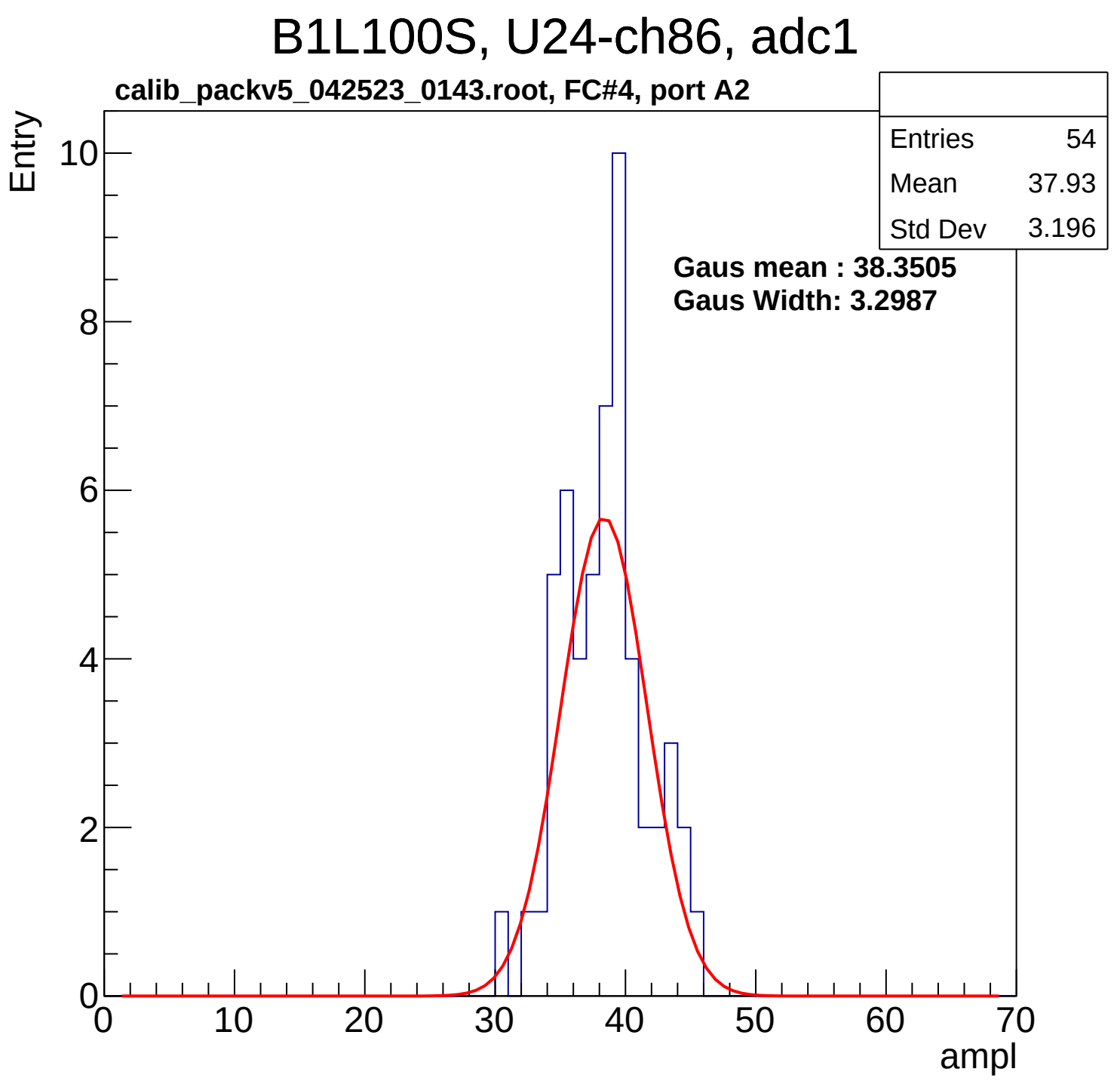
B1L100S, U24-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 37.93
Std Dev 3.196
Gaus mean : 38.3505
Gaus Width: 3.2987
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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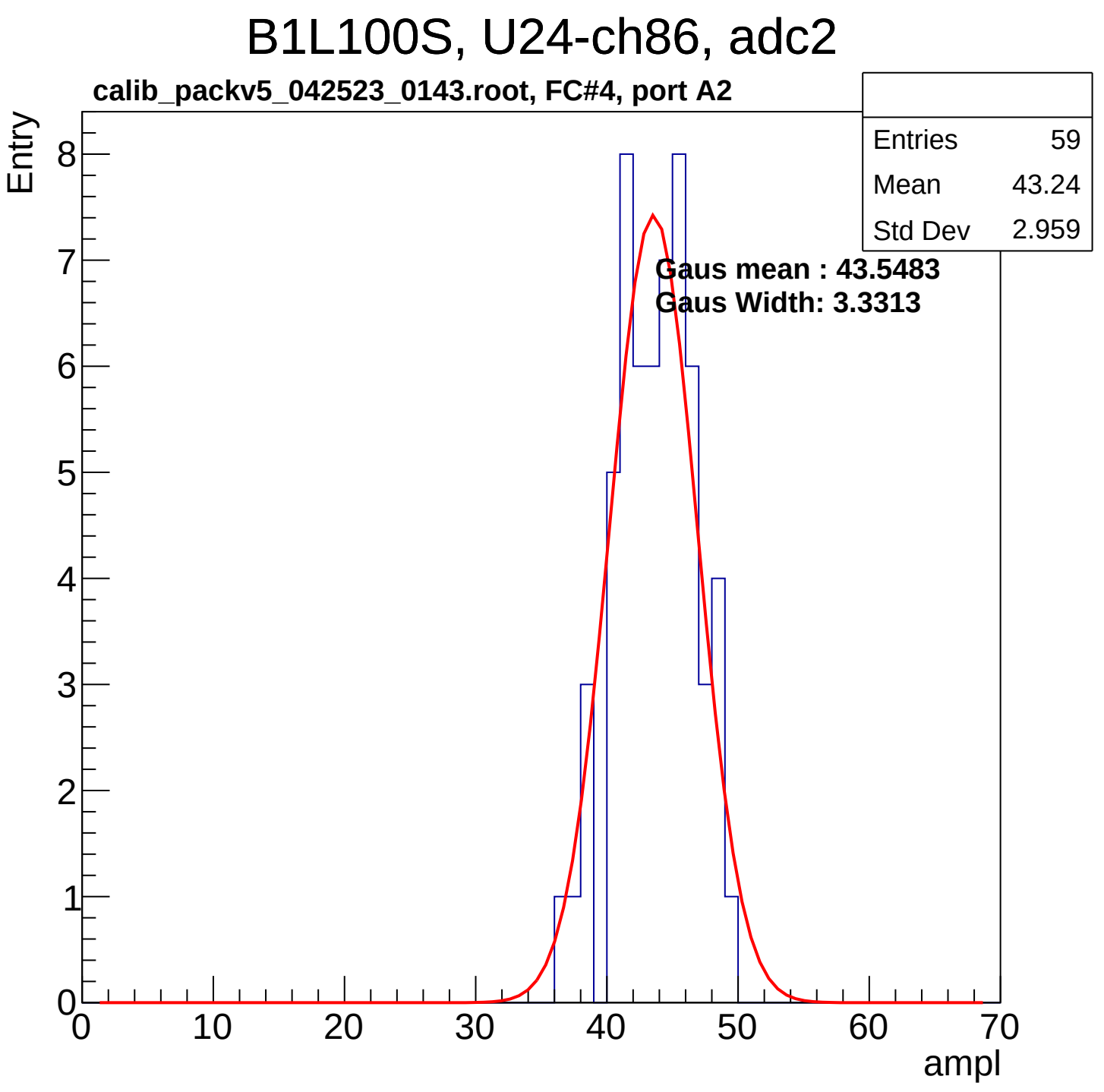
B1L100S, U24-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.24
Std Dev 2.959
Gaus mean : 43.5483
Gaus Width: 3.3313
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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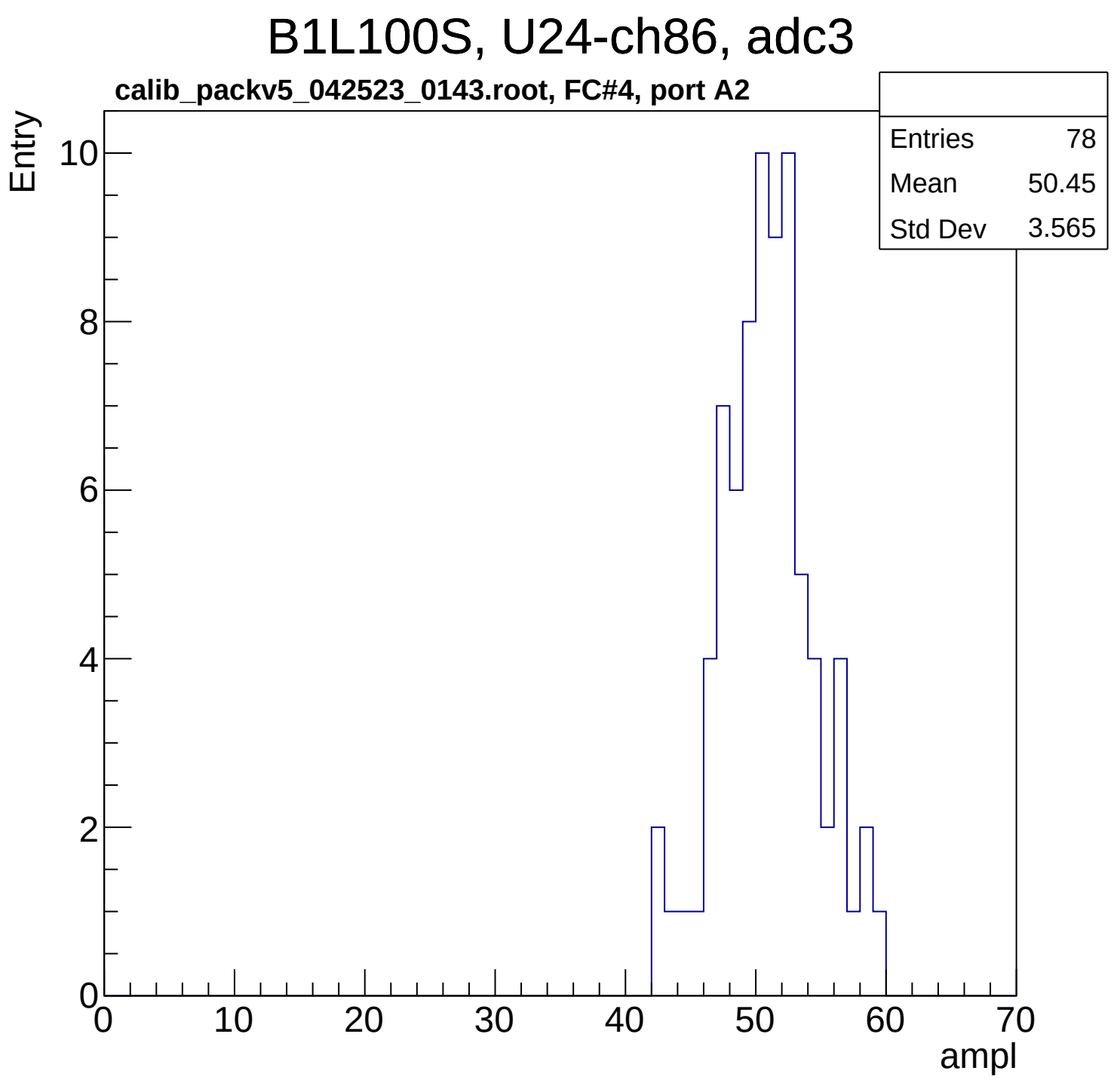
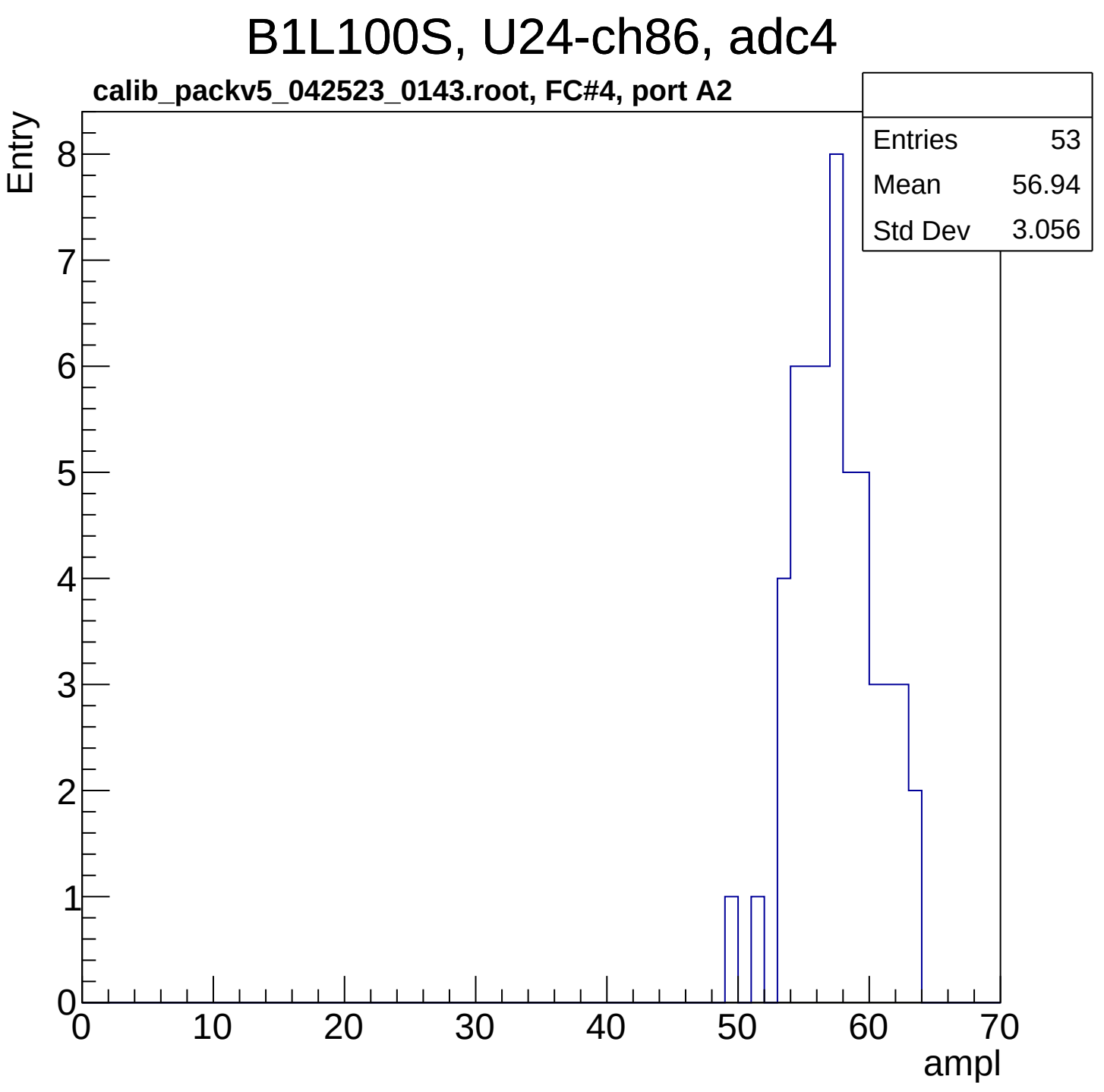

B1L100S, U24-ch86, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 50.45
Std Dev 3.565
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch86, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.94
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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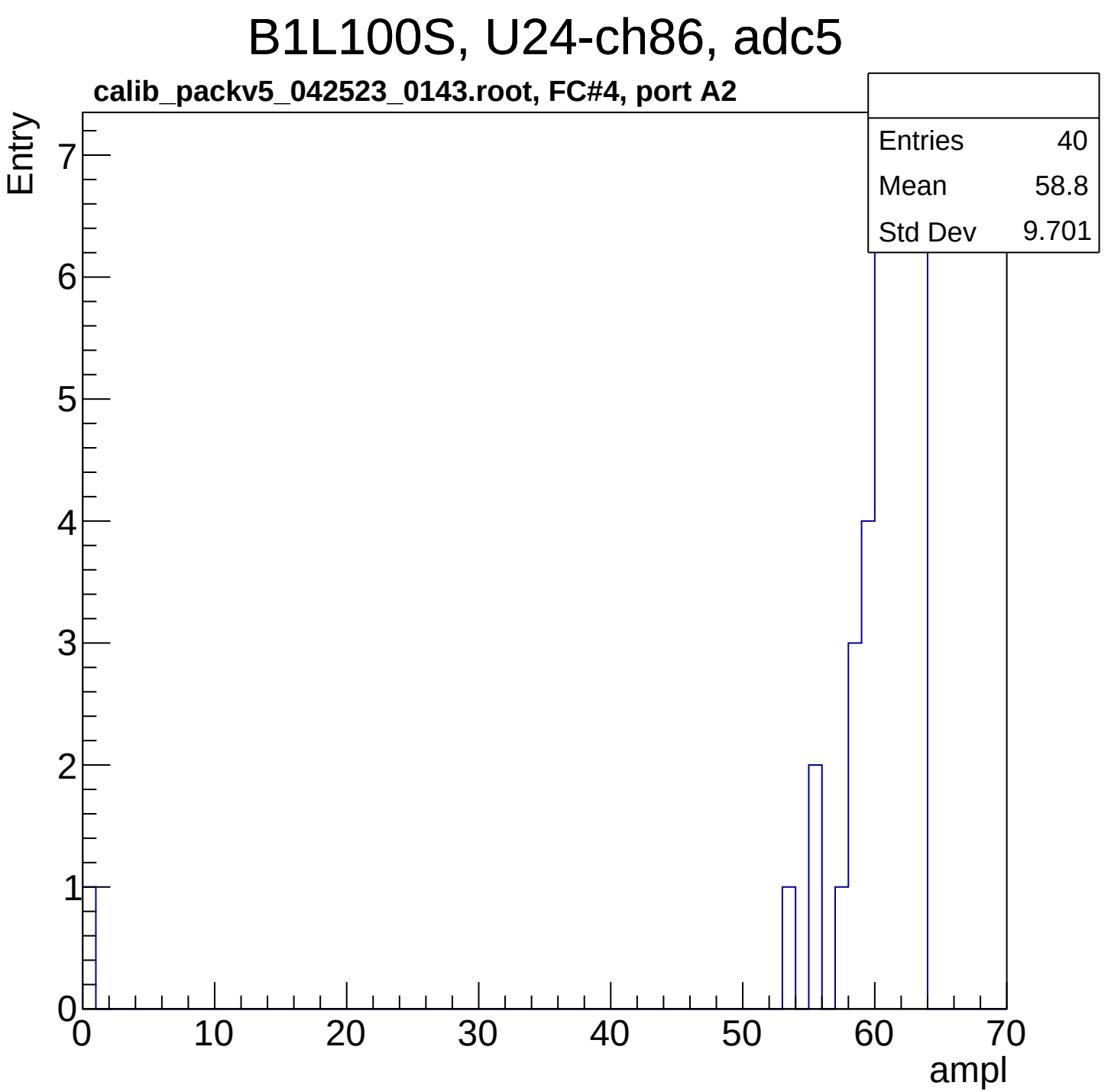
B1L100S, U24-ch86, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.8
Std Dev 9.701
Entry
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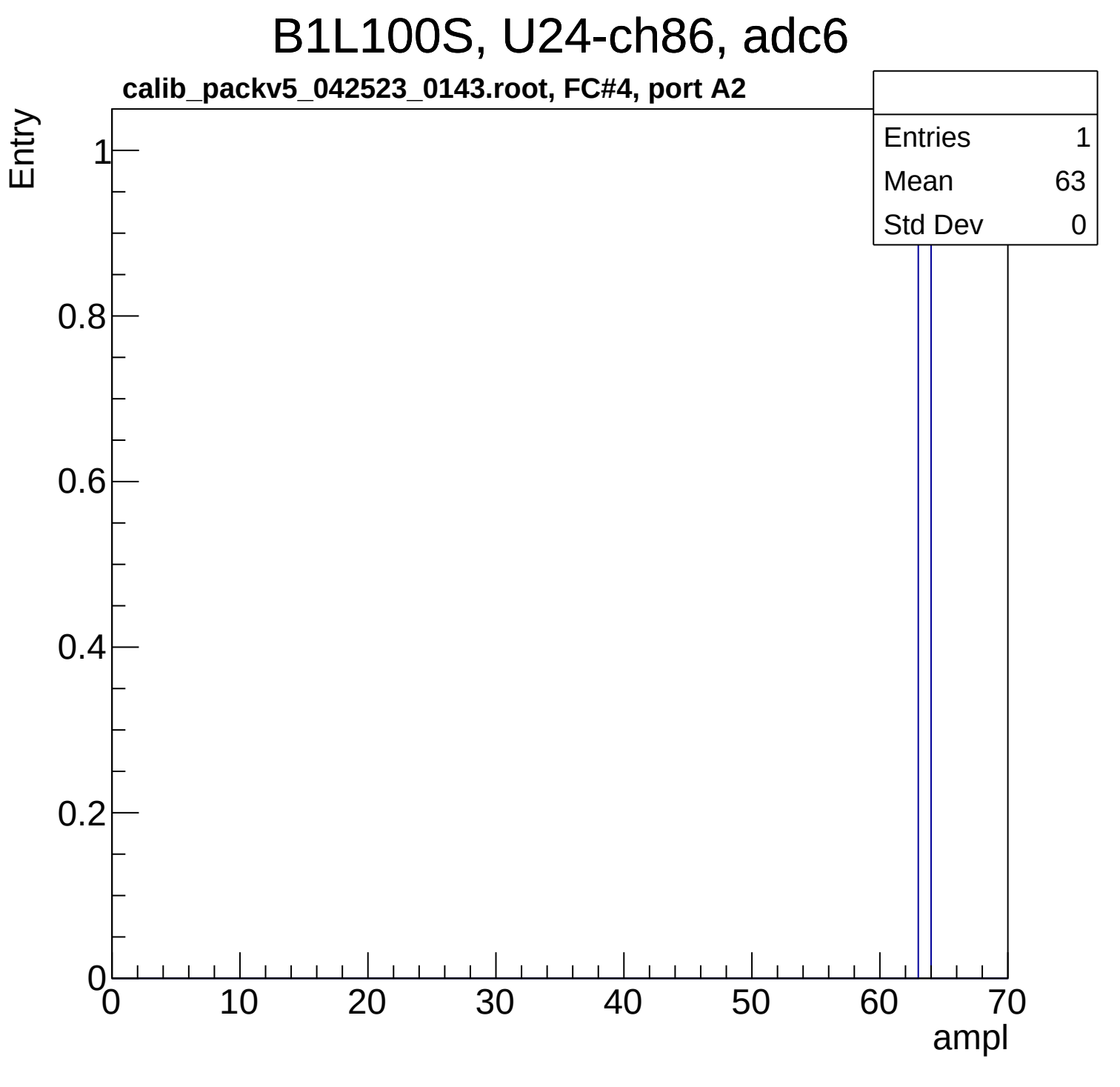
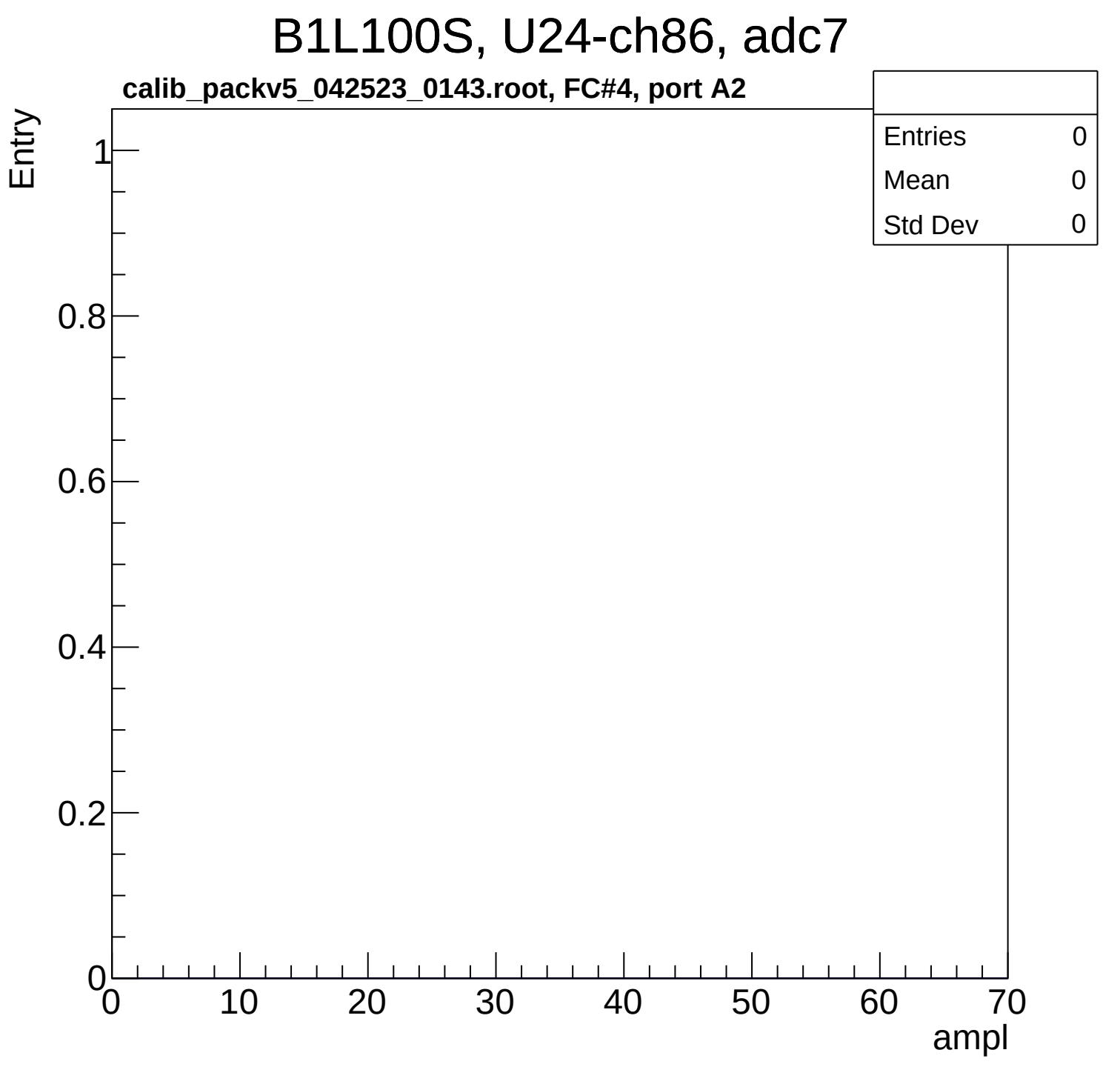
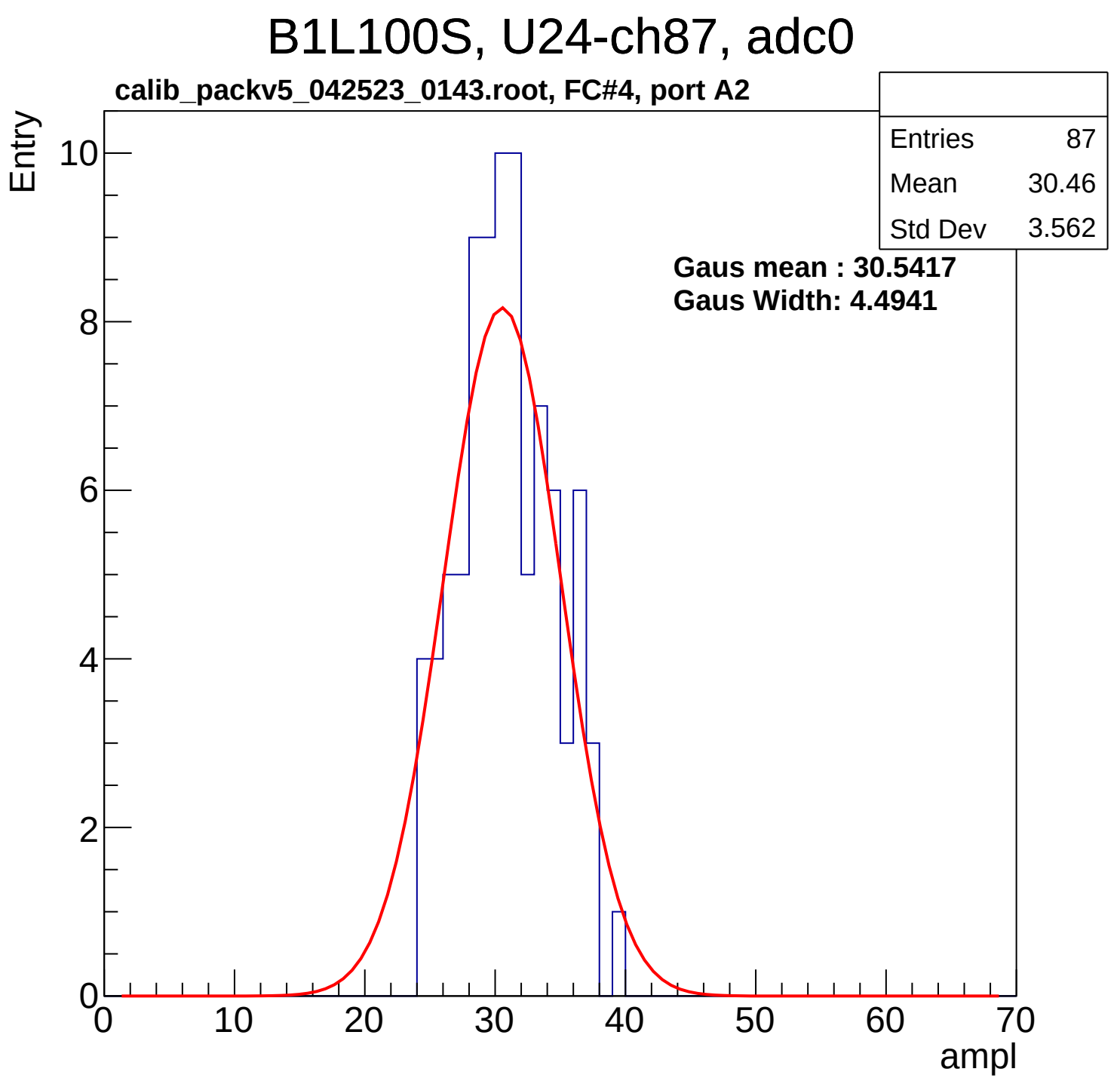
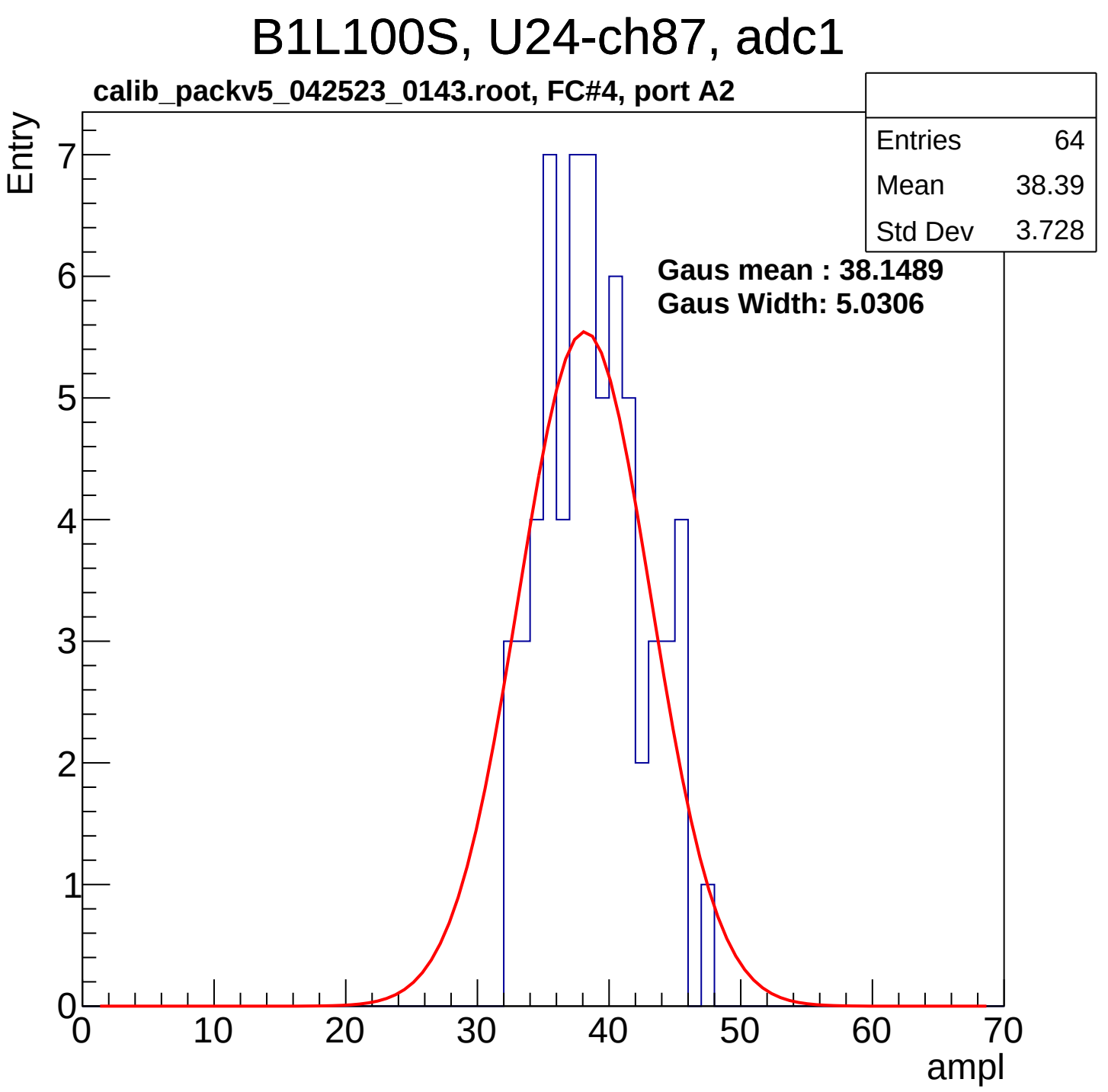
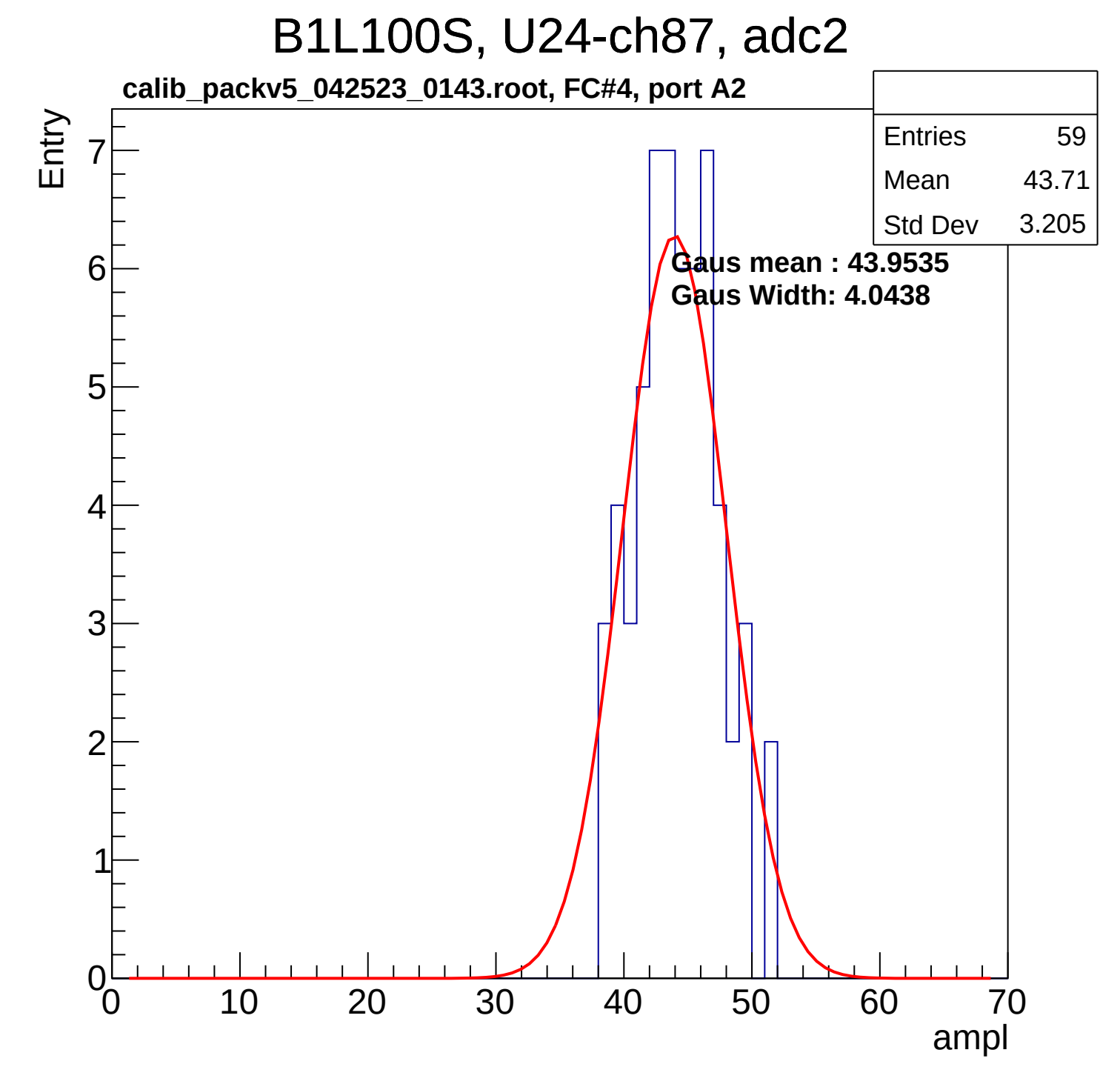
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch86, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch86, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 30.46
Std Dev 3.562
Gaus mean : 30.5417
Gaus Width: 4.4941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 38.39
Std Dev 3.728
Gaus mean : 38.1489
Gaus Width: 5.0306
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.71
Std Dev 3.205
Gaus mean : 43.9535
Gaus Width: 4.0438
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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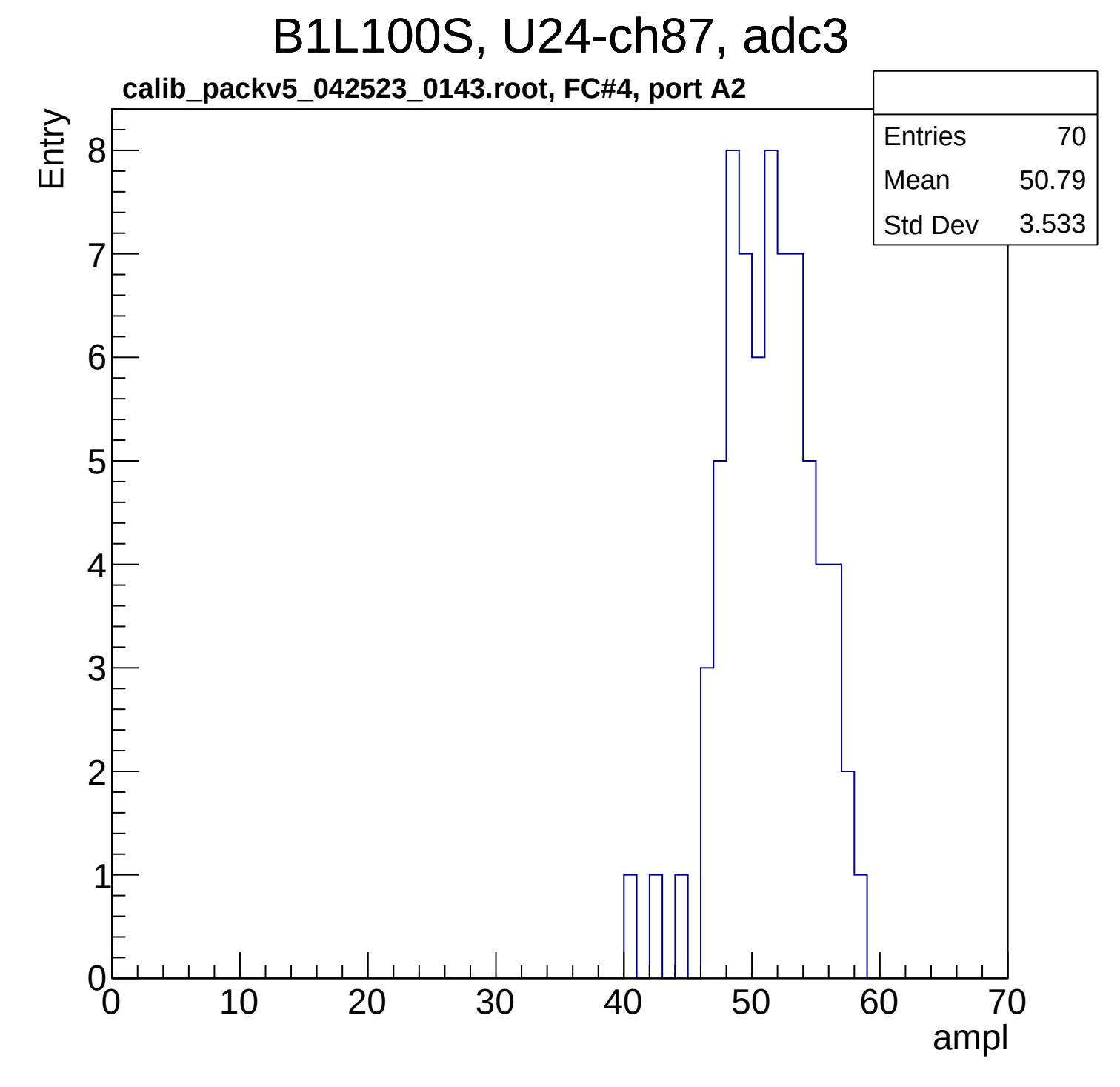
B1L100S, U24-ch87, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.79
Std Dev 3.533
Entry
ampl
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4
5
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0
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60
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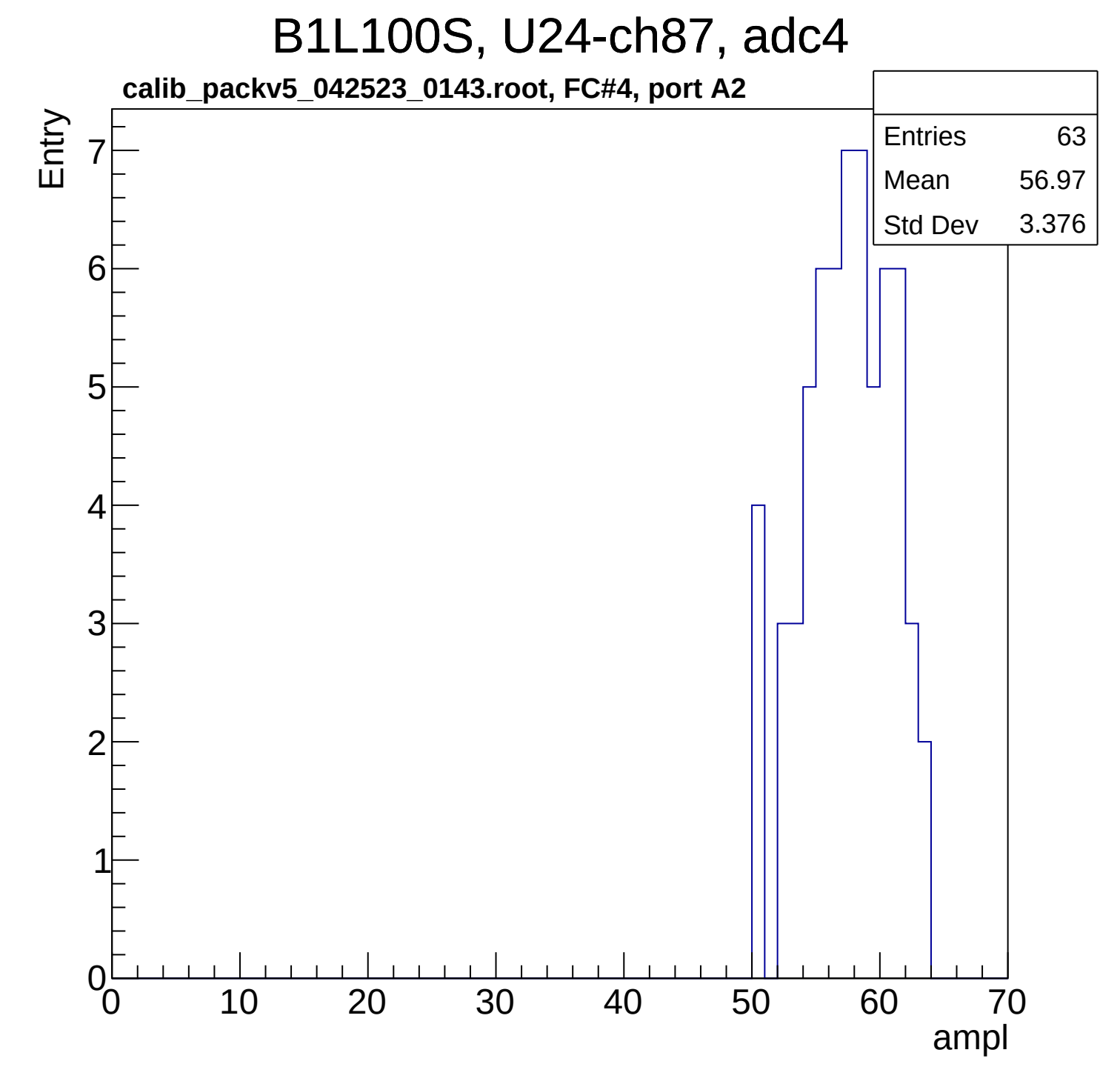
B1L100S, U24-ch87, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.97
Std Dev 3.376
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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60
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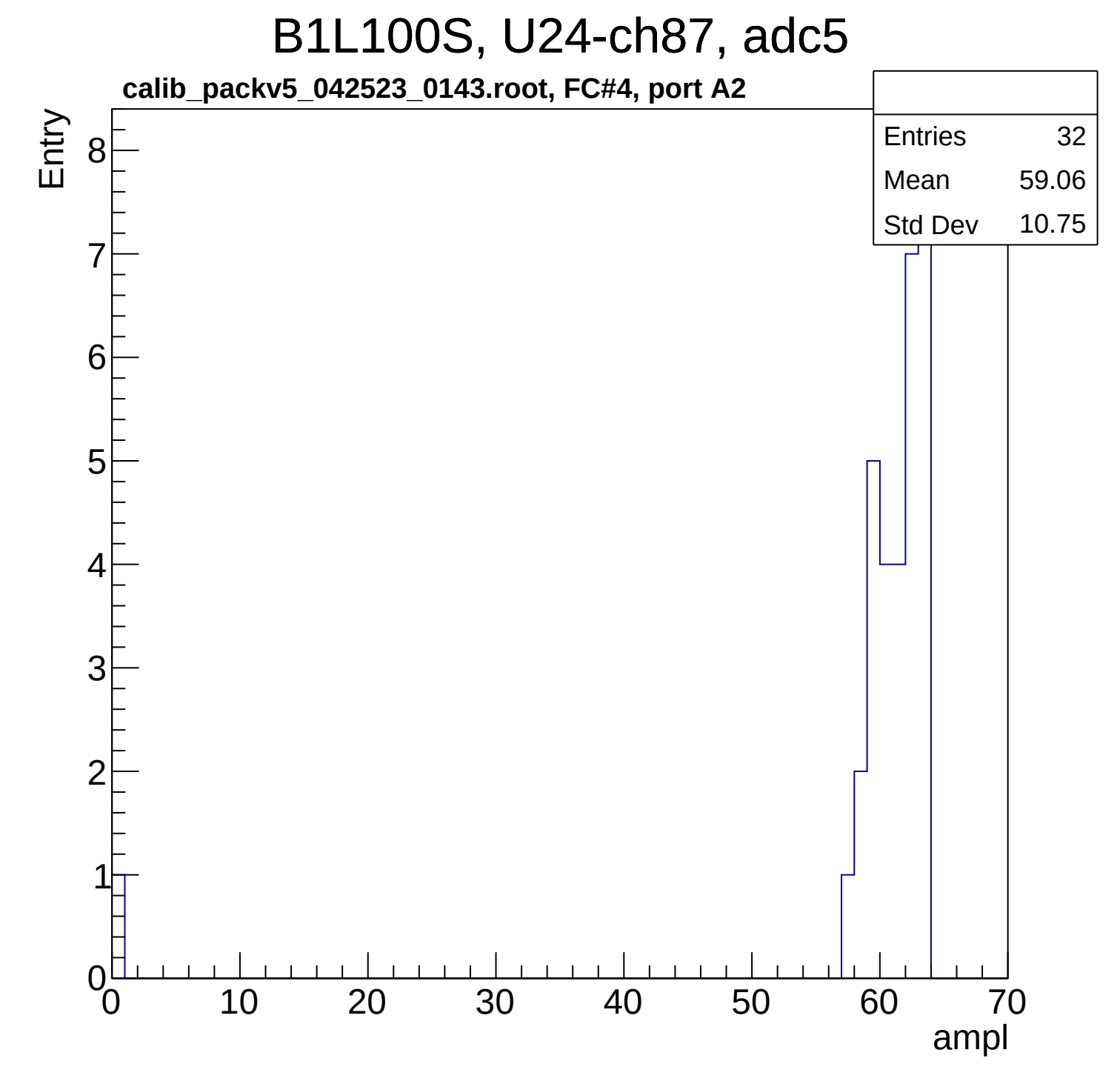
B1L100S, U24-ch87, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.06
Std Dev 10.75
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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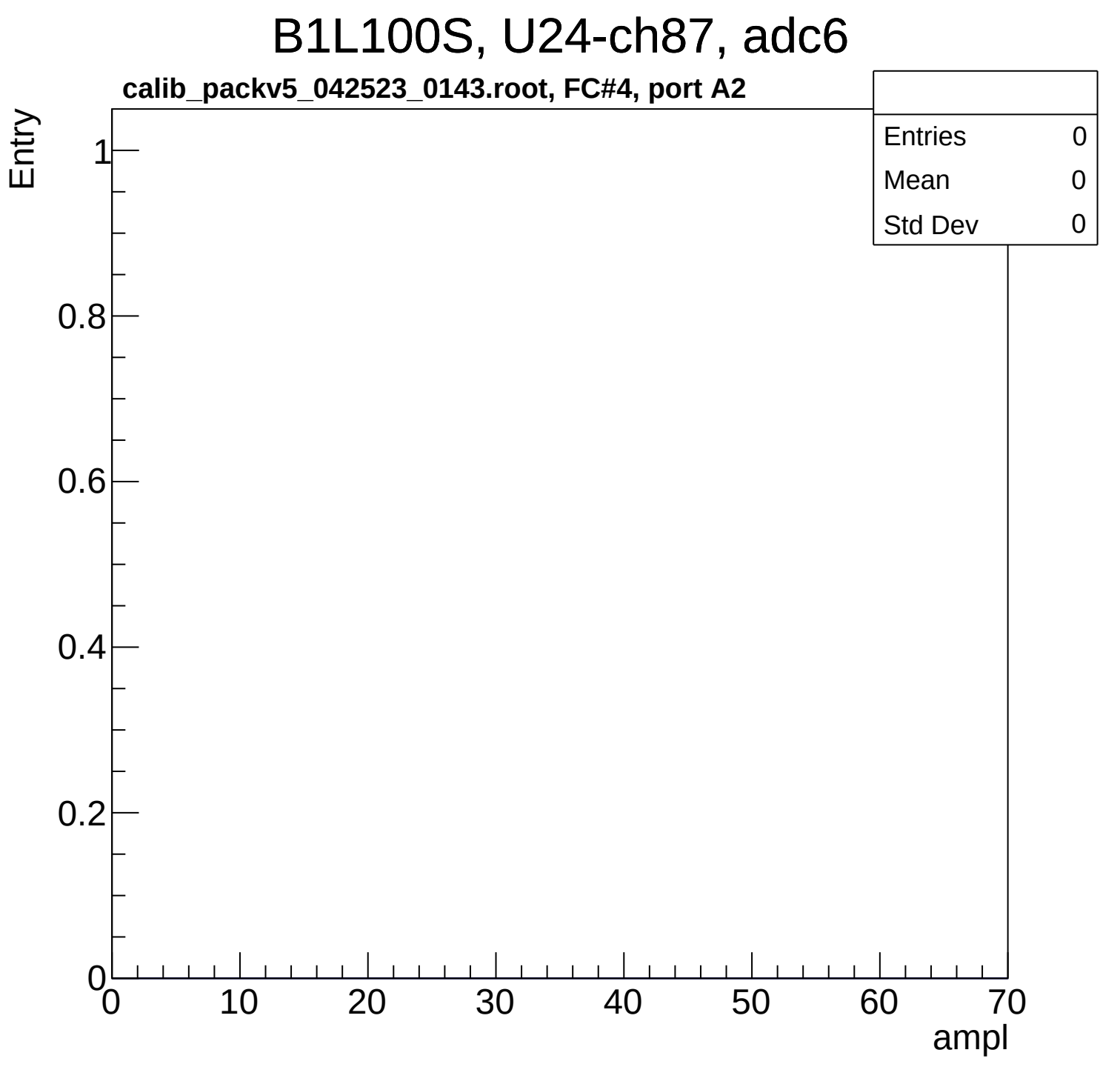
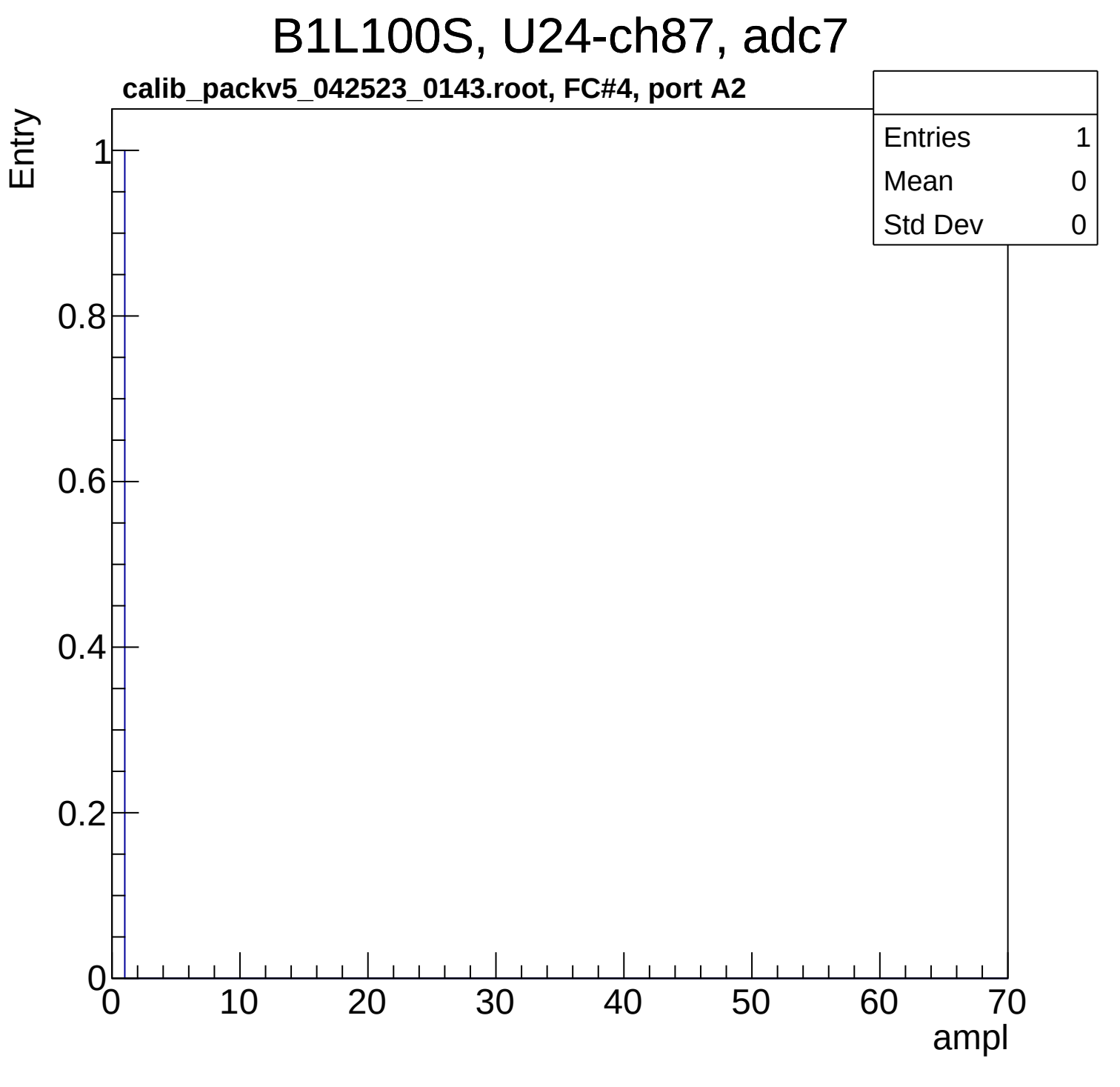

B1L100S, U24-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch87, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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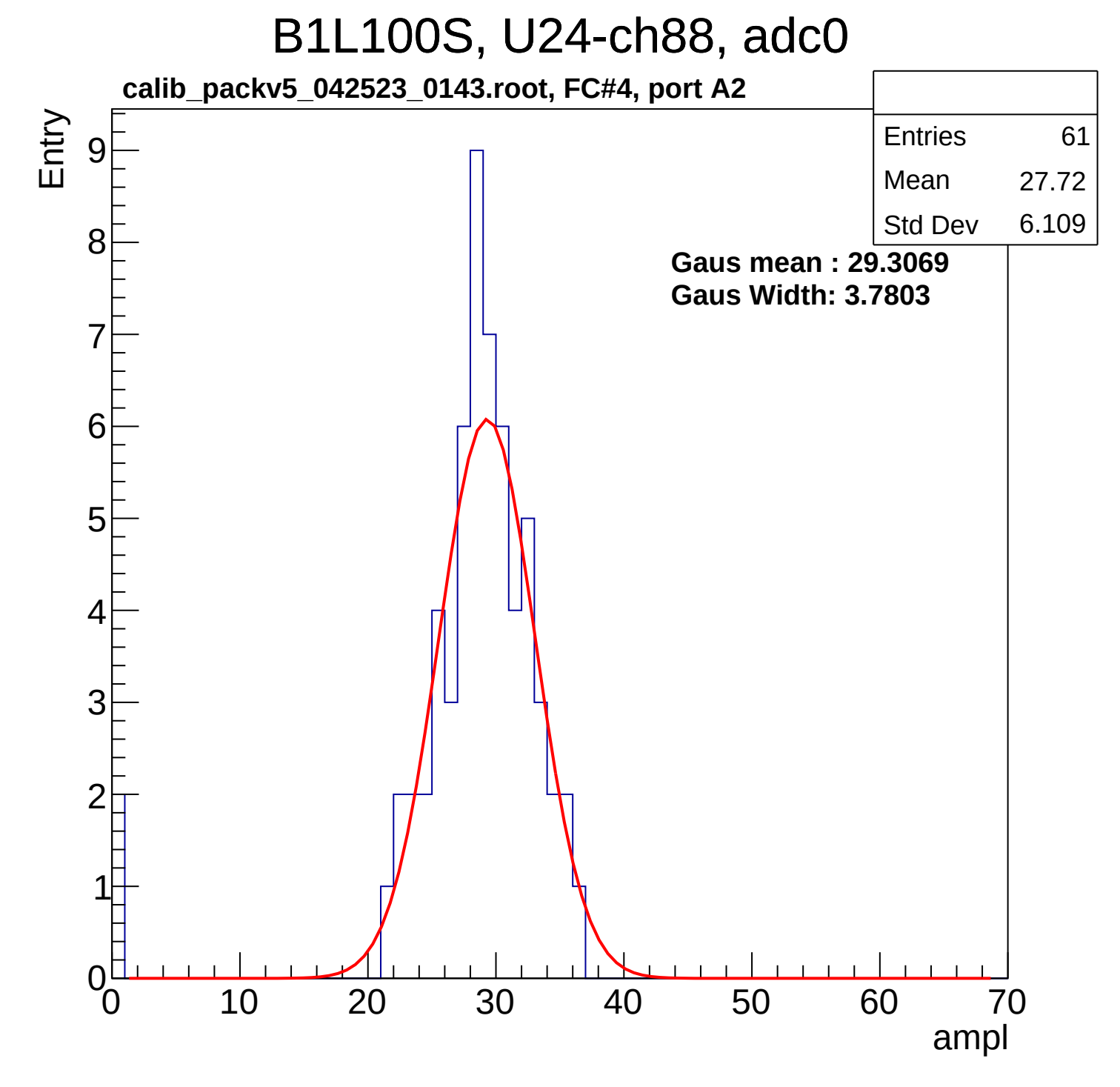
50
60
70
ampl


B1L100S, U24-ch88, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 27.72
Std Dev 6.109
Gaus mean : 29.3069
Gaus Width: 3.7803
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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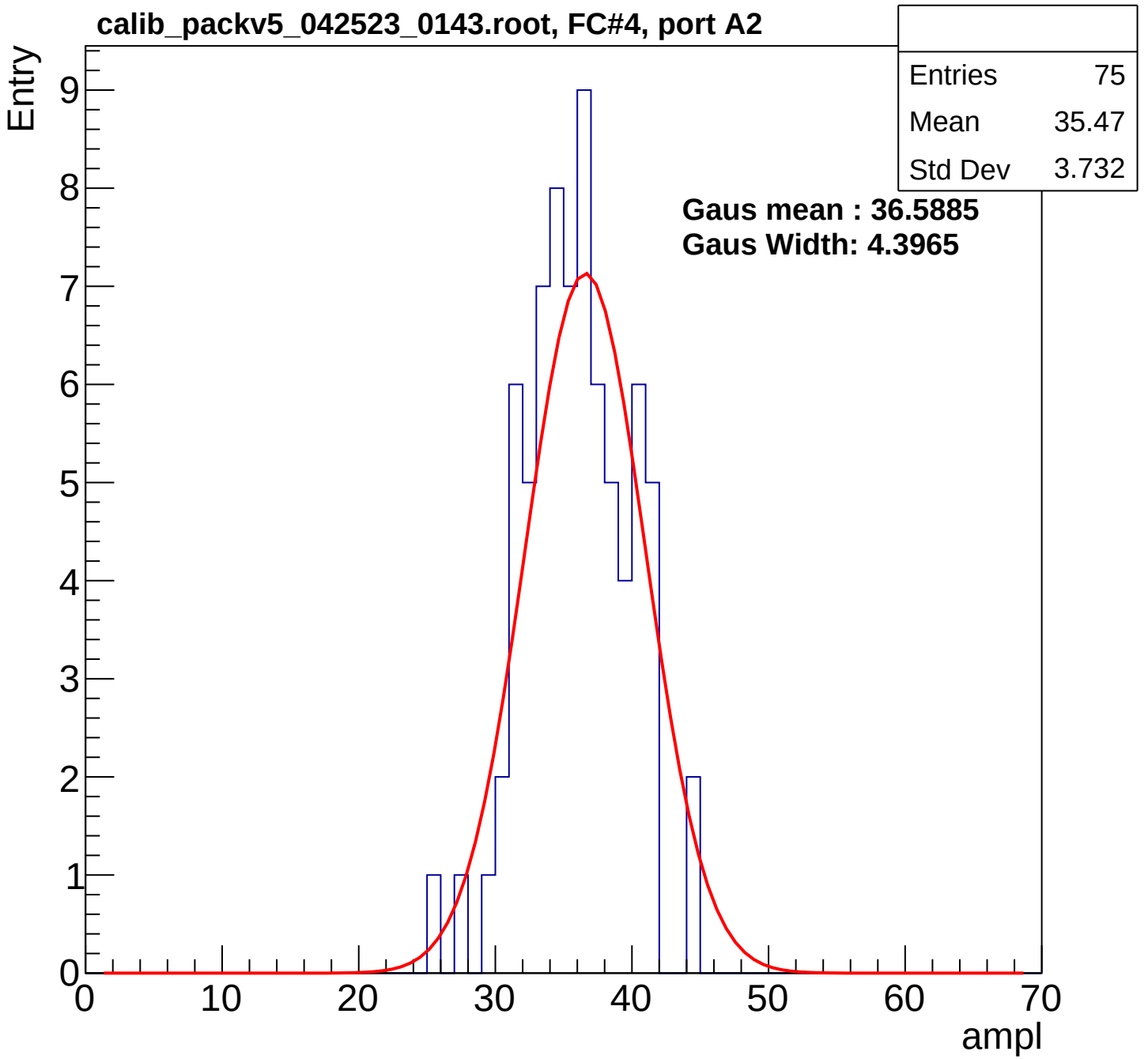
B1L100S, U24-ch88, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.47
Std Dev 3.732
Gaus mean : 36.5885
Gaus Width: 4.3965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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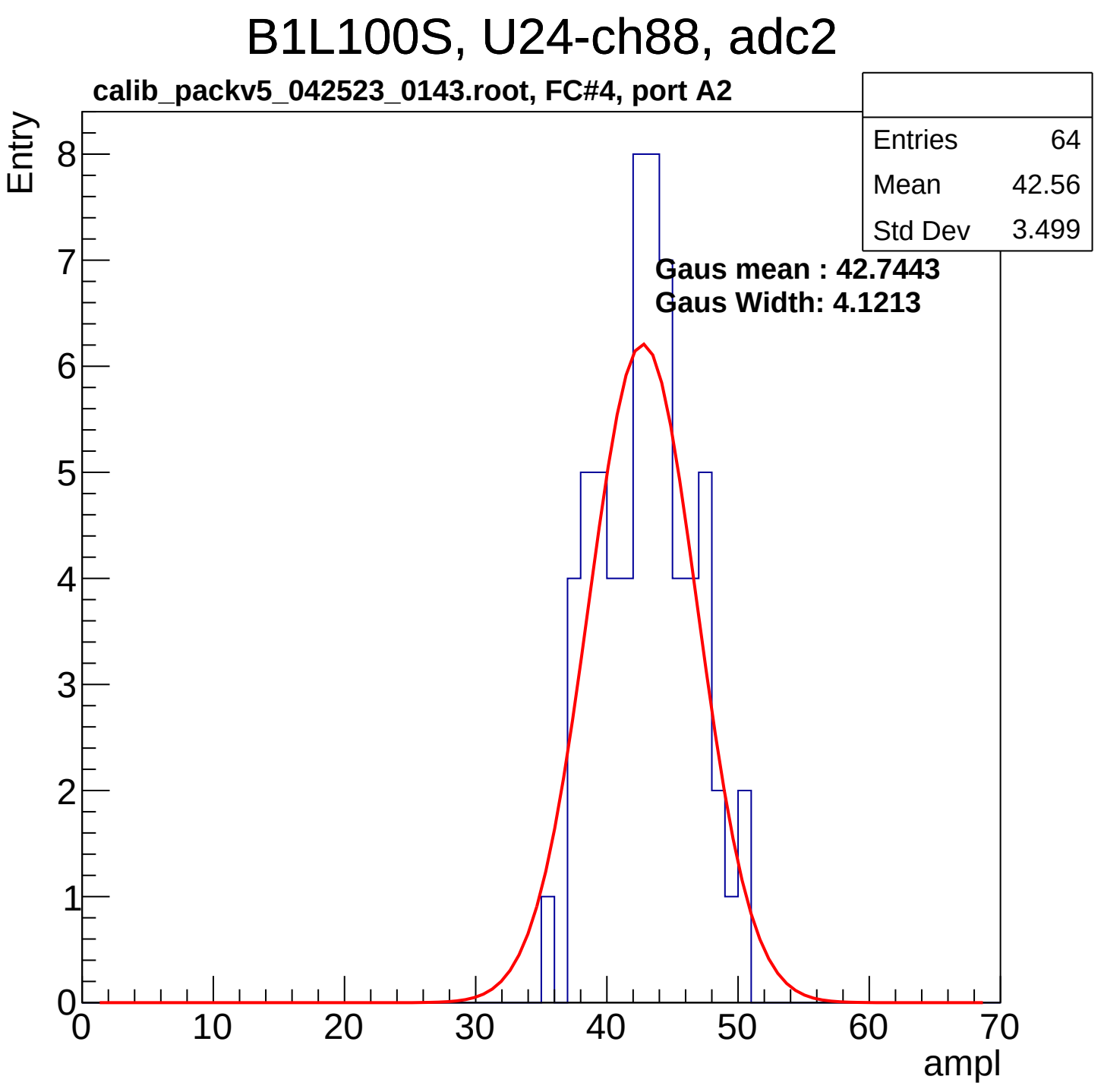
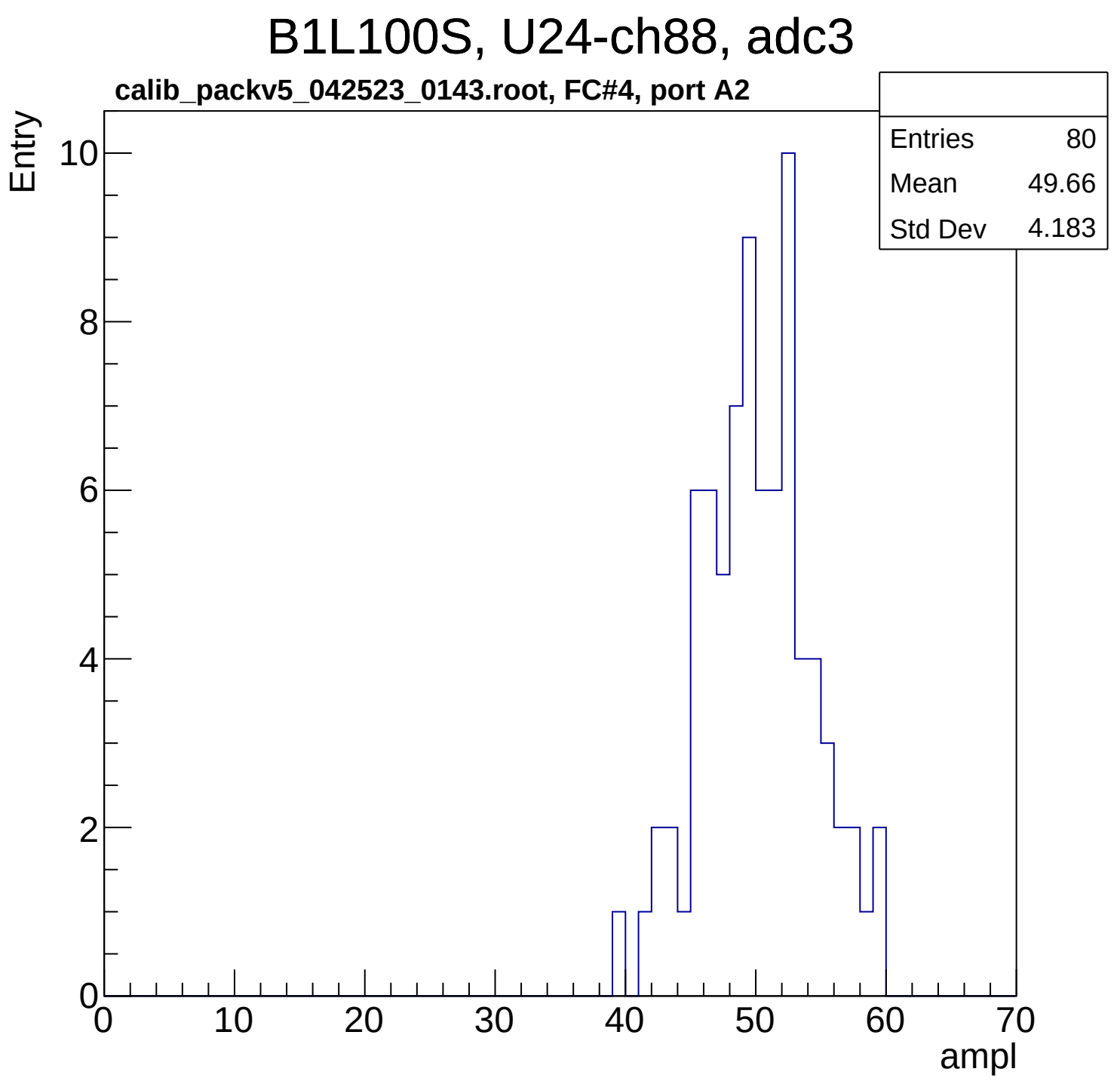

B1L100S, U24-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.56
Std Dev 3.499
Gaus mean : 42.7443
Gaus Width: 4.1213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 49.66
Std Dev 4.183
Entry
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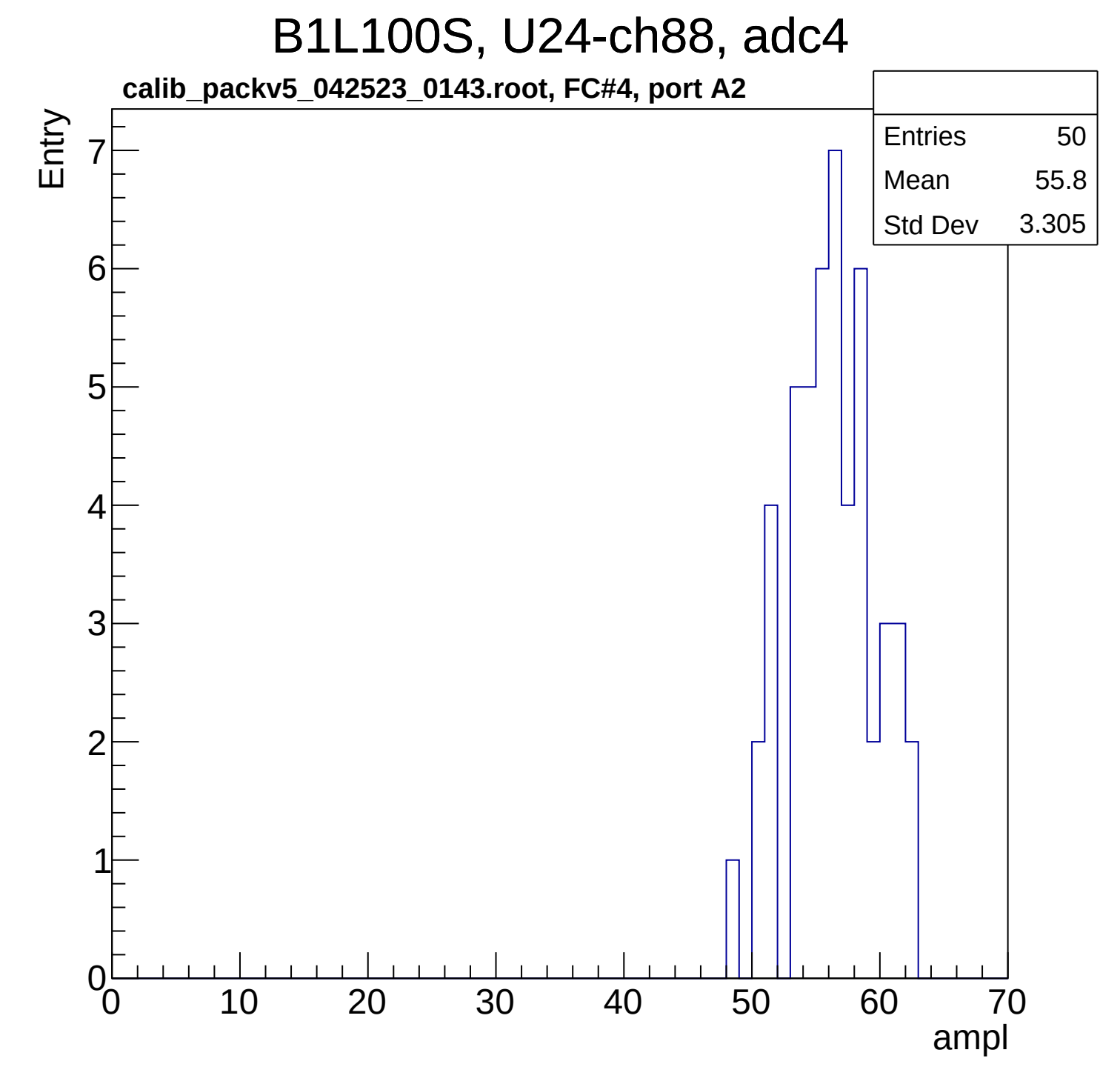
70
ampl


B1L100S, U24-ch88, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.8
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
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0
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40
50
60
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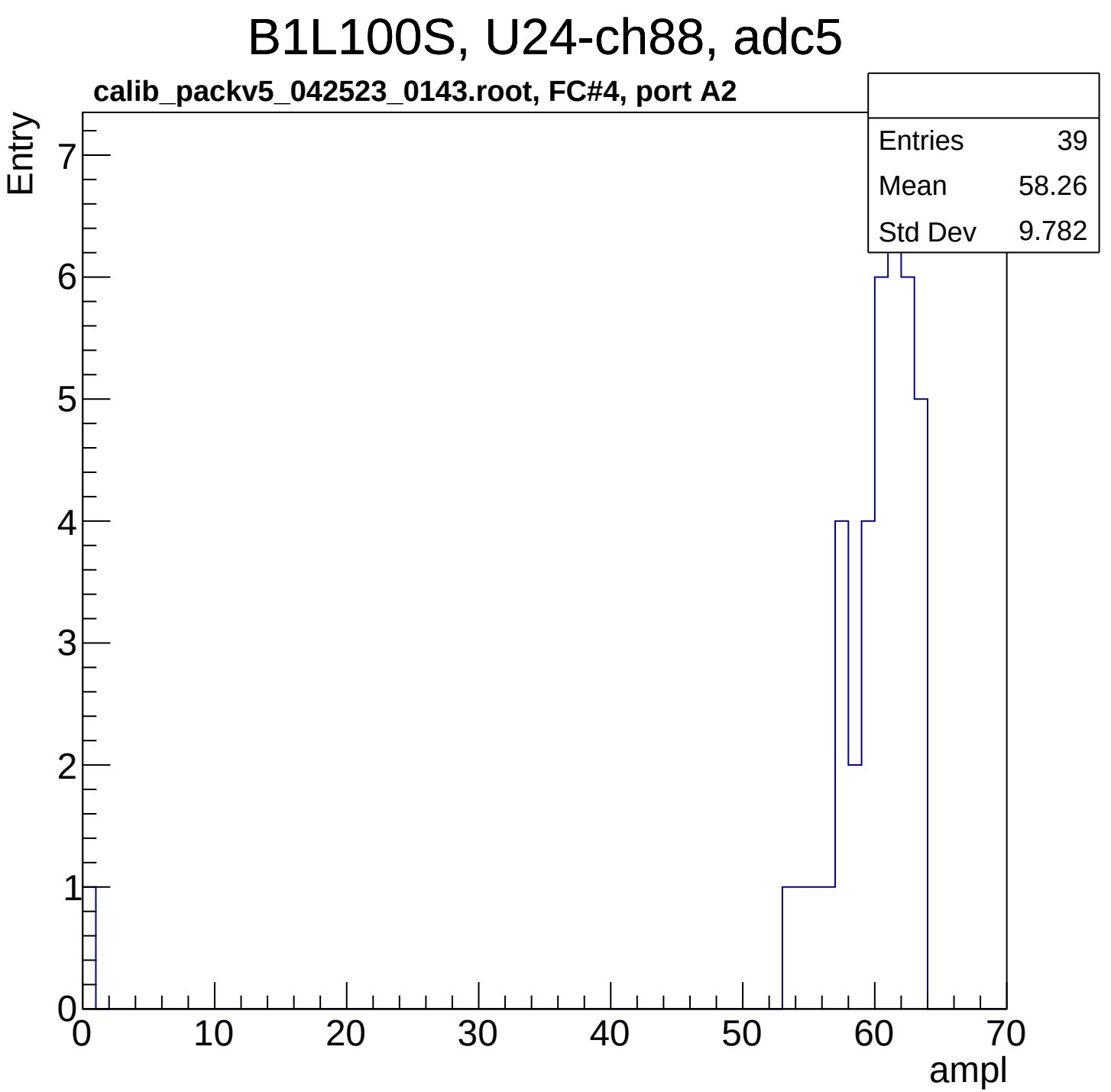
B1L100S, U24-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.26
Std Dev 9.782
Entry
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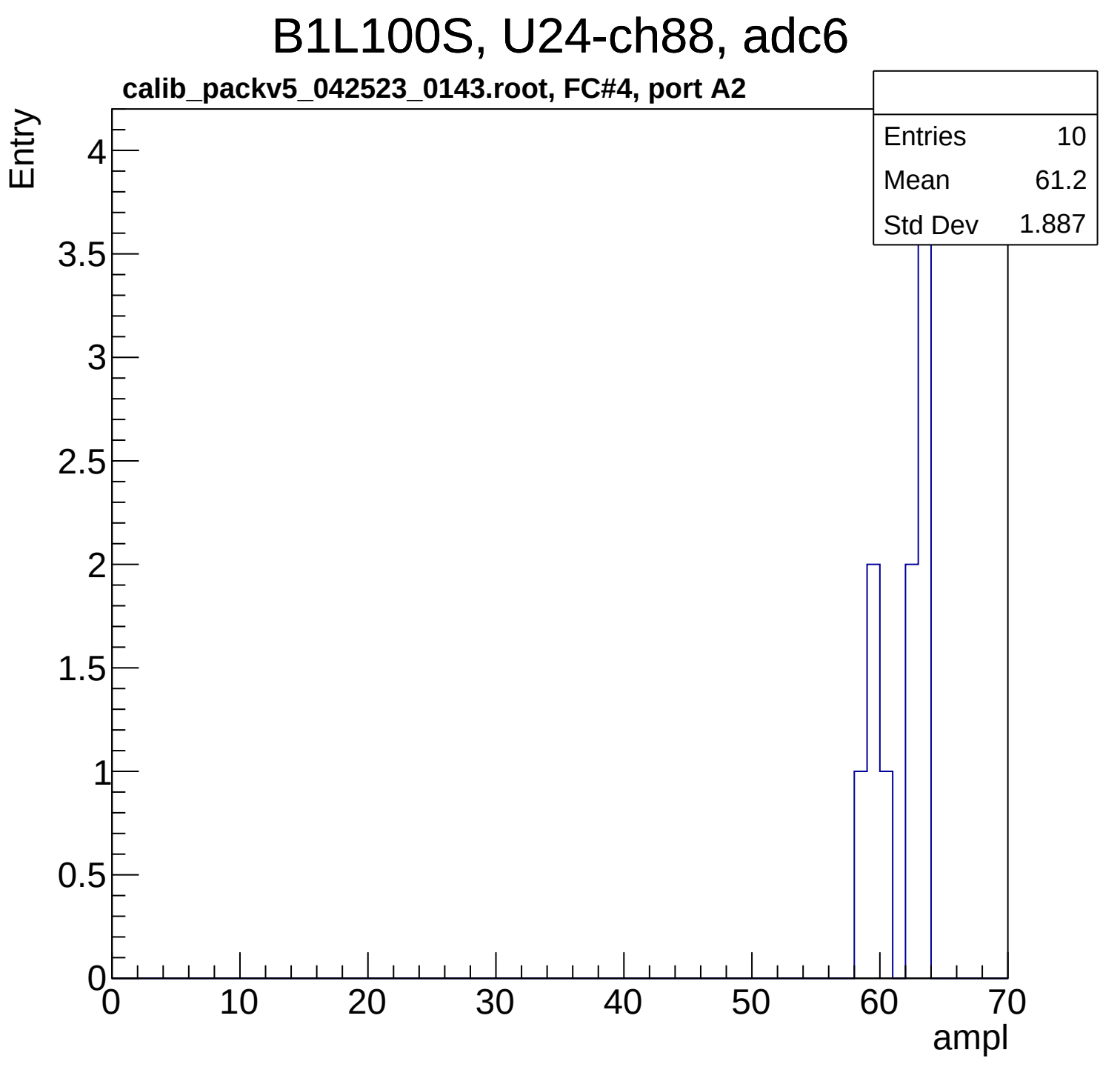
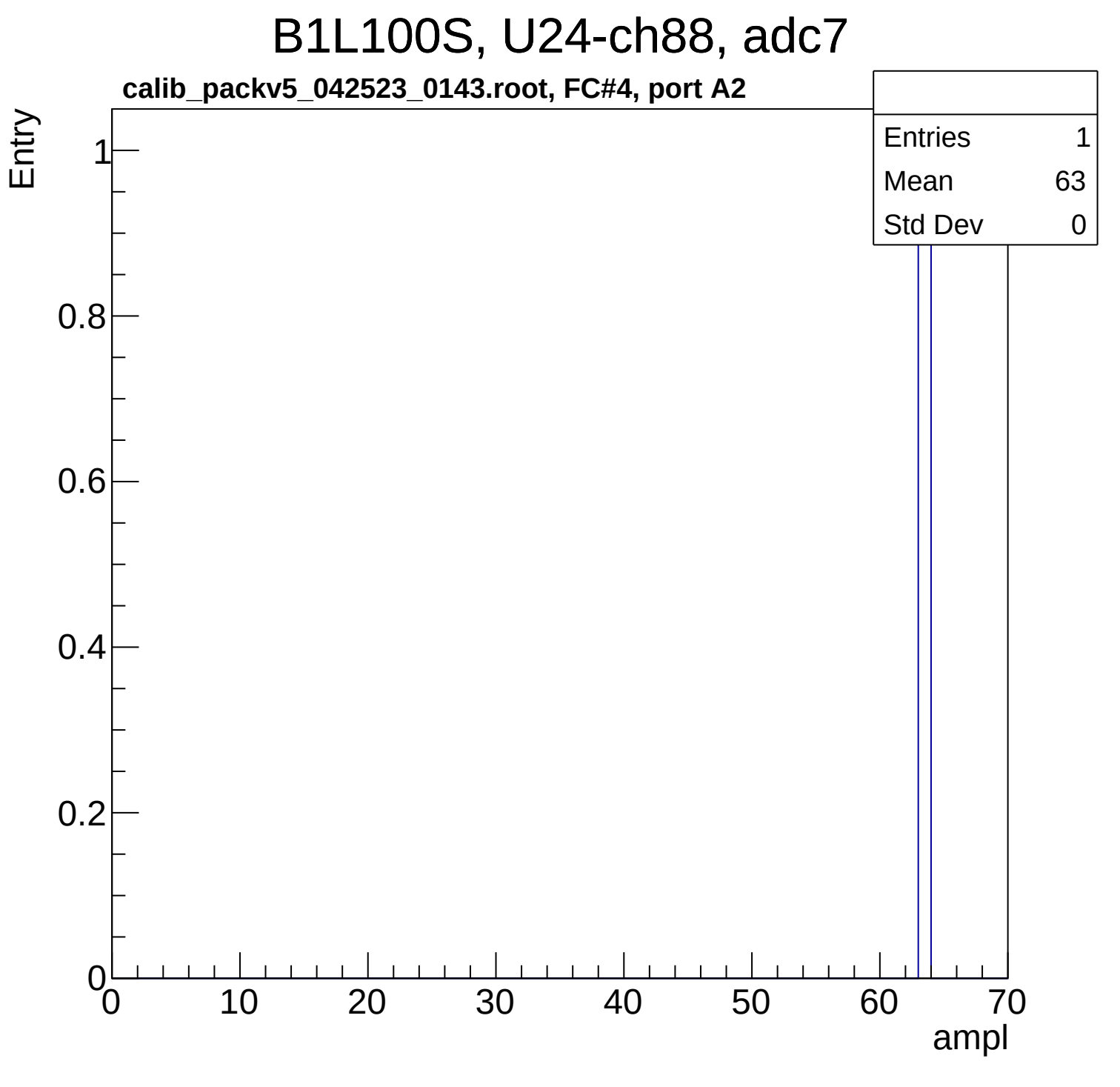
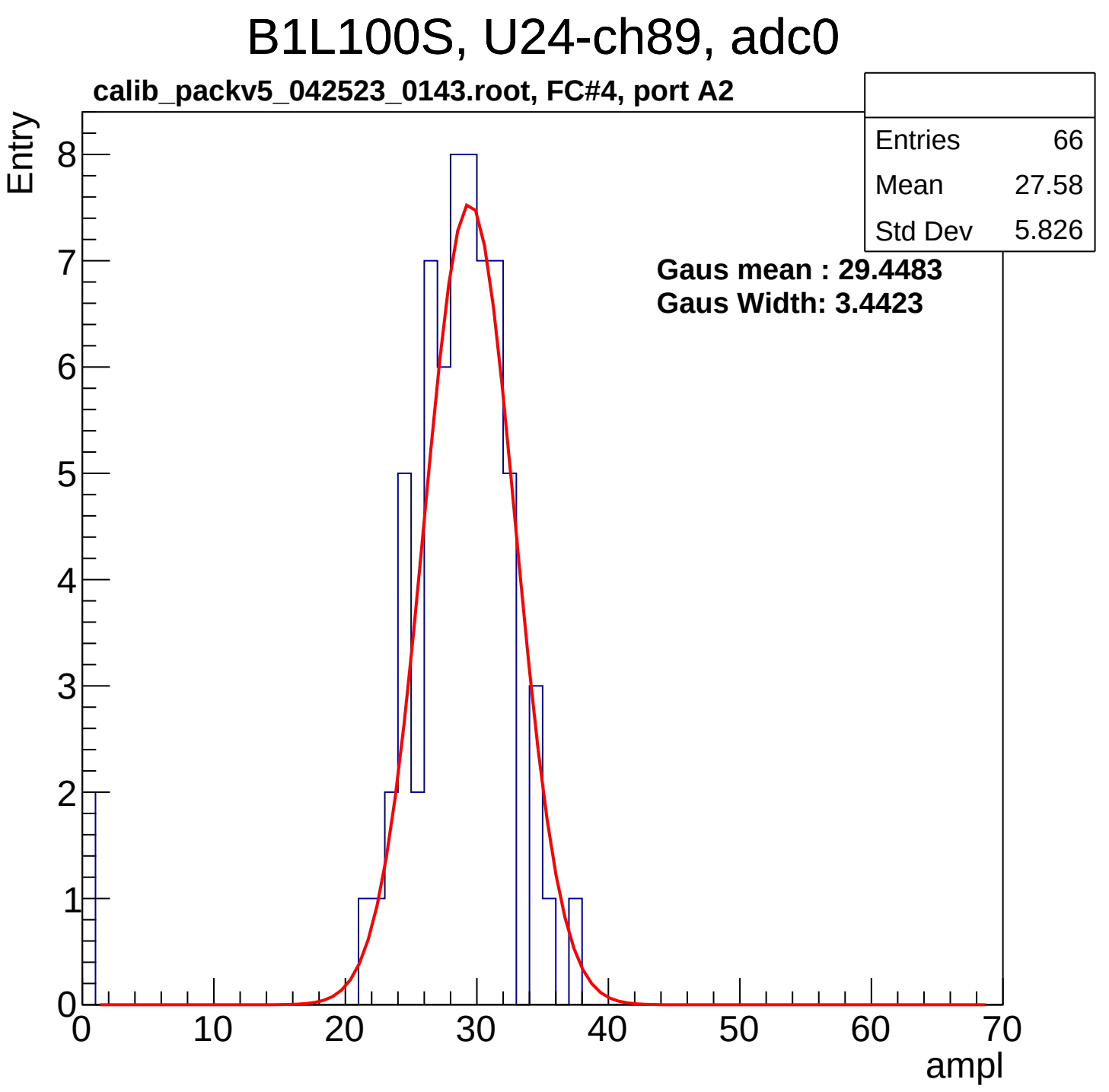
60
70
ampl


B1L100S, U24-ch88, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.2
Std Dev 1.887
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U24-ch88, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 27.58
Std Dev 5.826
Gaus mean : 29.4483
Gaus Width: 3.4423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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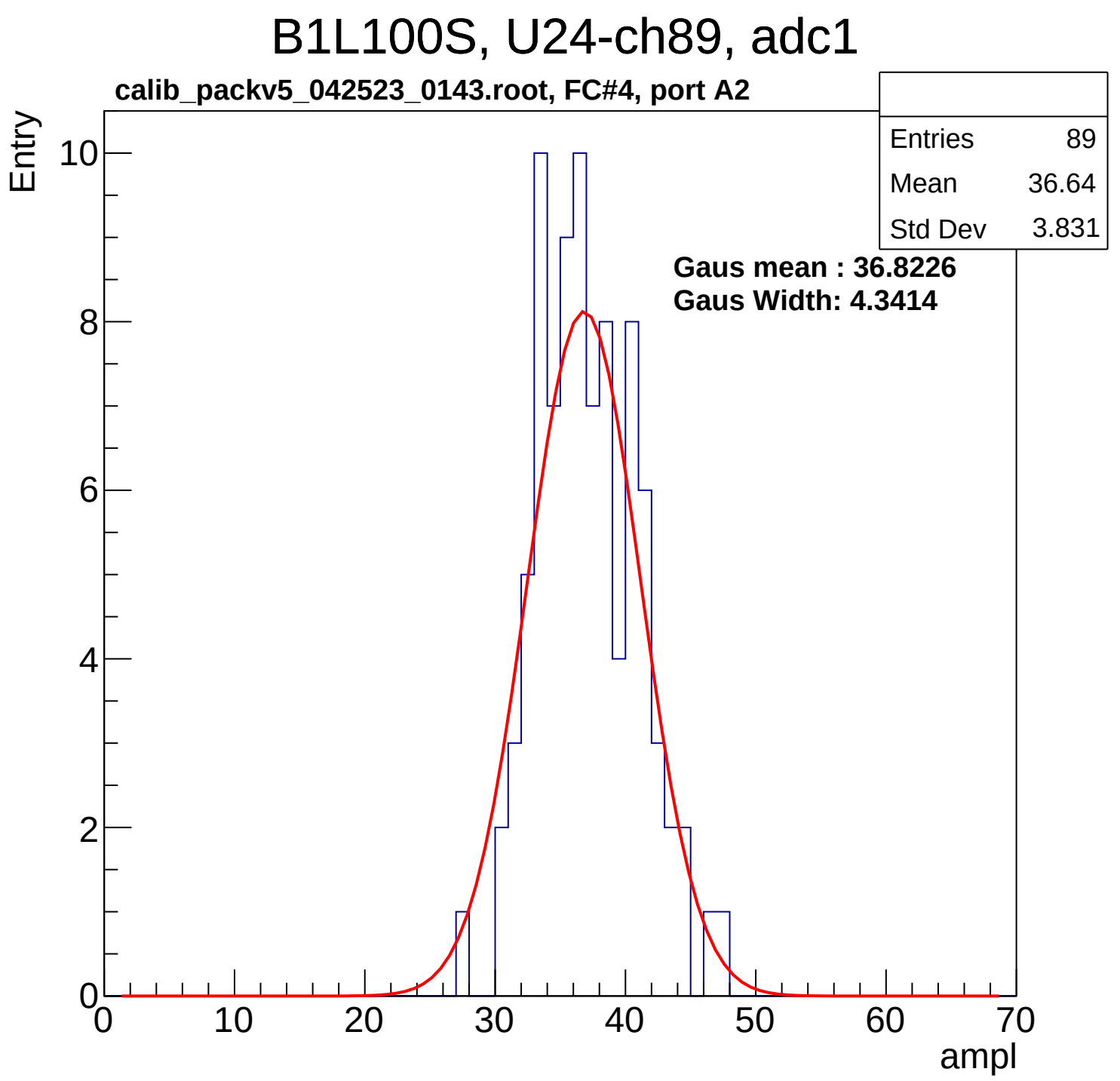
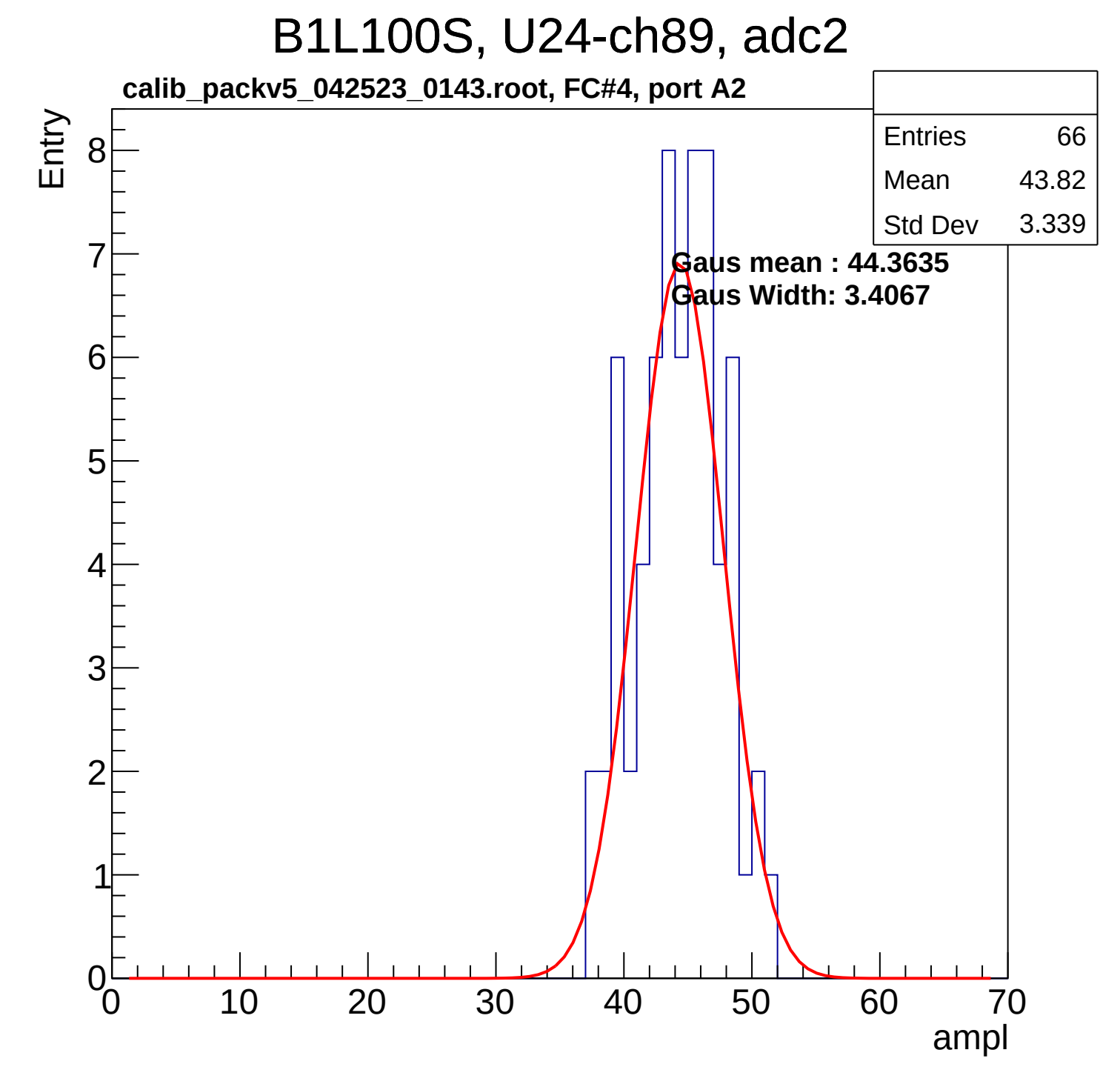

B1L100S, U24-ch89, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 36.64
Std Dev 3.831
Gaus mean : 36.8226
Gaus Width: 4.3414
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.82
Std Dev 3.339
Gaus mean : 44.3635
Gaus Width: 3.4067
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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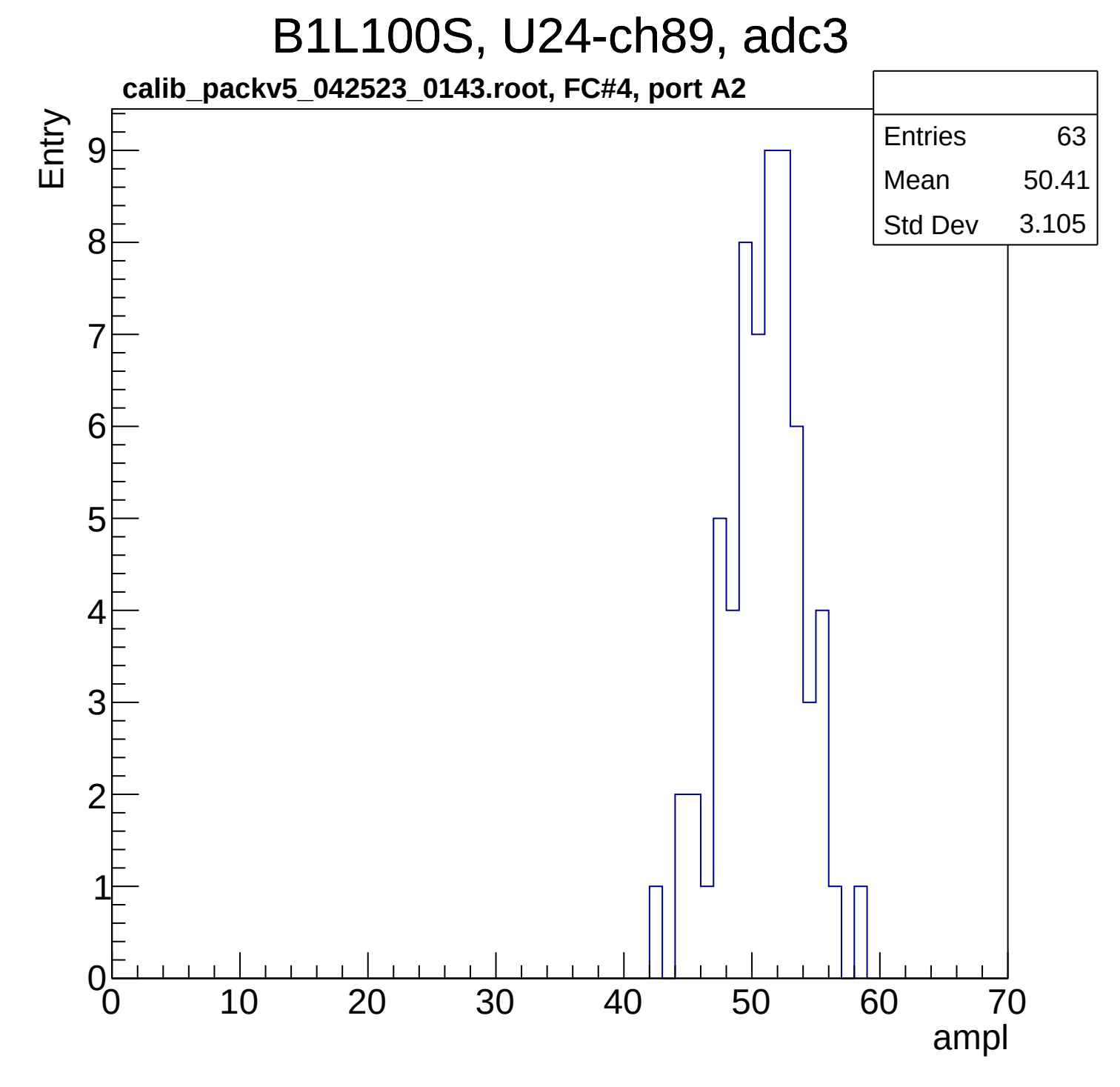
B1L100S, U24-ch89, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.41
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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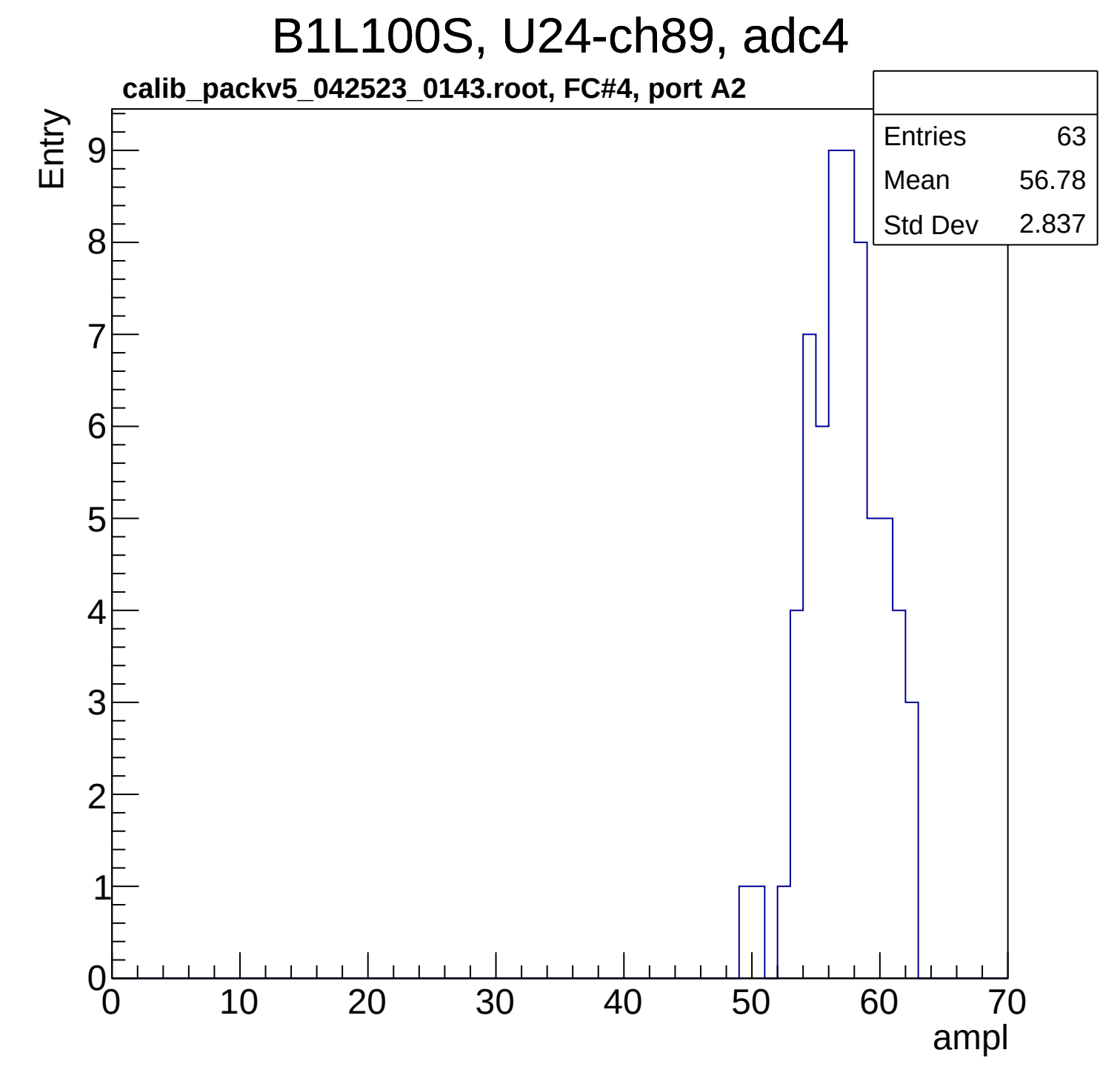
B1L100S, U24-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.78
Std Dev 2.837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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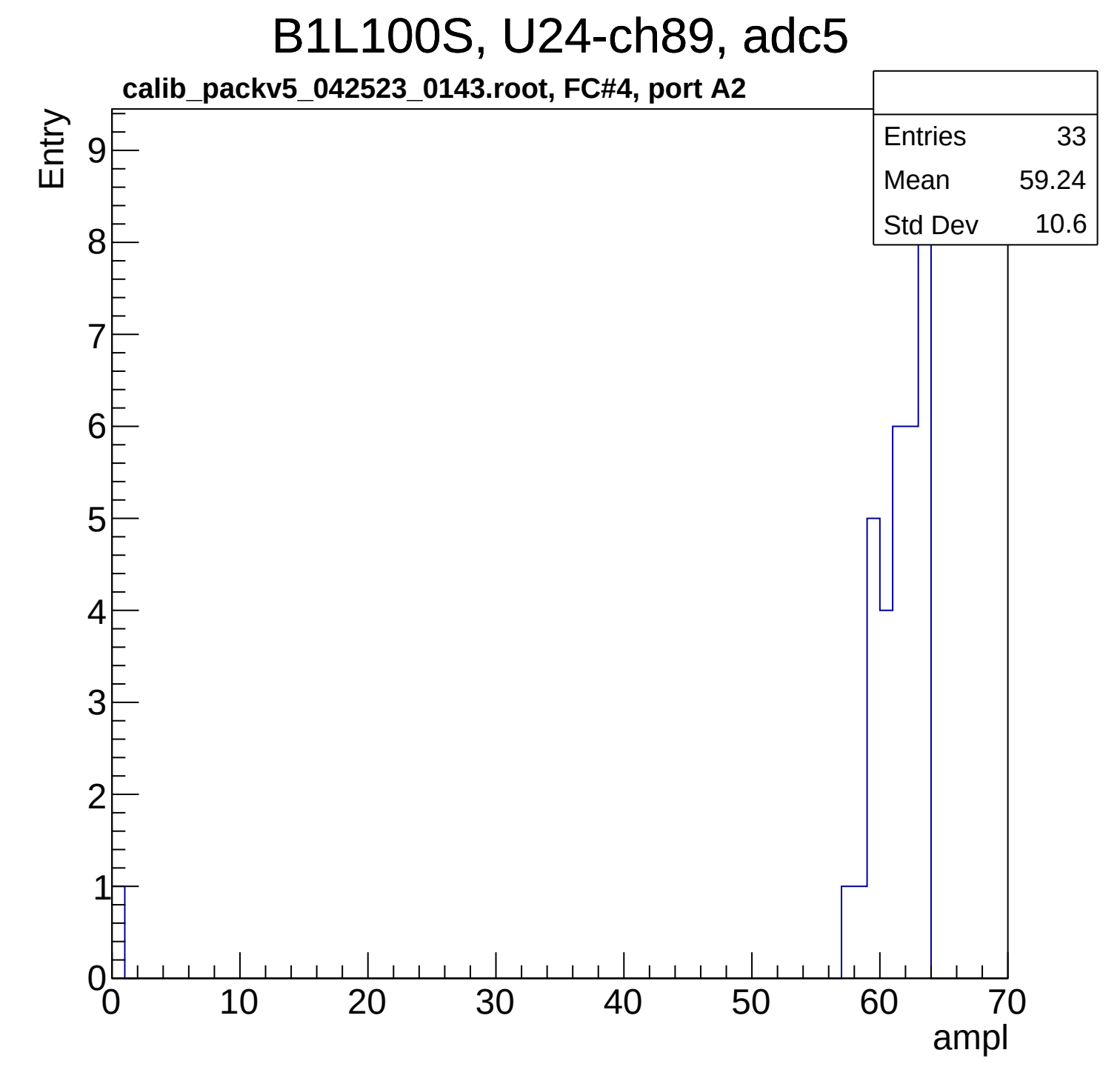
B1L100S, U24-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.24
Std Dev 10.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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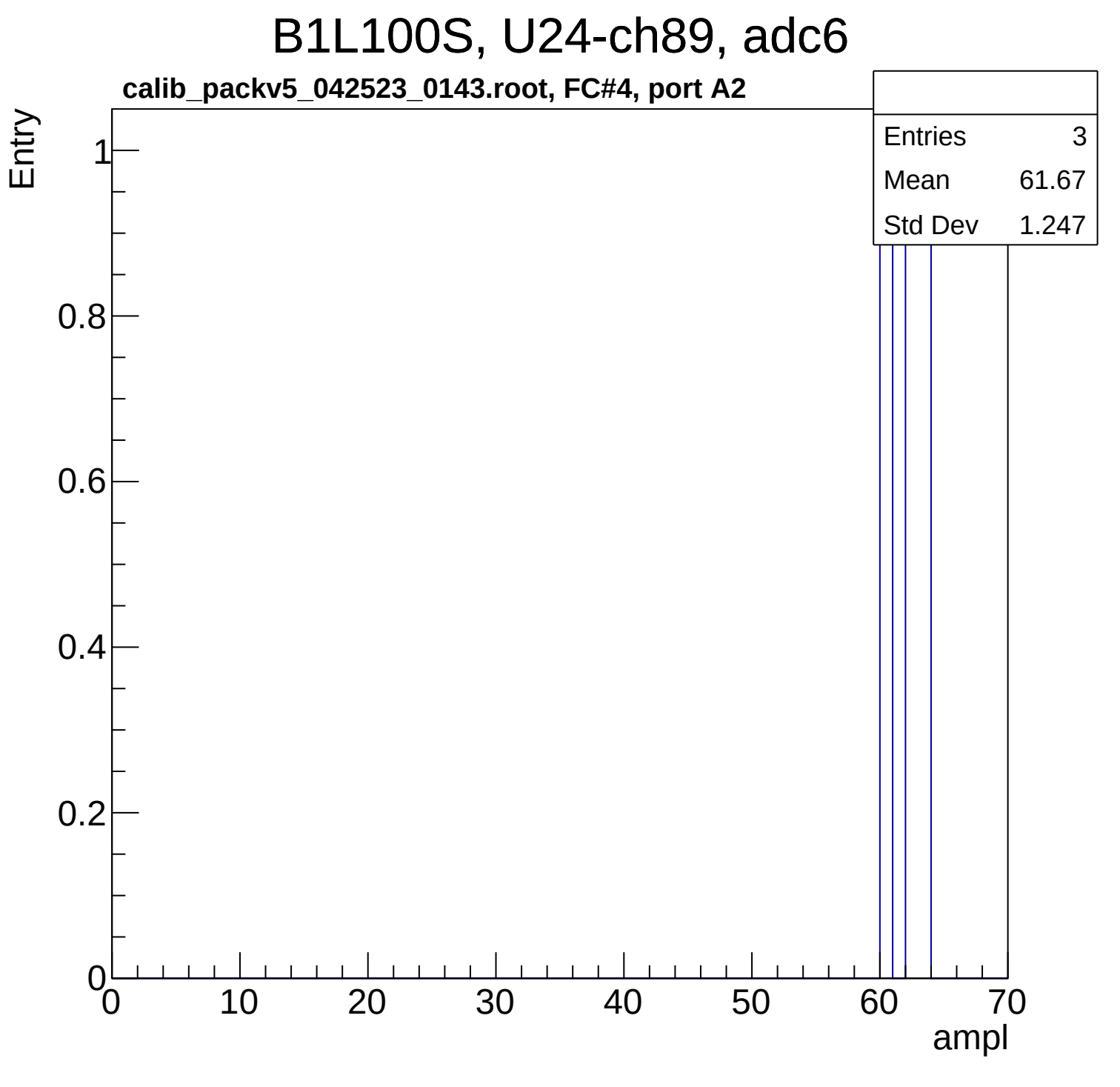
B1L100S, U24-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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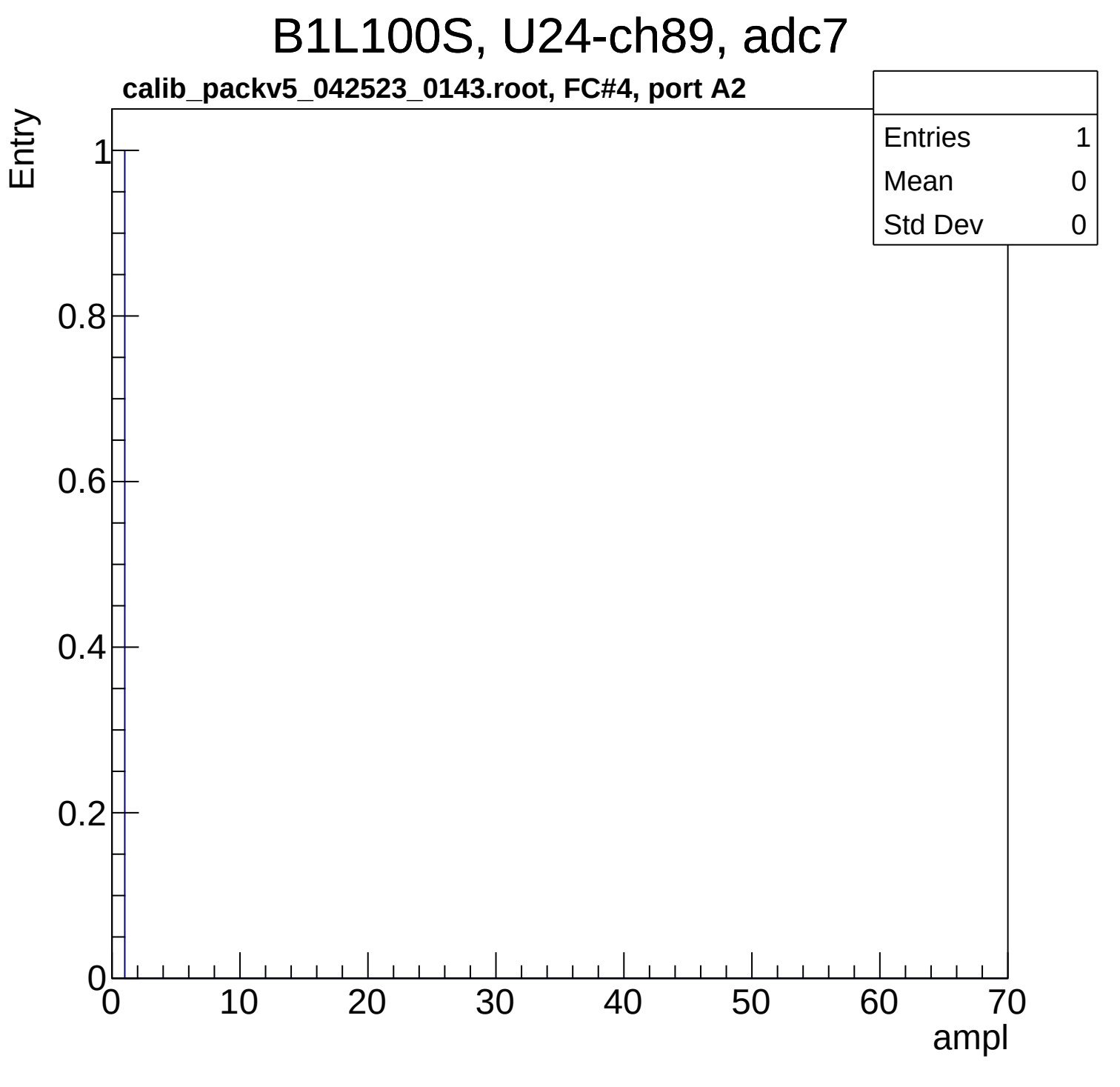
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch89, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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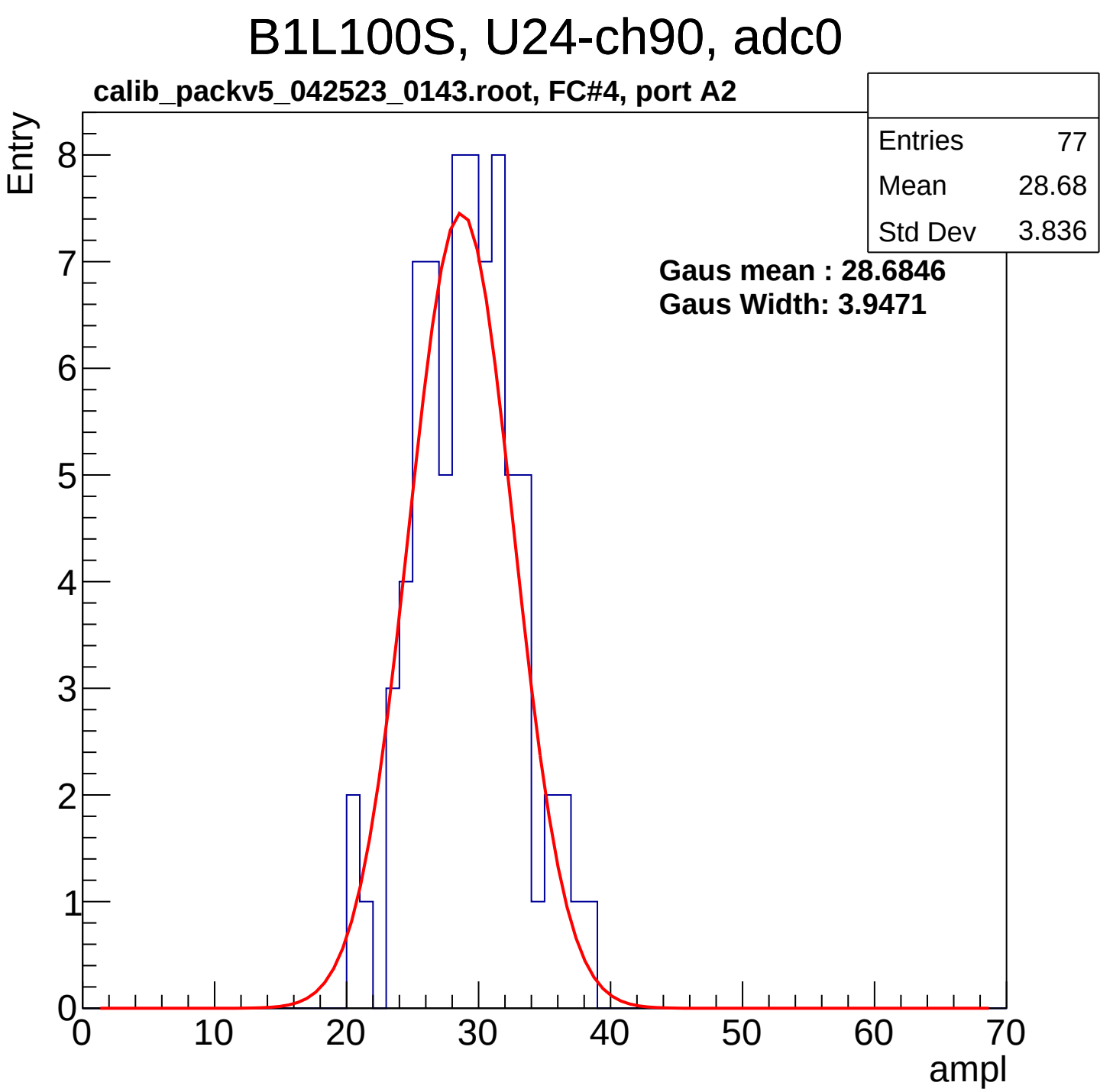
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.68
Std Dev 3.836
Gaus mean : 28.6846
Gaus Width: 3.9471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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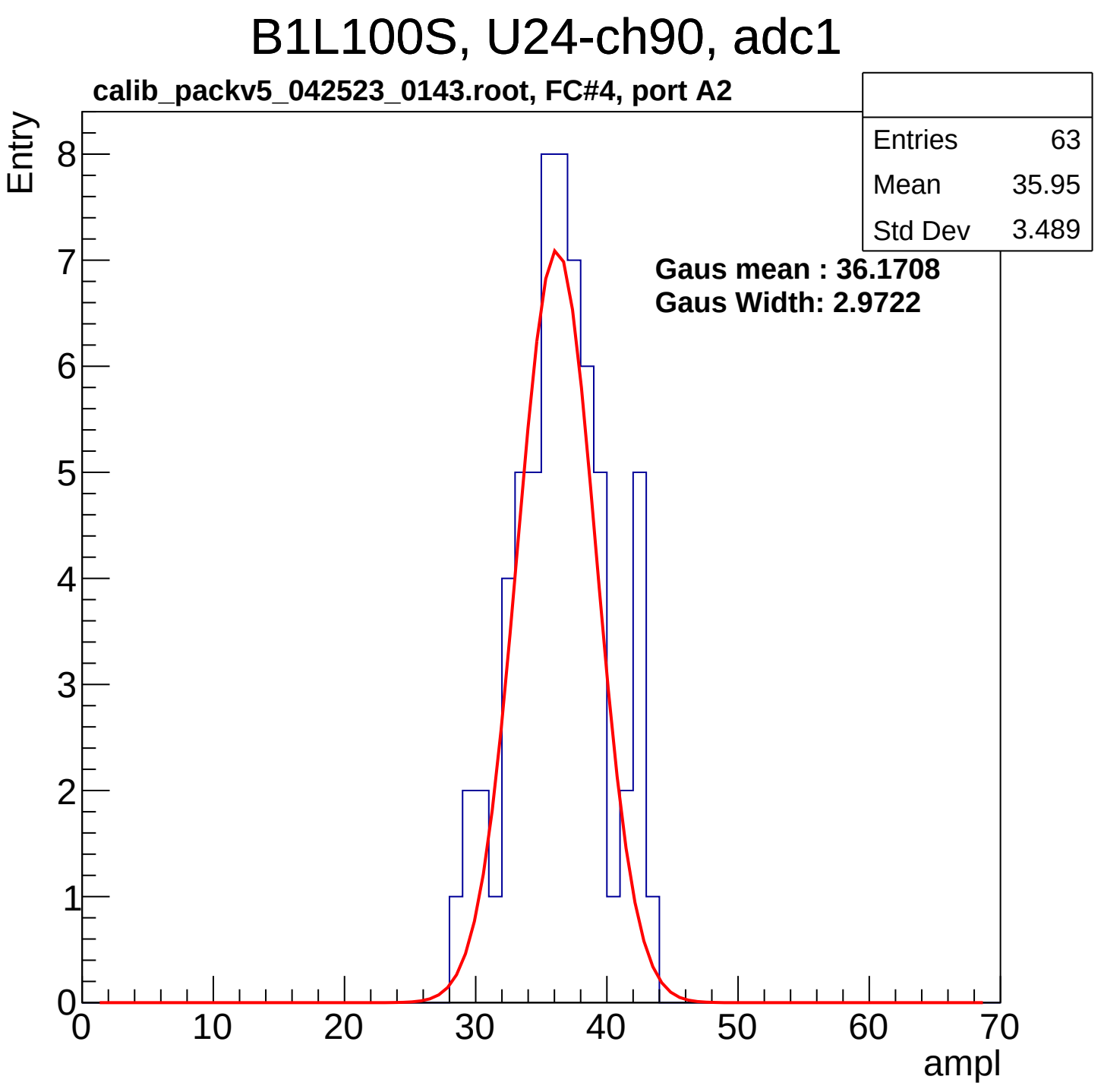
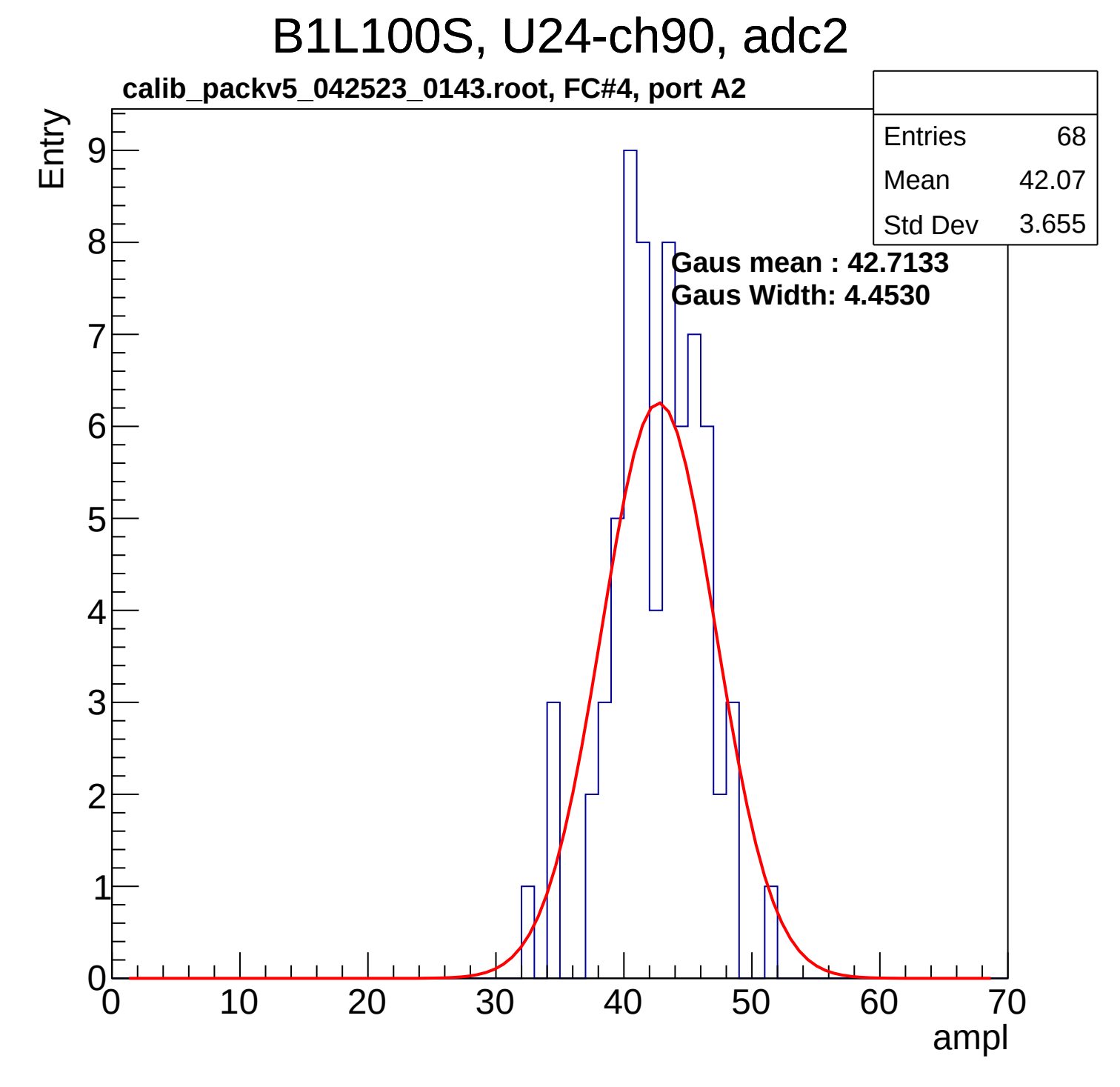

B1L100S, U24-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.95
Std Dev 3.489
Gaus mean : 36.1708
Gaus Width: 2.9722
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.07
Std Dev 3.655
Gaus mean : 42.7133
Gaus Width: 4.4530
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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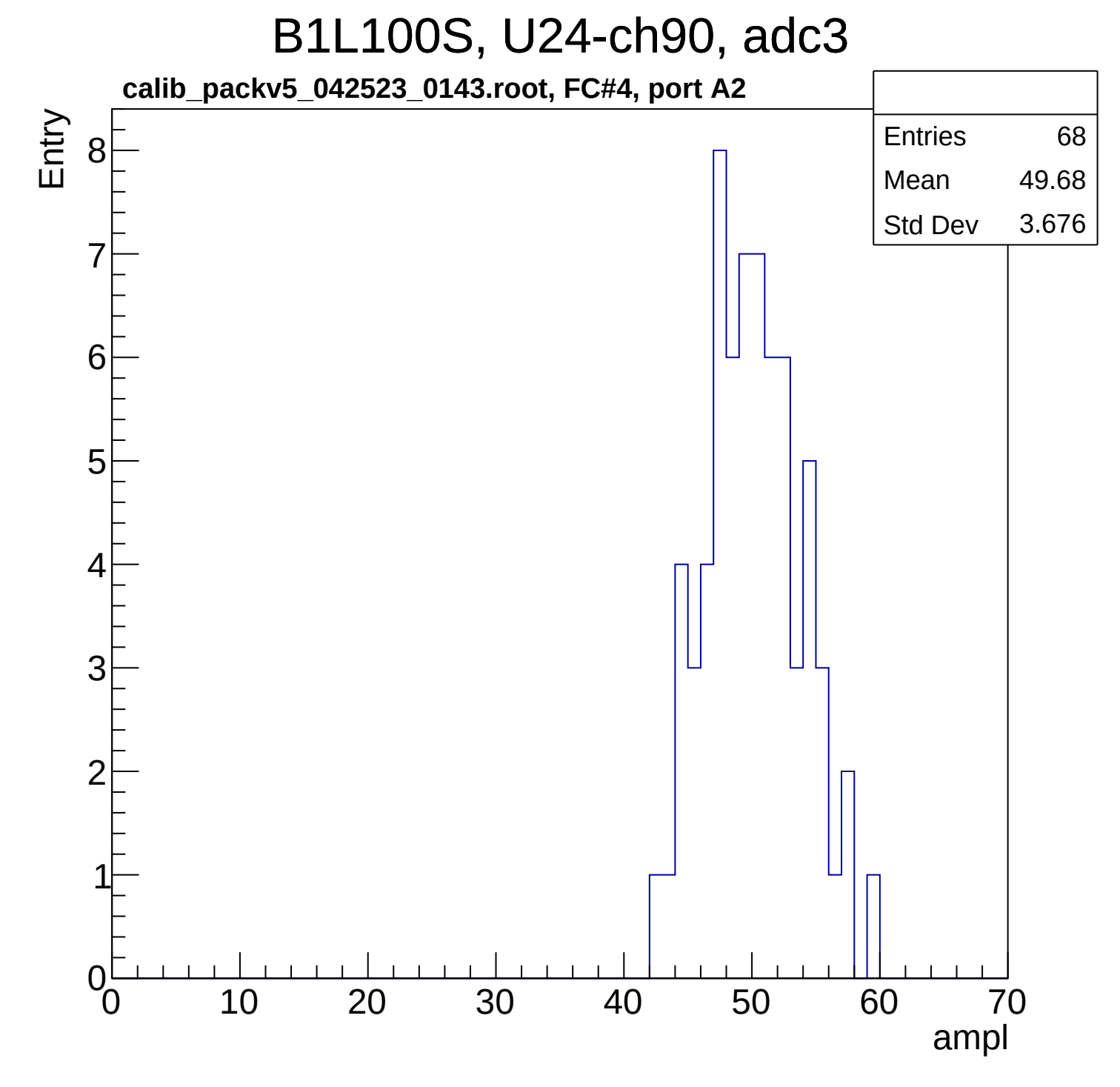
B1L100S, U24-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.68
Std Dev 3.676
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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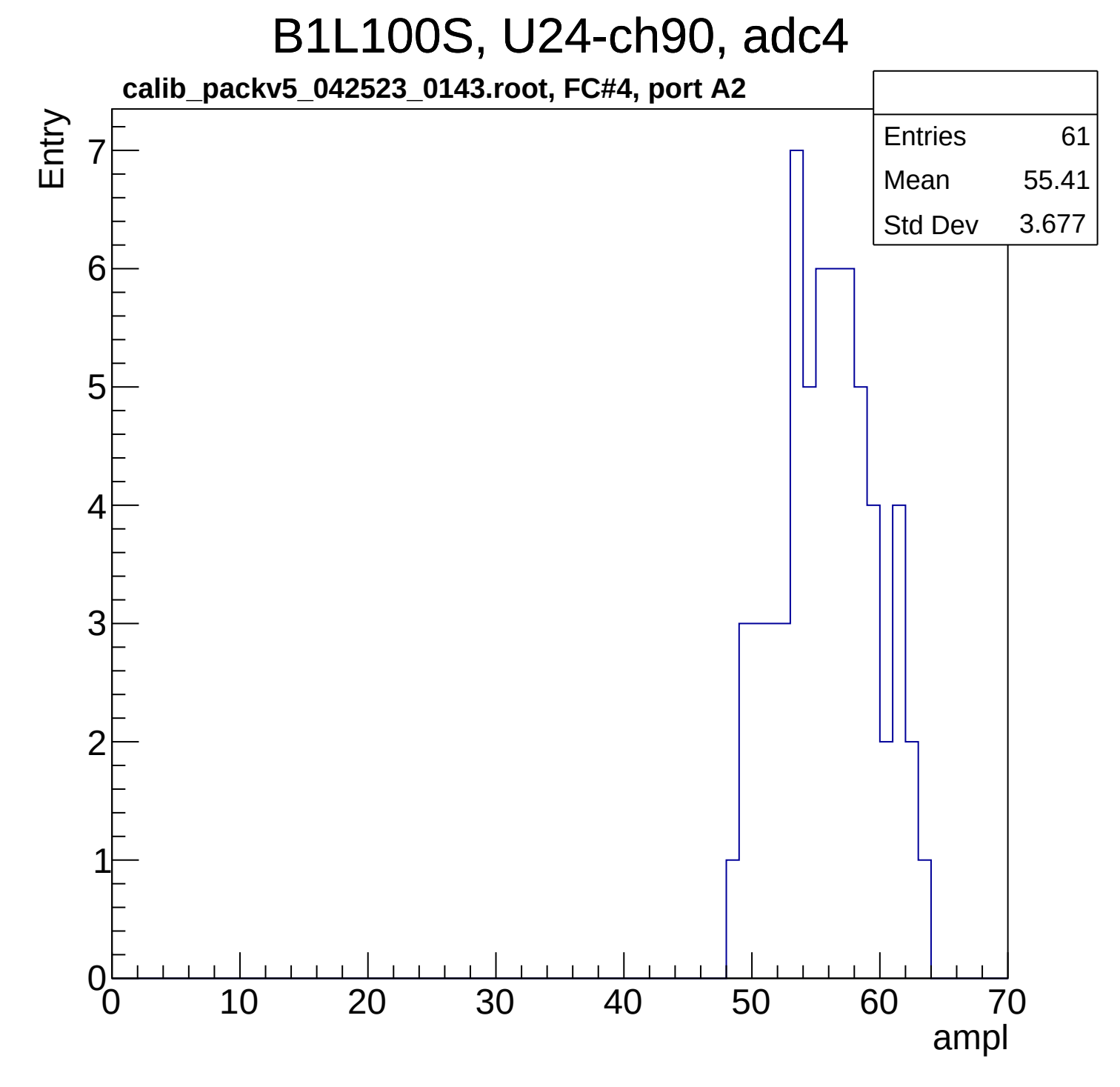
40
50
60
70


B1L100S, U24-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.41
Std Dev 3.677
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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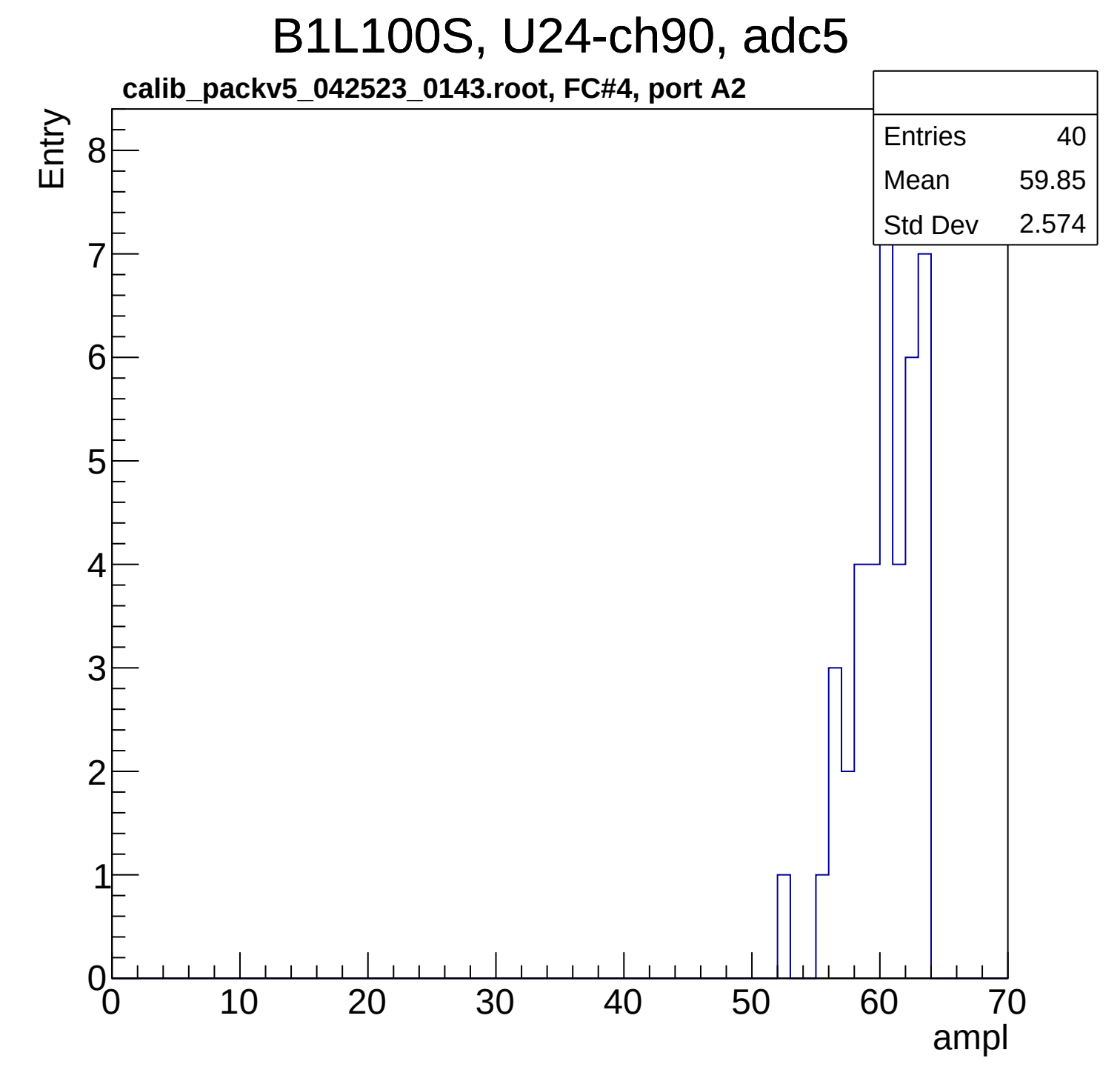
B1L100S, U24-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 59.85
Std Dev 2.574
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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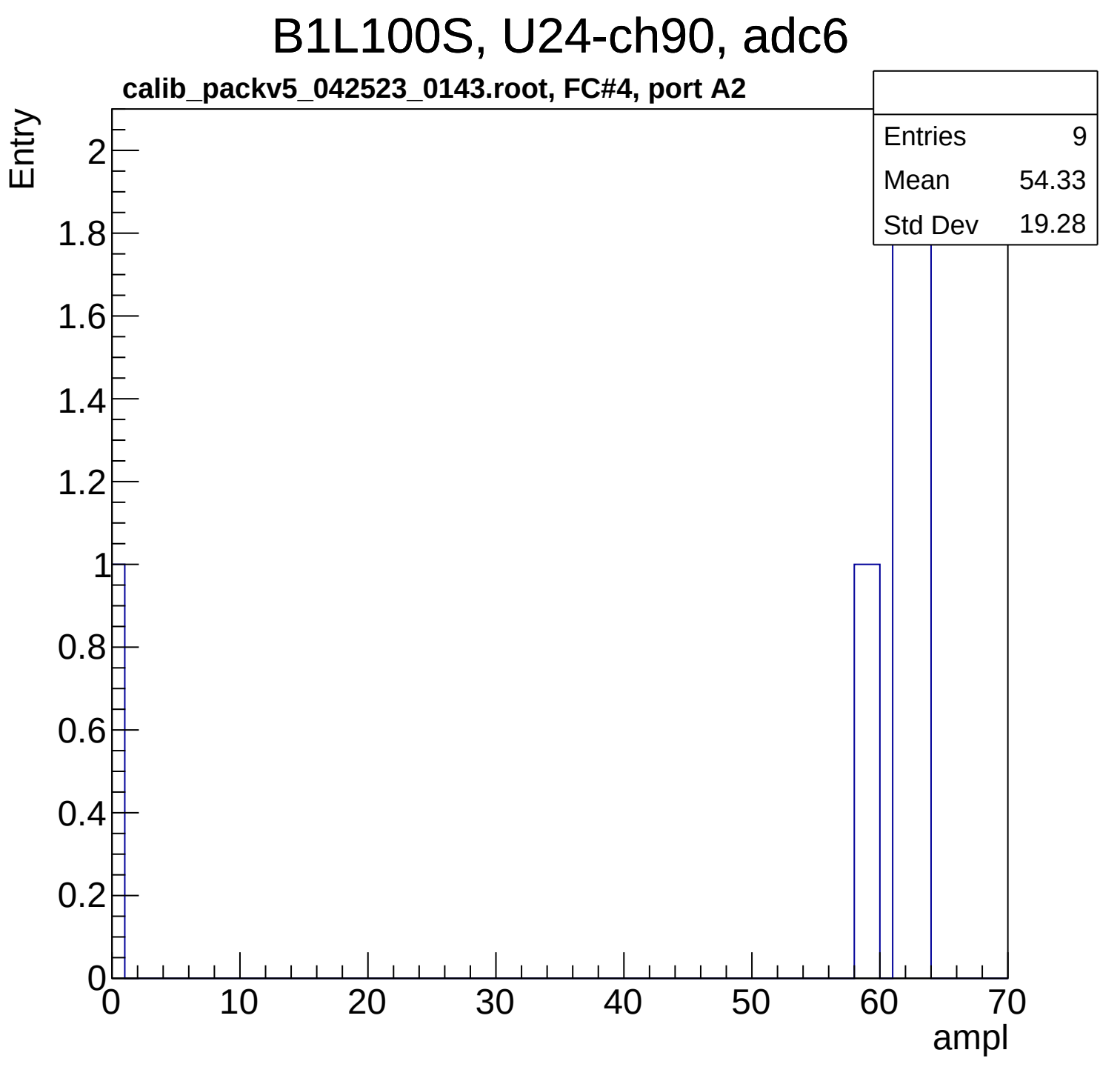
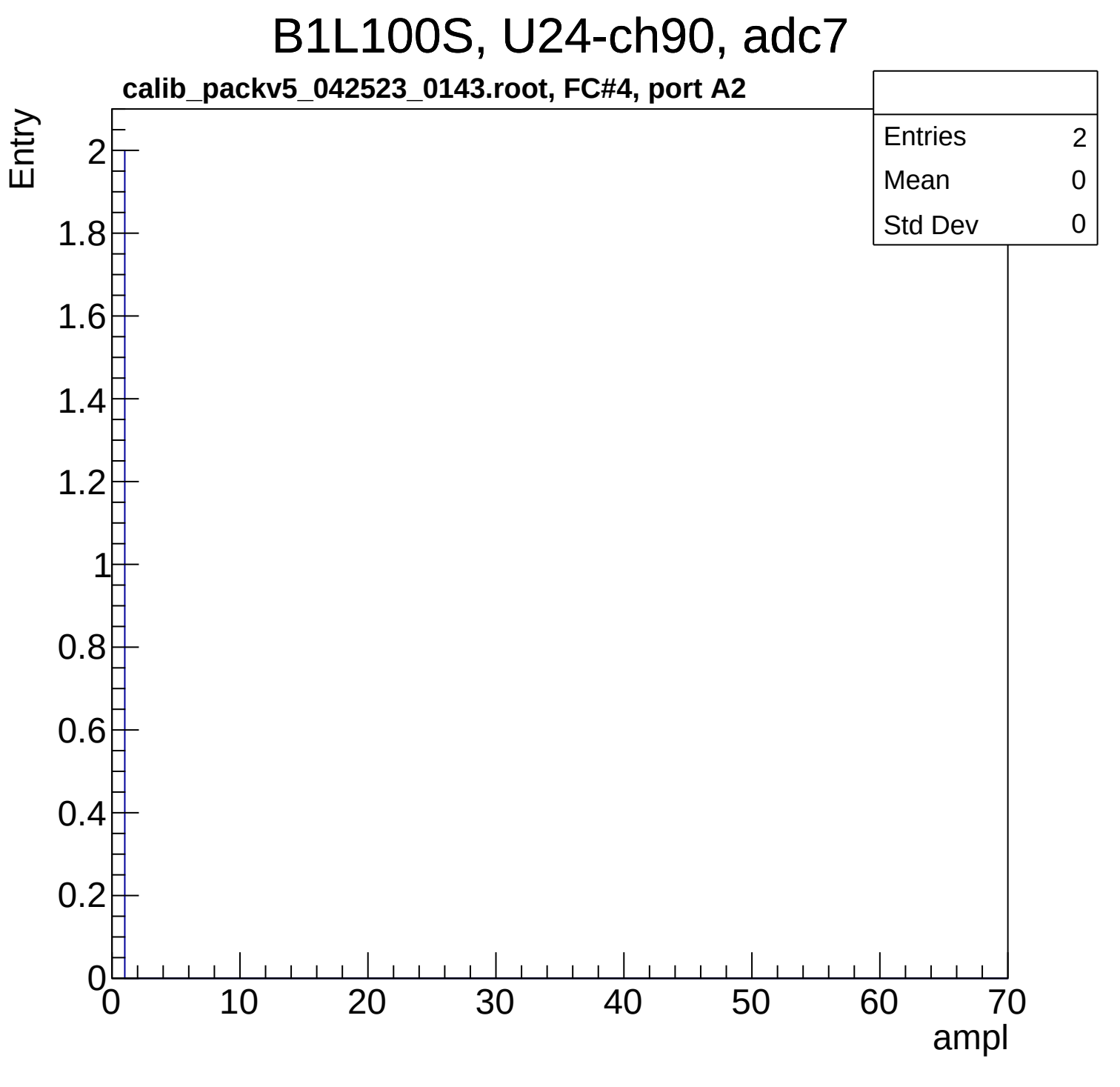
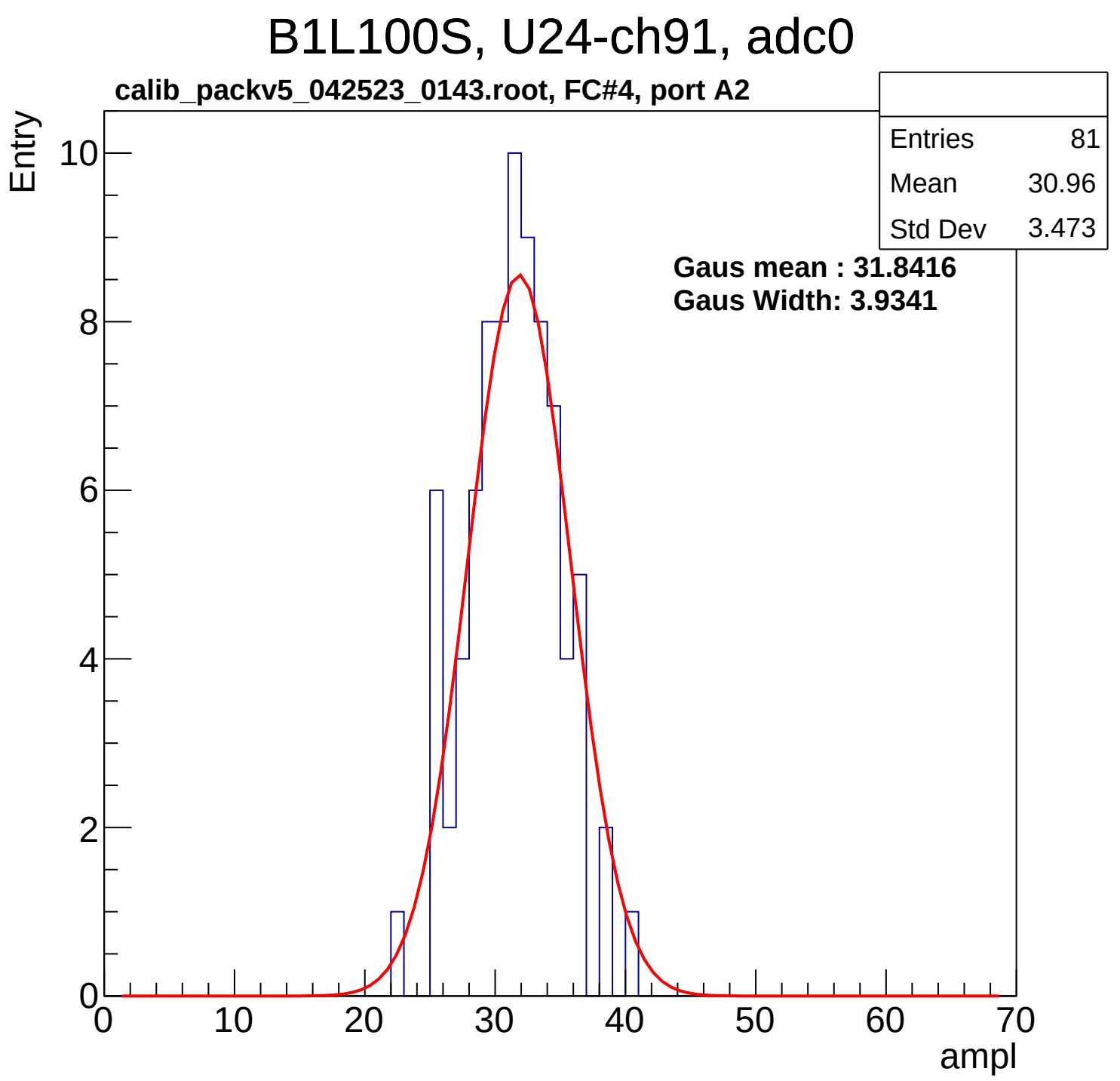

B1L100S, U24-ch90, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 54.33
Std Dev 19.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.96
Std Dev 3.473
Gaus mean : 31.8416
Gaus Width: 3.9341
Entry
ampl
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2
4
6
8
10
0
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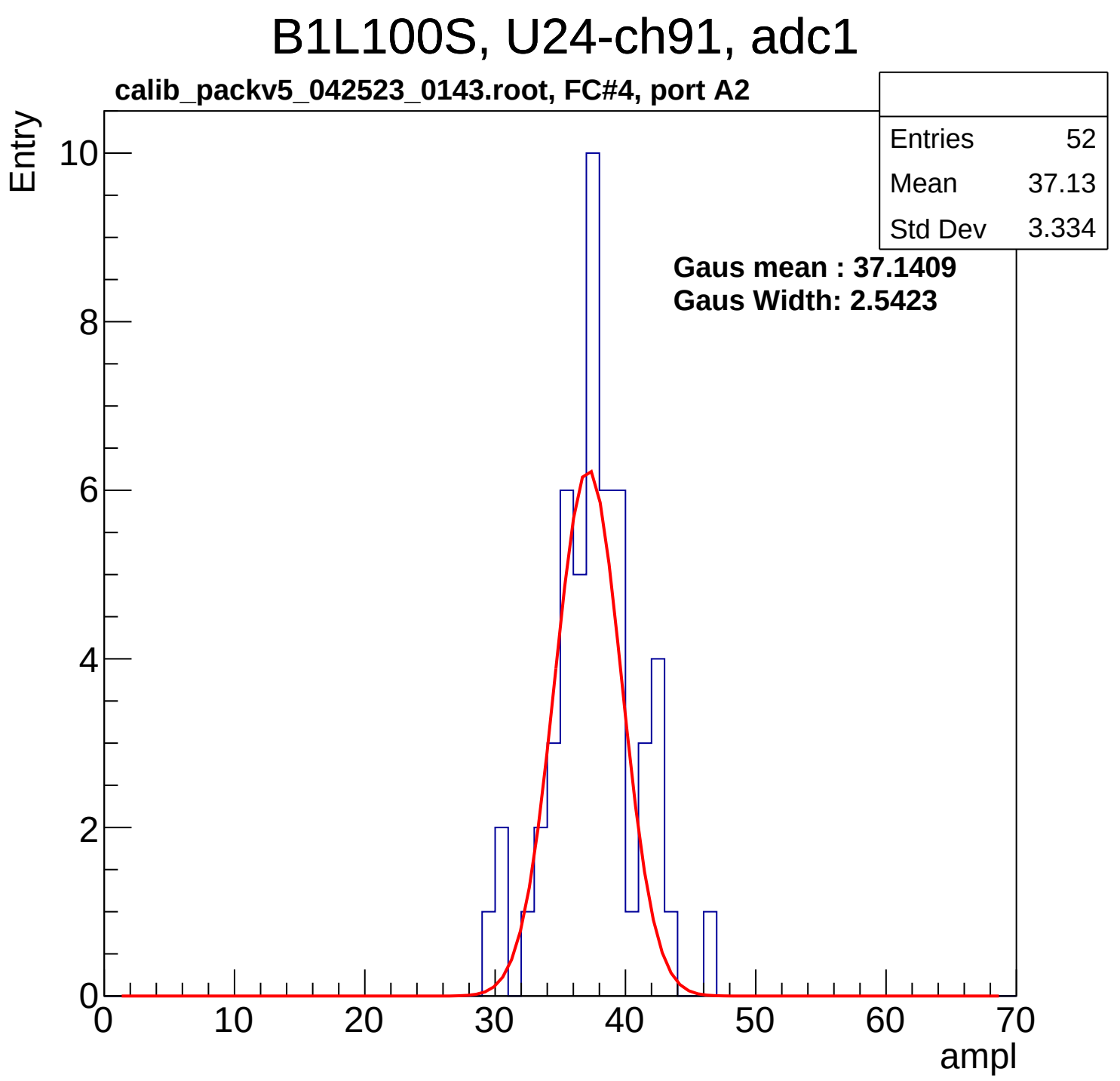
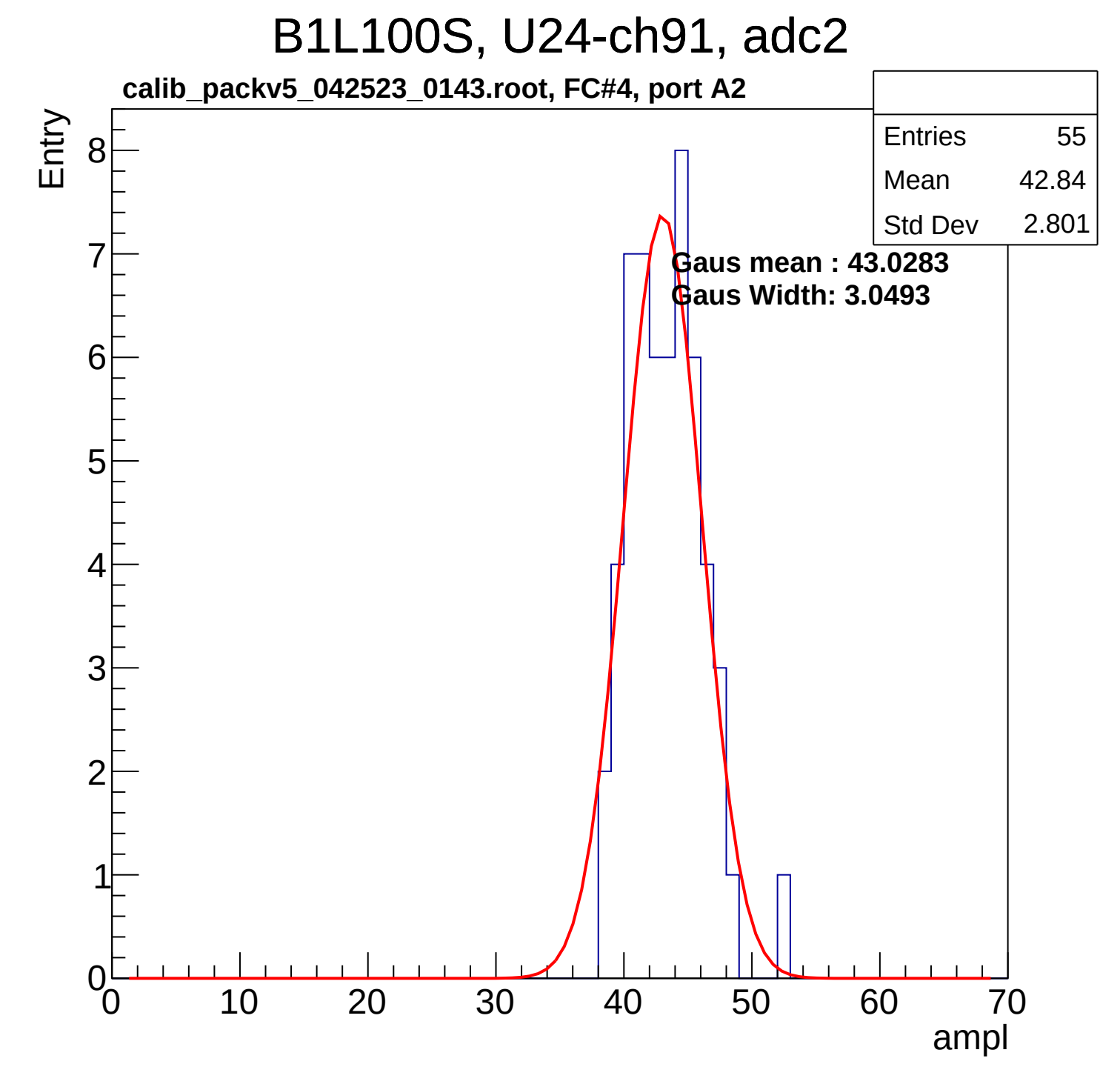
20
30
40
50
60
70


B1L100S, U24-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 37.13
Std Dev 3.334
Gaus mean : 37.1409
Gaus Width: 2.5423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 42.84
Std Dev 2.801
Gaus mean : 43.0283
Gaus Width: 3.0493
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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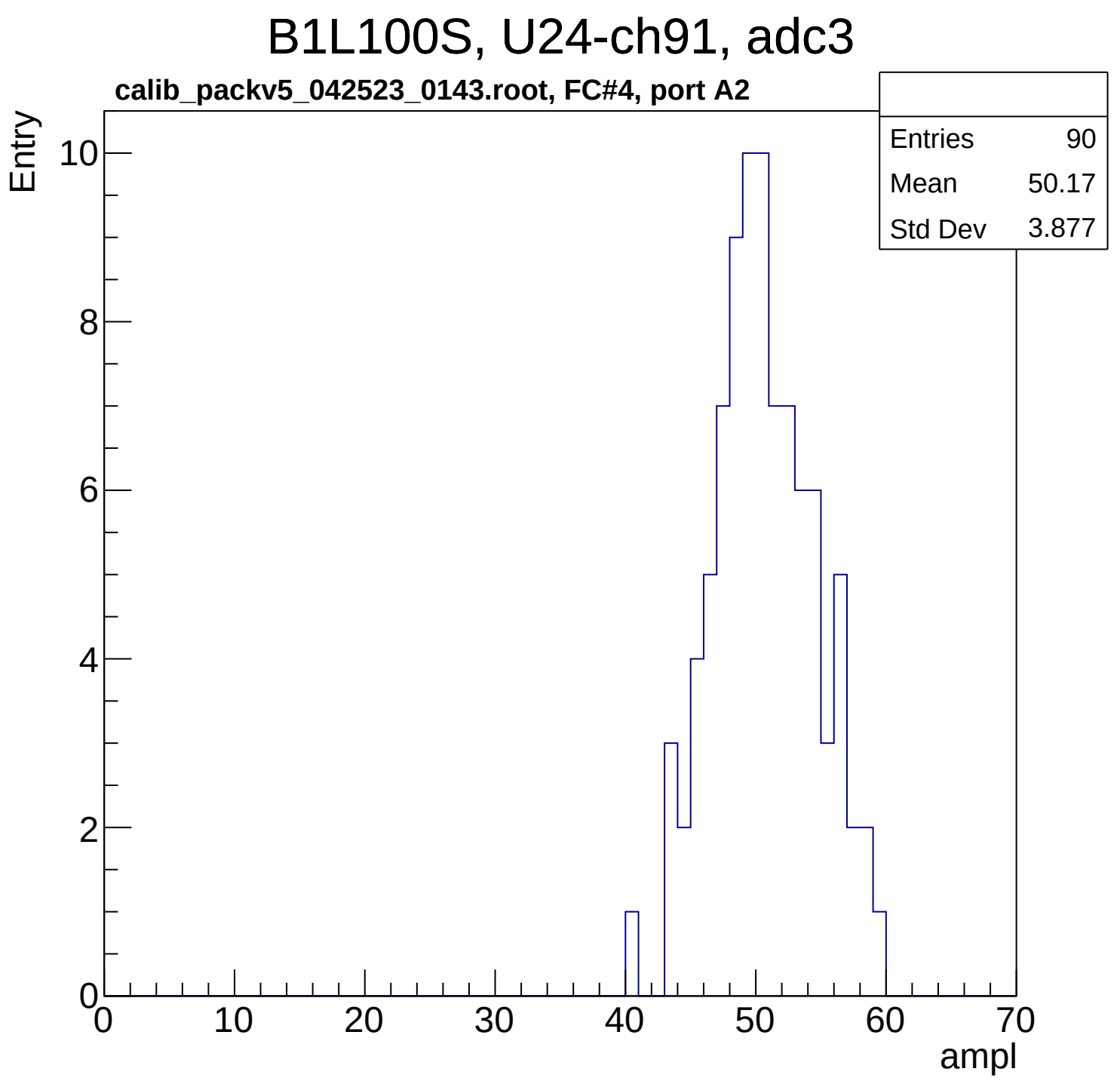
B1L100S, U24-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 50.17
Std Dev 3.877
Entry
ampl
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10
0
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40
50
60
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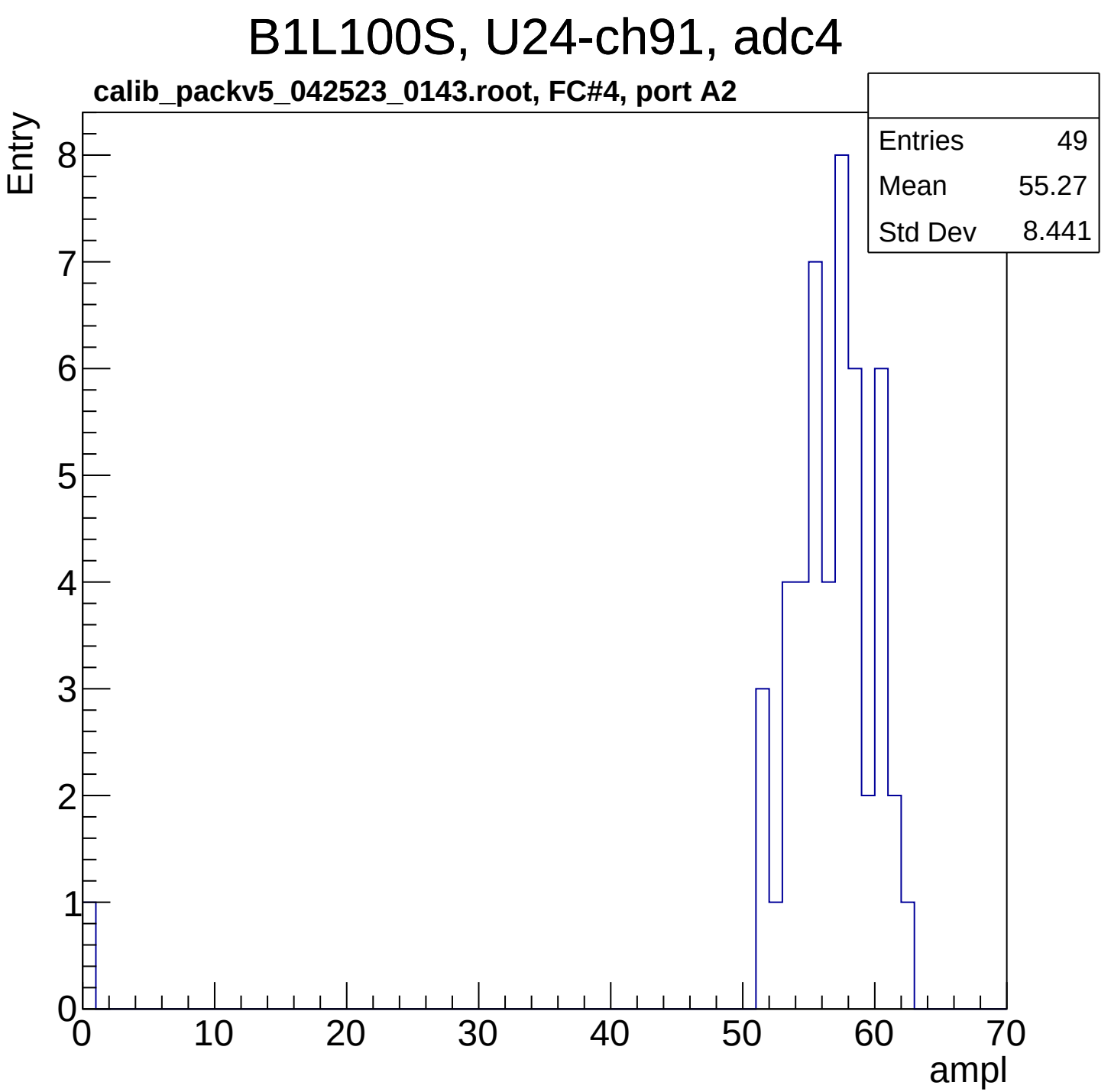
B1L100S, U24-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.27
Std Dev 8.441
Entry
ampl
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1
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3
4
5
6
7
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0
10
20
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40
50
60
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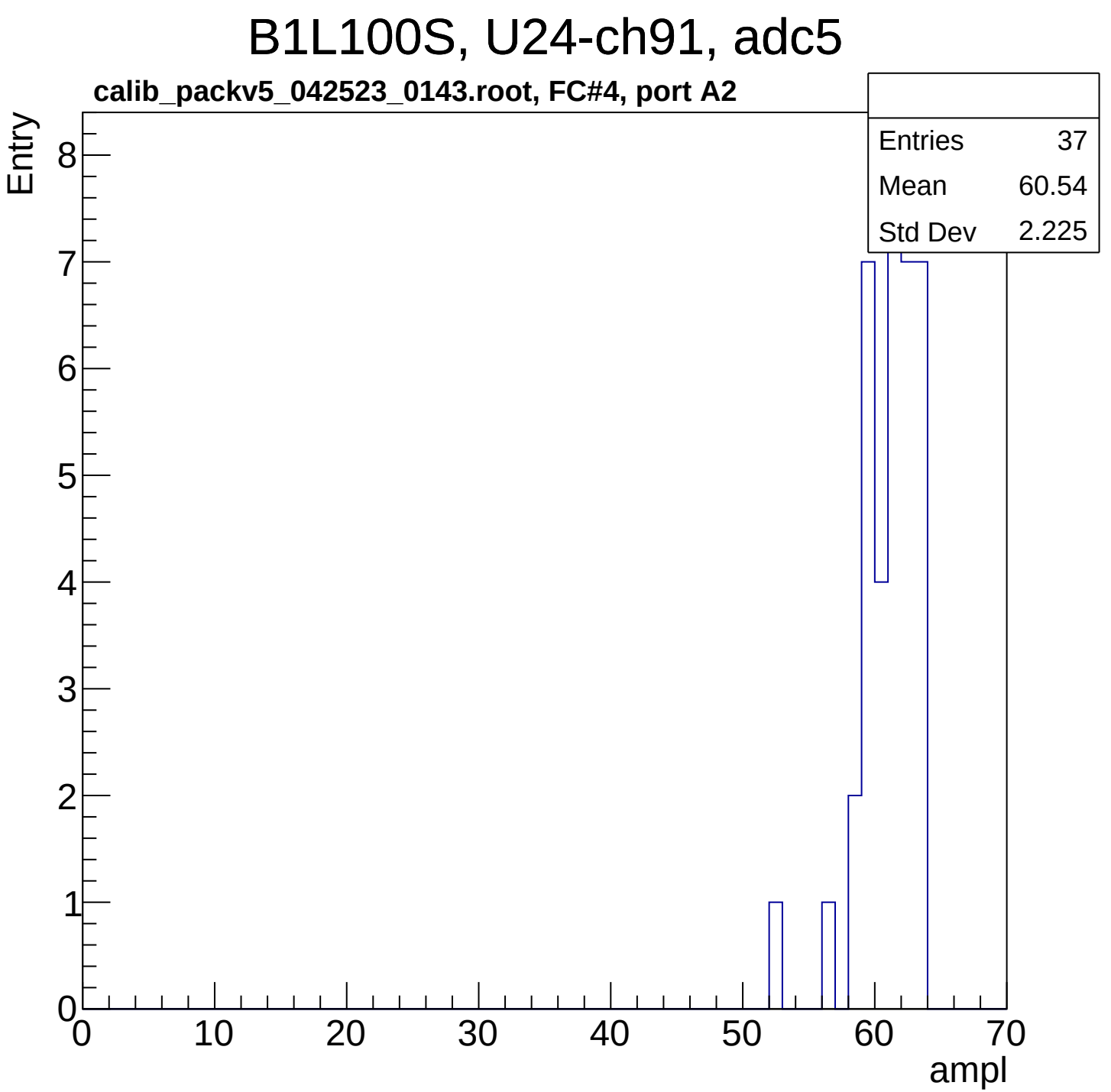
B1L100S, U24-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.54
Std Dev 2.225
Entry
ampl
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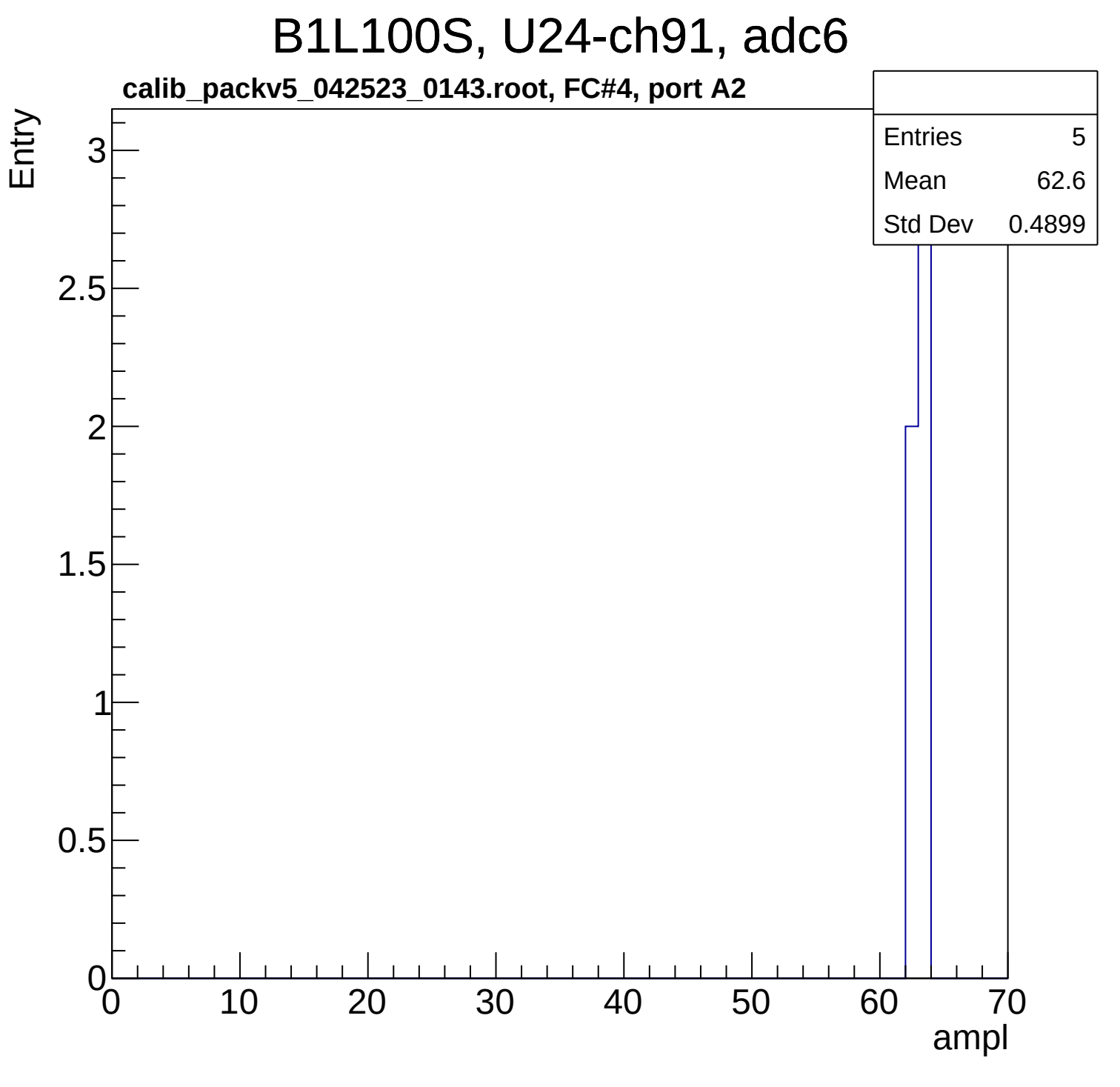
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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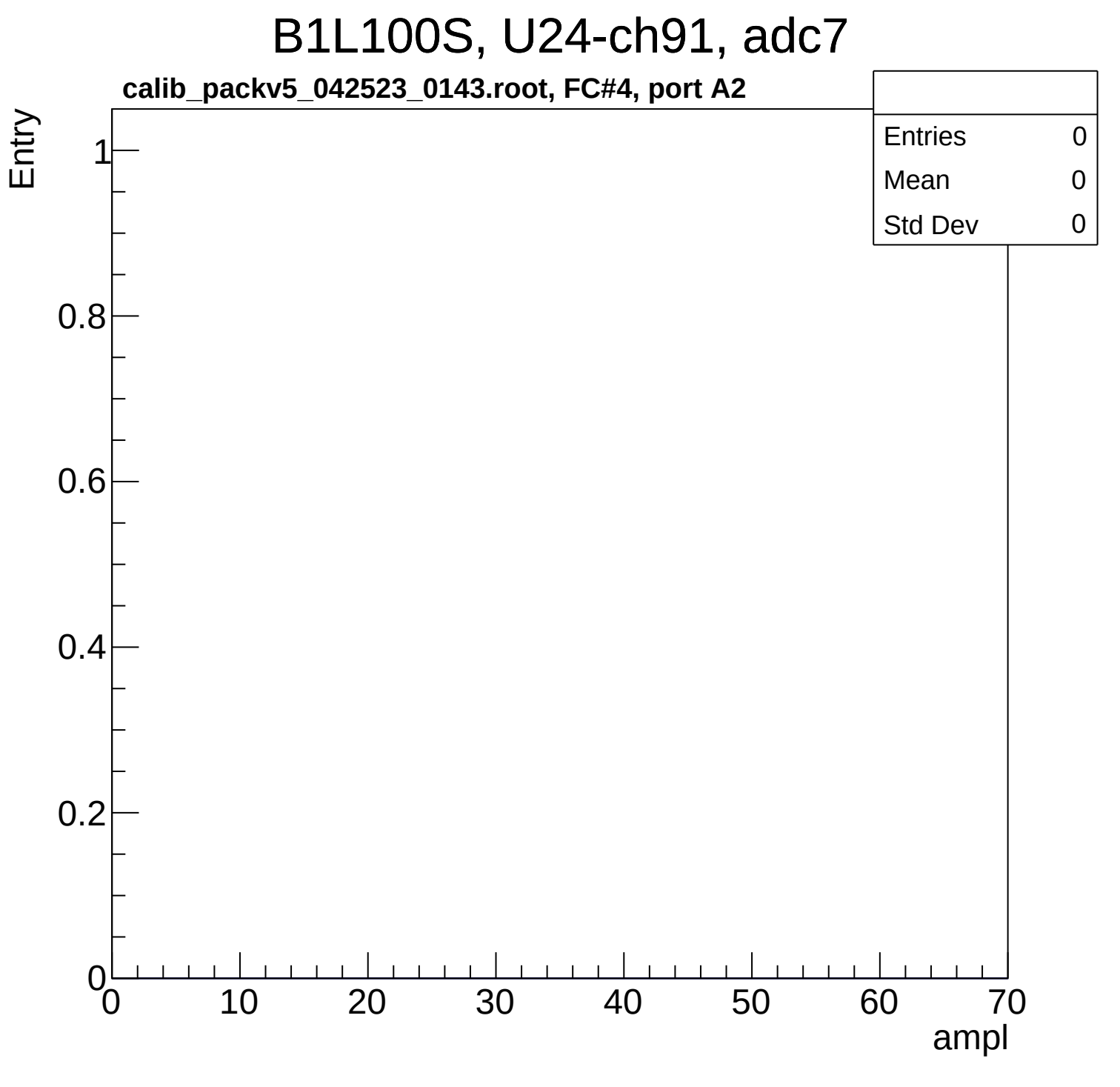
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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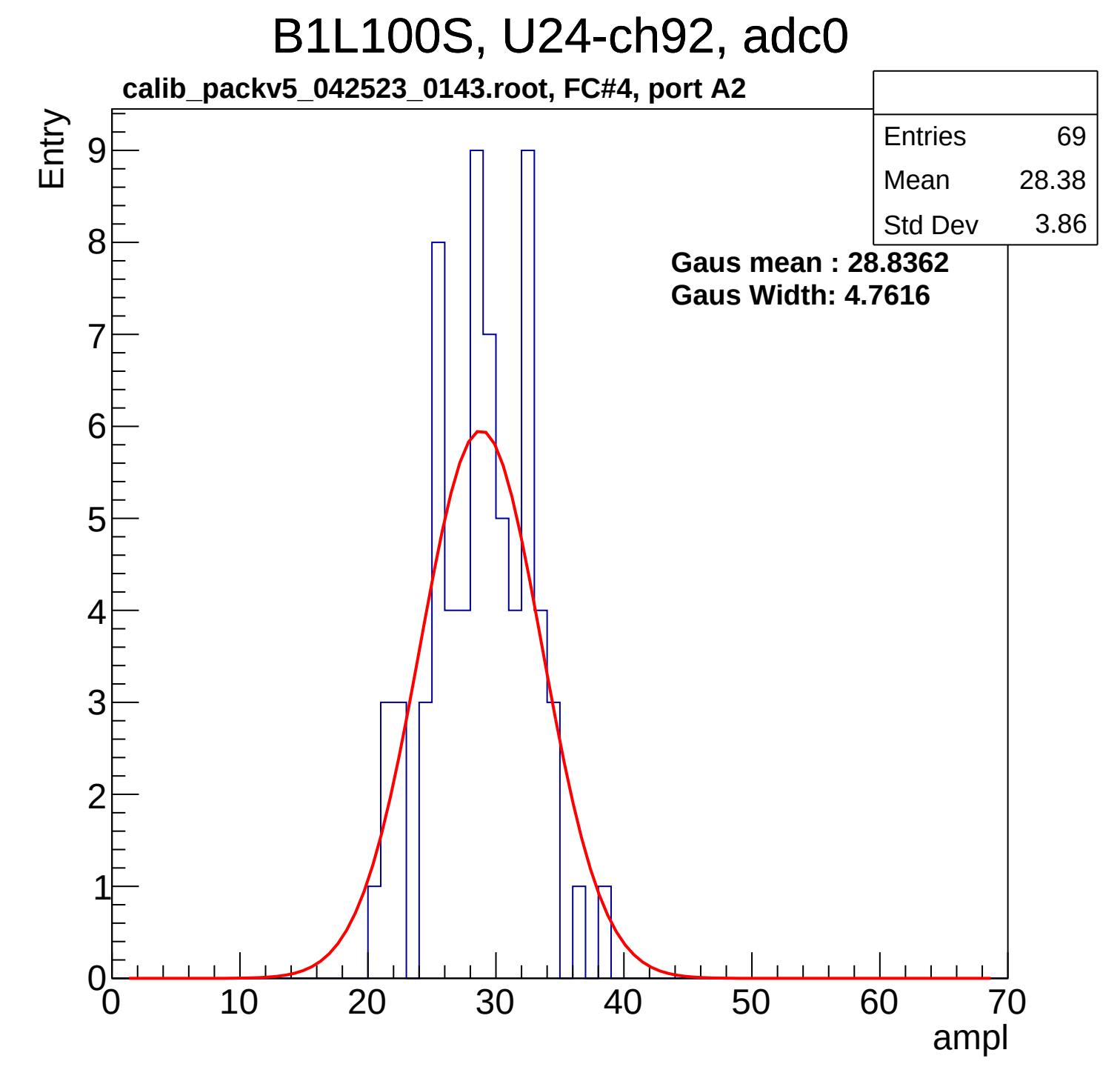
50
60
70
ampl


B1L100S, U24-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.38
Std Dev 3.86
Gaus mean : 28.8362
Gaus Width: 4.7616
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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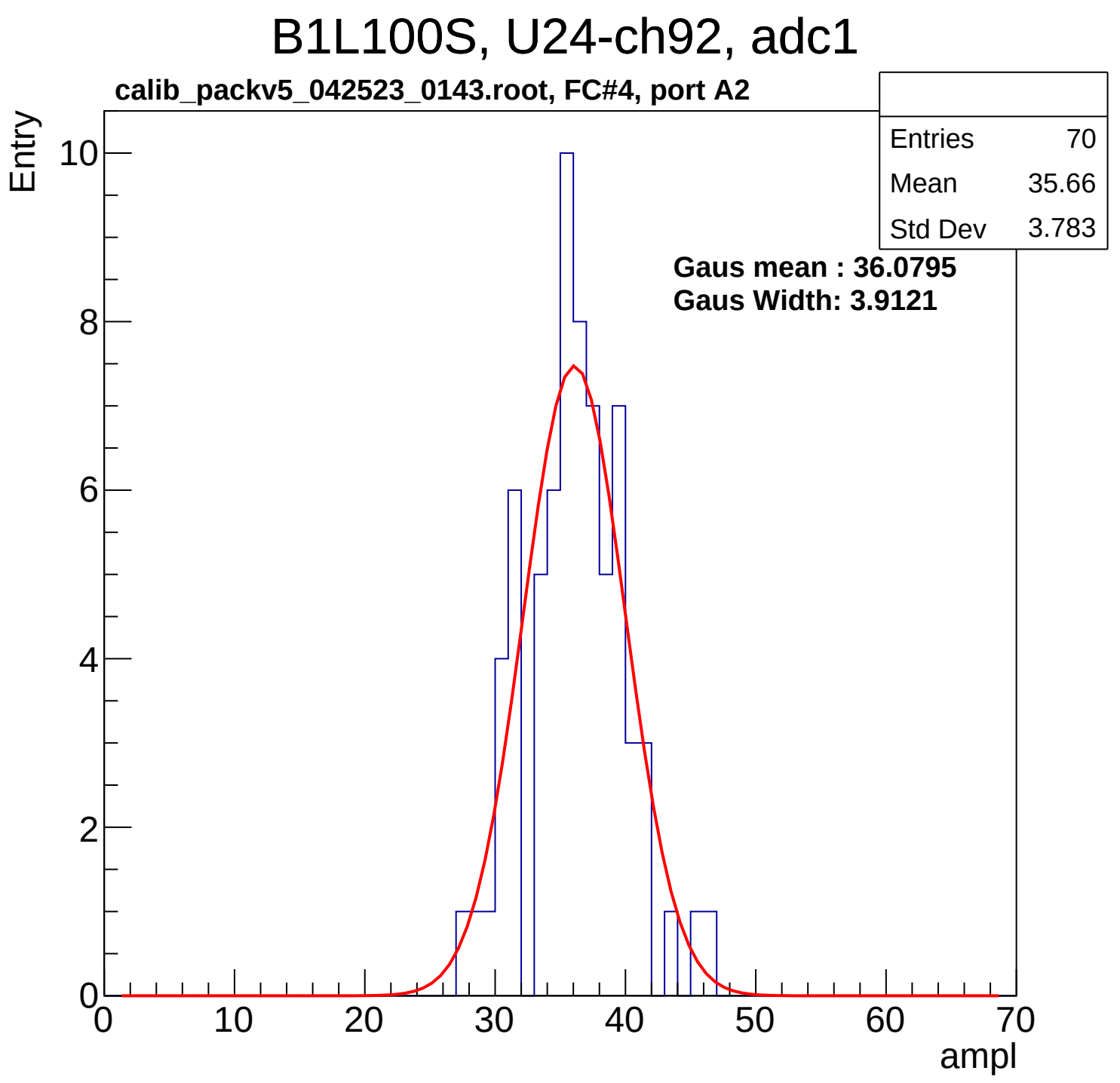
B1L100S, U24-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.66
Std Dev 3.783
Gaus mean : 36.0795
Gaus Width: 3.9121
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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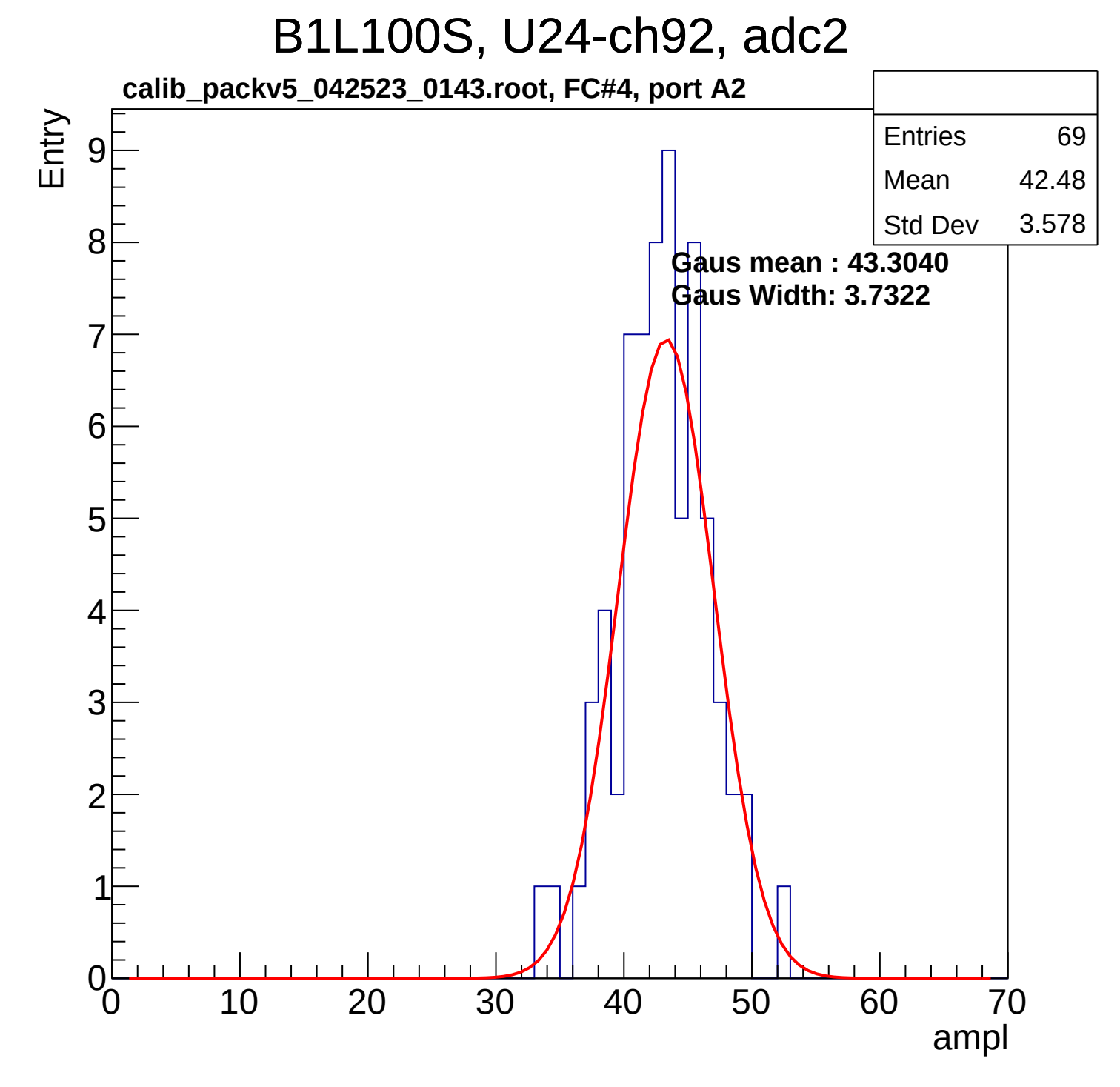
B1L100S, U24-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.48
Std Dev 3.578
Gaus mean : 43.3040
Gaus Width: 3.7322
Entry
ampl
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3
4
5
6
7
8
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0
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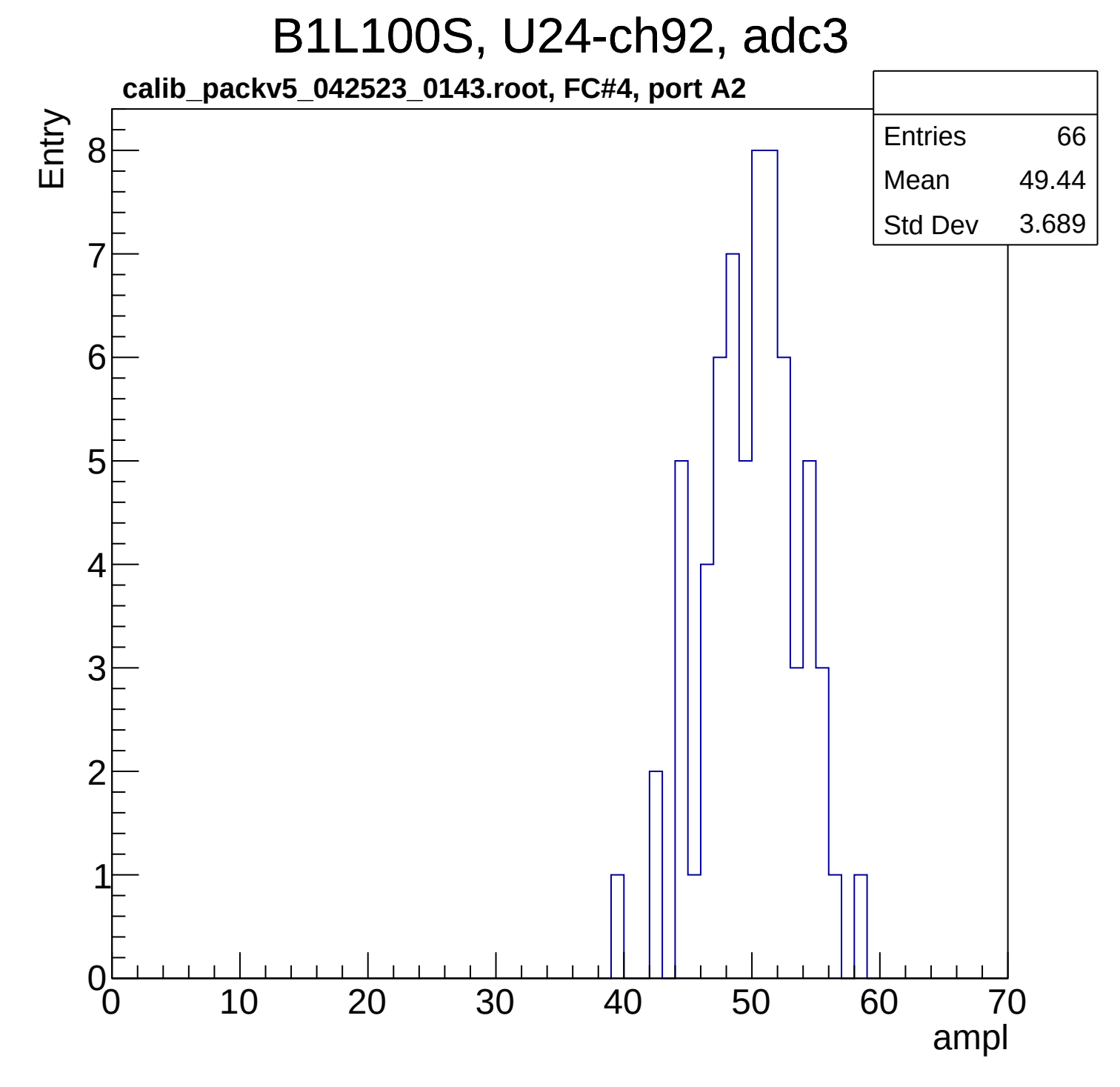
B1L100S, U24-ch92, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.44
Std Dev 3.689
Entry
ampl
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0
10
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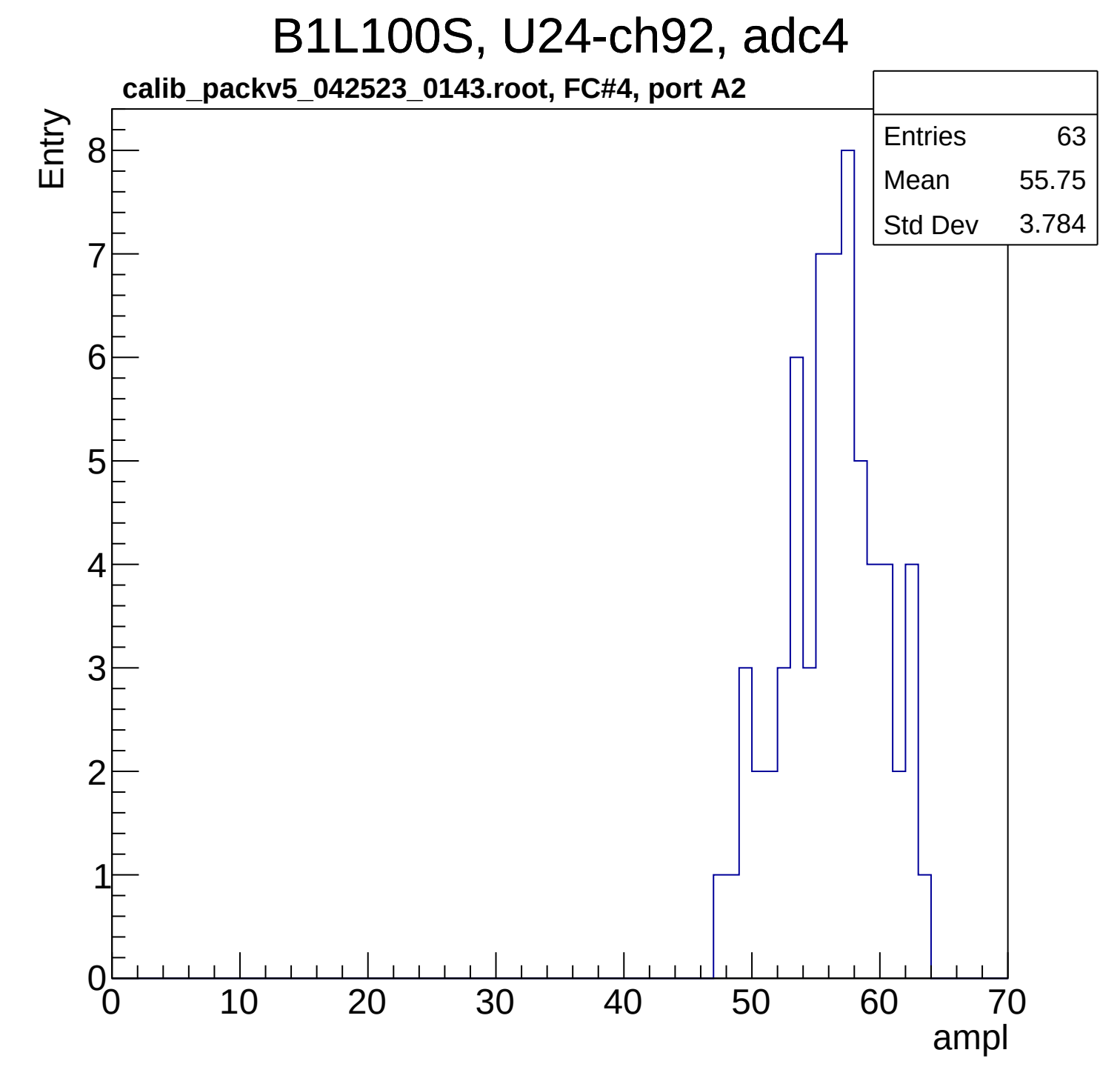
B1L100S, U24-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.75
Std Dev 3.784
Entry
ampl
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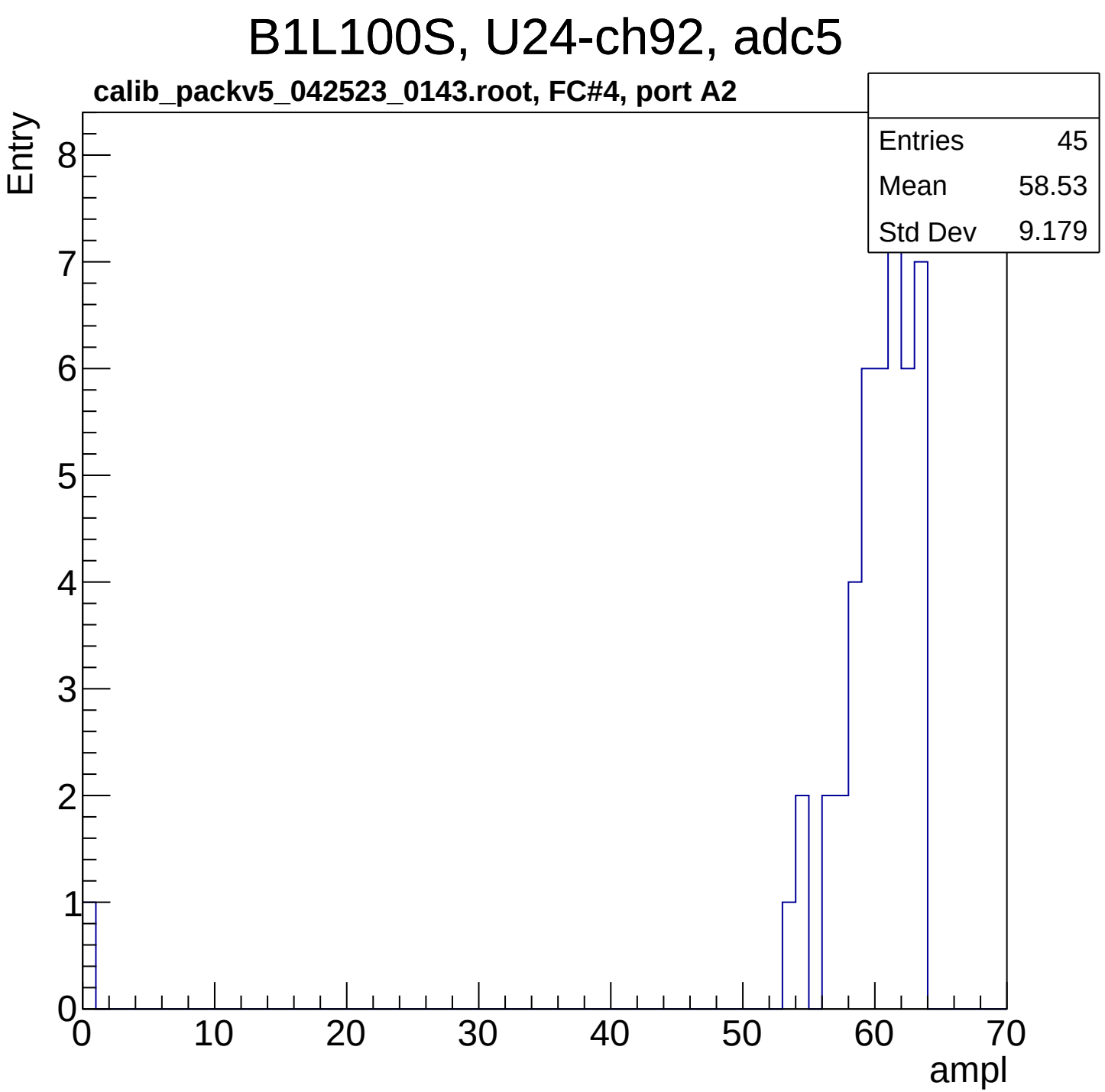
B1L100S, U24-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.53
Std Dev 9.179
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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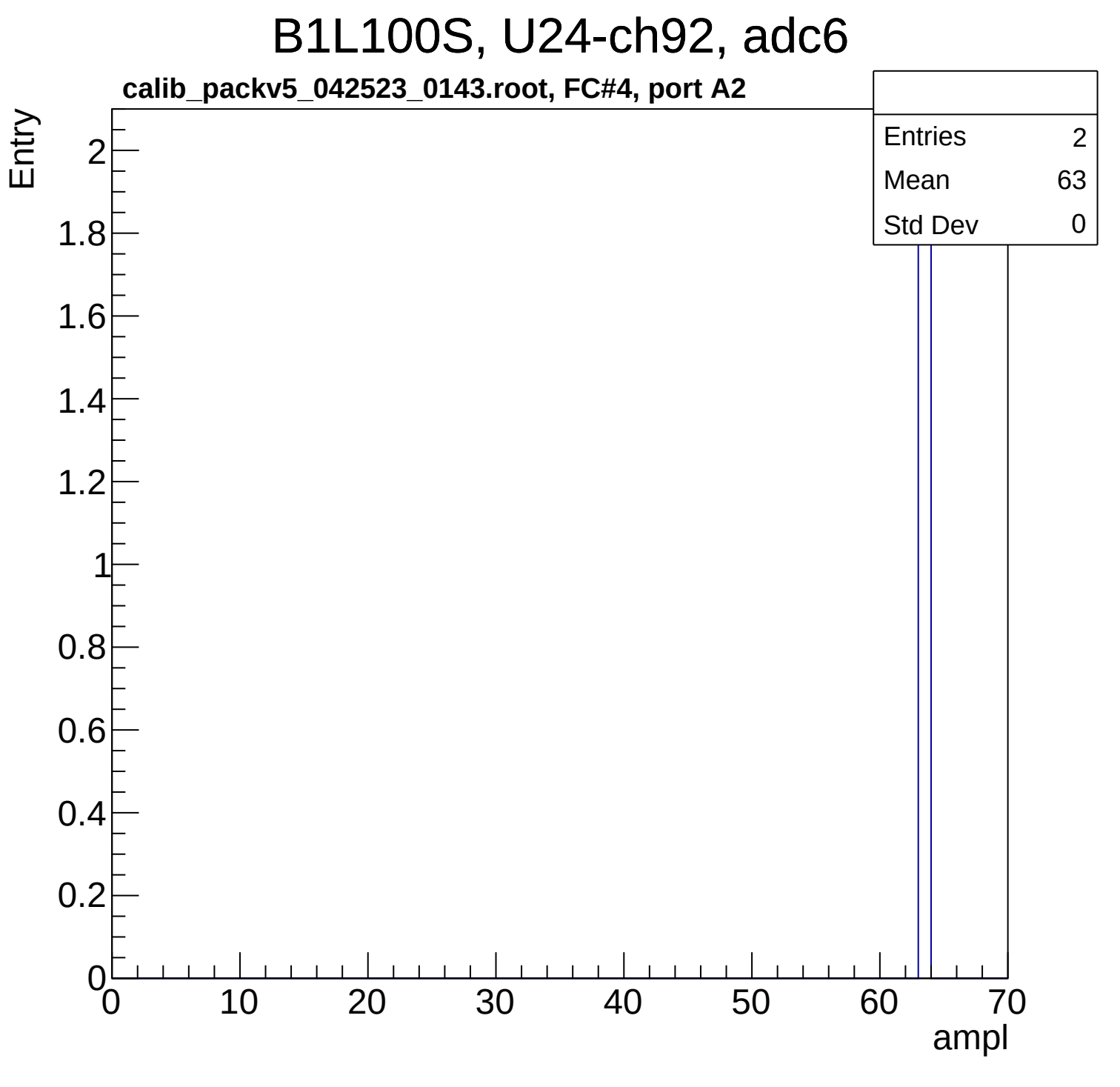
B1L100S, U24-ch92, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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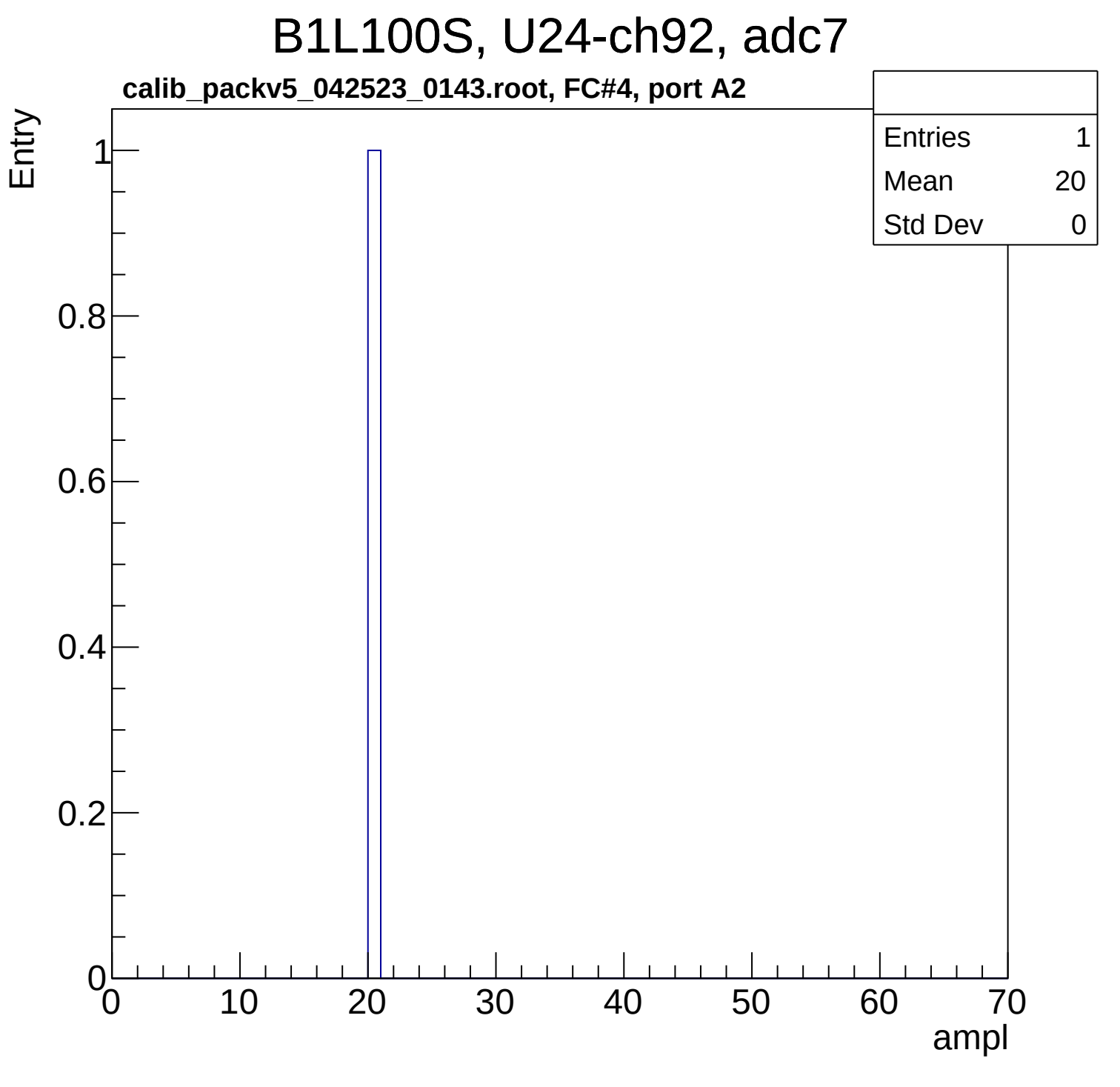
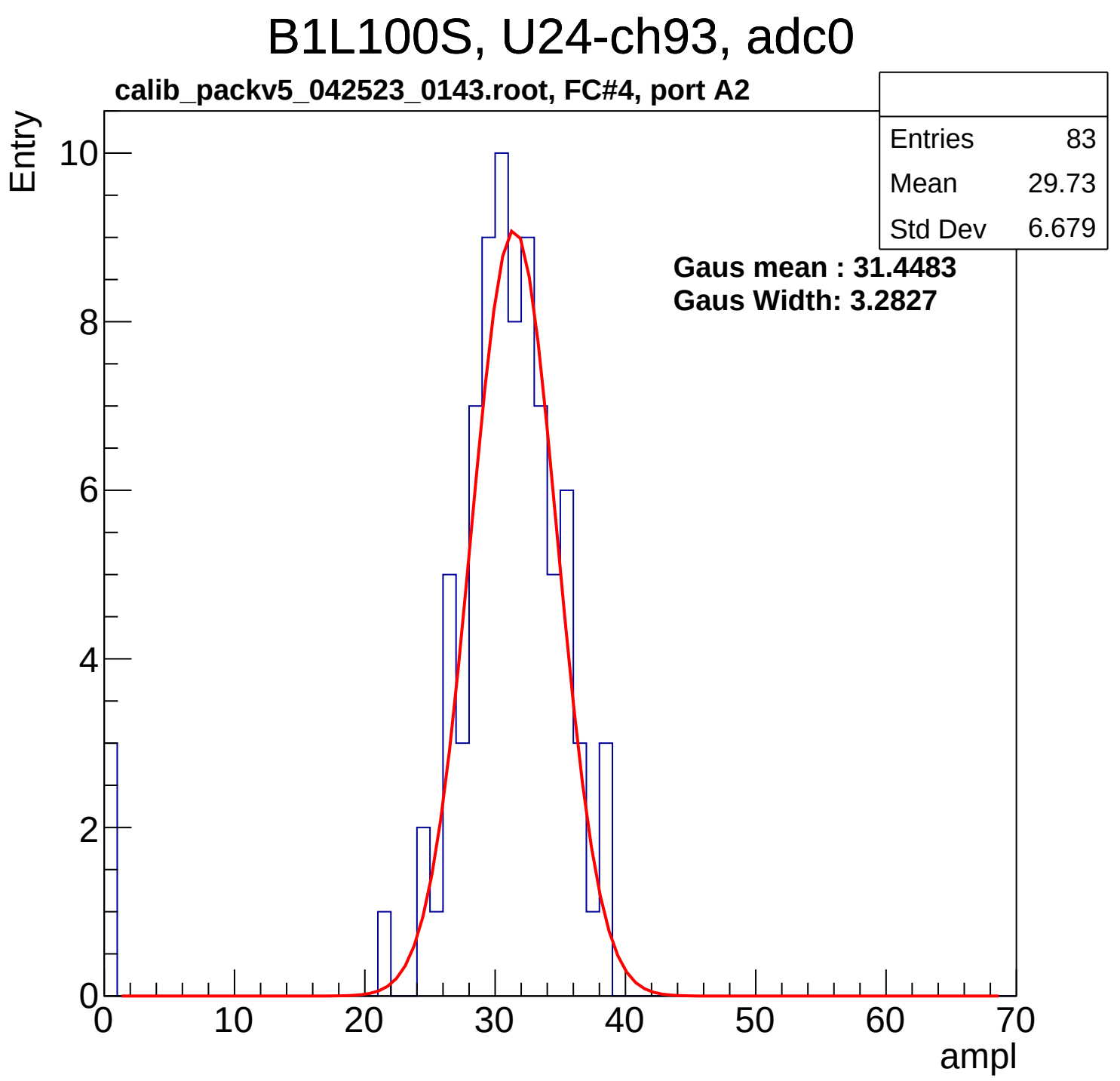

B1L100S, U24-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch93, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 29.73
Std Dev 6.679
Gaus mean : 31.4483
Gaus Width: 3.2827
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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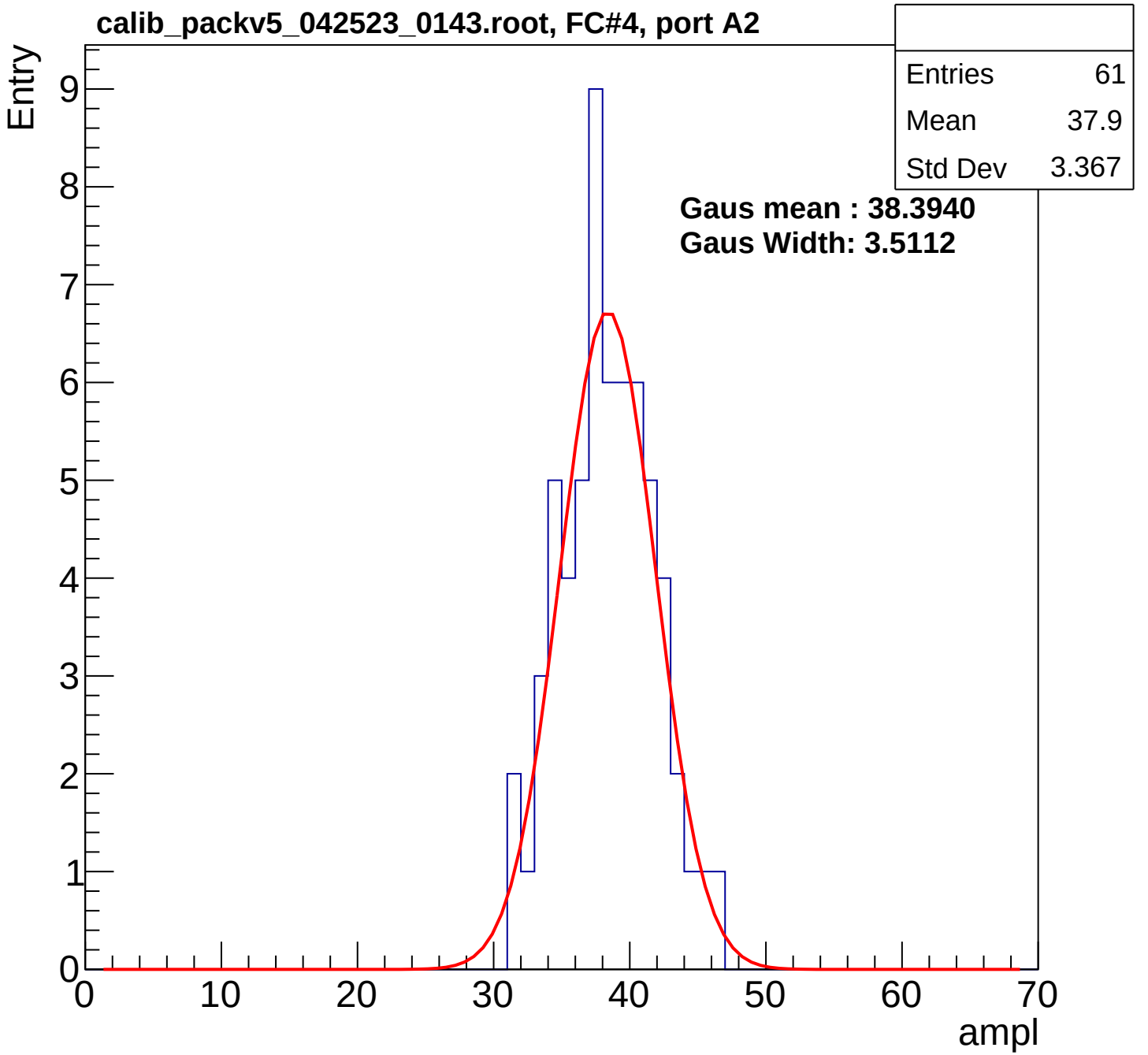
B1L100S, U24-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.9
Std Dev 3.367
Gaus mean : 38.3940
Gaus Width: 3.5112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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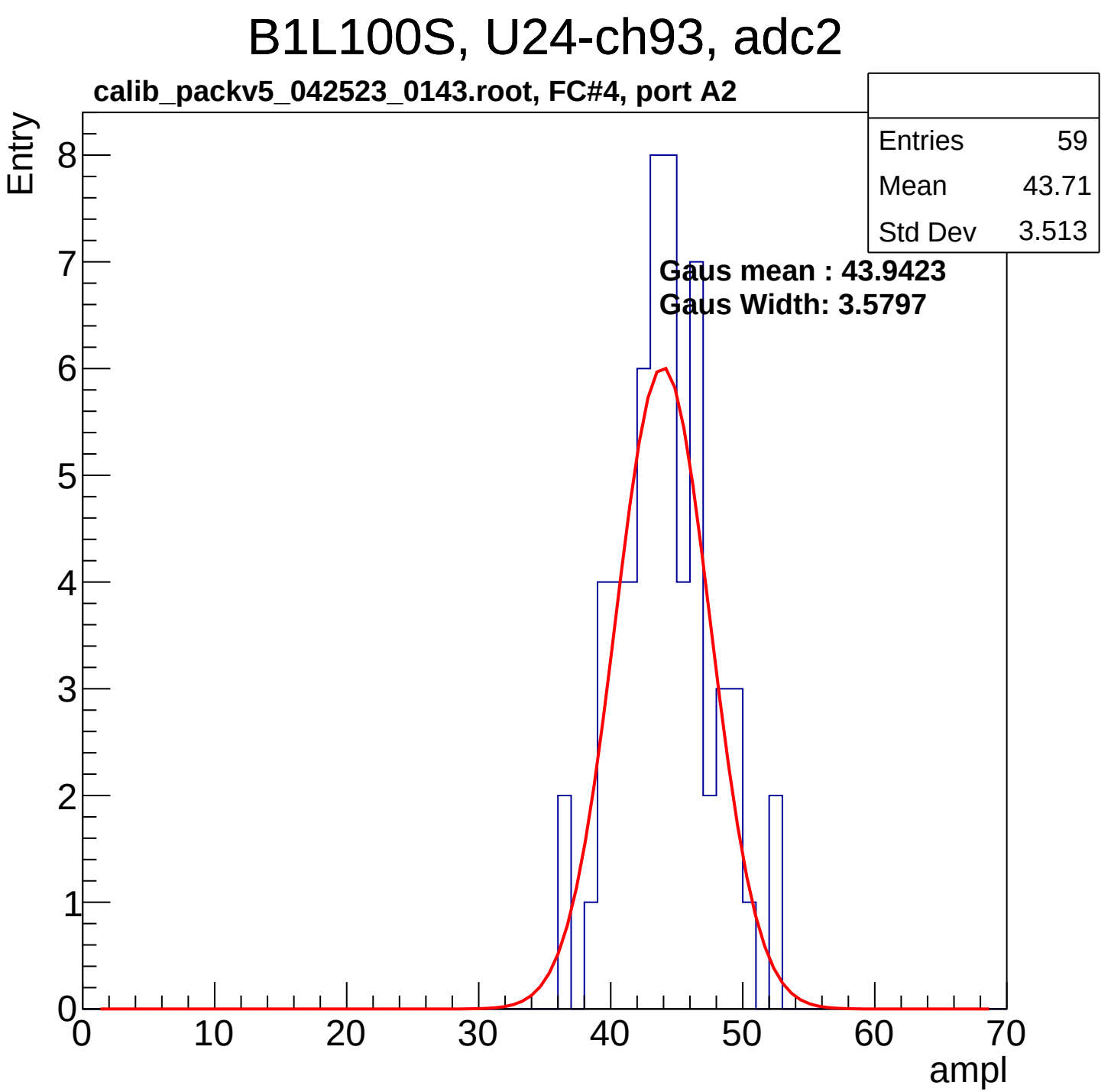
B1L100S, U24-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.71
Std Dev 3.513
Gaus mean : 43.9423
Gaus Width: 3.5797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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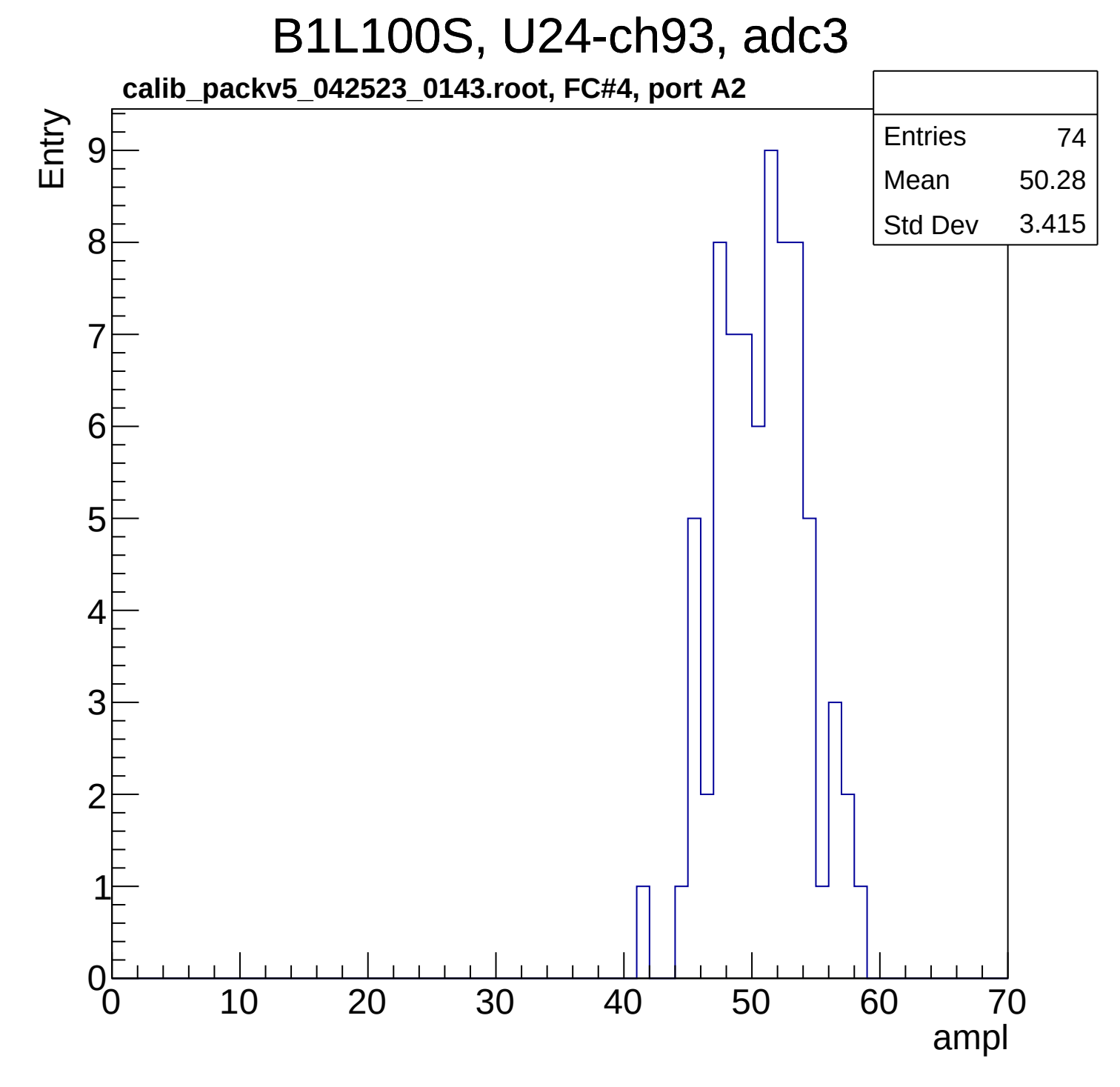
B1L100S, U24-ch93, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.28
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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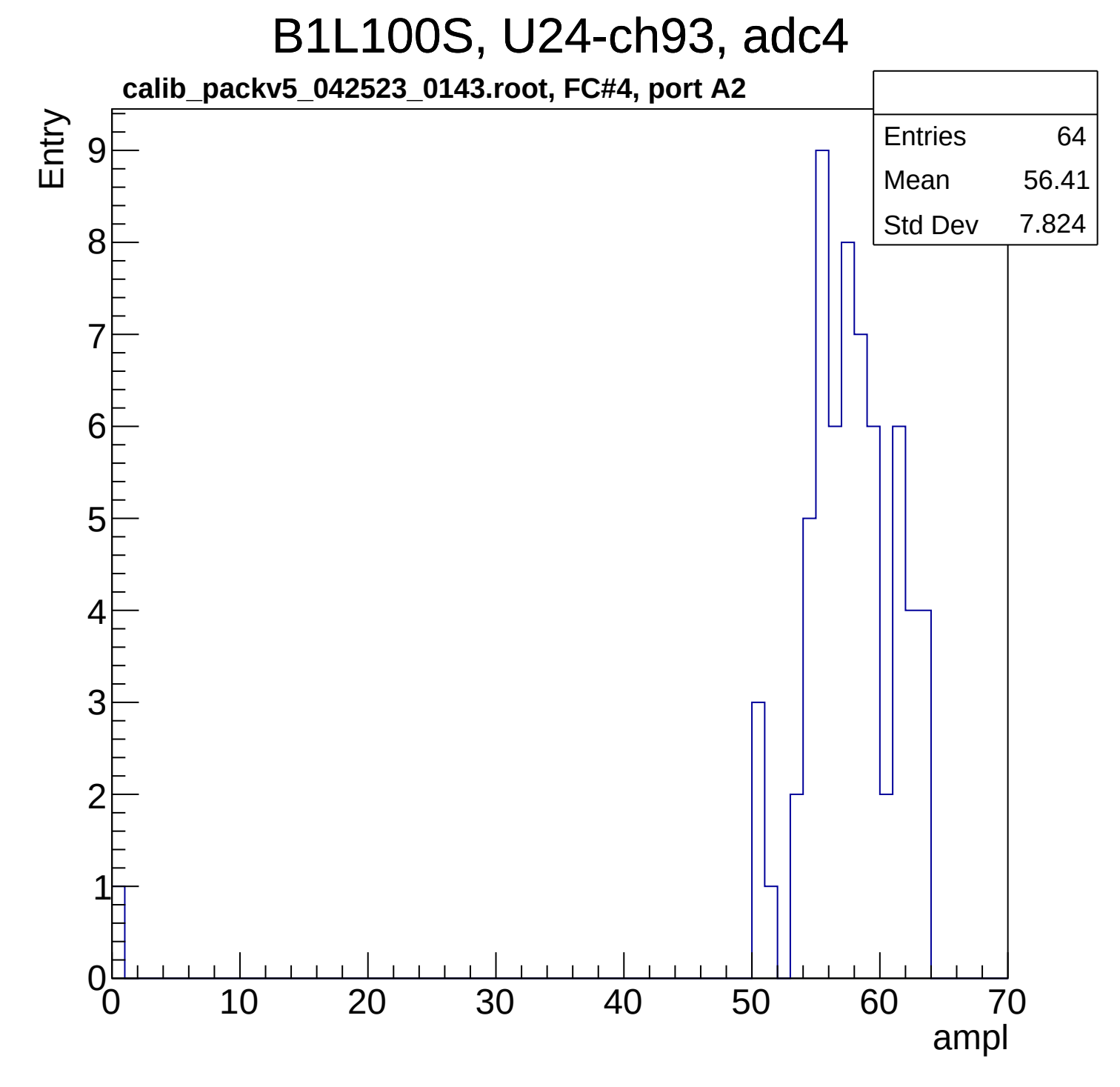
B1L100S, U24-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 56.41
Std Dev 7.824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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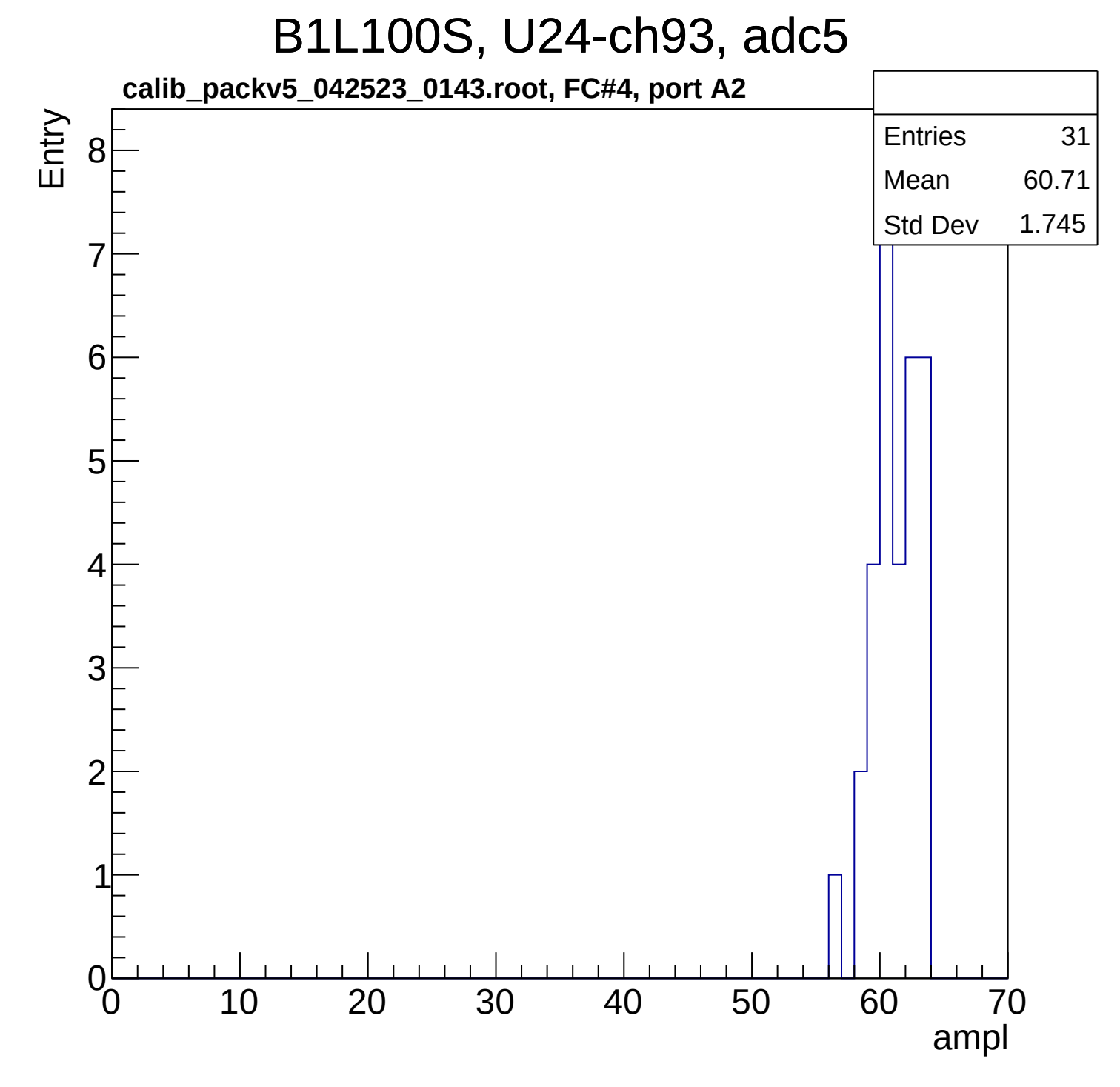
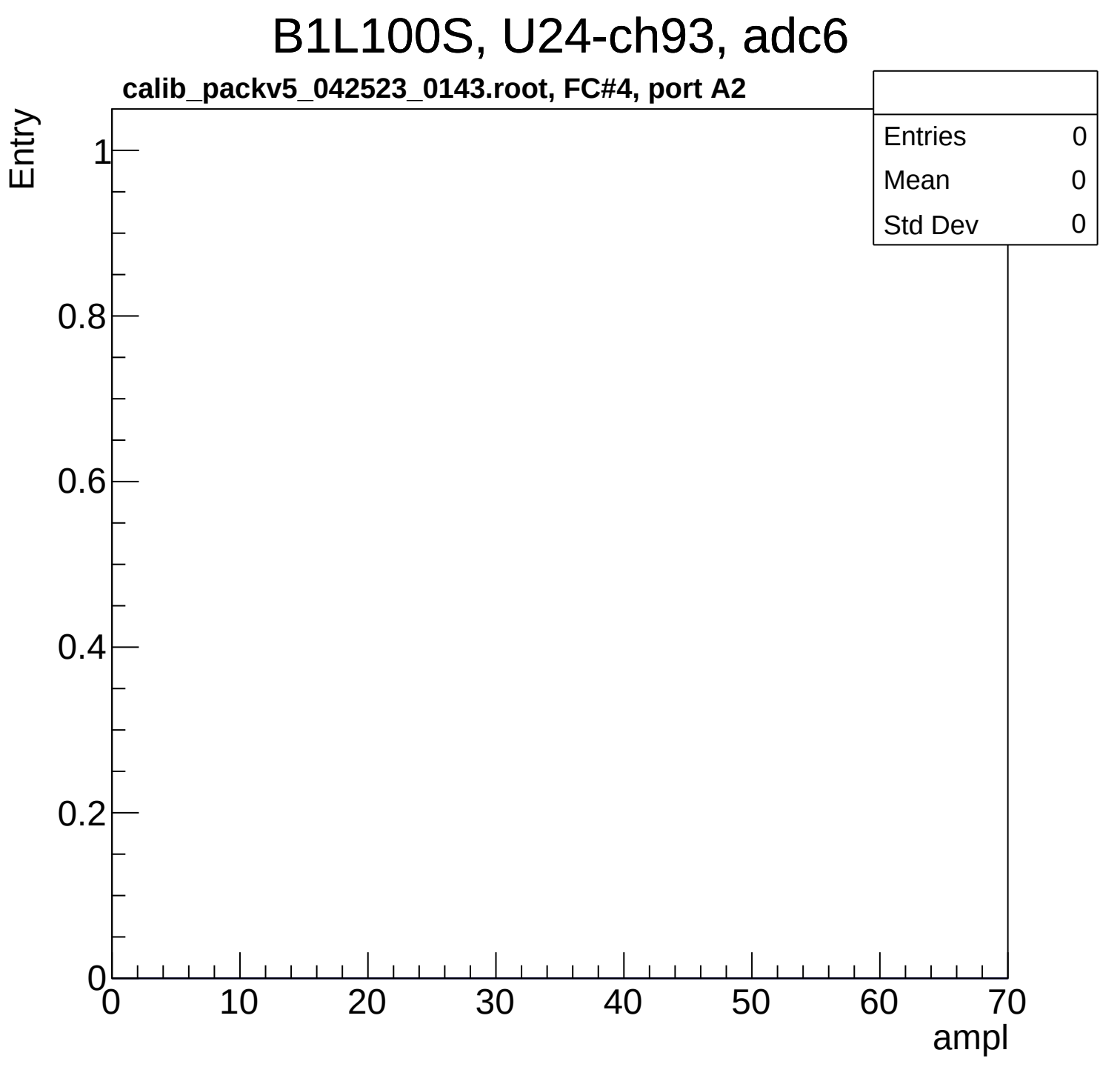
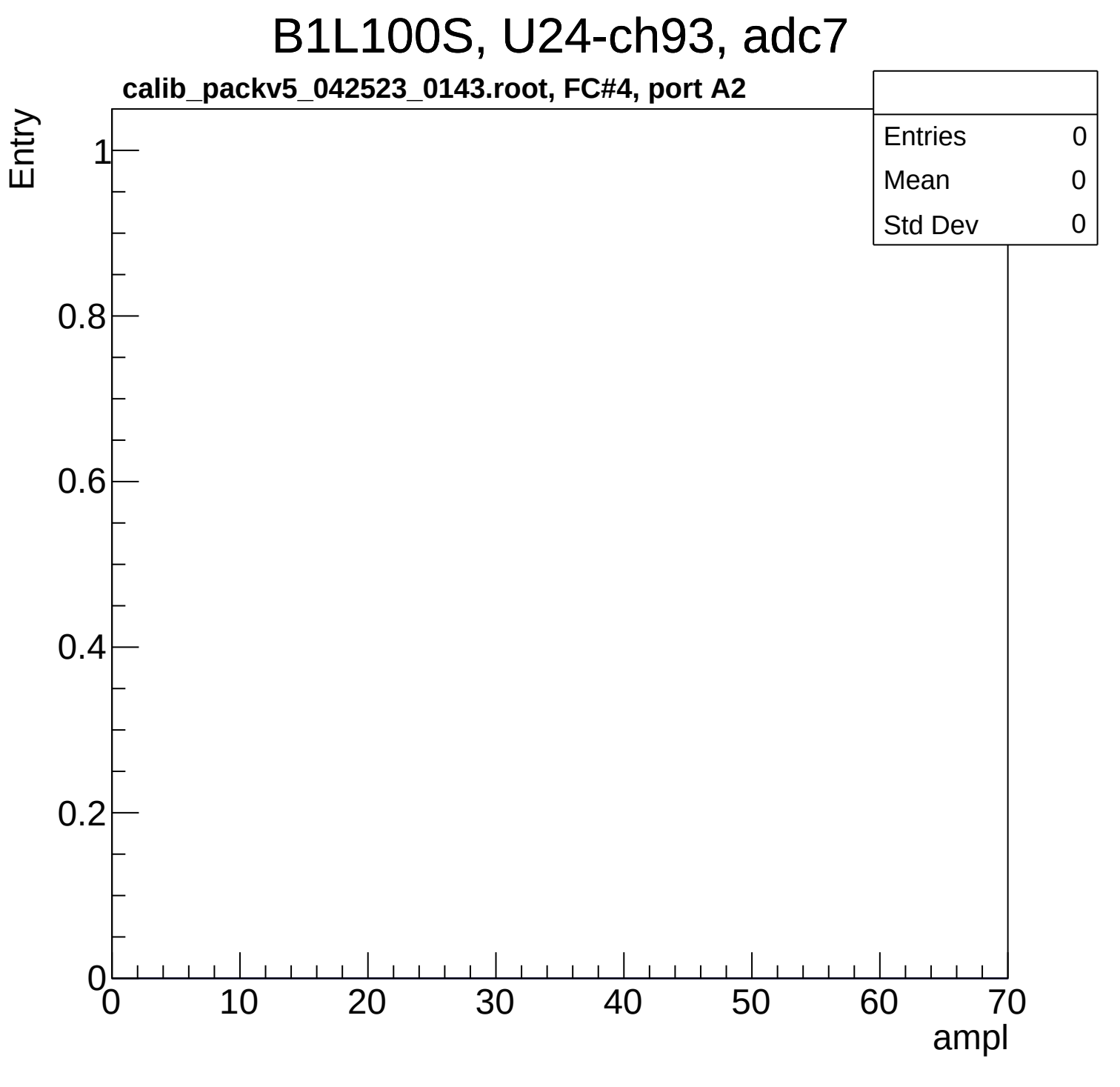
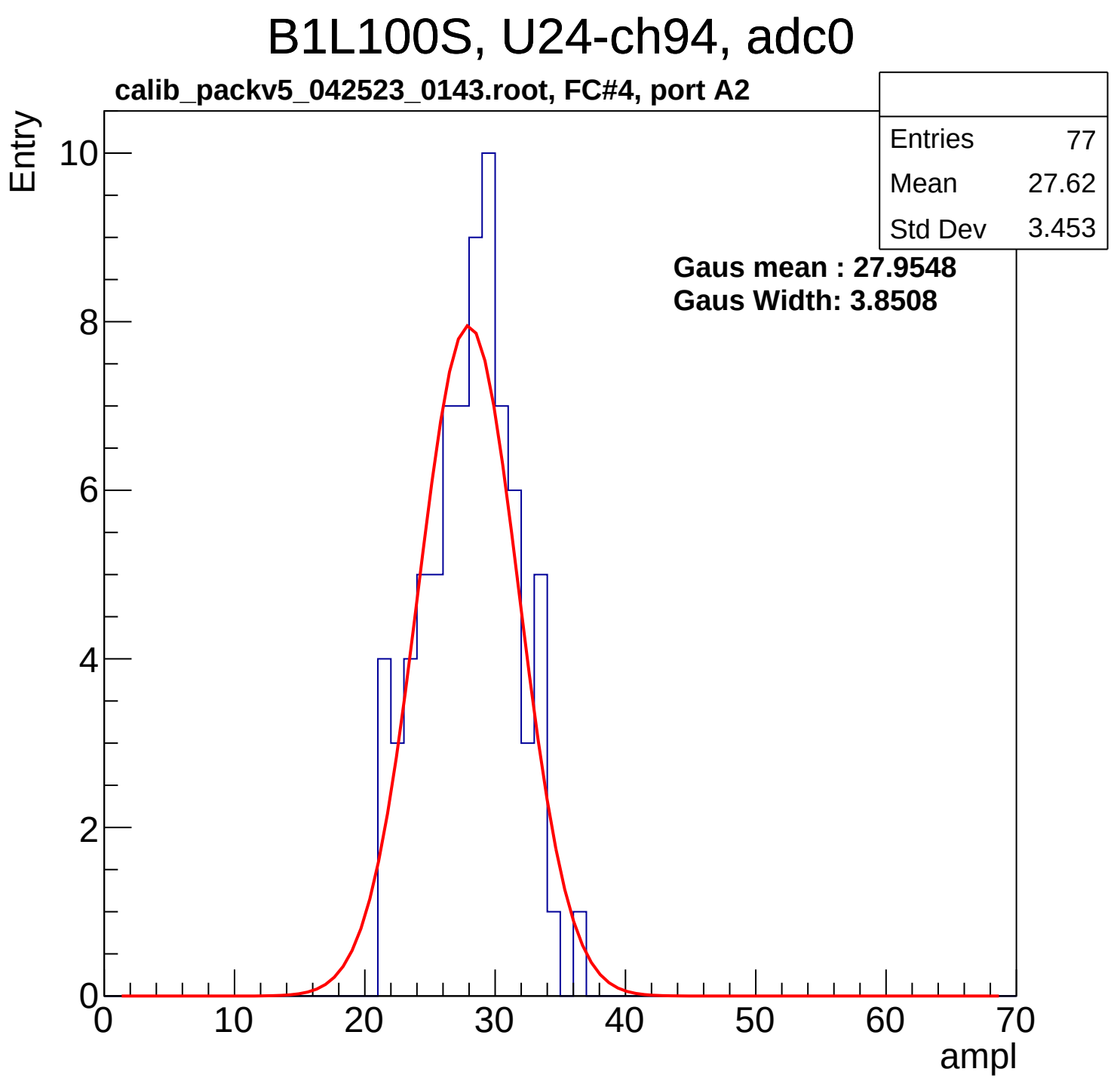
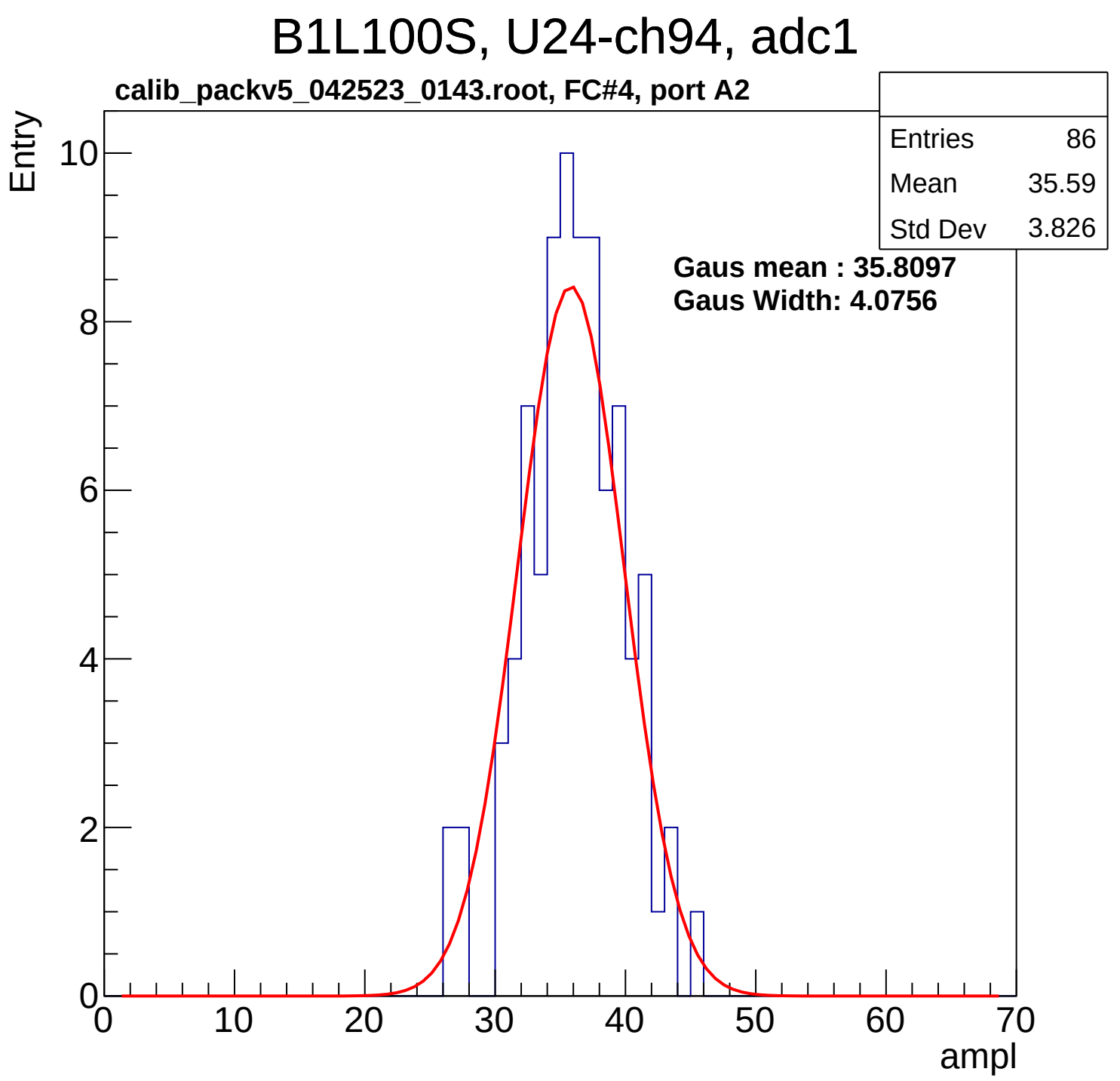
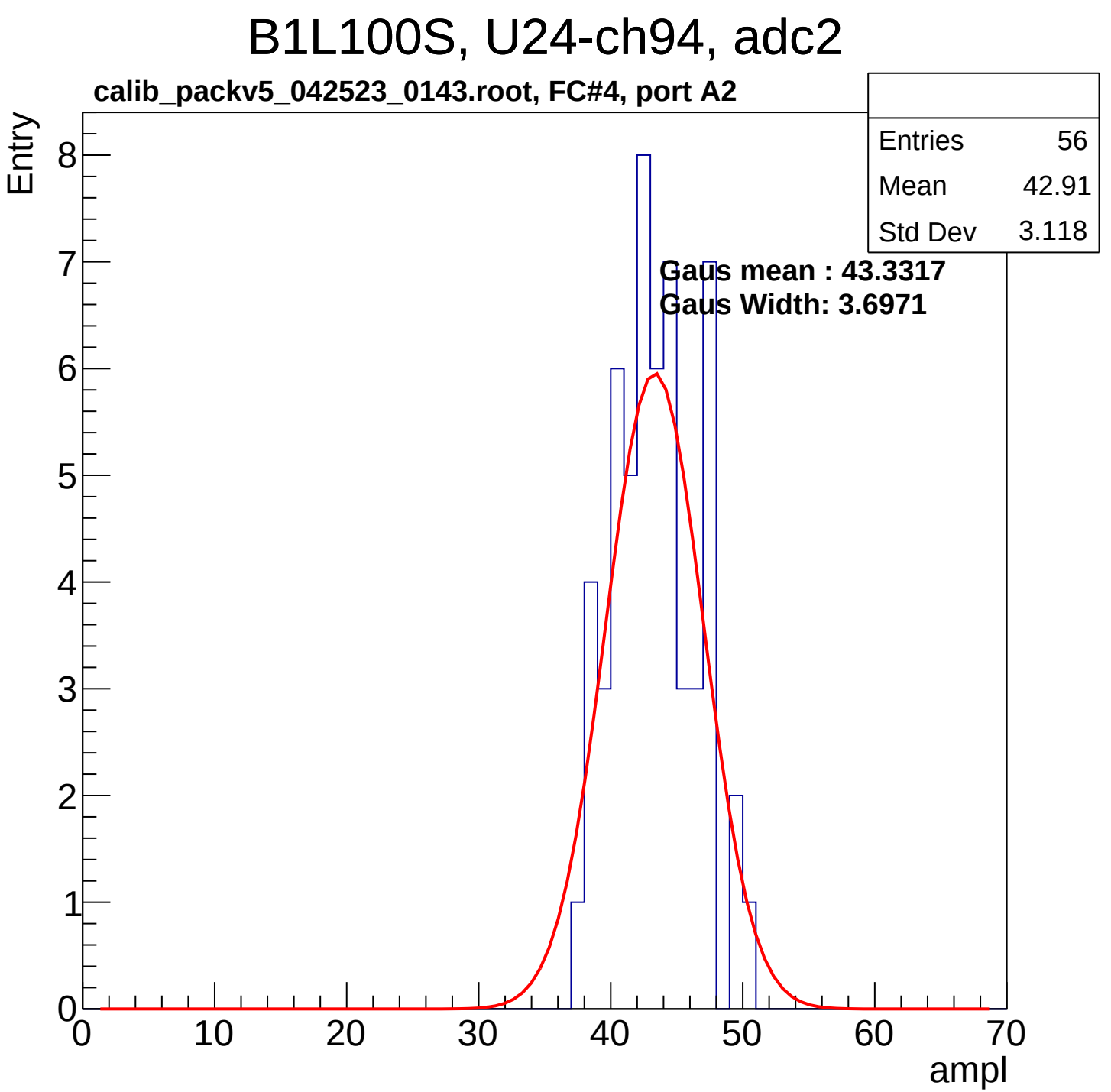

B1L100S, U24-ch93, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.71
Std Dev 1.745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

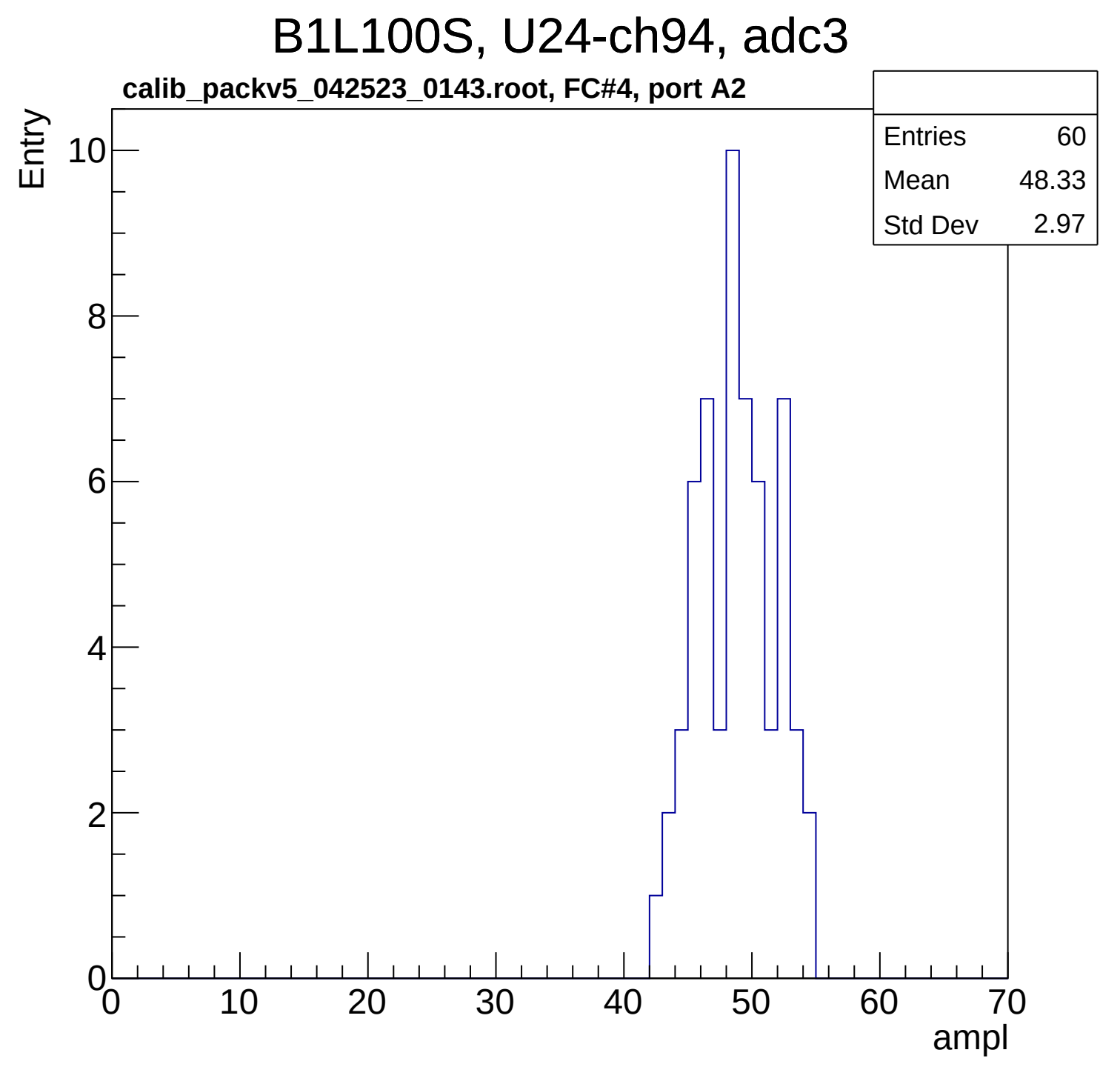

B1L100S, U24-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

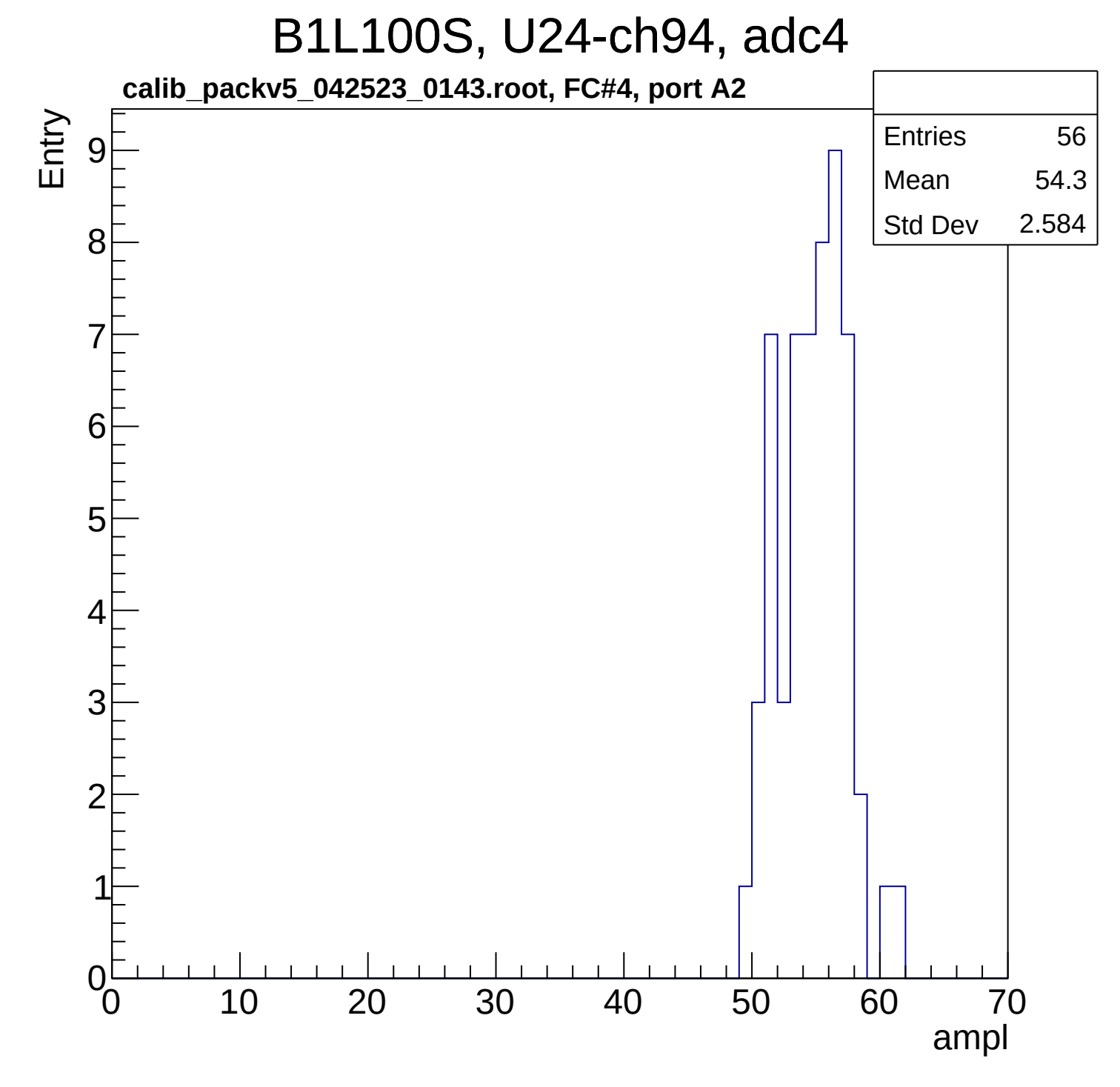

B1L100S, U24-ch93, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

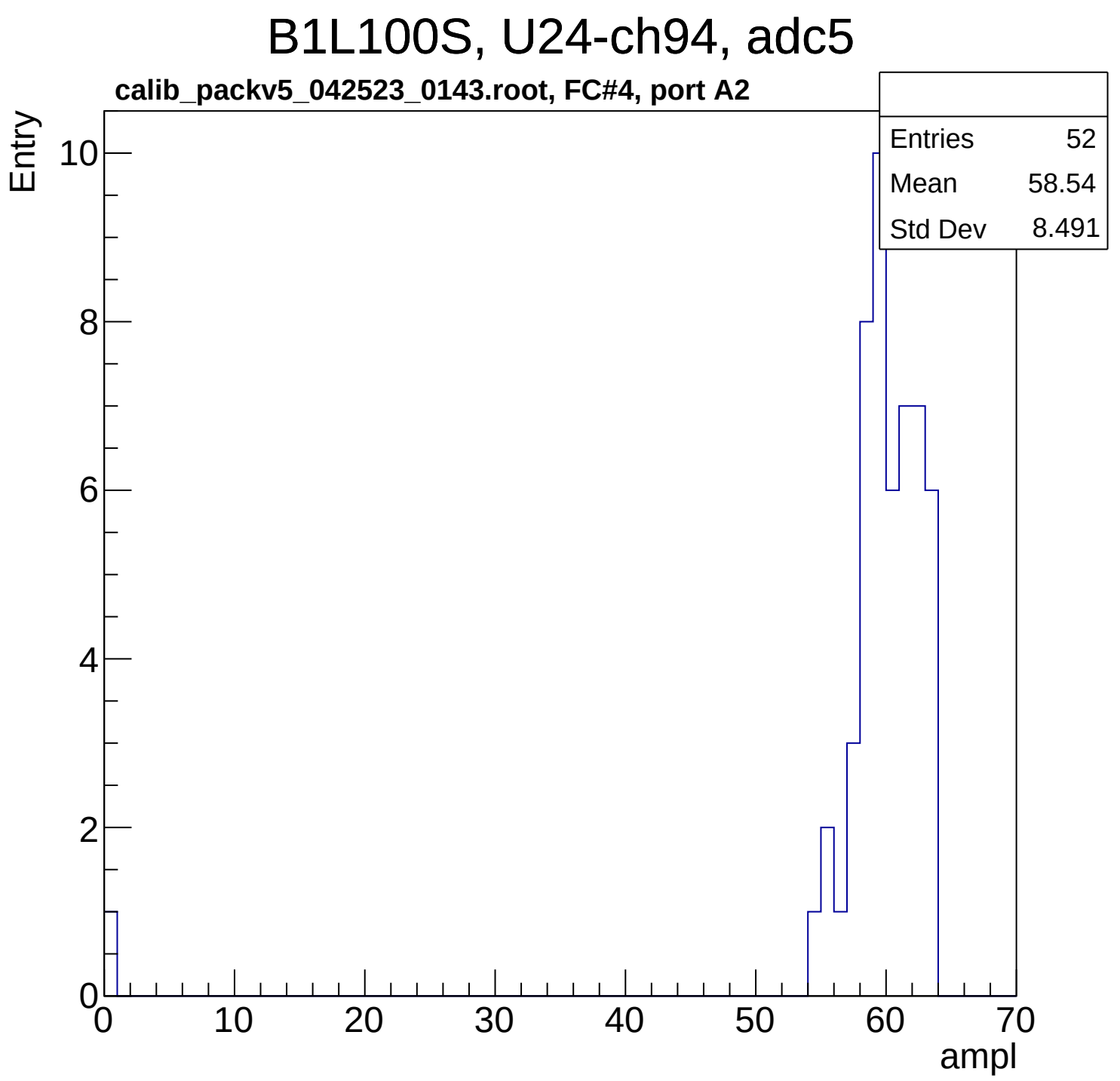

B1L100S, U24-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 27.62
Std Dev 3.453
Gaus mean : 27.9548
Gaus Width: 3.8508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

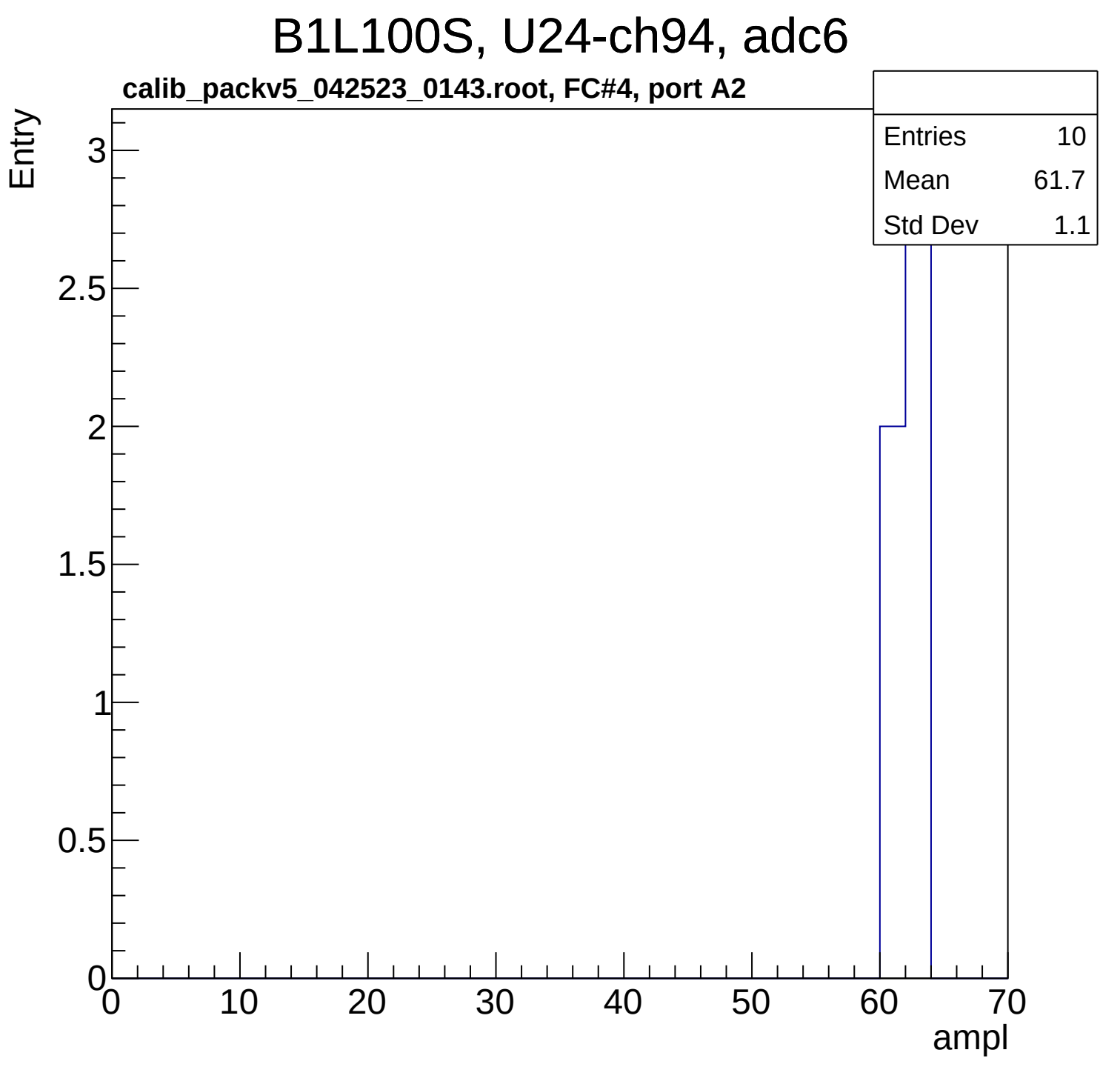

B1L100S, U24-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 35.59
Std Dev 3.826
Gaus mean : 35.8097
Gaus Width: 4.0756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 42.91
Std Dev 3.118
Gaus mean : 43.3317
Gaus Width: 3.6971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 48.33
Std Dev 2.97
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 54.3
Std Dev 2.584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch94, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.54
Std Dev 8.491
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch94, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.7
Std Dev 1.1
Entry
ampl
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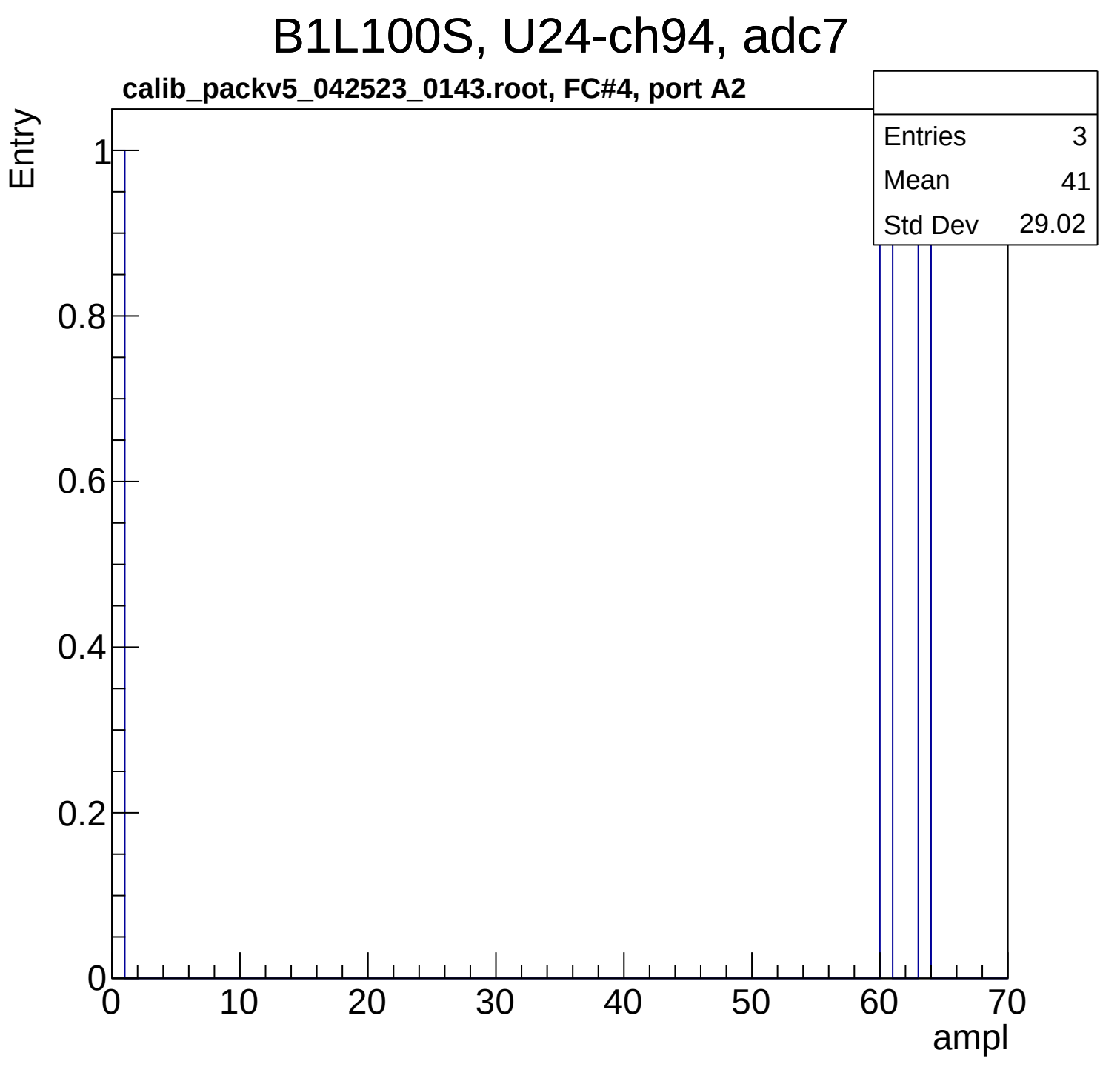
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch94, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41
Std Dev 29.02
Entry
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0.6
0.4
0.2
0
0
10
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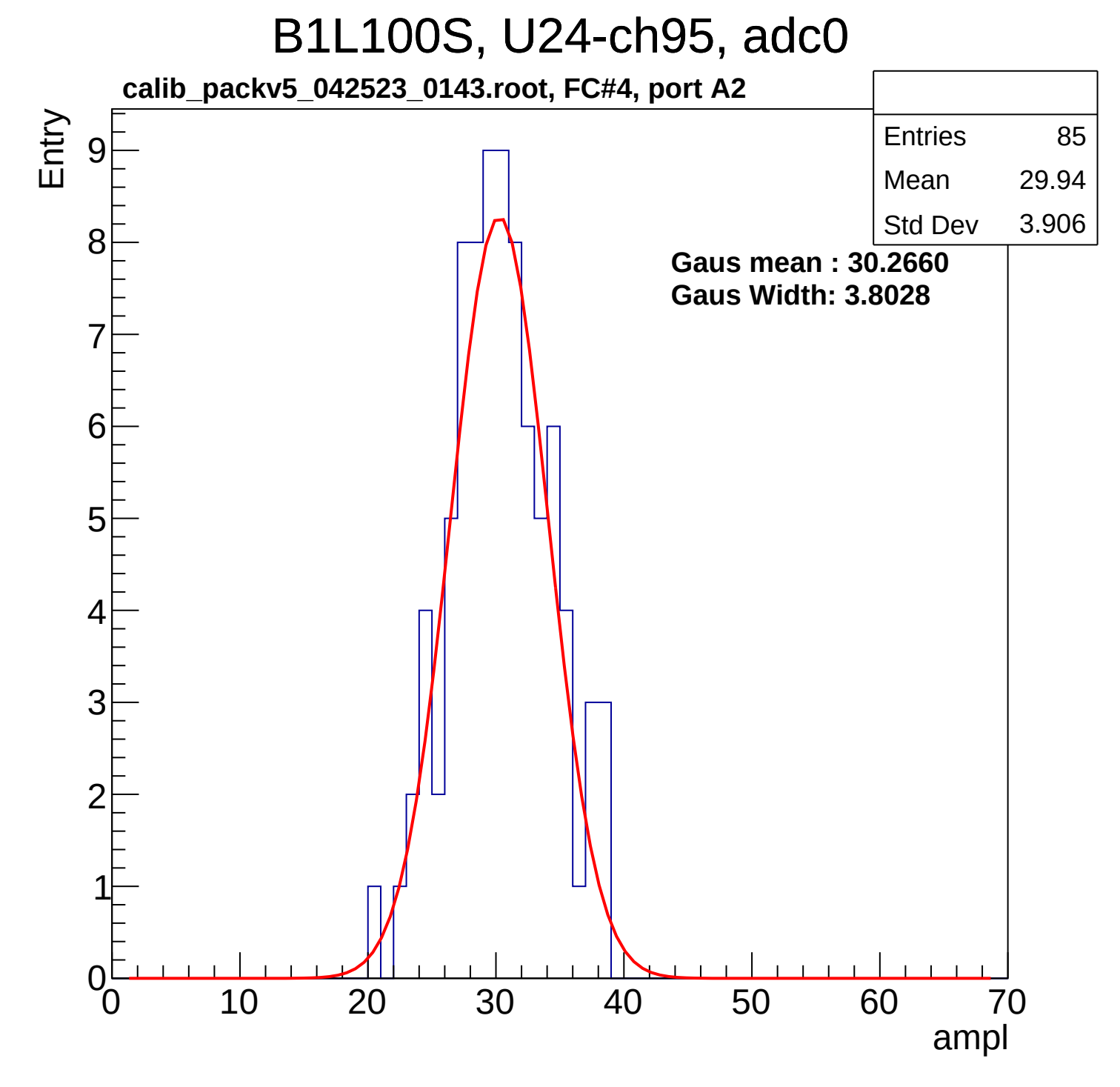
50
60
70
ampl


B1L100S, U24-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 29.94
Std Dev 3.906
Gaus mean : 30.2660
Gaus Width: 3.8028
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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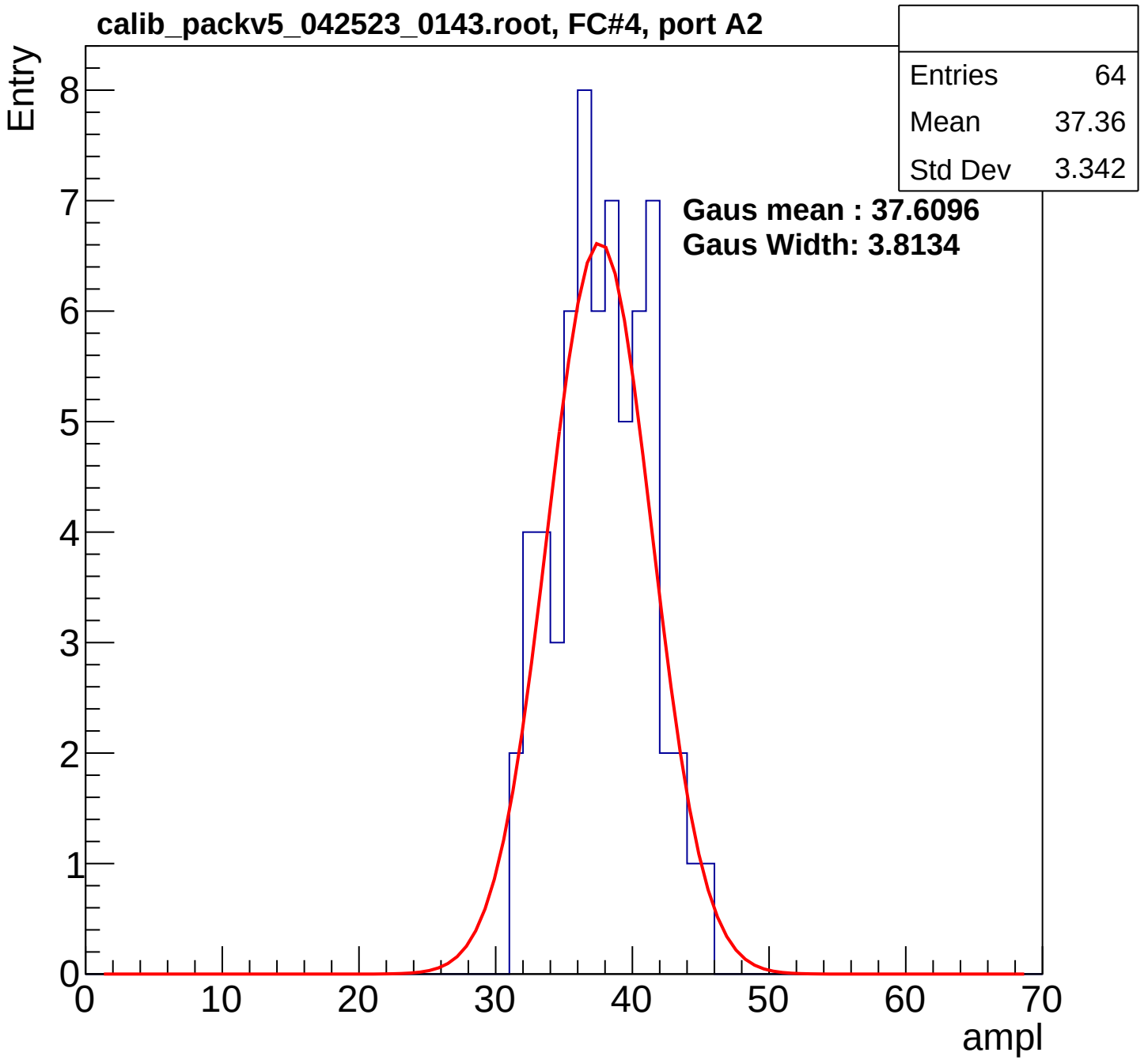
B1L100S, U24-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.36
Std Dev 3.342
Gaus mean : 37.6096
Gaus Width: 3.8134
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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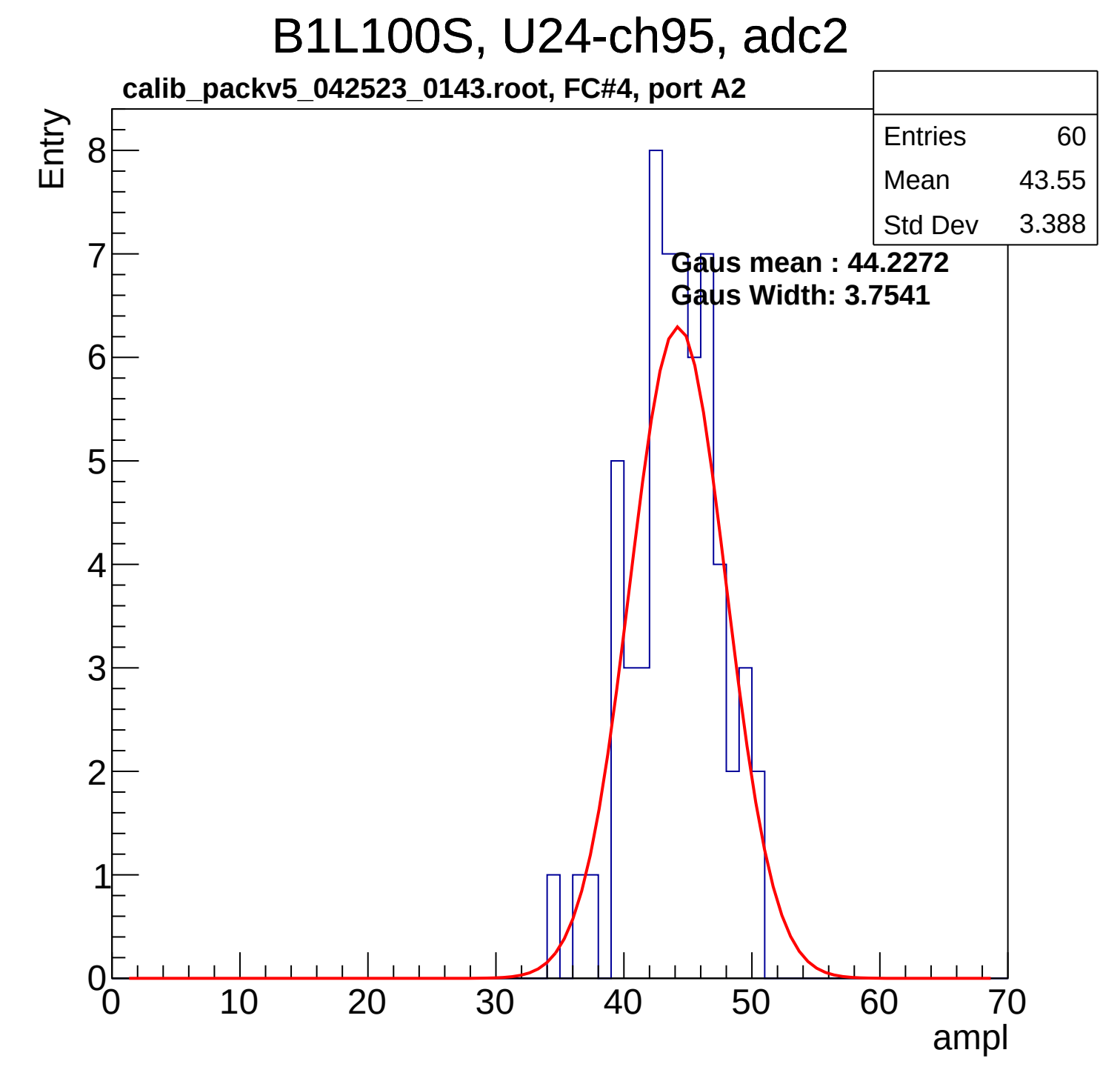
B1L100S, U24-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.55
Std Dev 3.388
Gaus mean : 44.2272
Gaus Width: 3.7541
Entry
ampl
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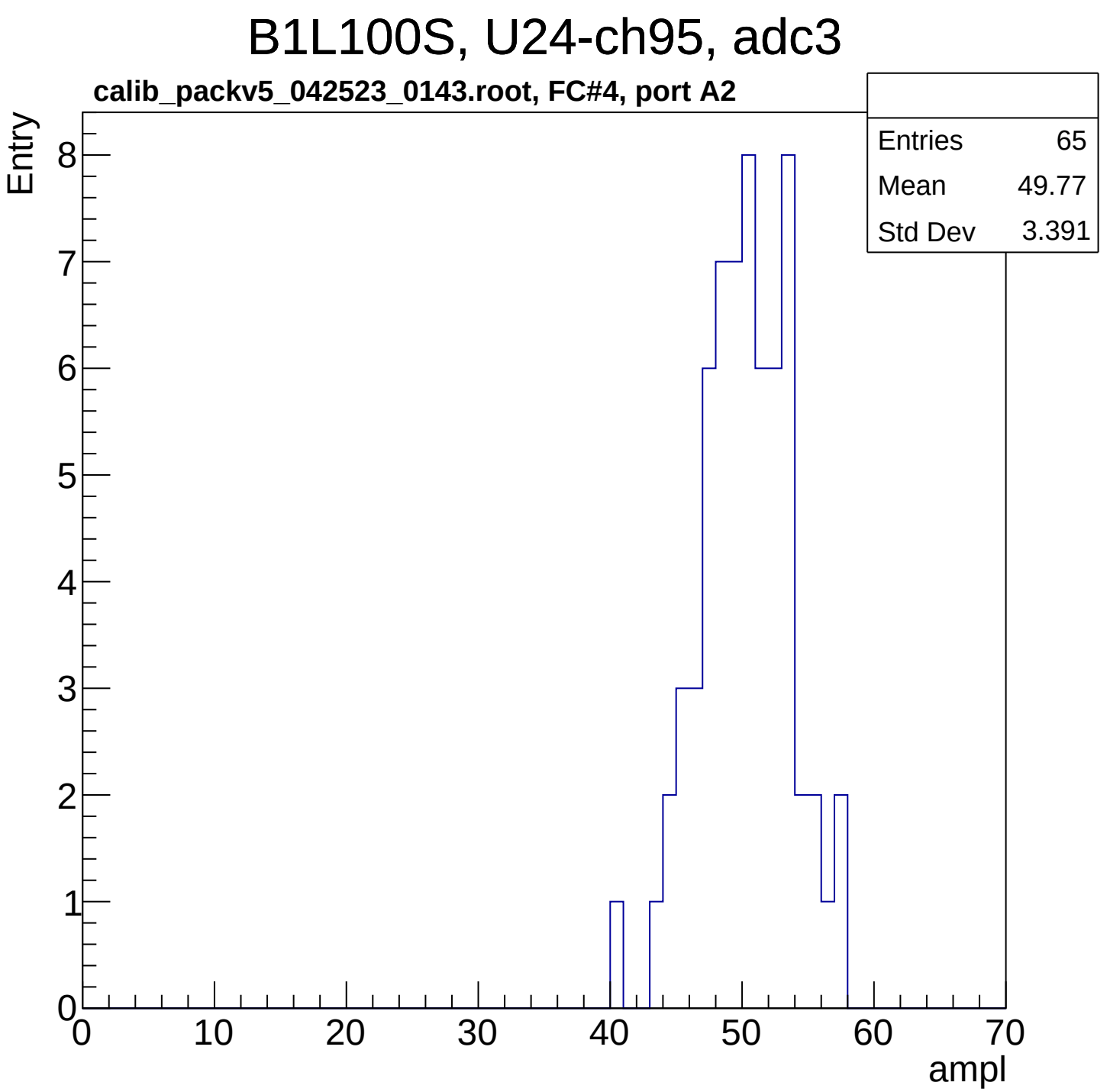
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.77
Std Dev 3.391
Entry
ampl
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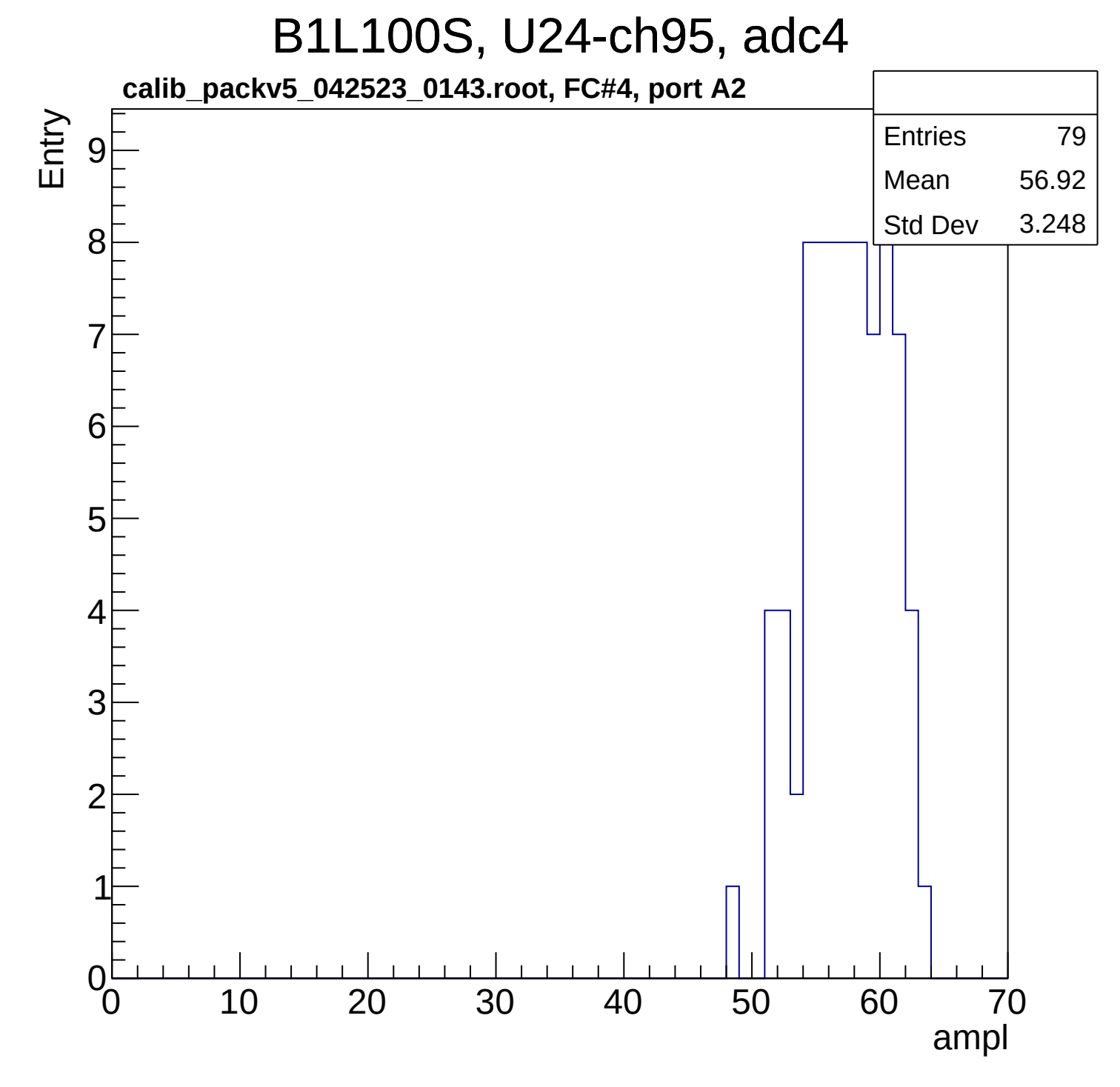
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch95, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 56.92
Std Dev 3.248
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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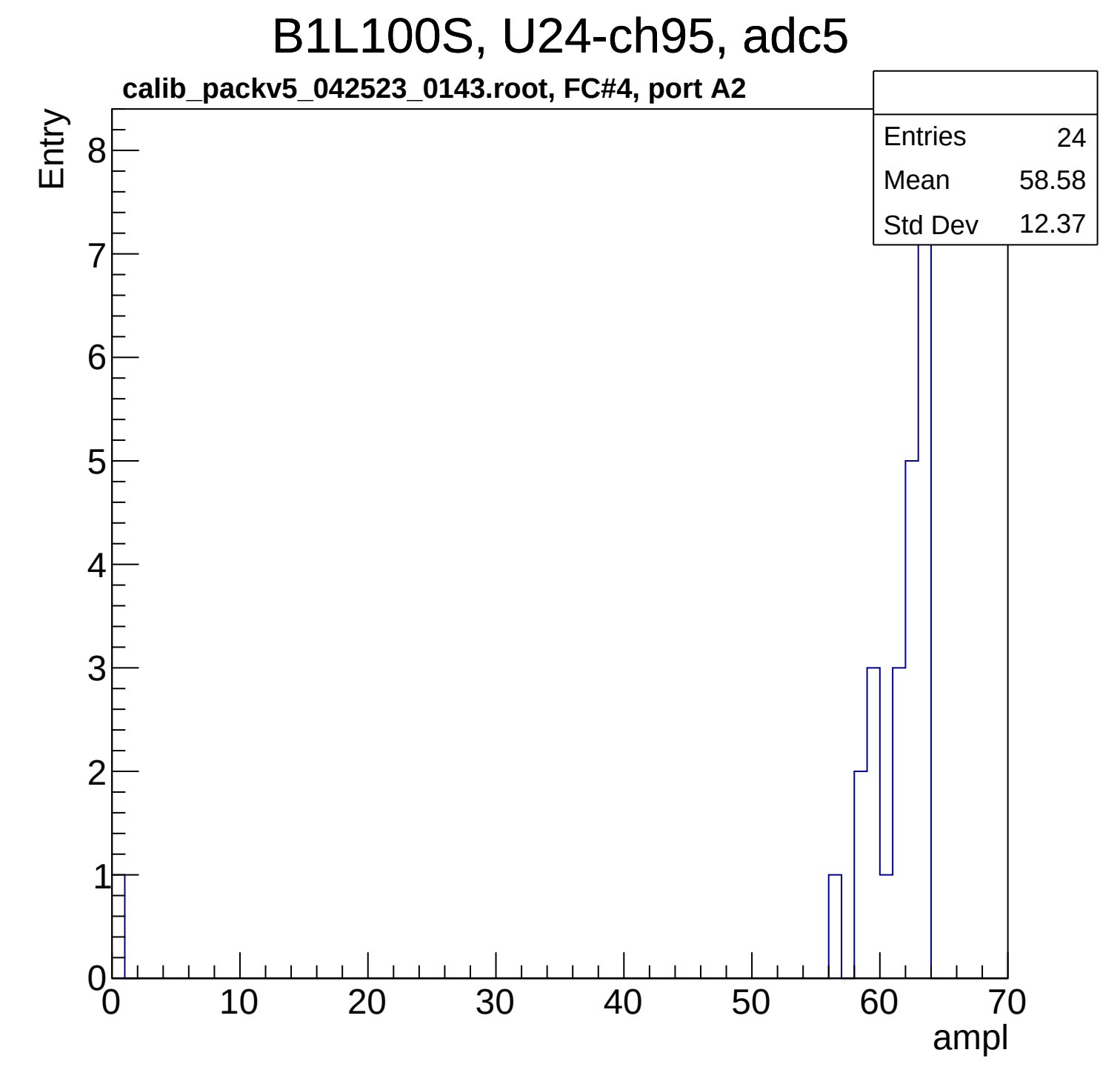
B1L100S, U24-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.58
Std Dev 12.37
Entry
ampl
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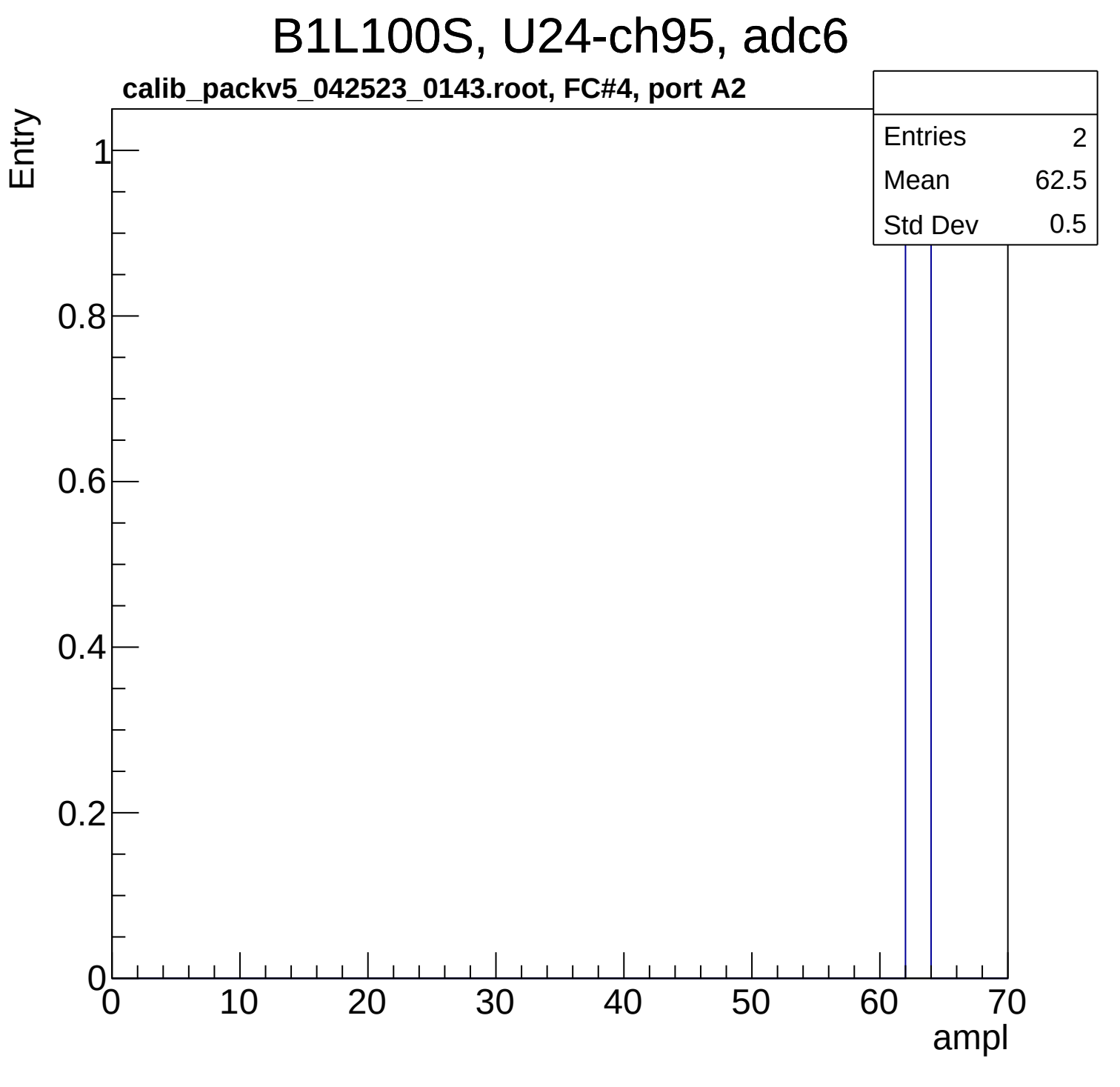
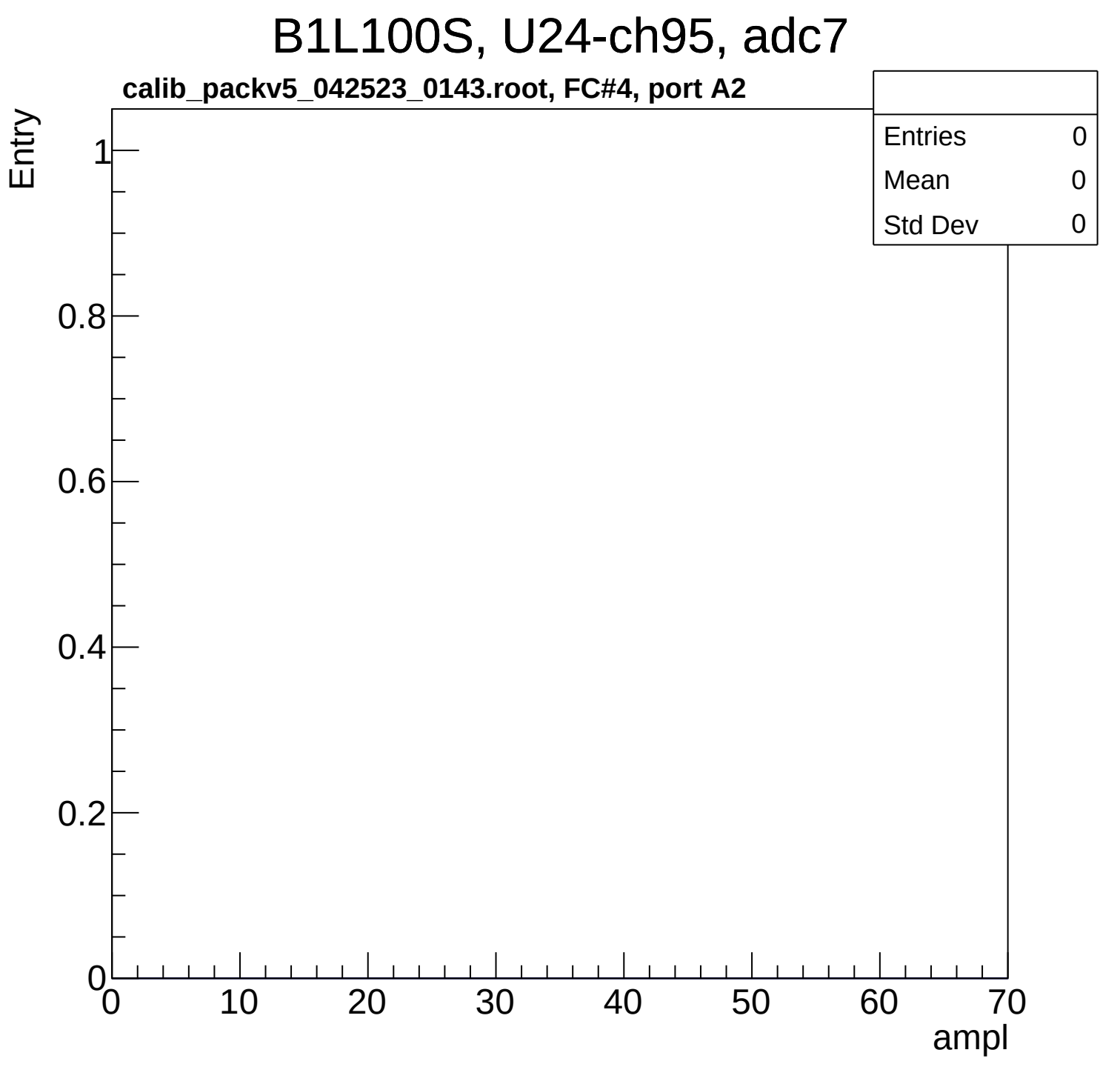
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch95, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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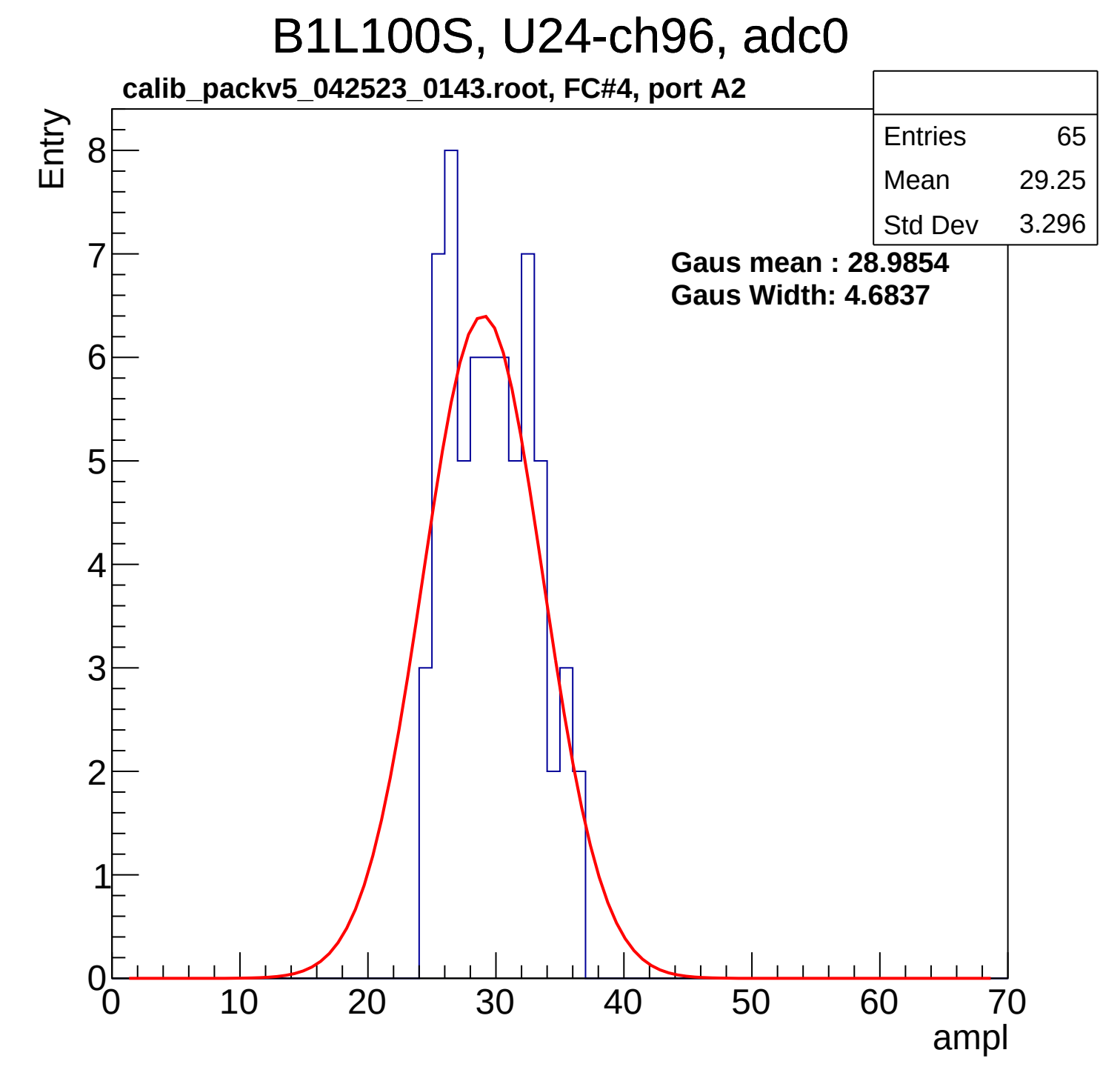
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.25
Std Dev 3.296
Gaus mean : 28.9854
Gaus Width: 4.6837
Entry
ampl
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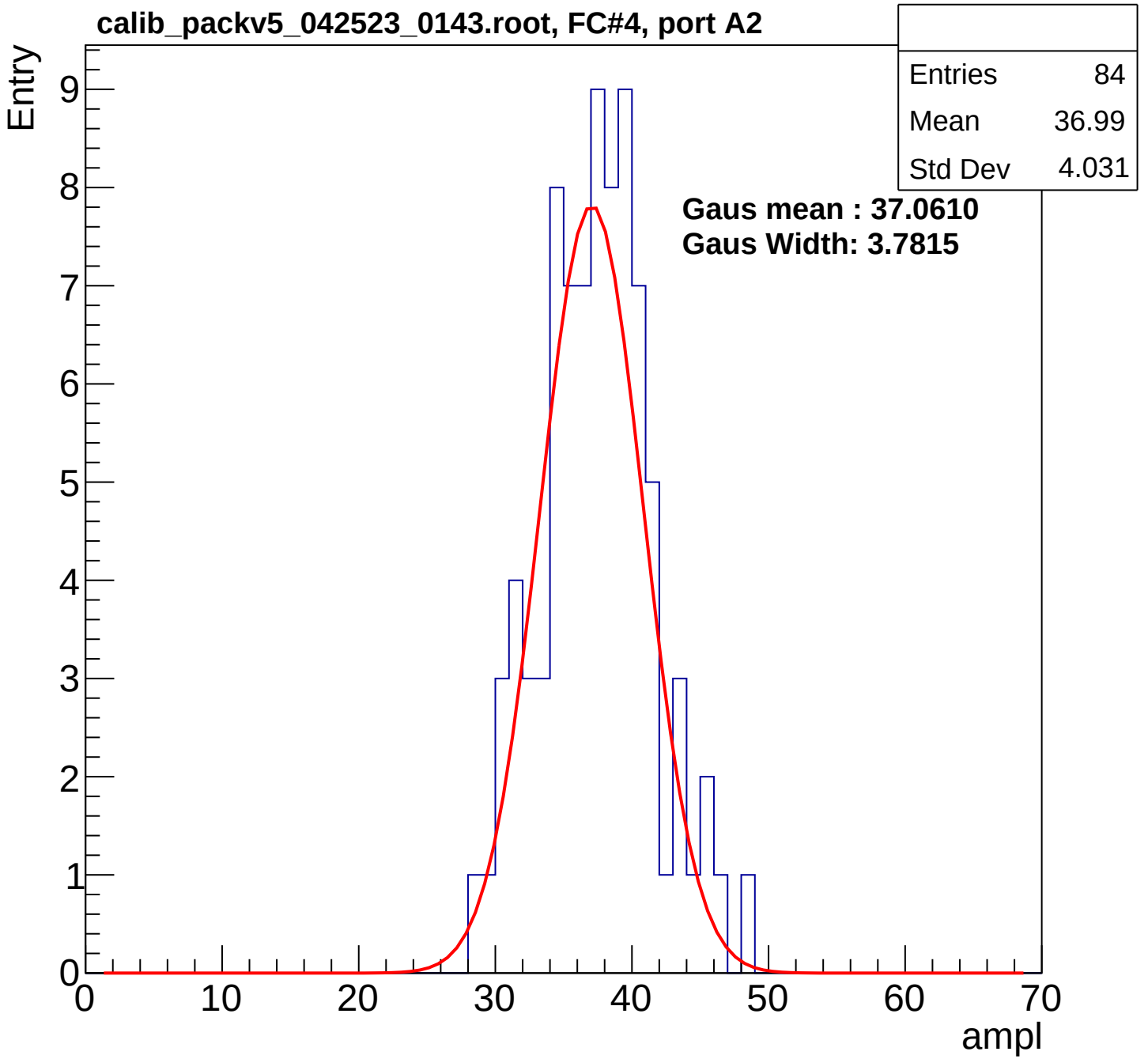
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 36.99
Std Dev 4.031
Gaus mean : 37.0610
Gaus Width: 3.7815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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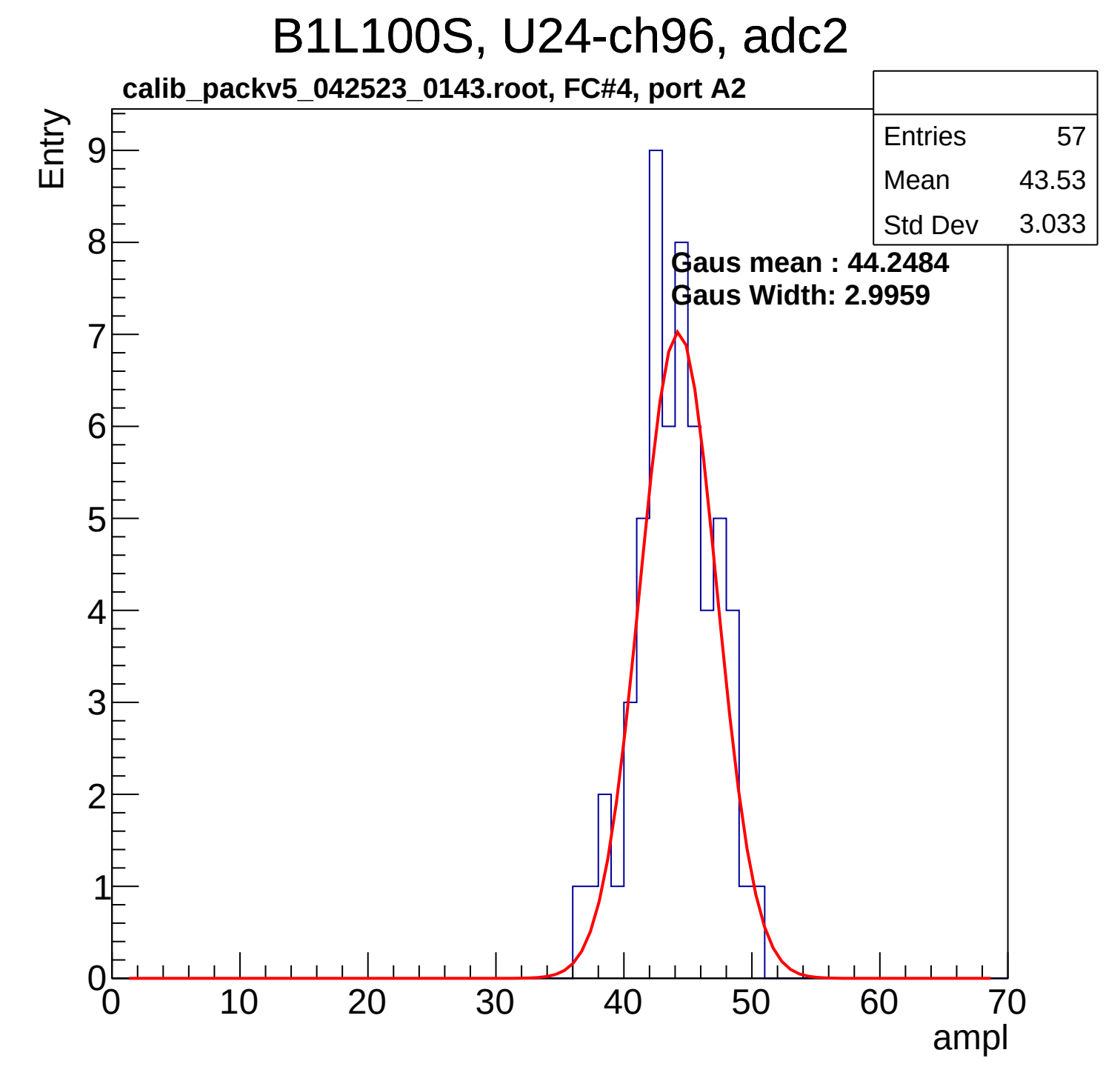
B1L100S, U24-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43.53
Std Dev 3.033
Gaus mean : 44.2484
Gaus Width: 2.9959
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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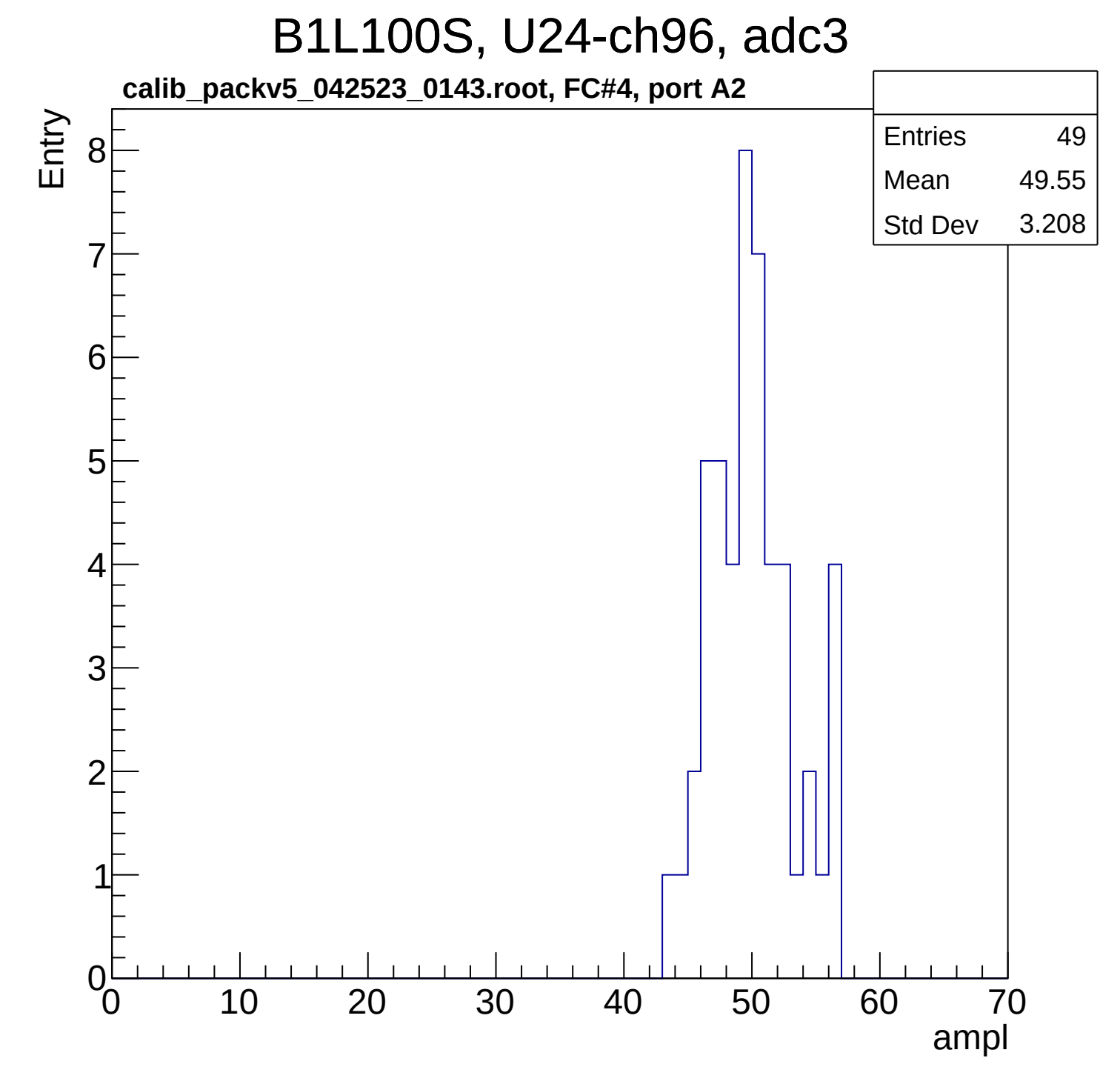
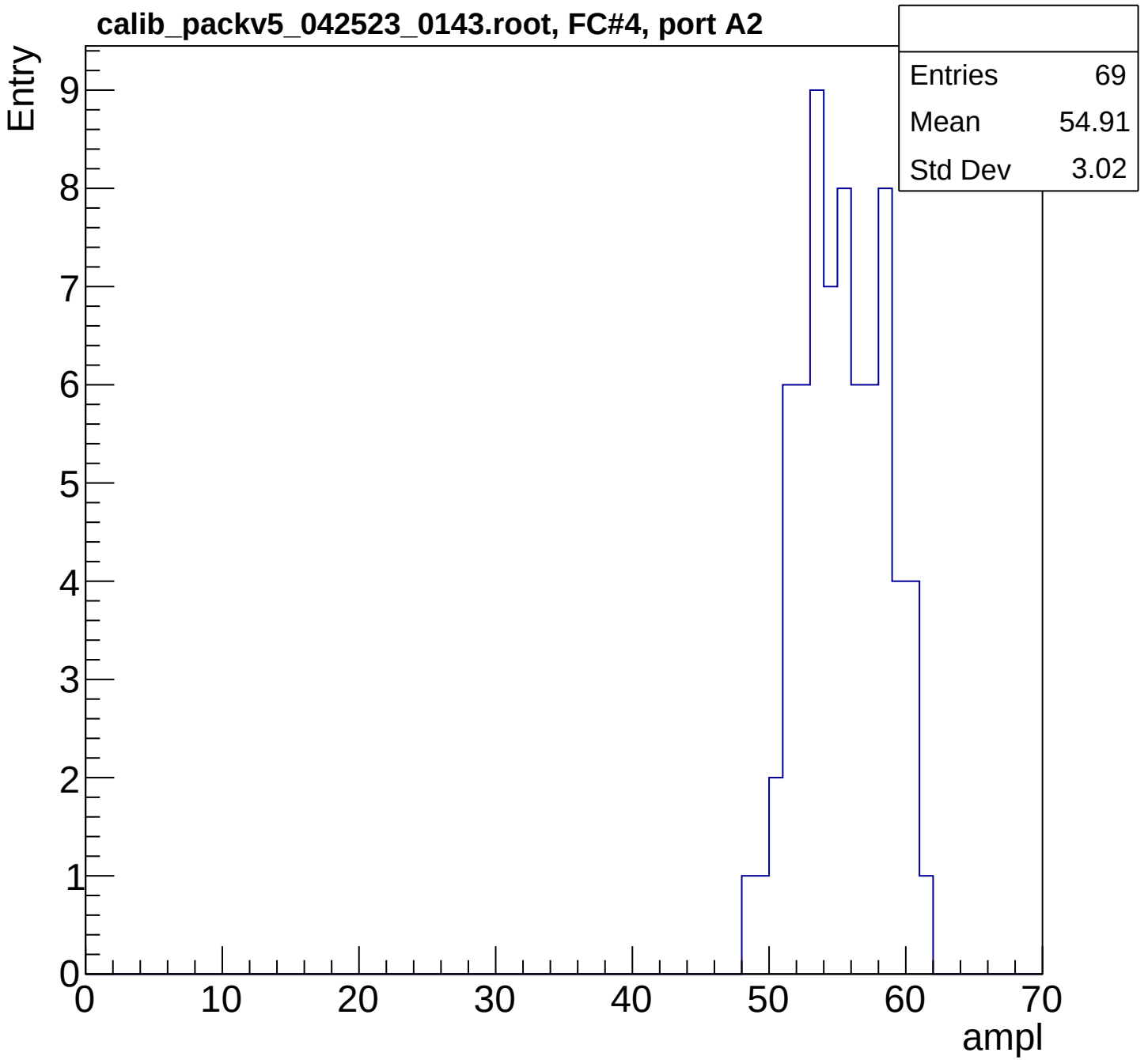

B1L100S, U24-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 49.55
Std Dev 3.208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 54.91
Std Dev 3.02
Entry
ampl
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0
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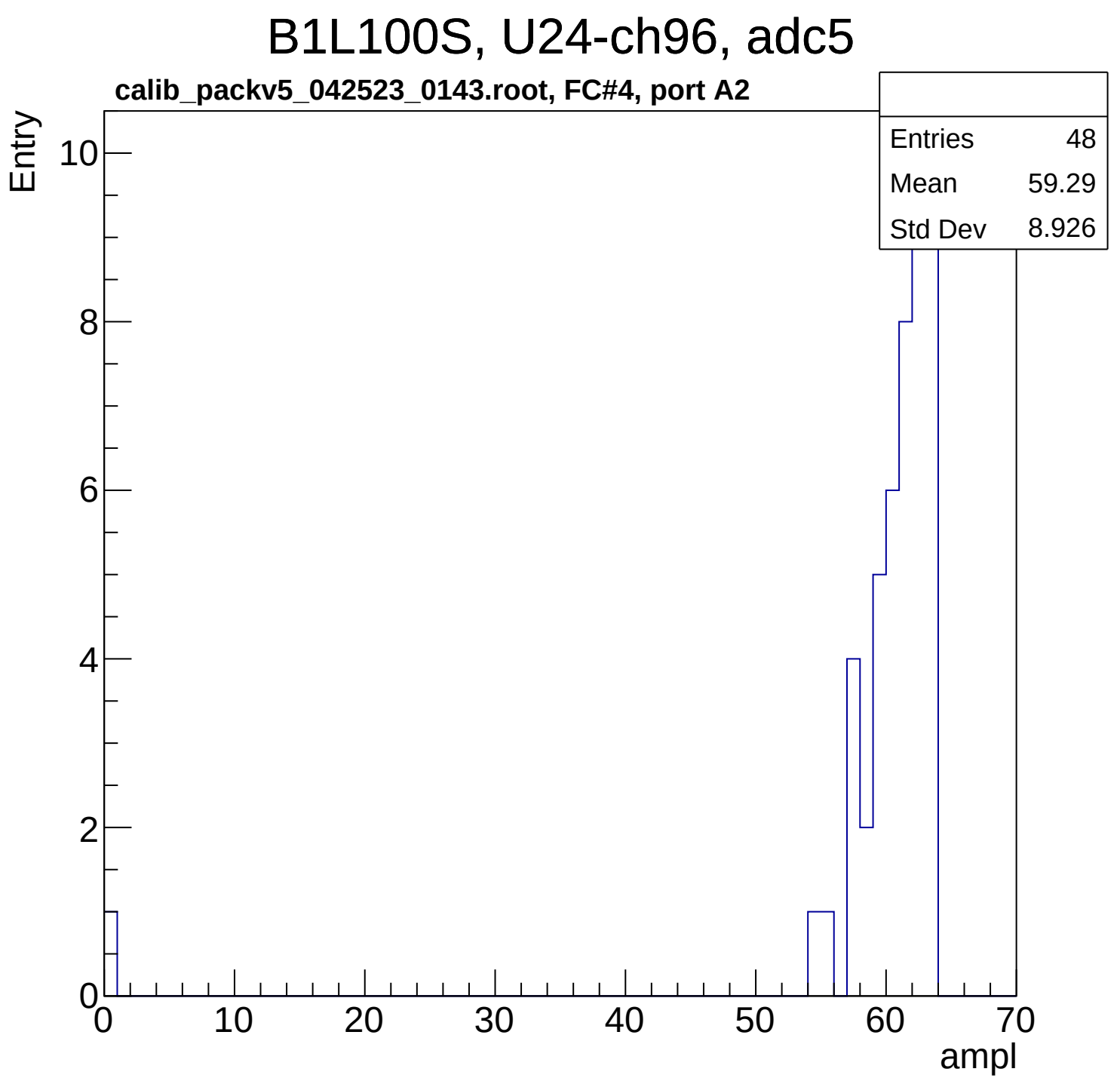
B1L100S, U24-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.29
Std Dev 8.926
Entry
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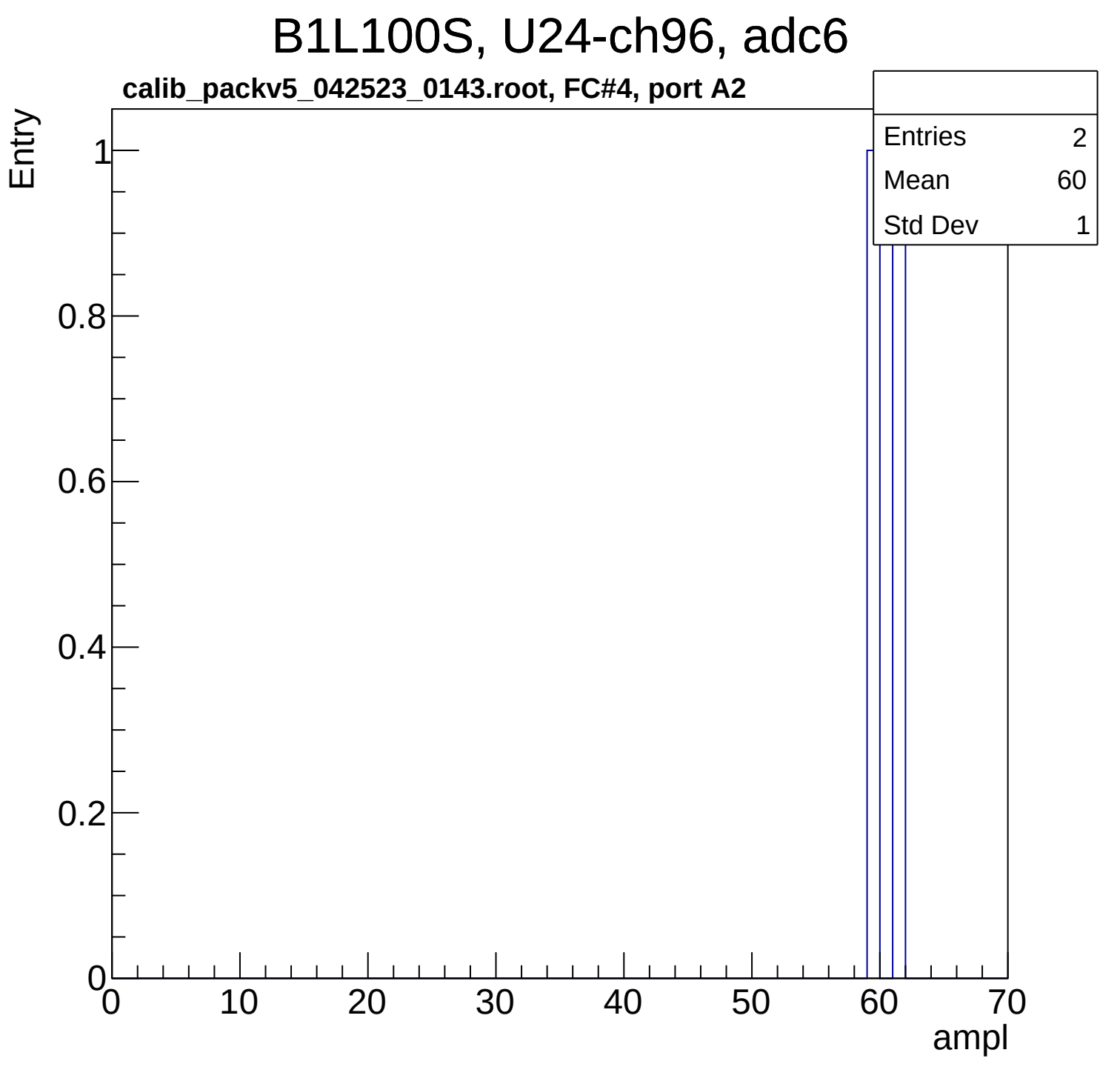
70
ampl


B1L100S, U24-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 60
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
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60
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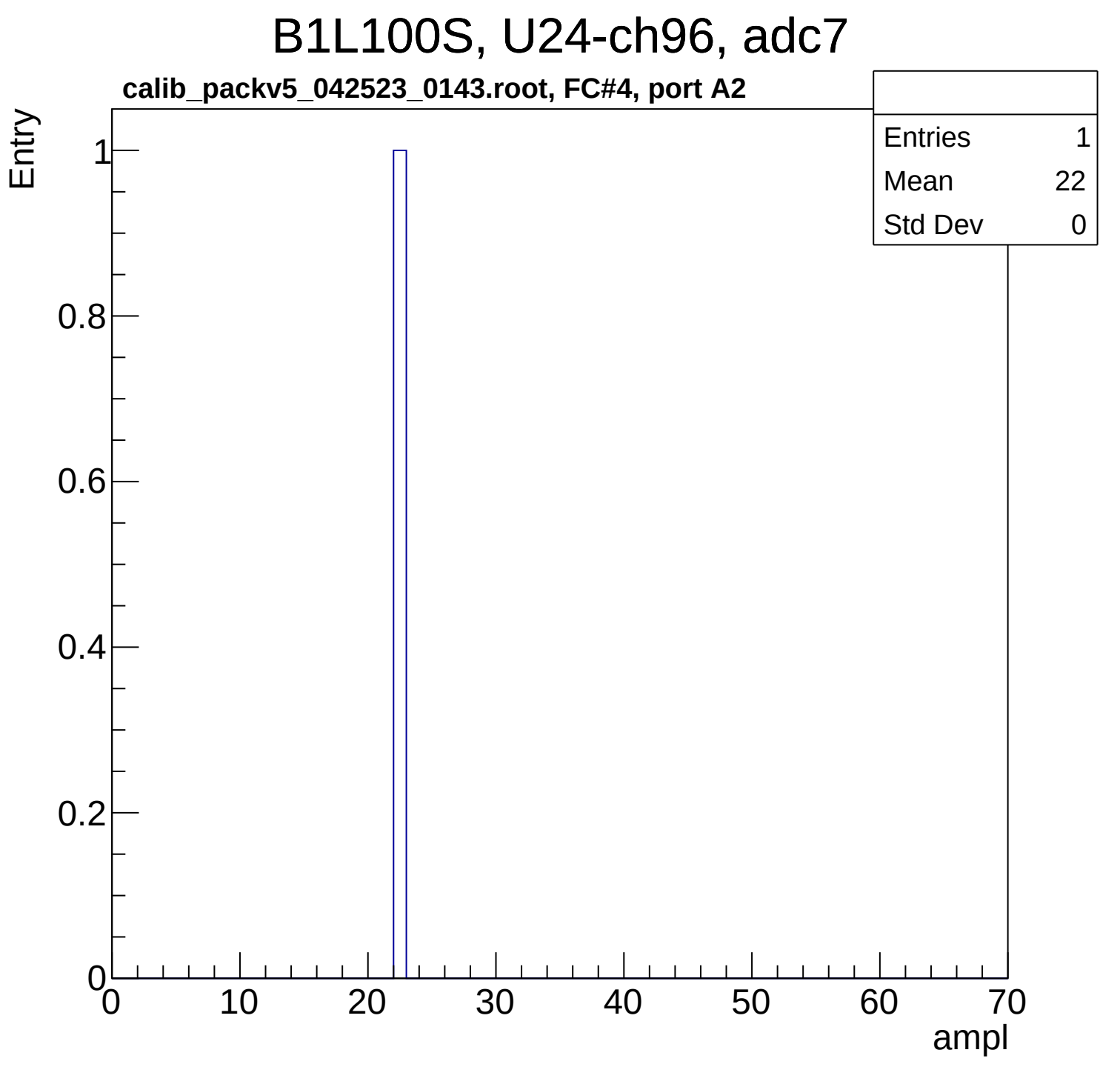
B1L100S, U24-ch96, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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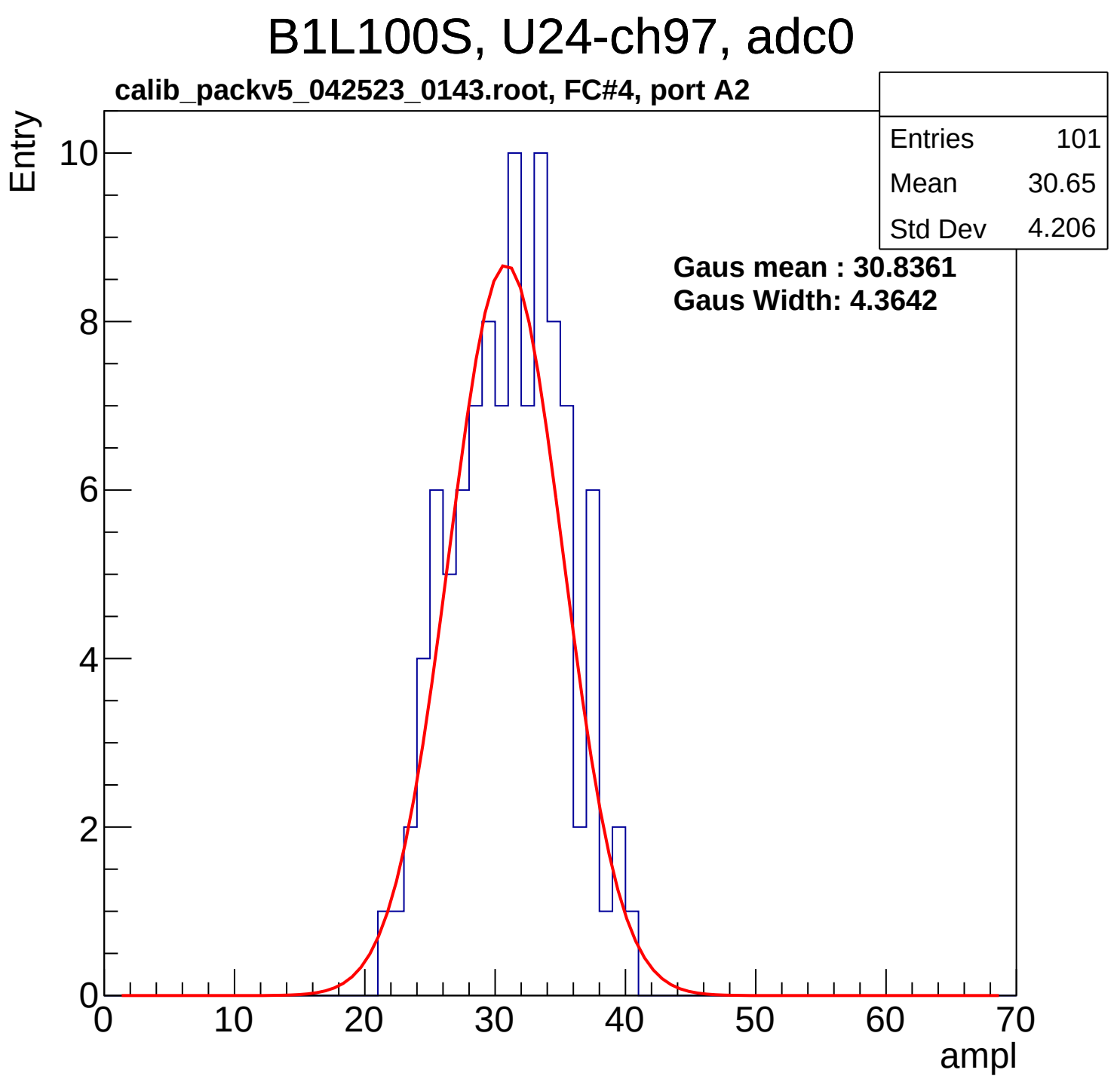
50
60
70
ampl


B1L100S, U24-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 101
Mean 30.65
Std Dev 4.206
Gaus mean : 30.8361
Gaus Width: 4.3642
Entry
ampl
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8
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0
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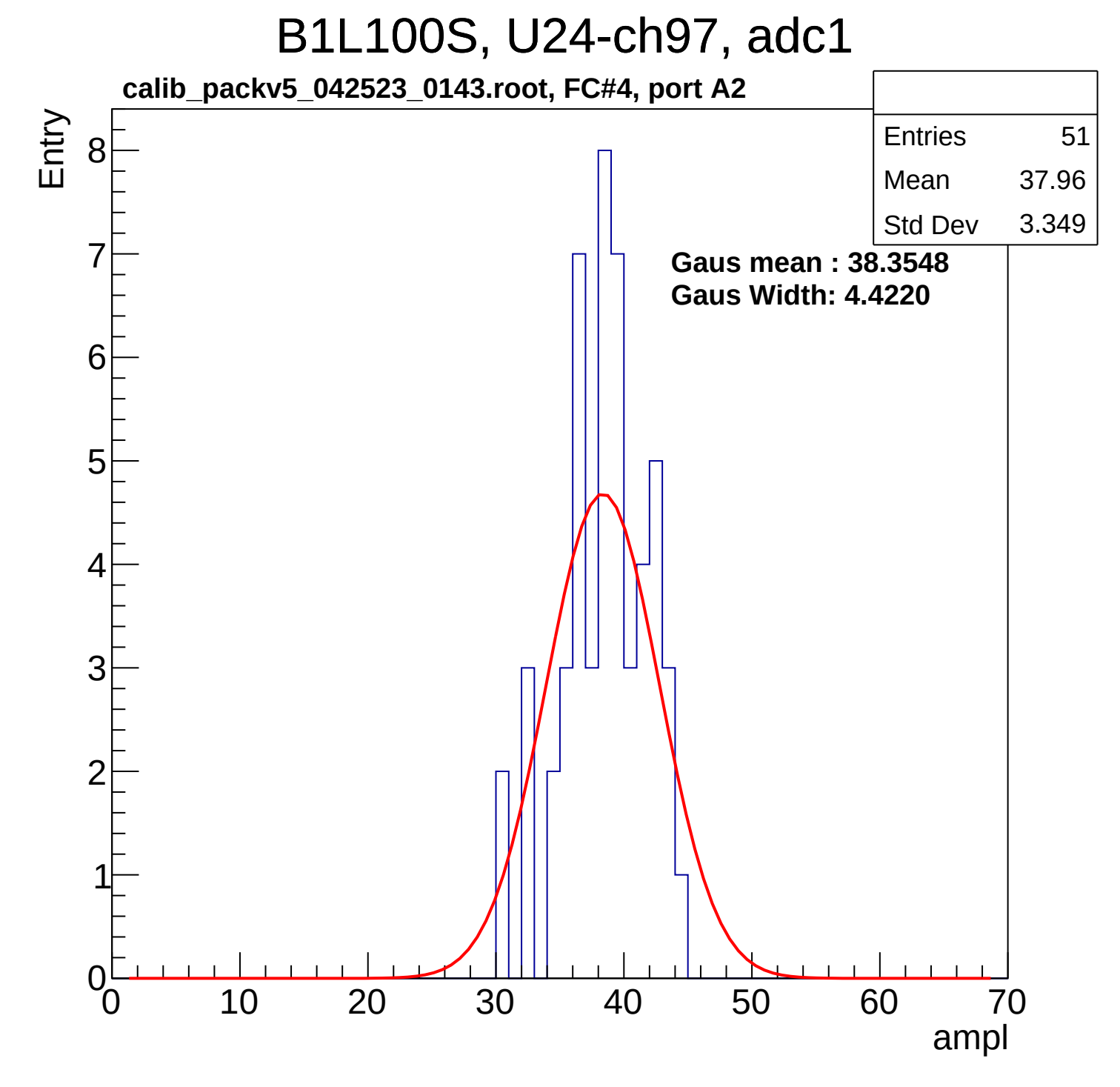
B1L100S, U24-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 37.96
Std Dev 3.349
Gaus mean : 38.3548
Gaus Width: 4.4220
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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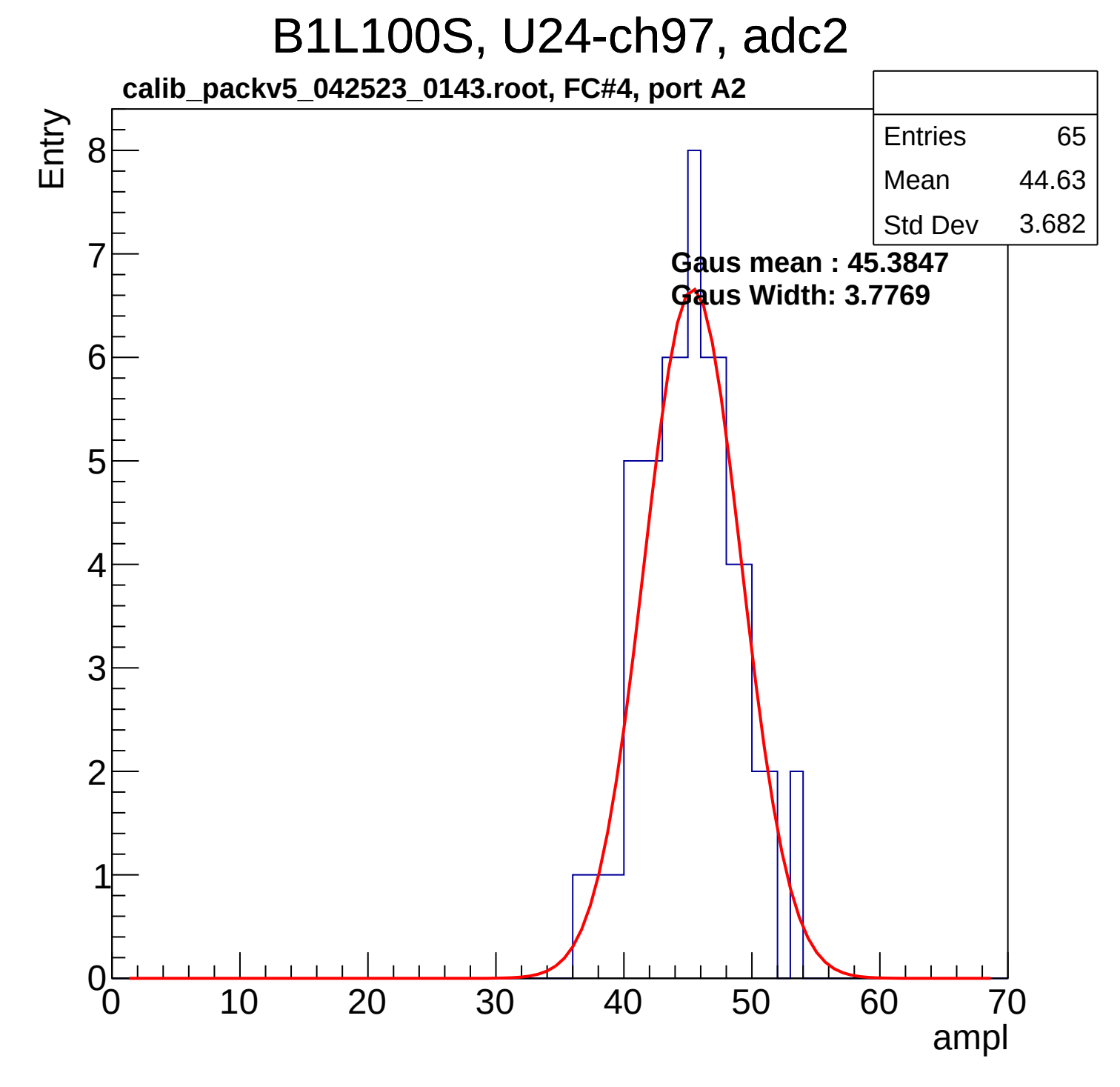
B1L100S, U24-ch97, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.63
Std Dev 3.682
Gaus mean : 45.3847
Gaus Width: 3.7769
Entry
ampl
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1
2
3
4
5
6
7
8
0
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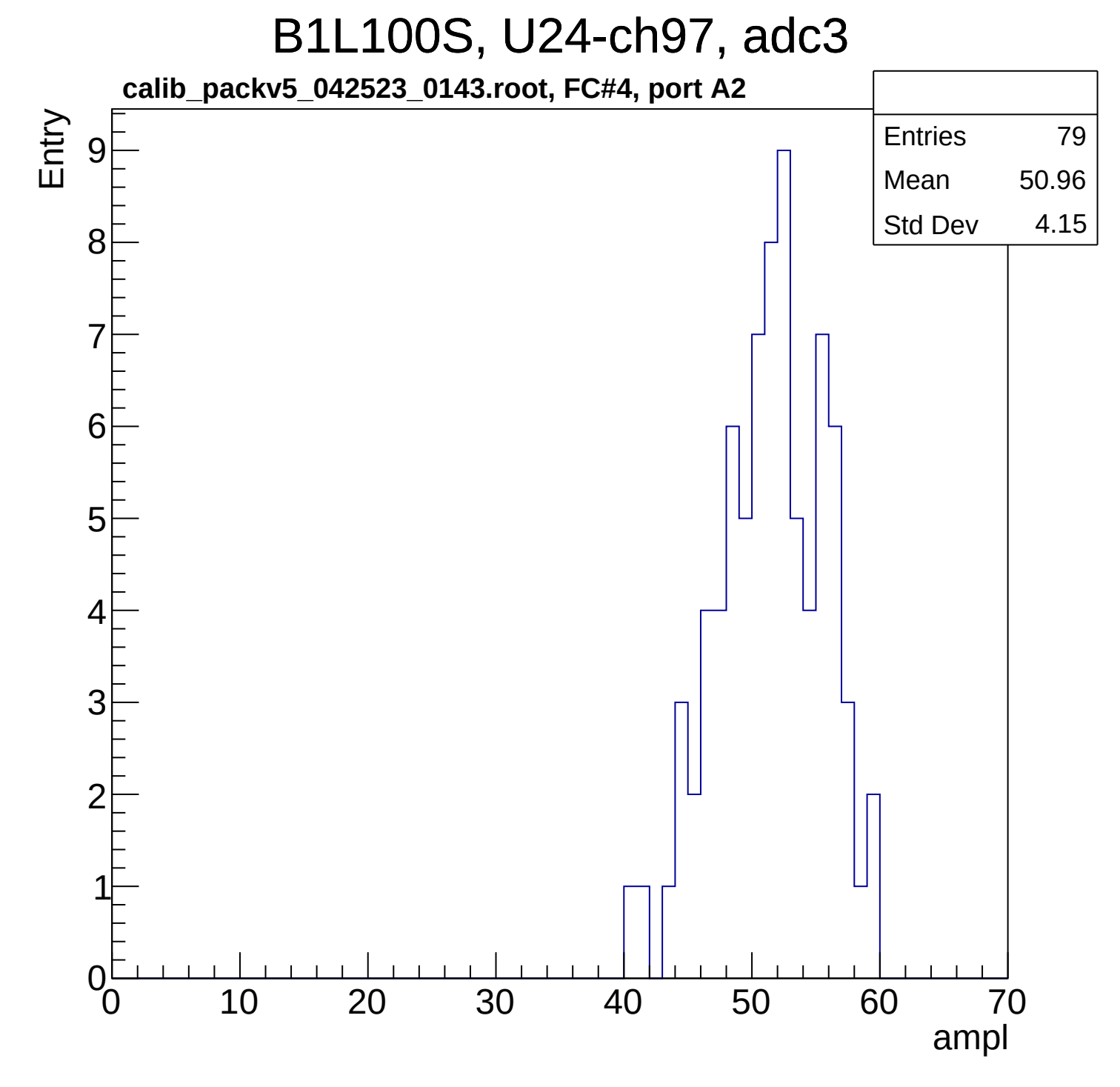
B1L100S, U24-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 50.96
Std Dev 4.15
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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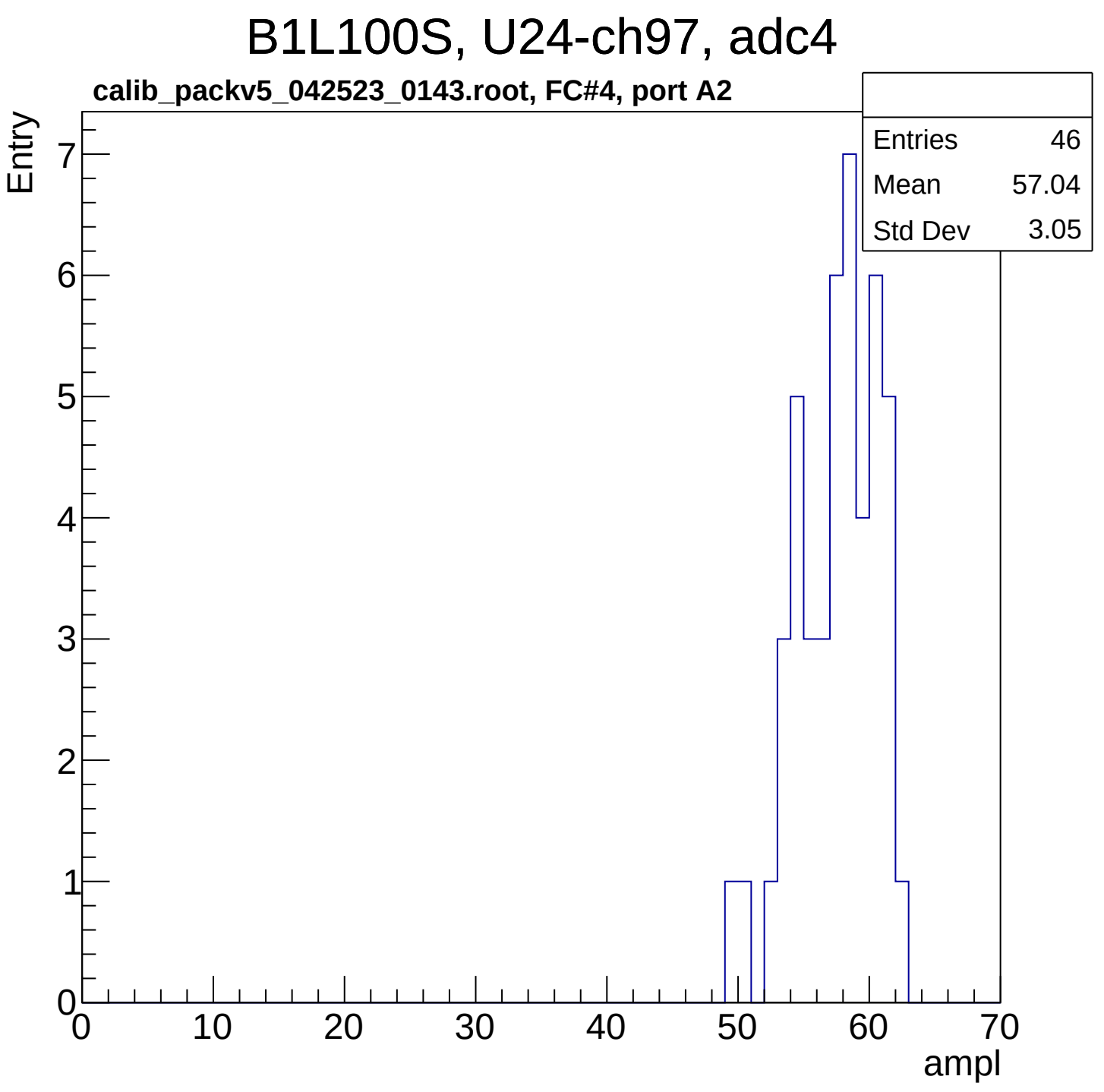
B1L100S, U24-ch97, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 57.04
Std Dev 3.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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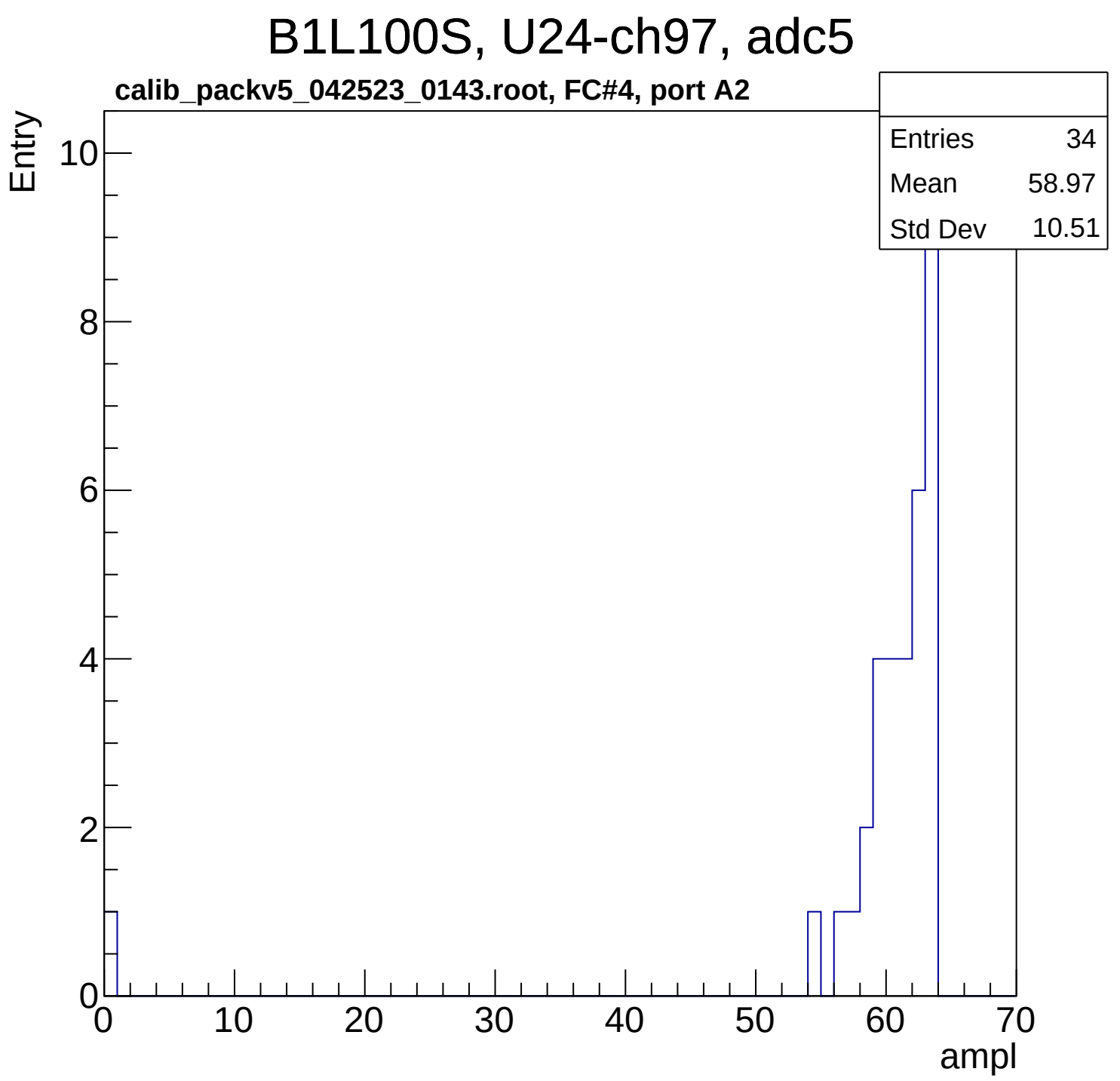
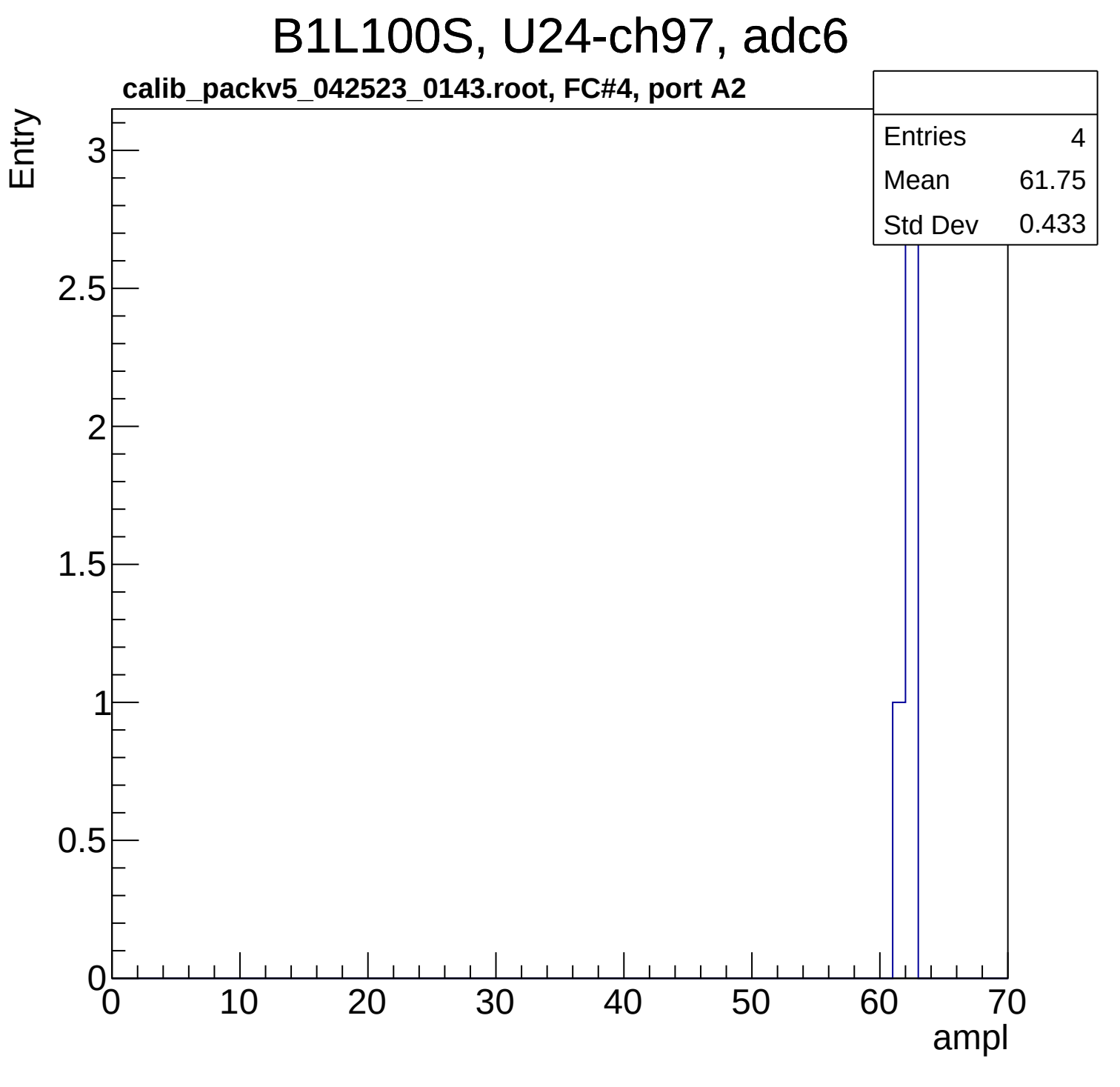
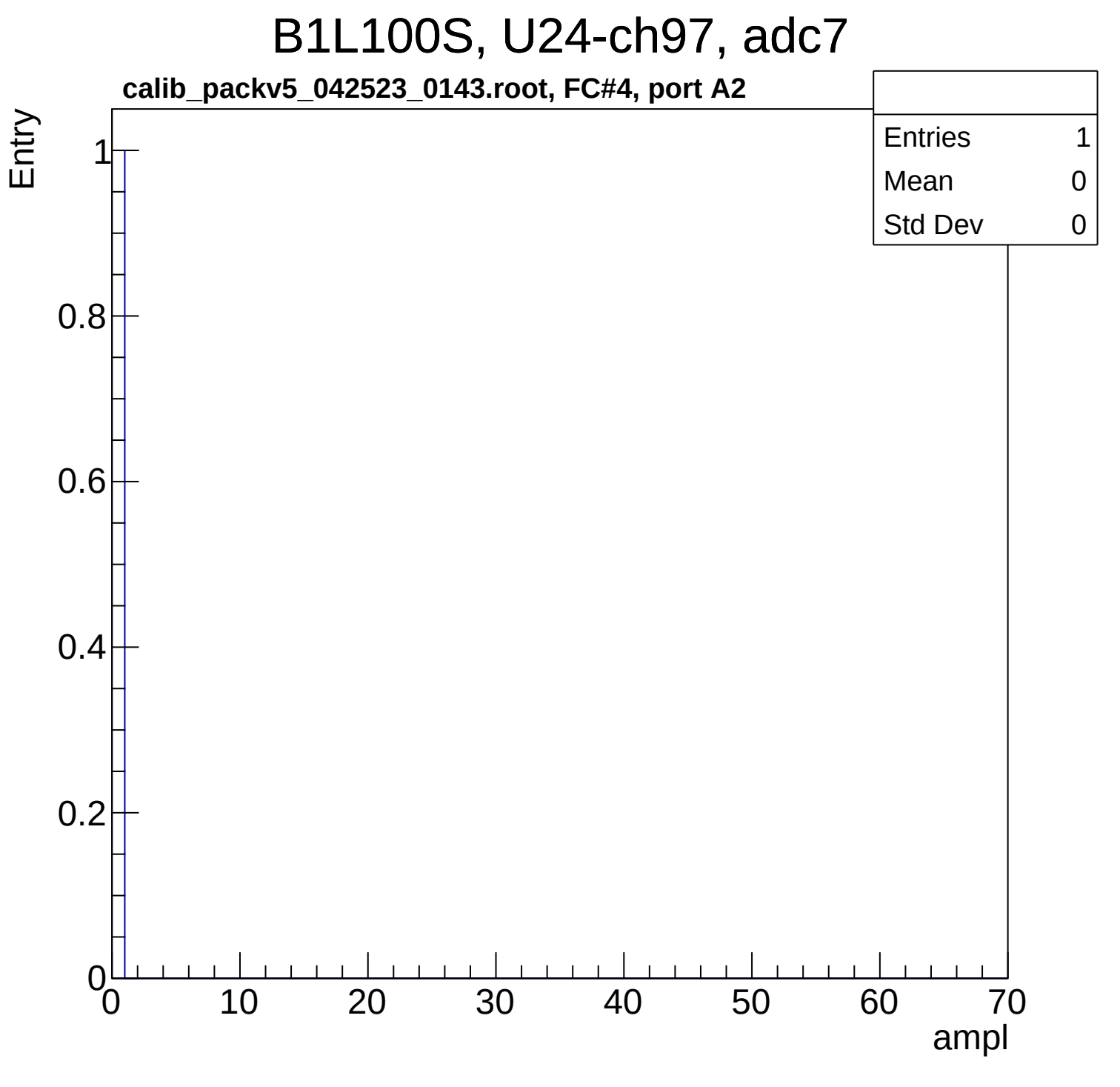

B1L100S, U24-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.97
Std Dev 10.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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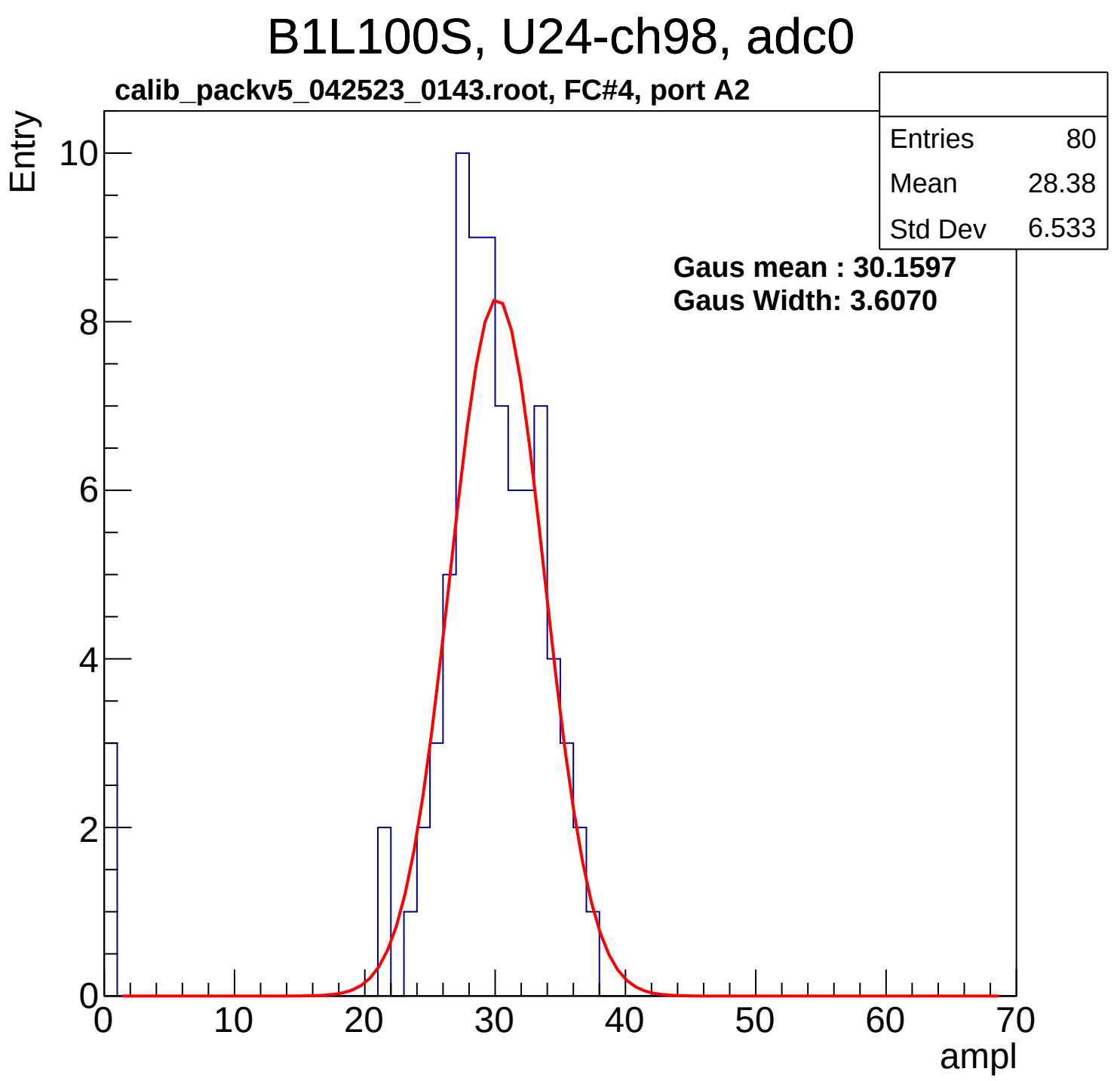
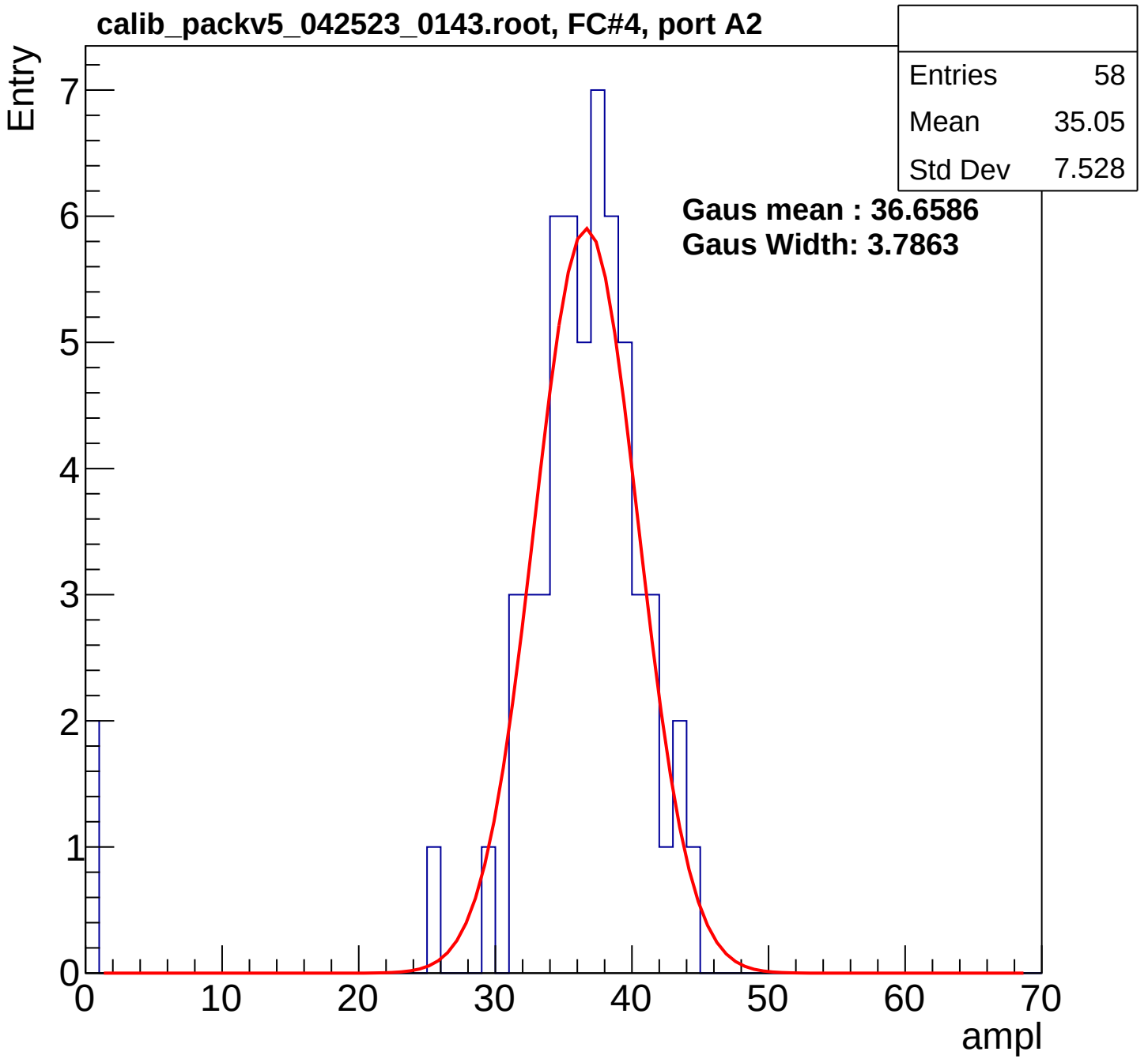
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.38
Std Dev 6.533
Gaus mean : 30.1597
Gaus Width: 3.6070
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 35.05
Std Dev 7.528
Gaus mean : 36.6586
Gaus Width: 3.7863
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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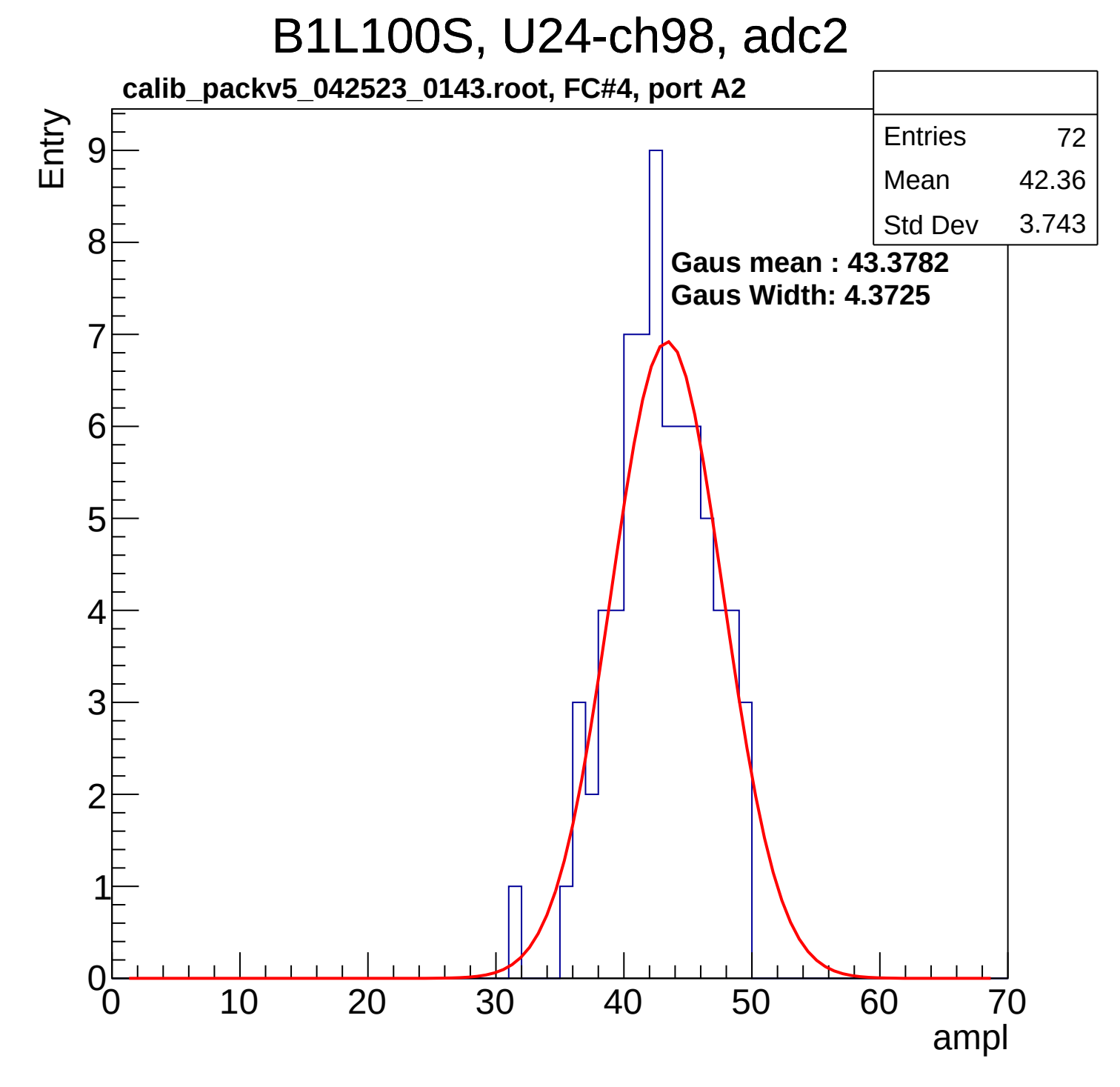
B1L100S, U24-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 42.36
Std Dev 3.743
Gaus mean : 43.3782
Gaus Width: 4.3725
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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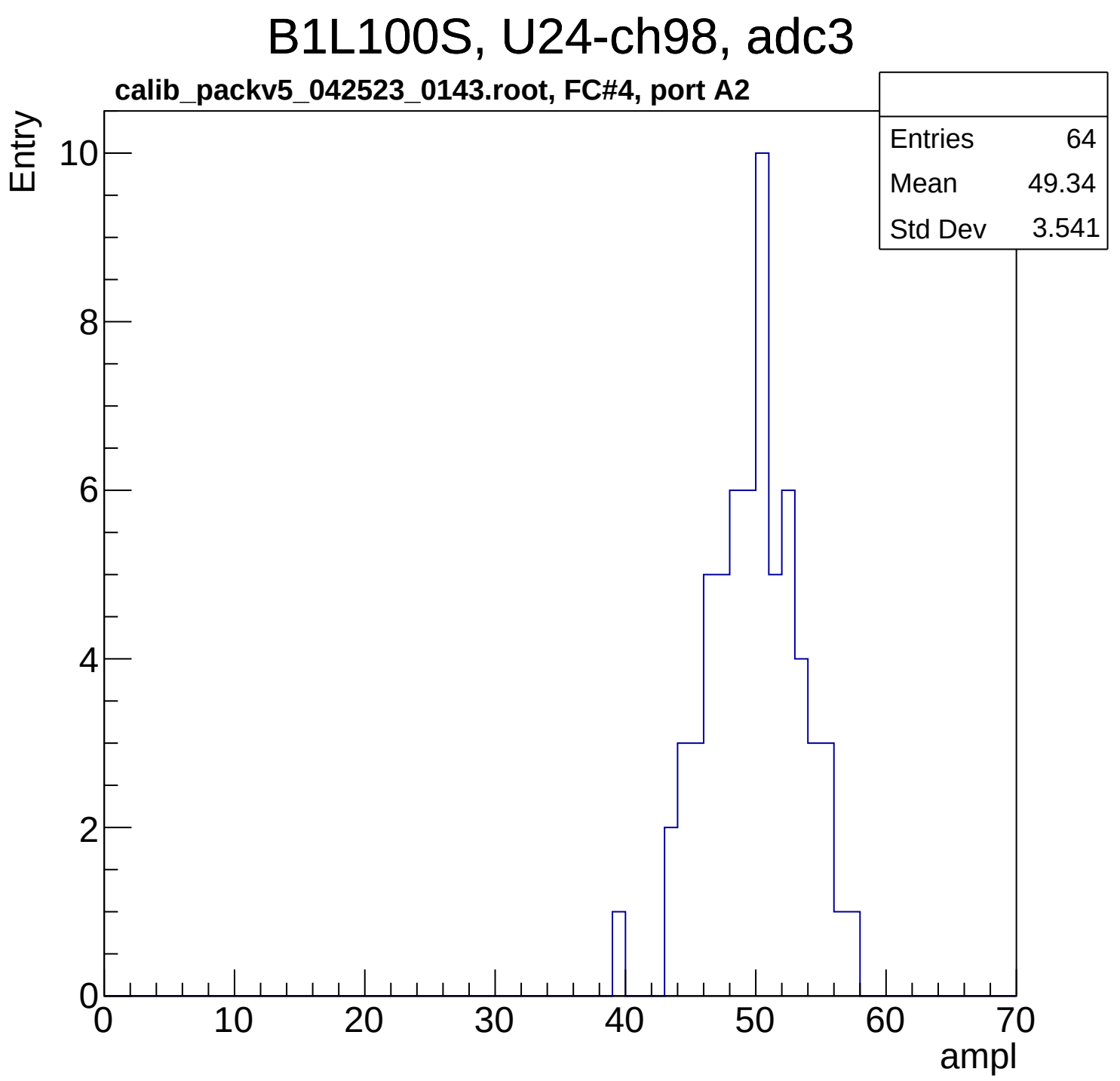
B1L100S, U24-ch98, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.34
Std Dev 3.541
Entry
ampl
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4
6
8
10
0
10
20
30
40
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60
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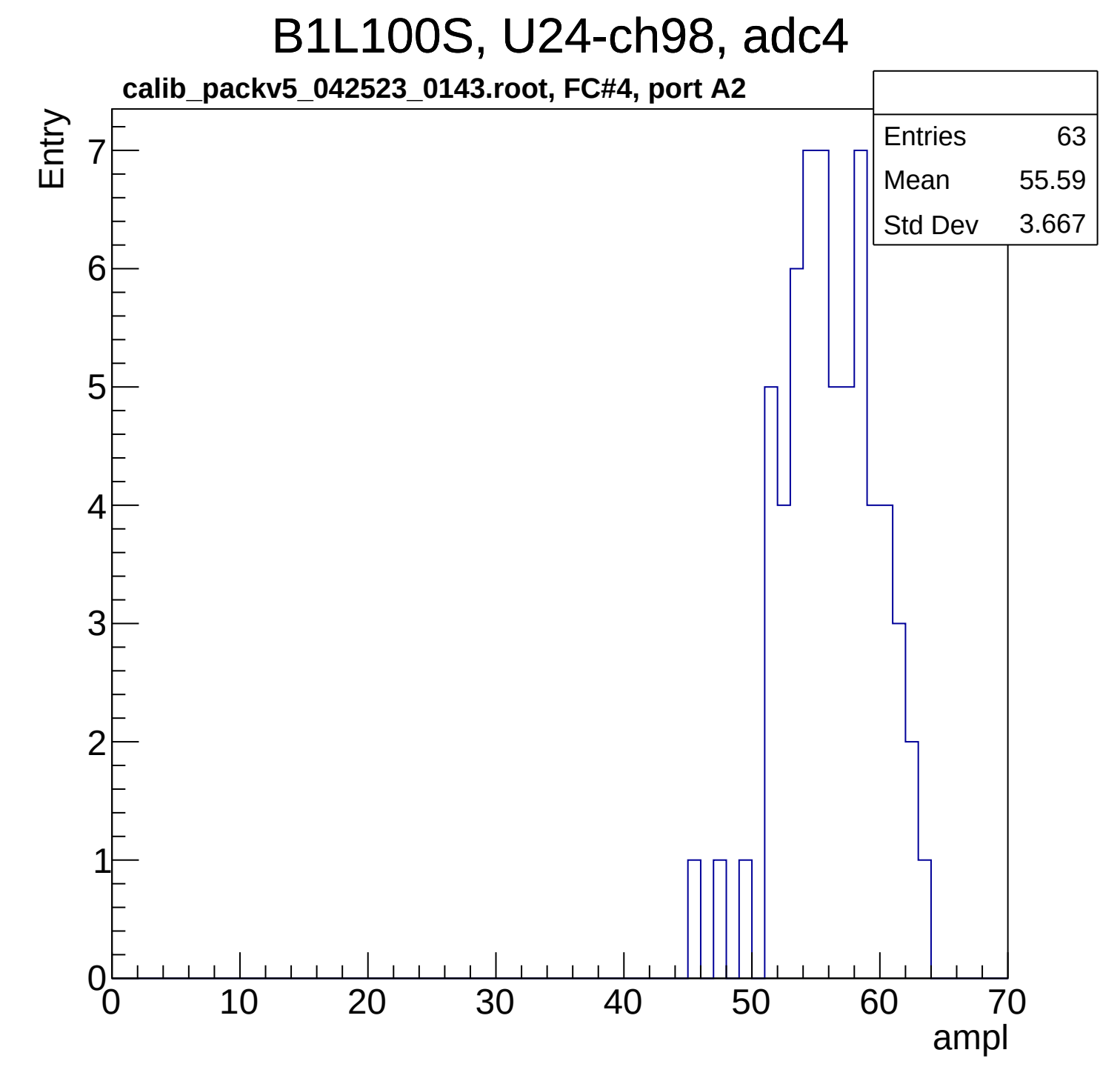
B1L100S, U24-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.59
Std Dev 3.667
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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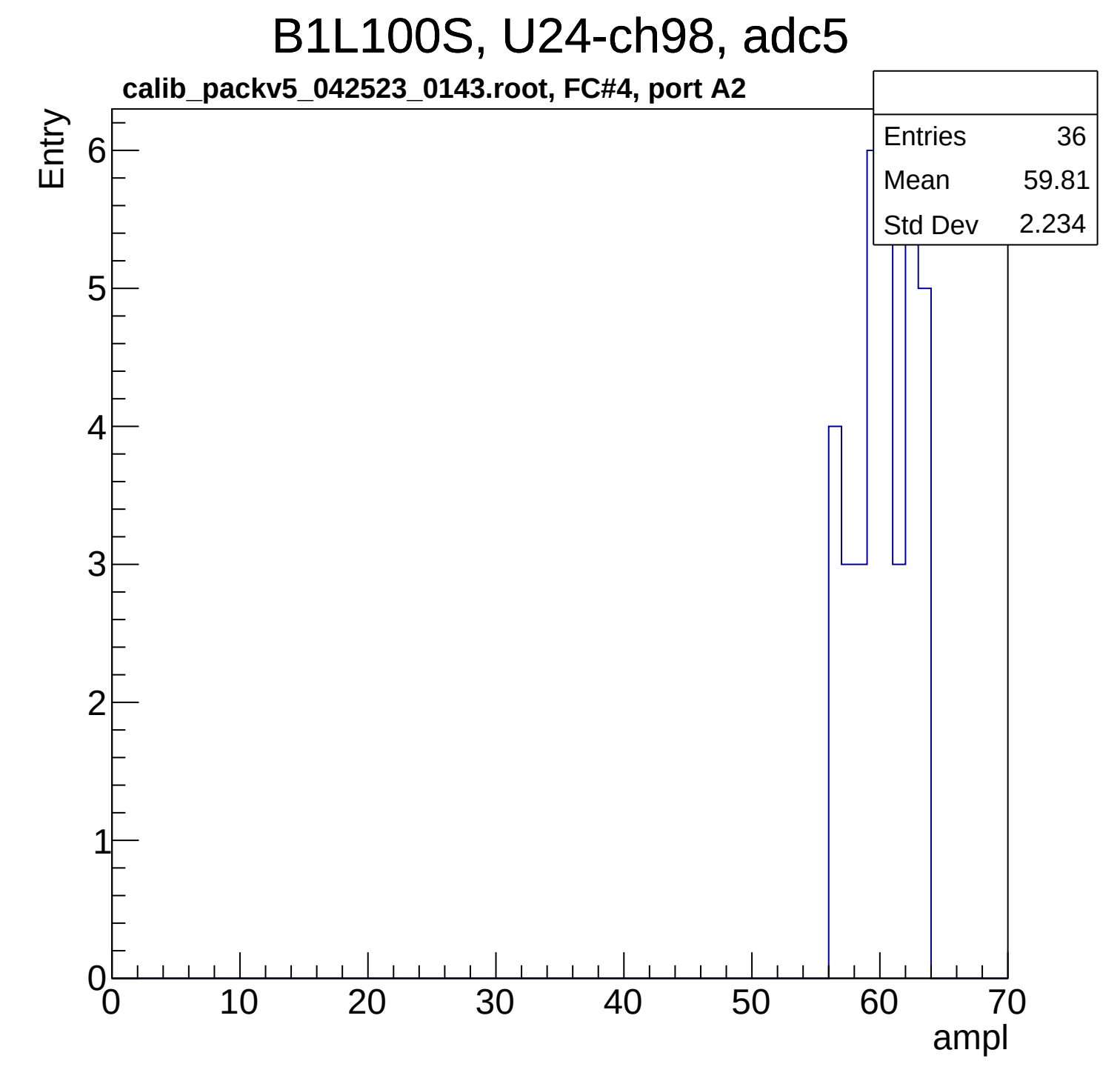
B1L100S, U24-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.81
Std Dev 2.234
Entry
ampl
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2
3
4
5
6
0
10
20
30
40
50
60
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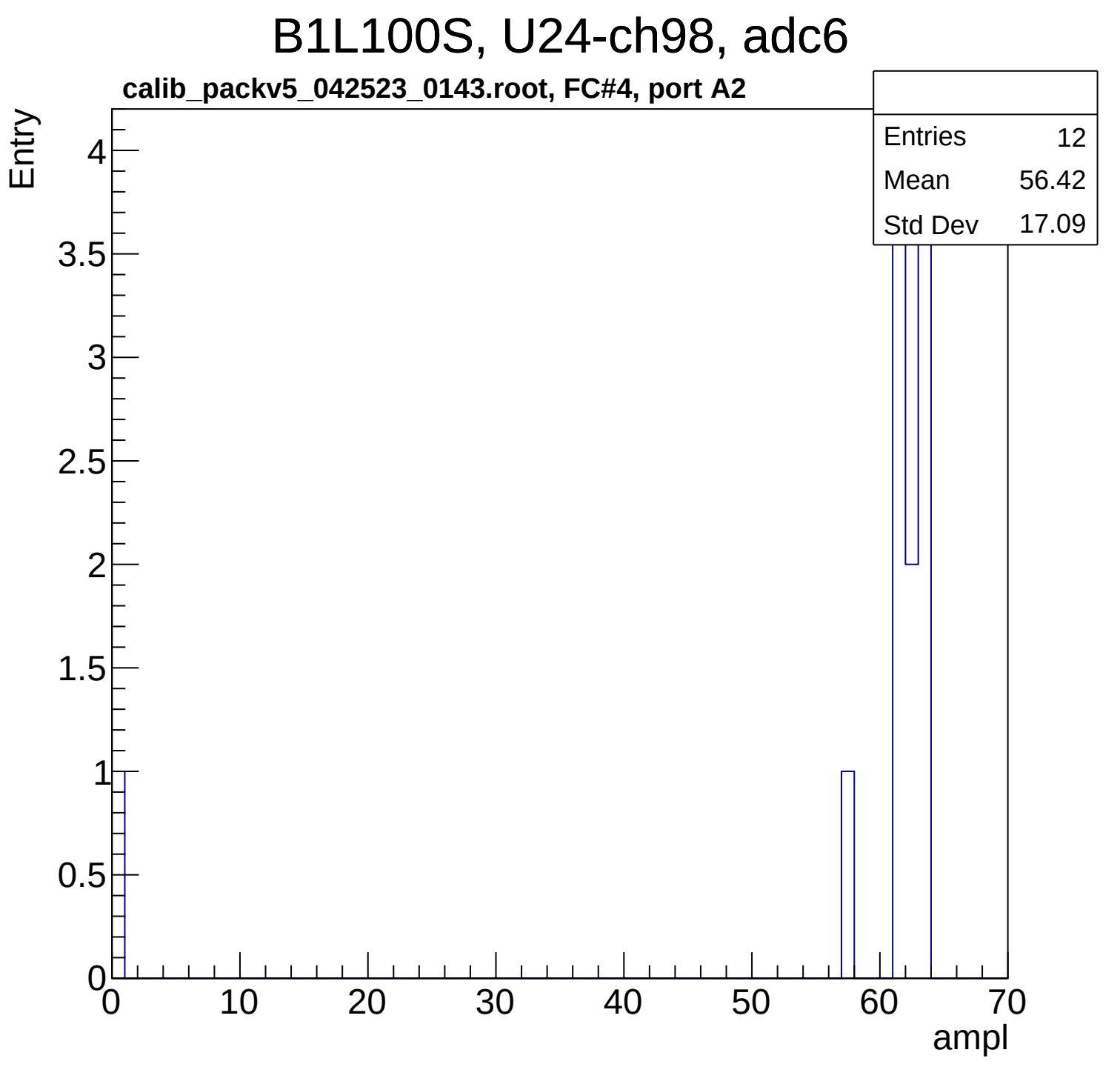
B1L100S, U24-ch98, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.42
Std Dev 17.09
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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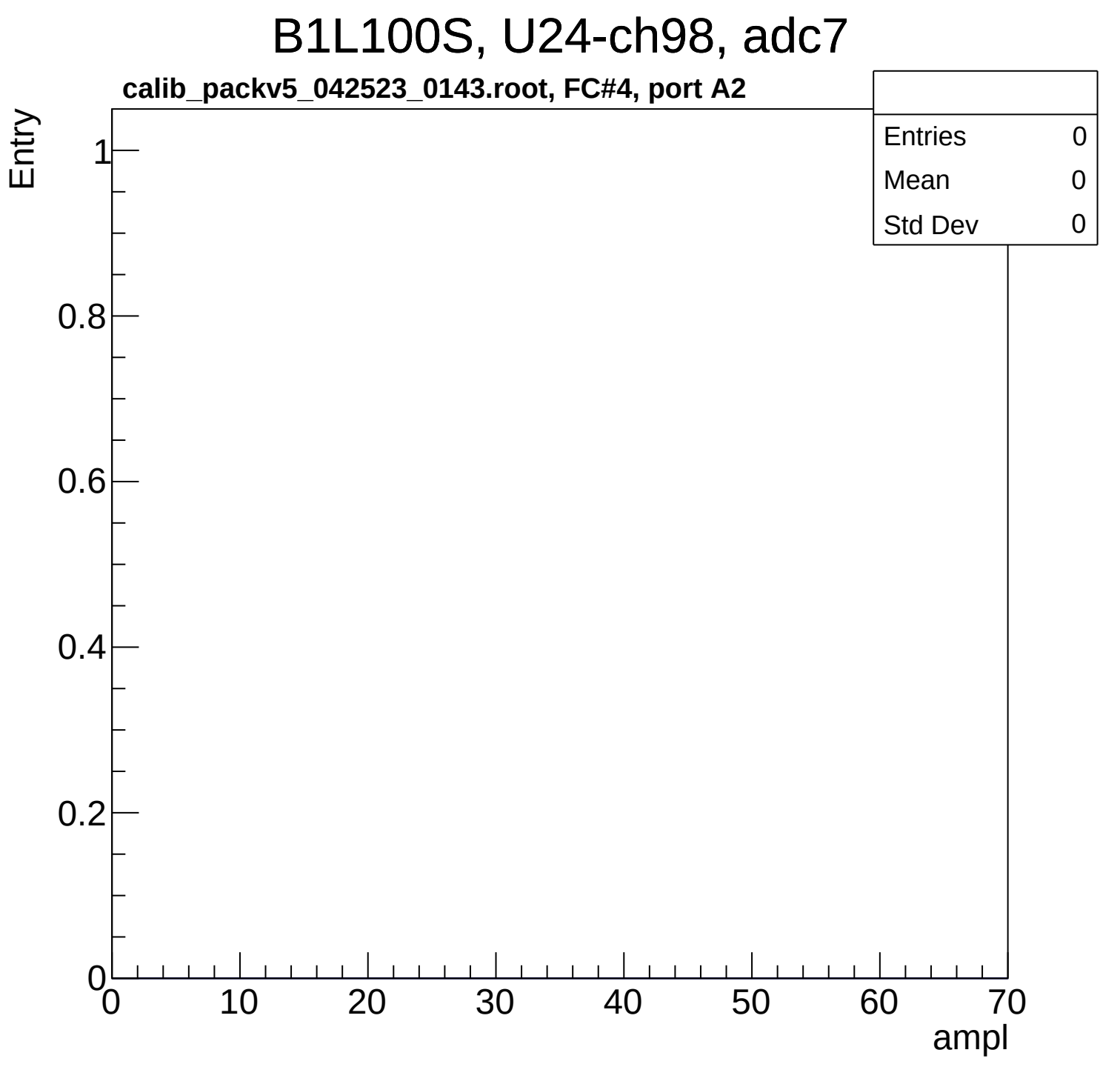
40
50
60
70


B1L100S, U24-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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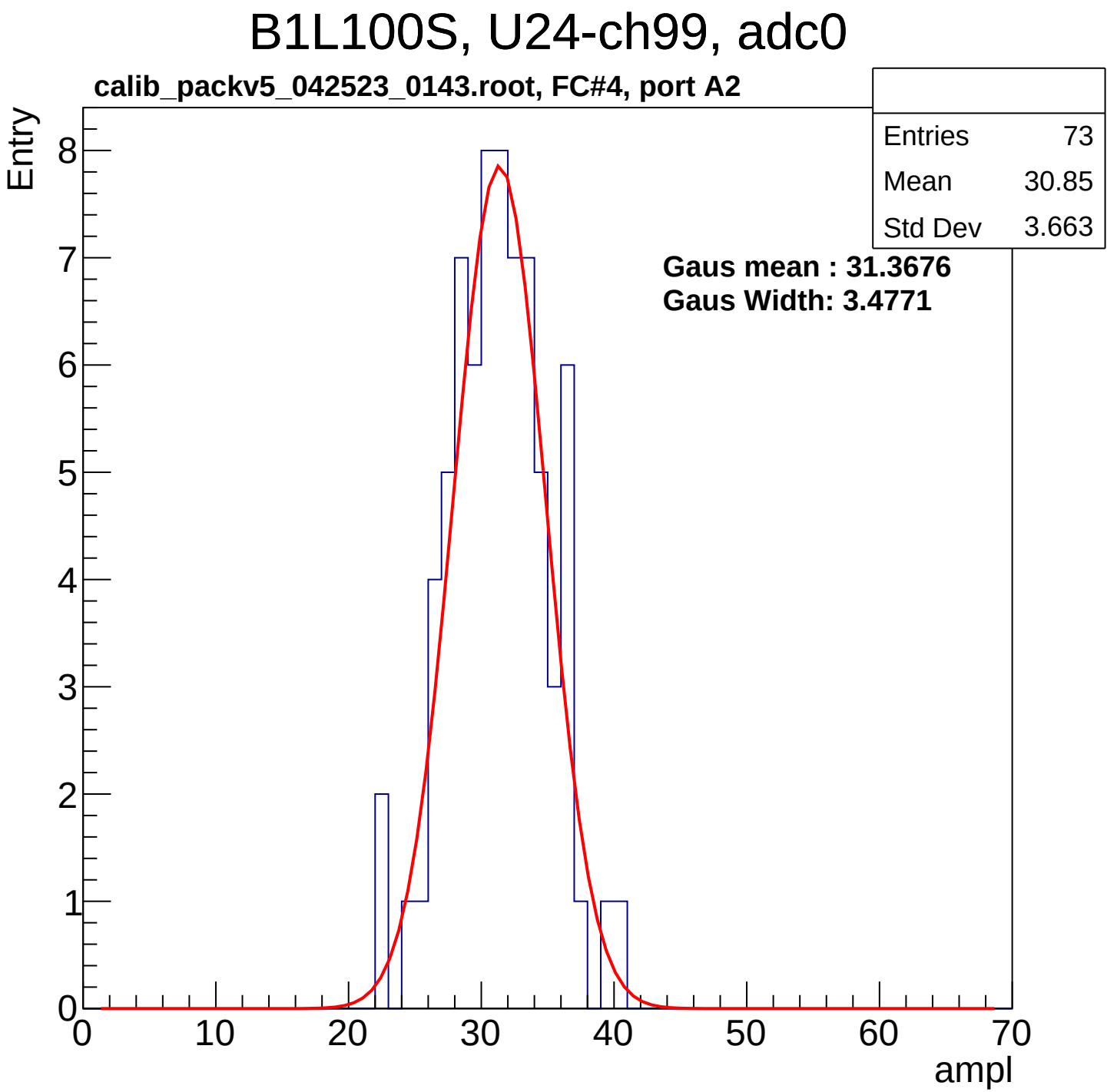
50
60
70
ampl


B1L100S, U24-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.85
Std Dev 3.663
Gaus mean : 31.3676
Gaus Width: 3.4771
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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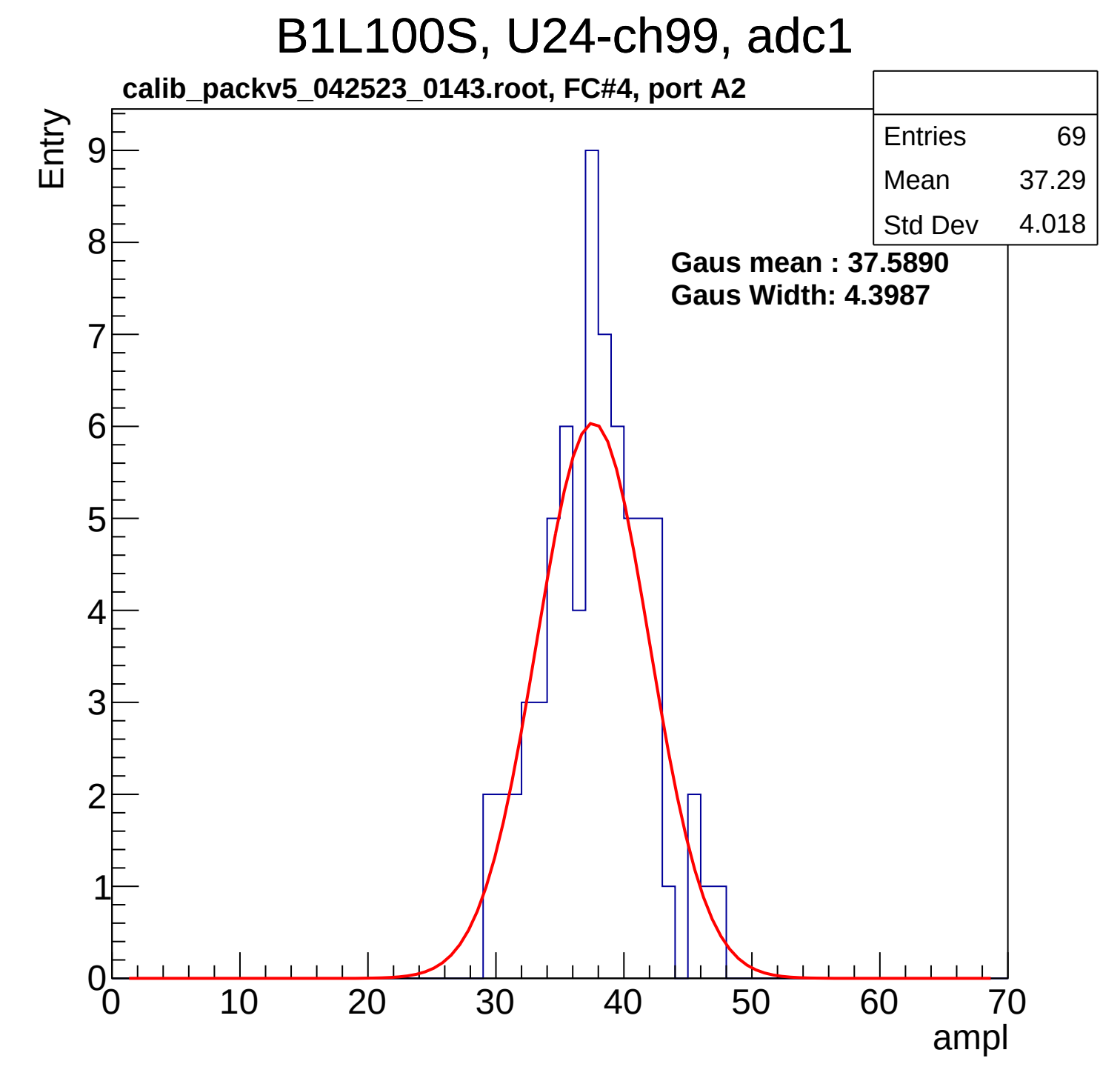
B1L100S, U24-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.29
Std Dev 4.018
Gaus mean : 37.5890
Gaus Width: 4.3987
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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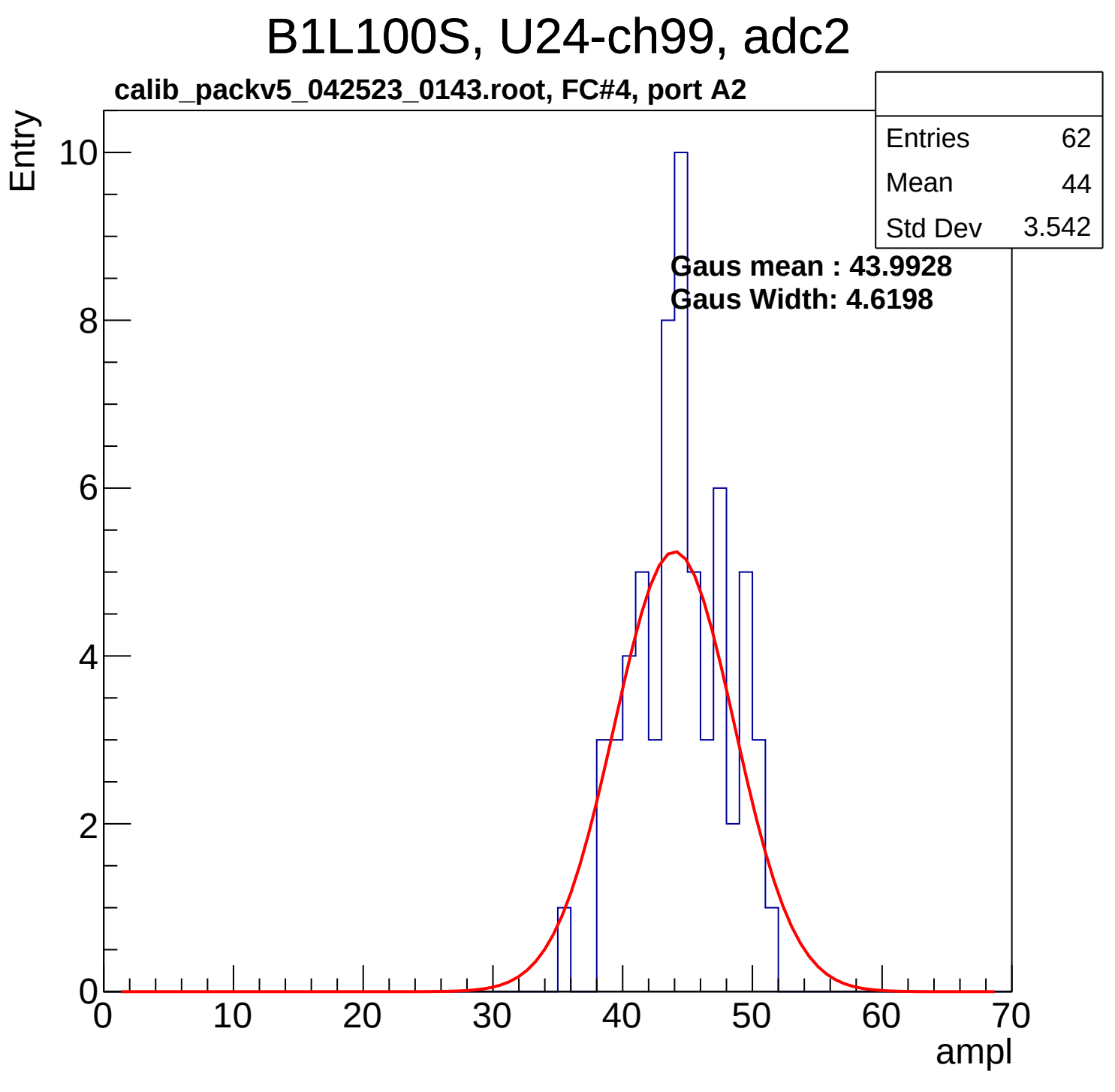
B1L100S, U24-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44
Std Dev 3.542
Gaus mean : 43.9928
Gaus Width: 4.6198
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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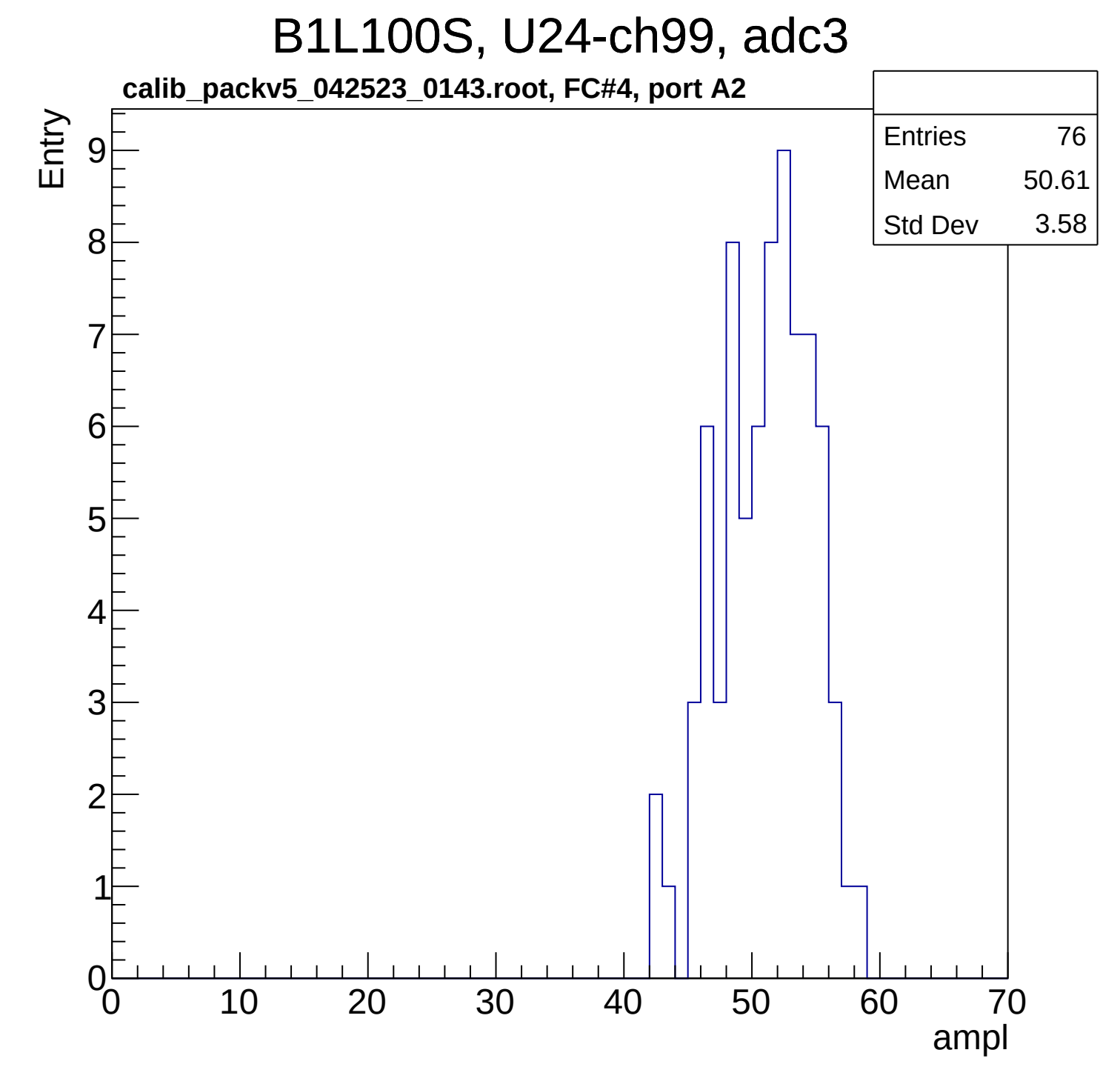
B1L100S, U24-ch99, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 50.61
Std Dev 3.58
Entry
ampl
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6
7
8
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0
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60
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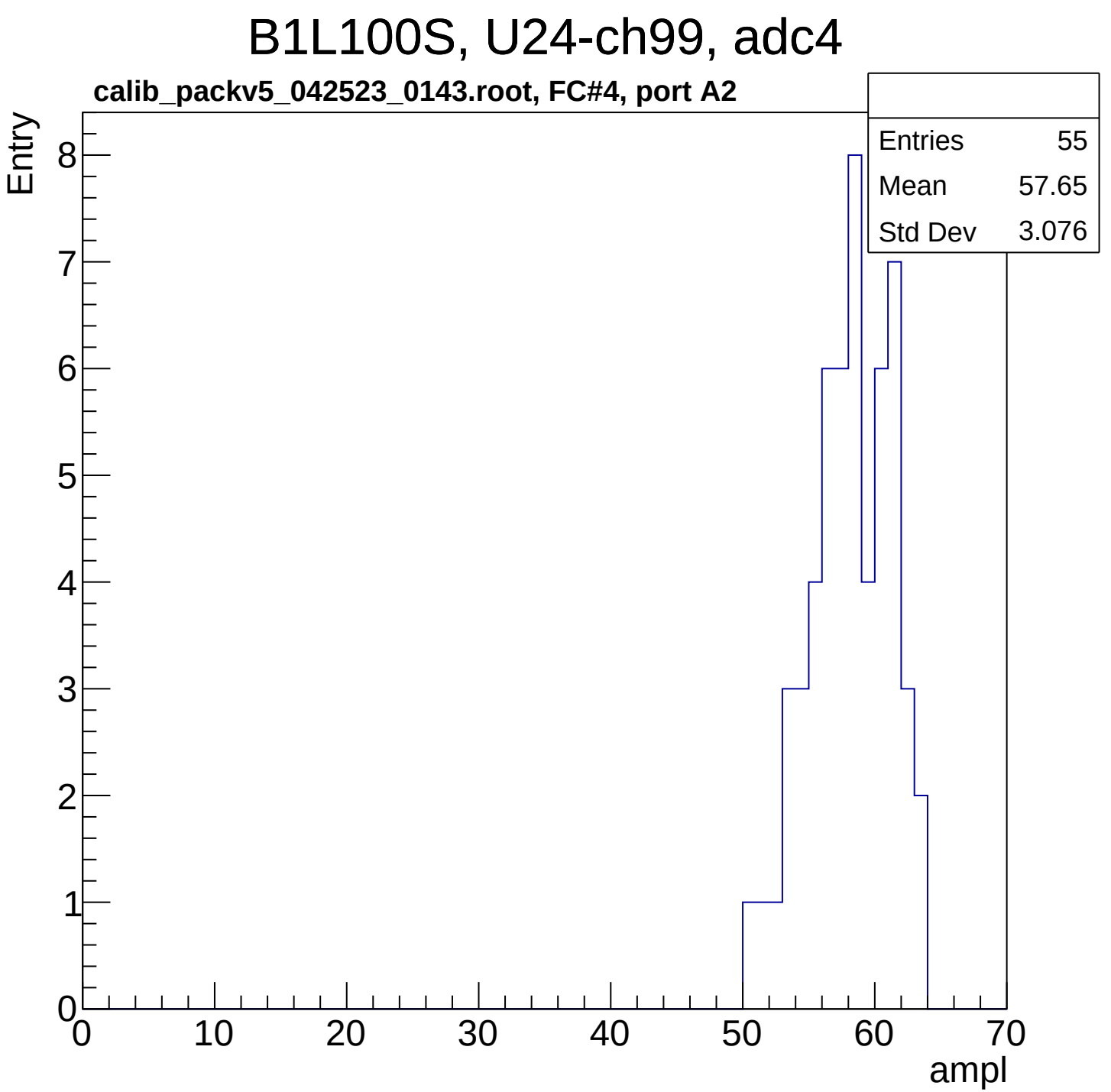
B1L100S, U24-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 57.65
Std Dev 3.076
Entry
ampl
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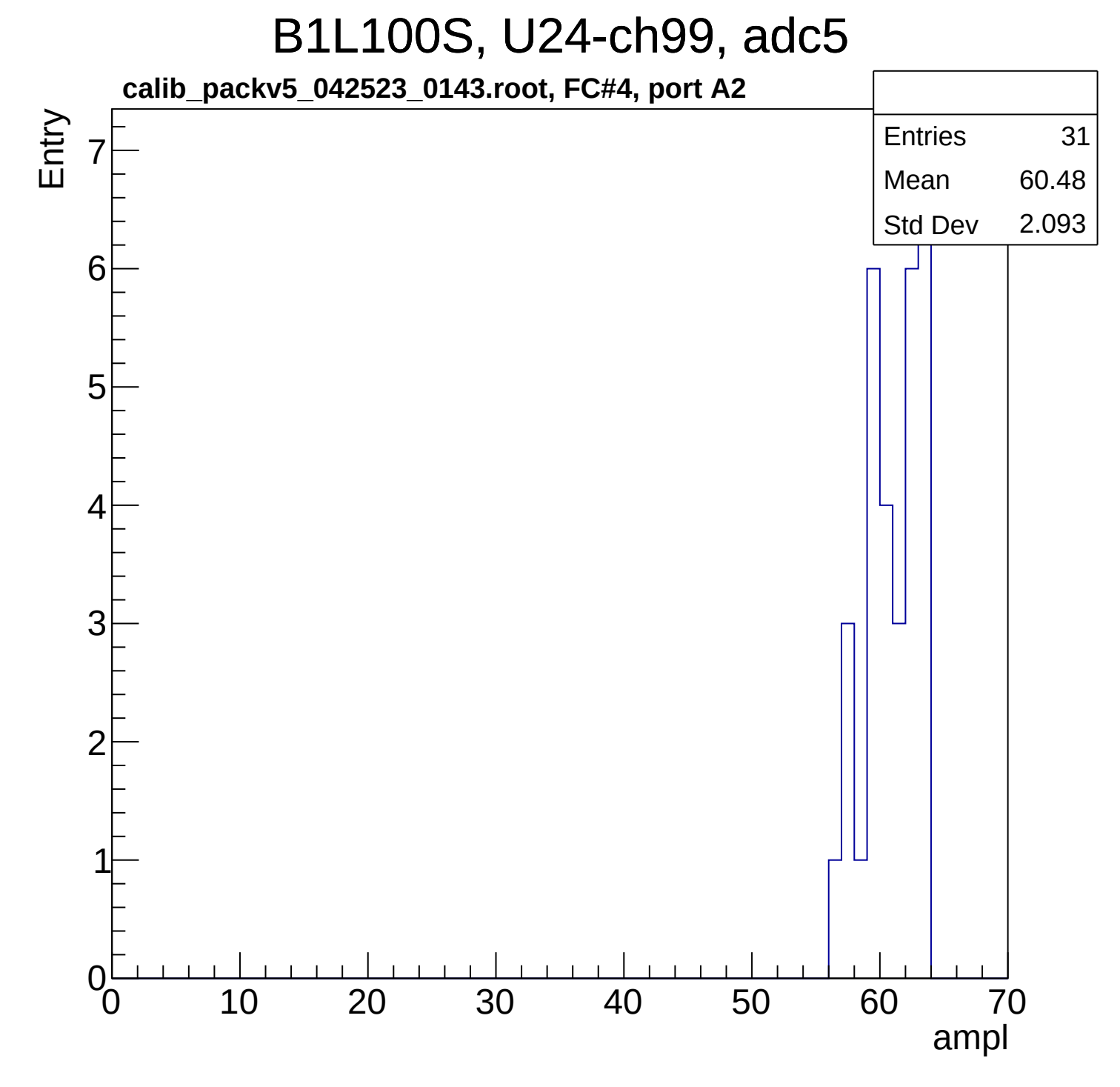
B1L100S, U24-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.48
Std Dev 2.093
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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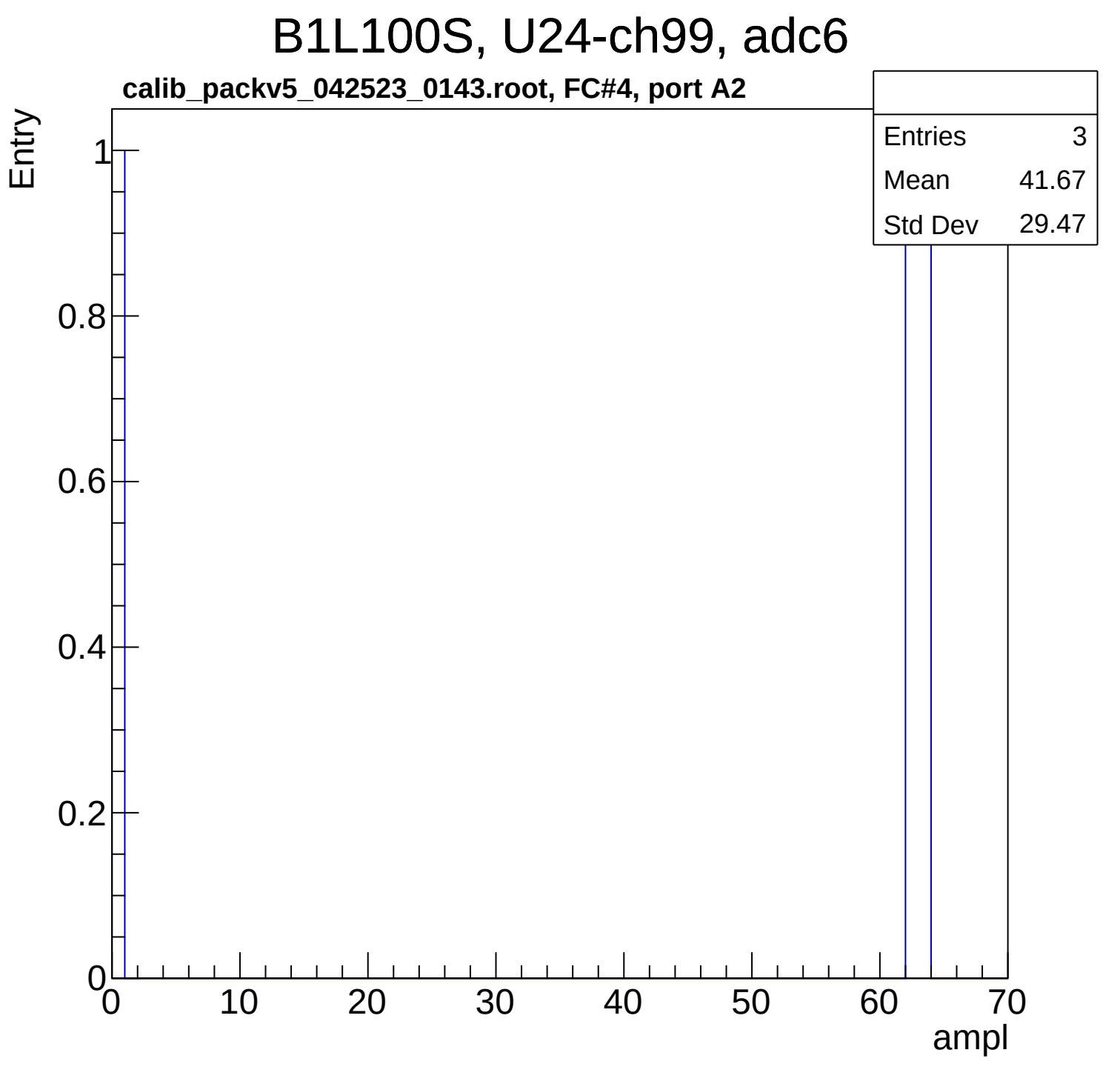
B1L100S, U24-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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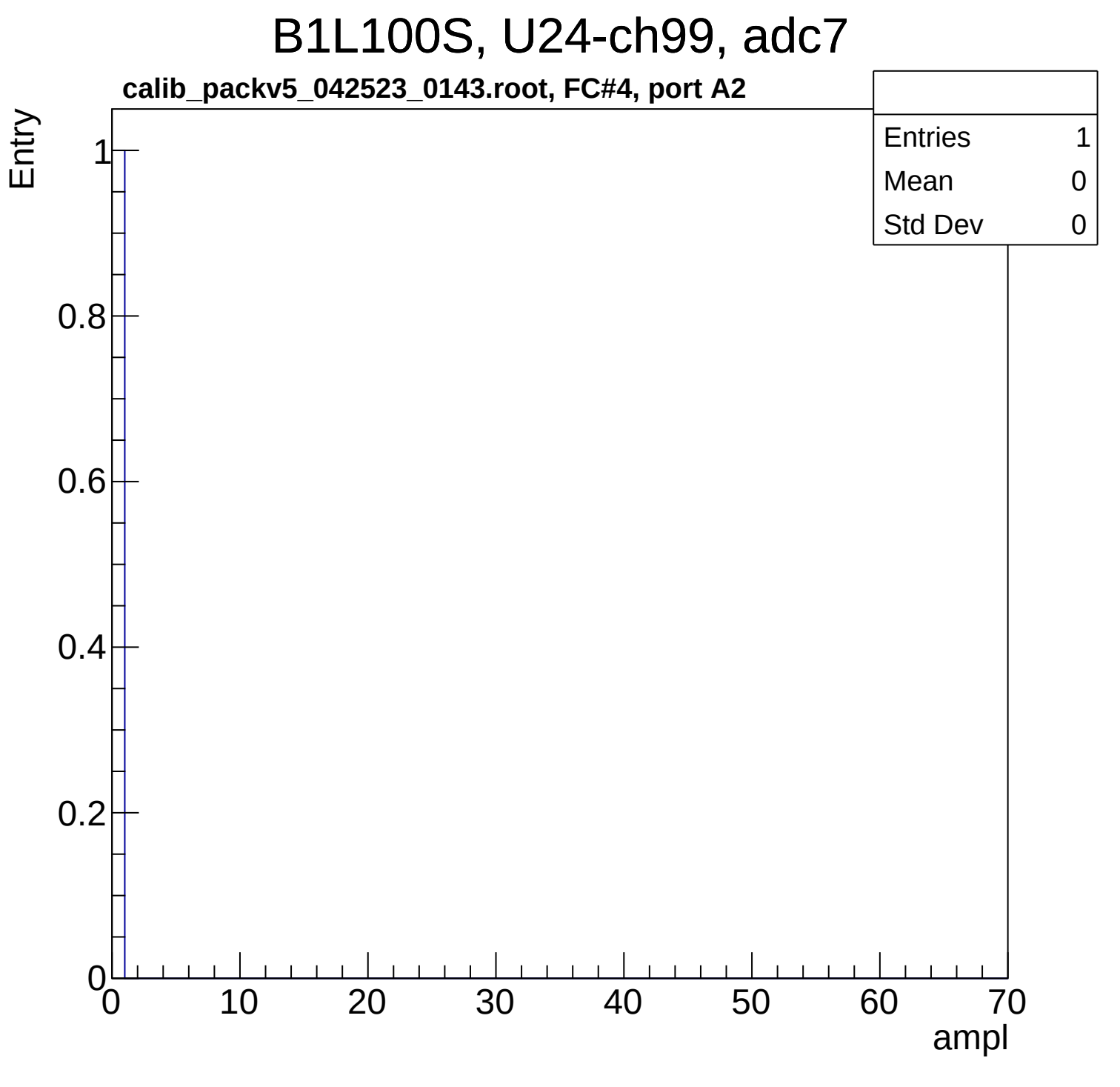
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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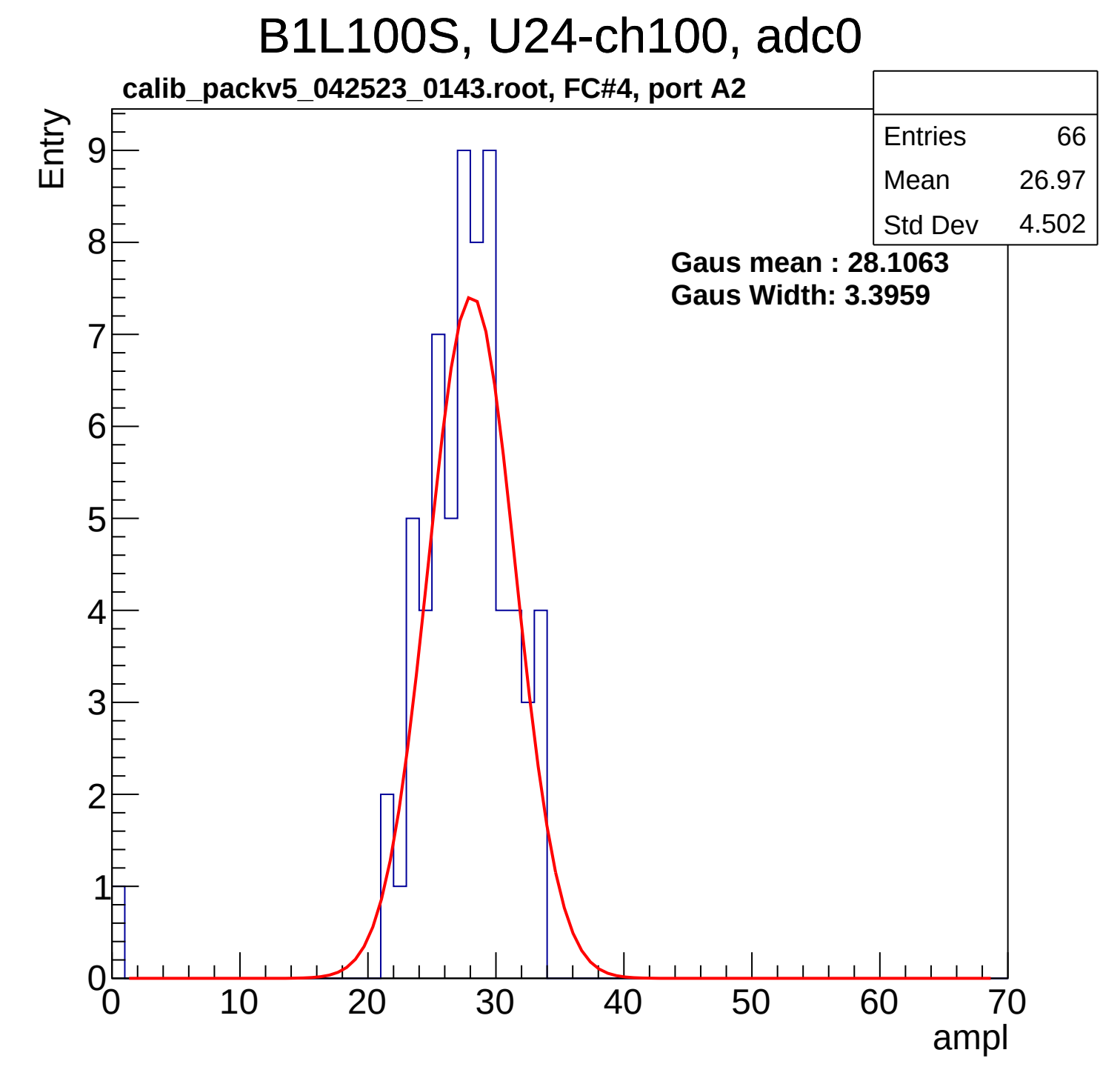
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch100, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 26.97
Std Dev 4.502
Gaus mean : 28.1063
Gaus Width: 3.3959
Entry
ampl
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3
4
5
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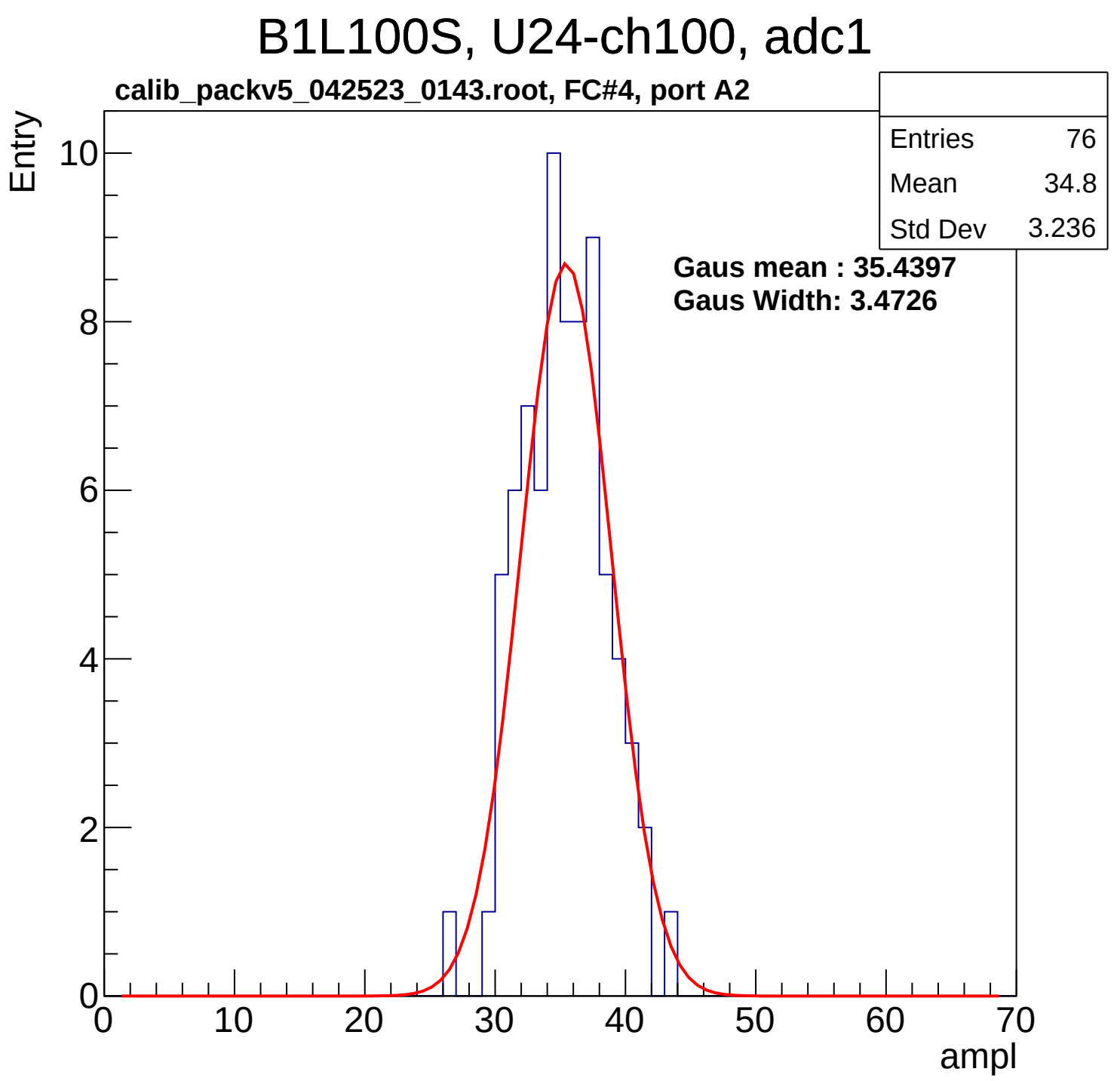
B1L100S, U24-ch100, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 34.8
Std Dev 3.236
Gaus mean : 35.4397
Gaus Width: 3.4726
Entry
ampl
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8
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0
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40
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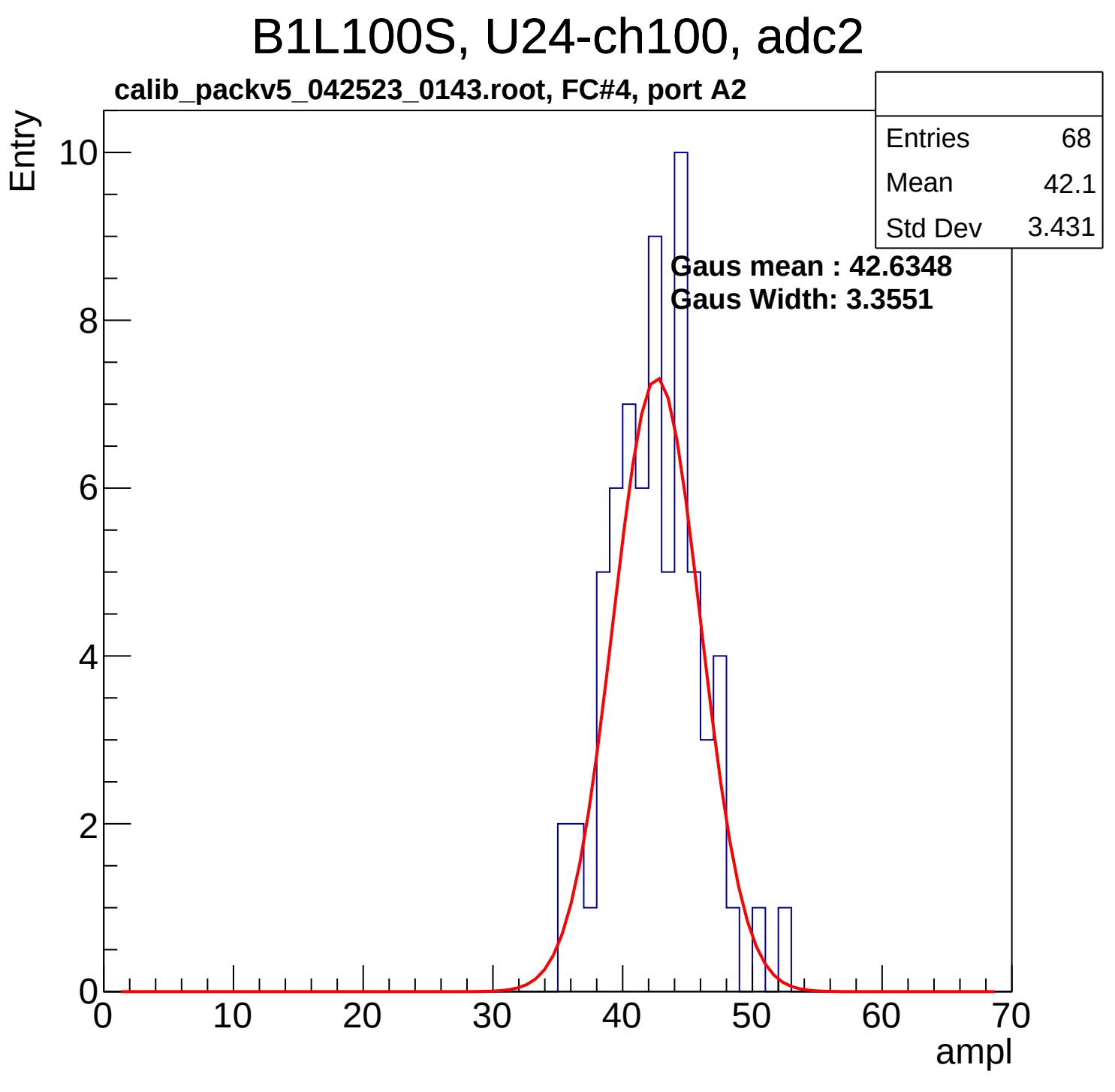
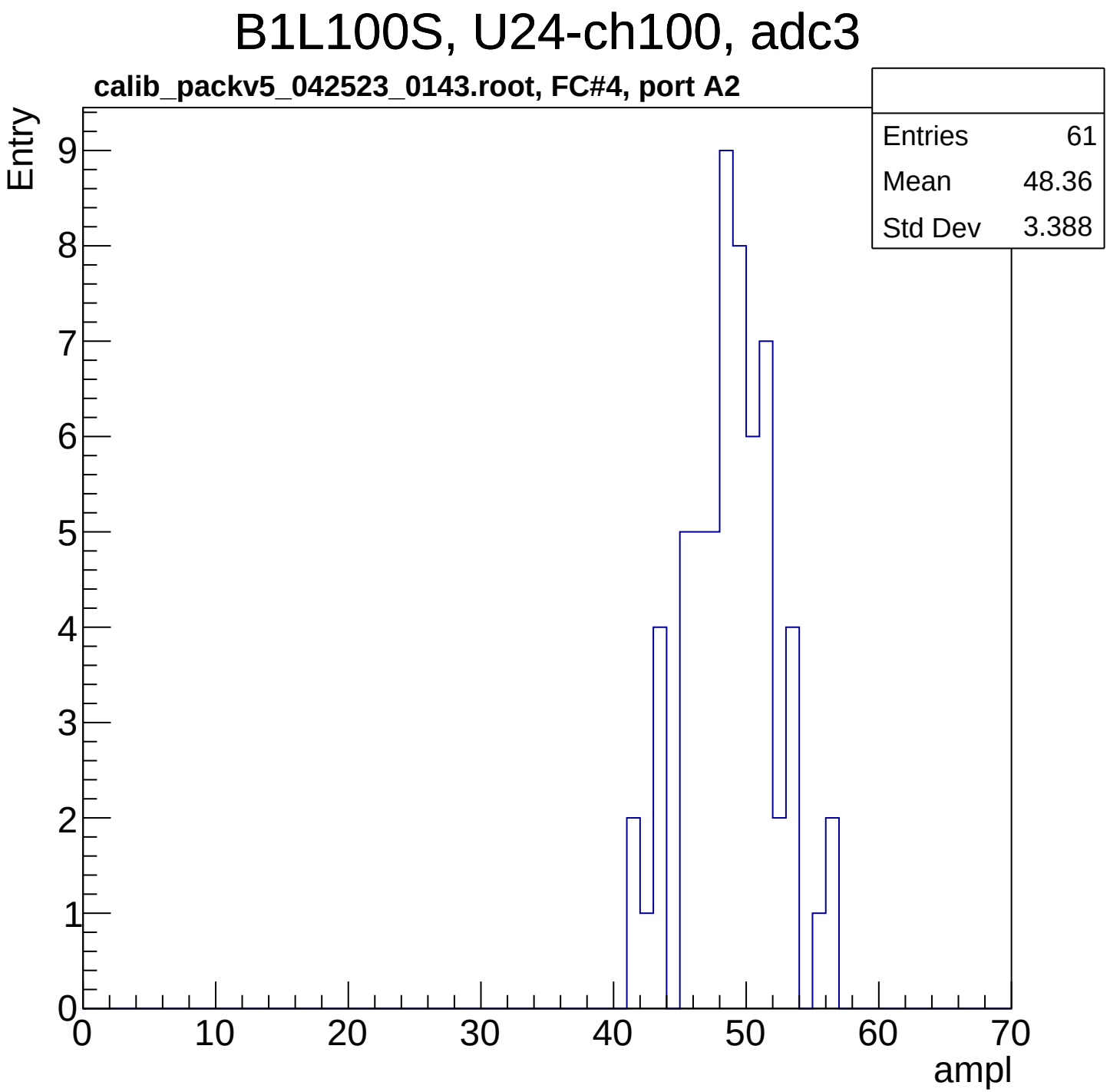

B1L100S, U24-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.1
Std Dev 3.431
Gaus mean : 42.6348
Gaus Width: 3.3551
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch100, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 48.36
Std Dev 3.388
Entry
ampl
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2
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4
5
6
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8
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0
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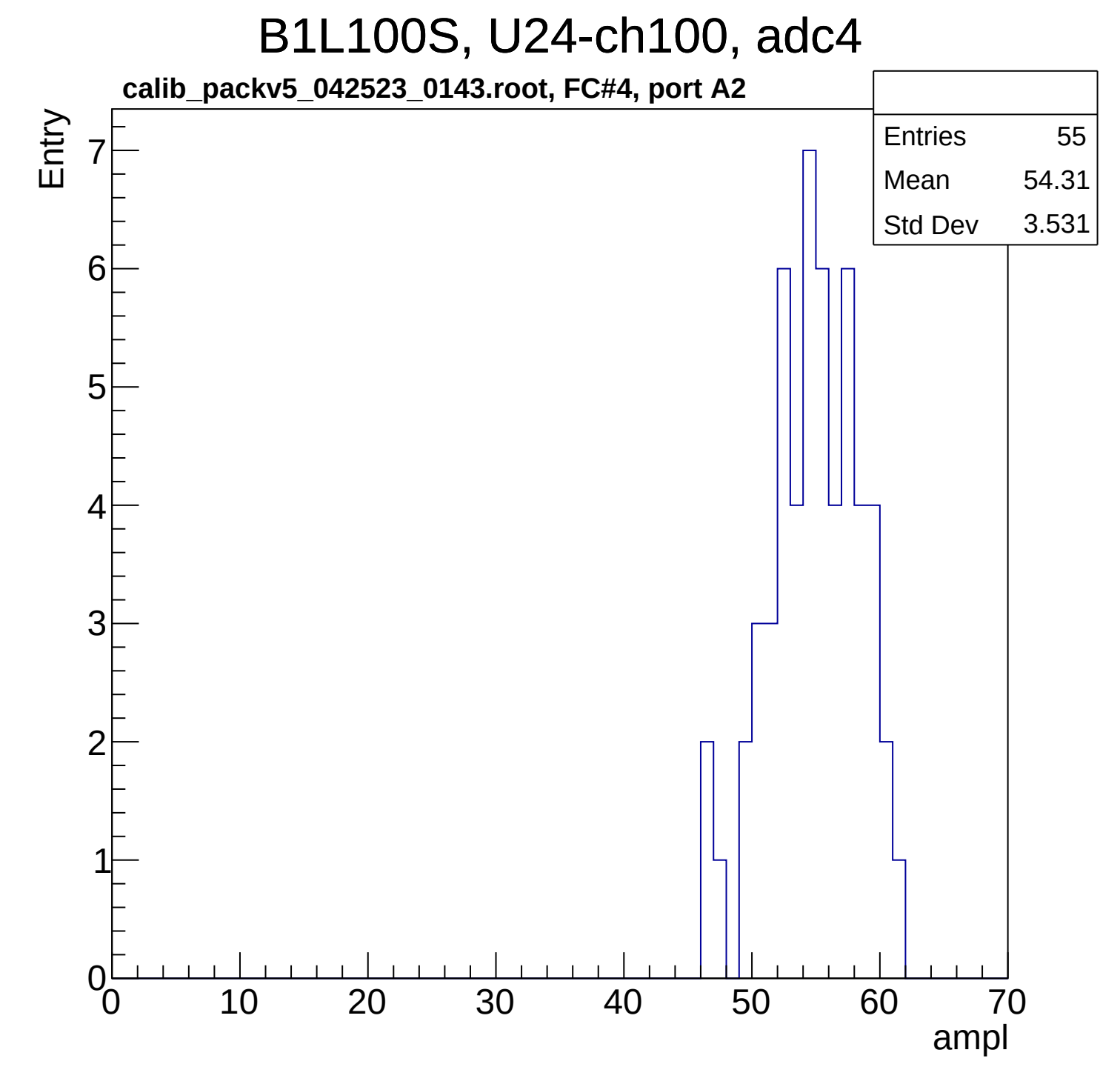
B1L100S, U24-ch100, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 54.31
Std Dev 3.531
Entry
ampl
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1
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4
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0
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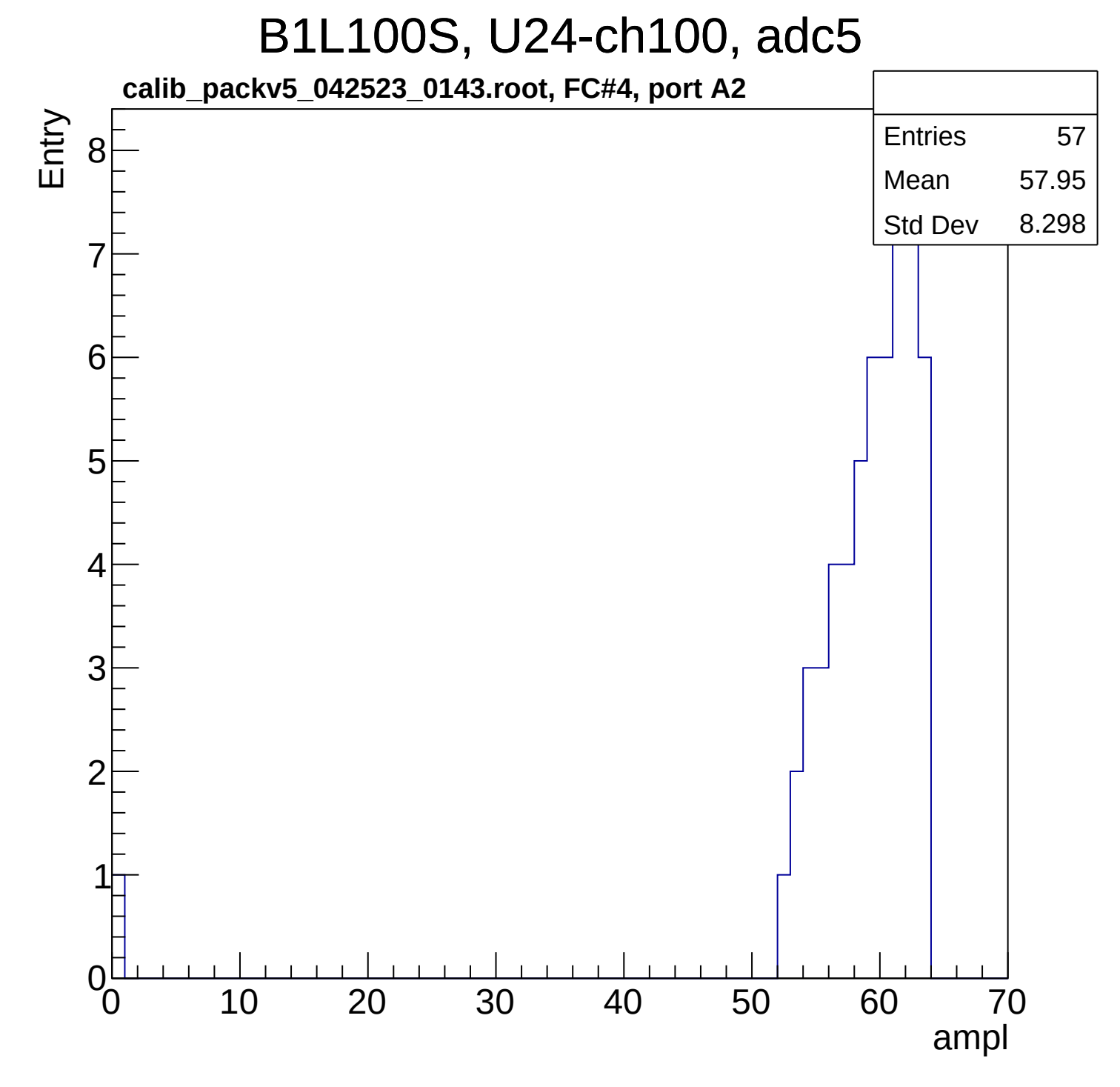
B1L100S, U24-ch100, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.95
Std Dev 8.298
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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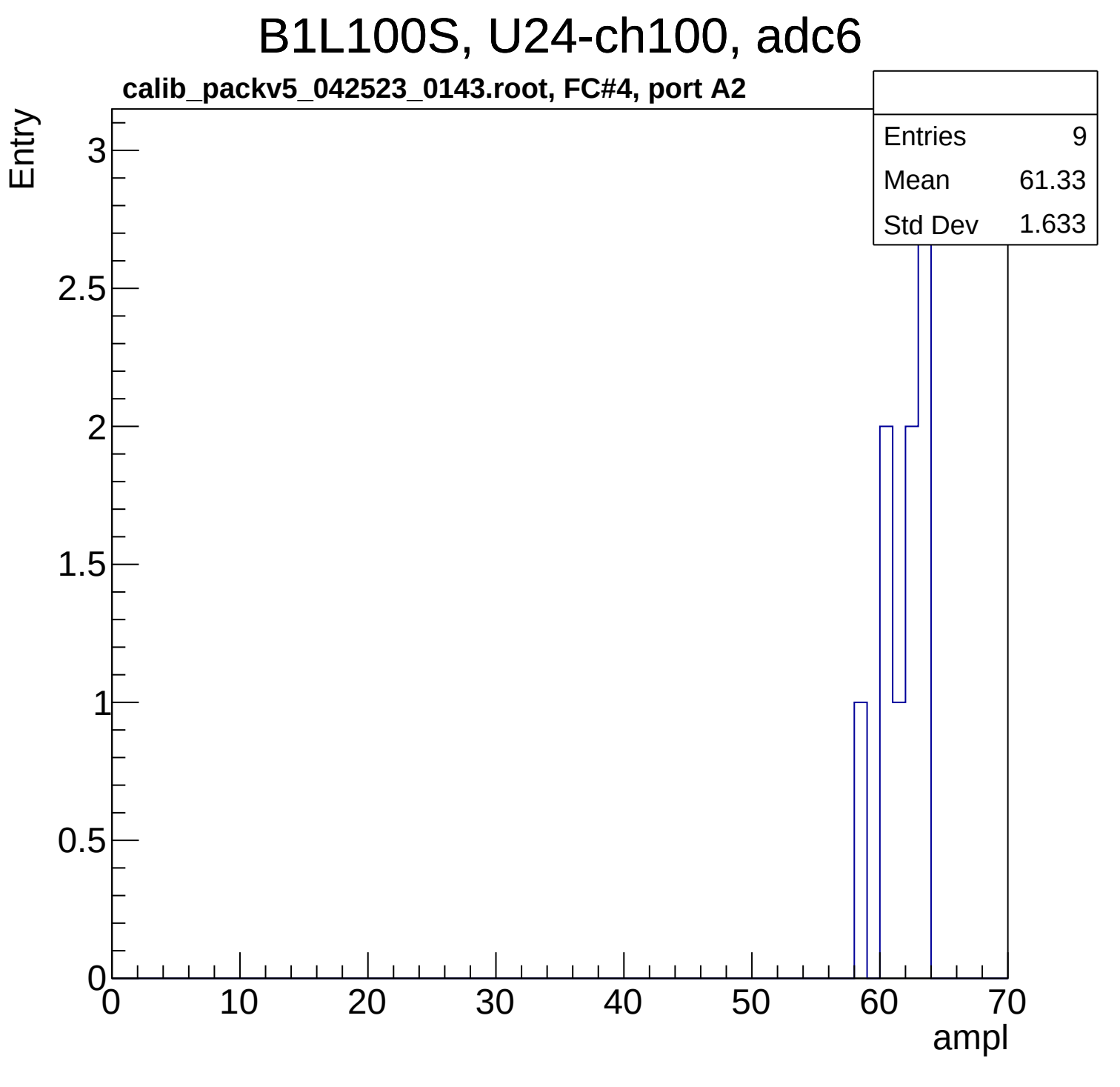
B1L100S, U24-ch100, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.33
Std Dev 1.633
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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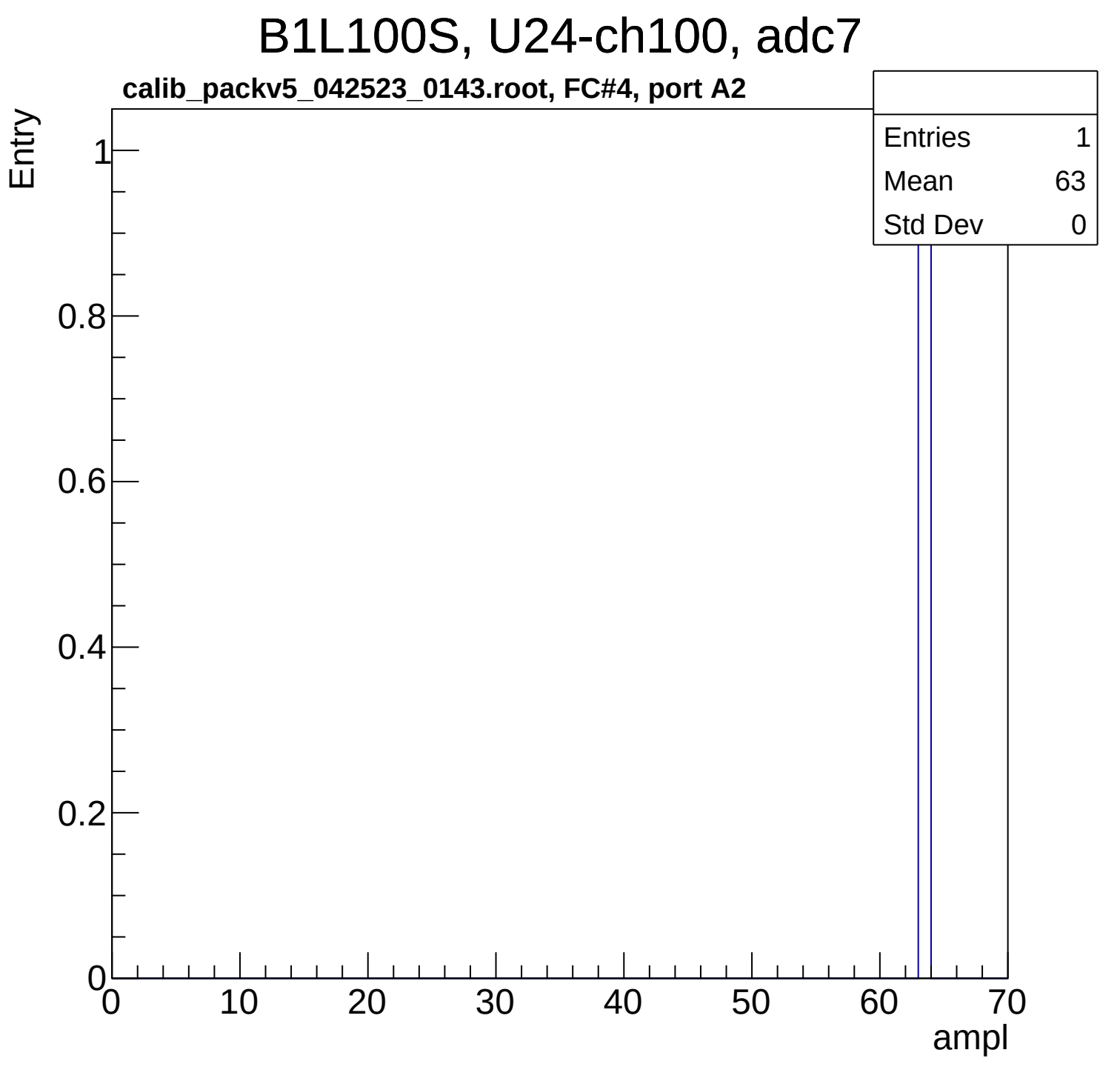
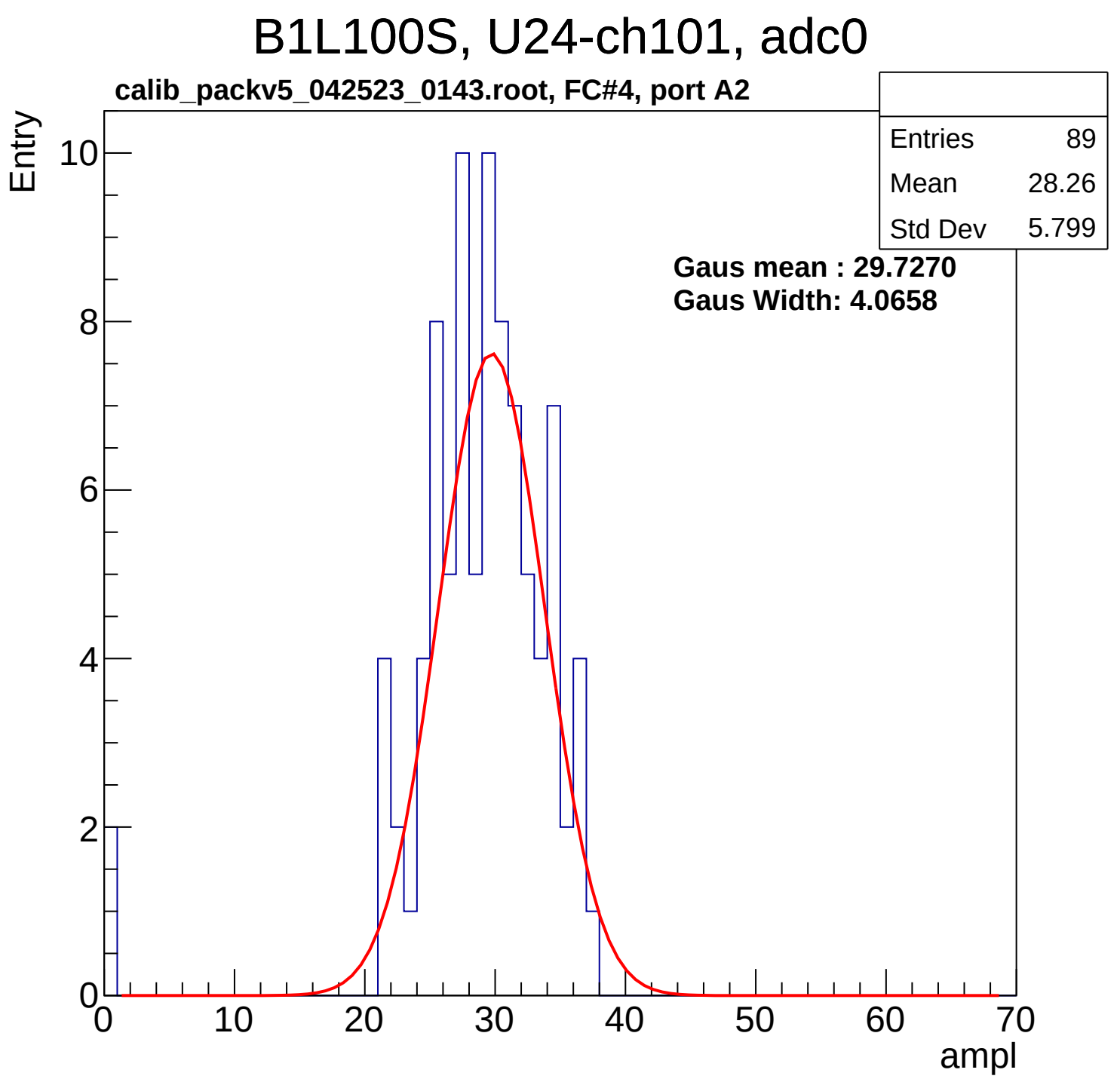

B1L100S, U24-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 28.26
Std Dev 5.799
Gaus mean : 29.7270
Gaus Width: 4.0658
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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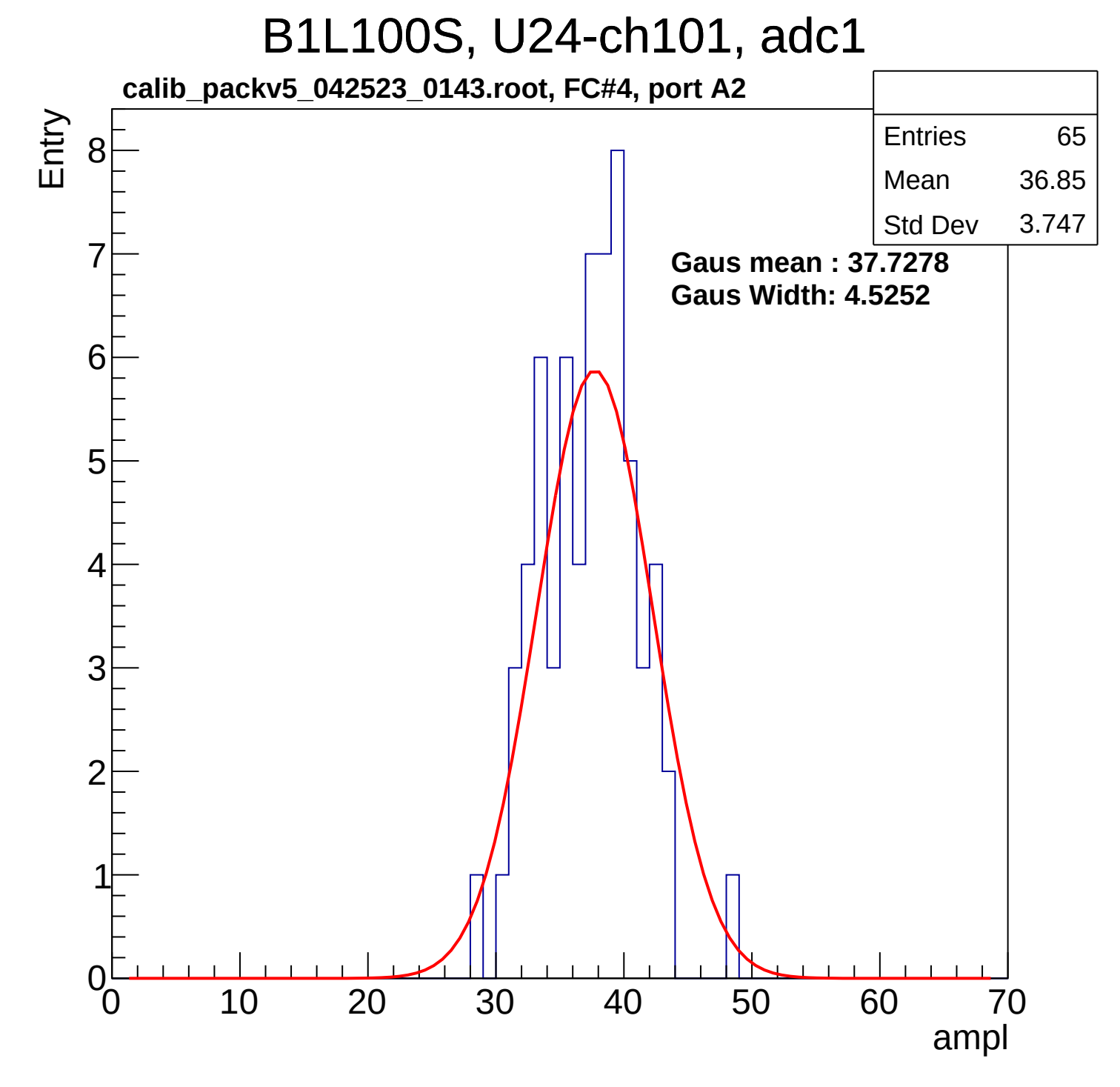
B1L100S, U24-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.85
Std Dev 3.747
Gaus mean : 37.7278
Gaus Width: 4.5252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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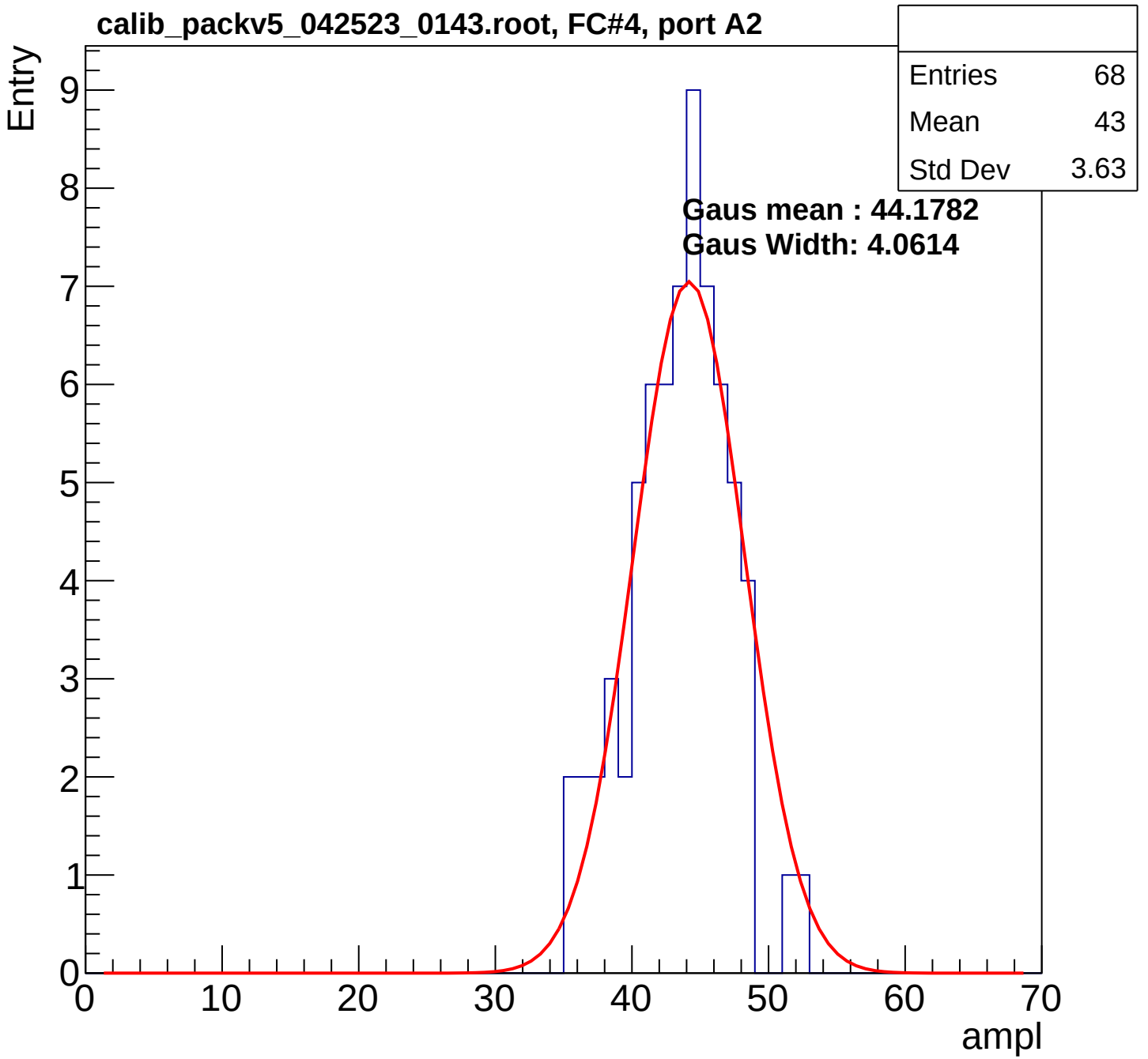
B1L100S, U24-ch101, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43
Std Dev 3.63
Gaus mean : 44.1782
Gaus Width: 4.0614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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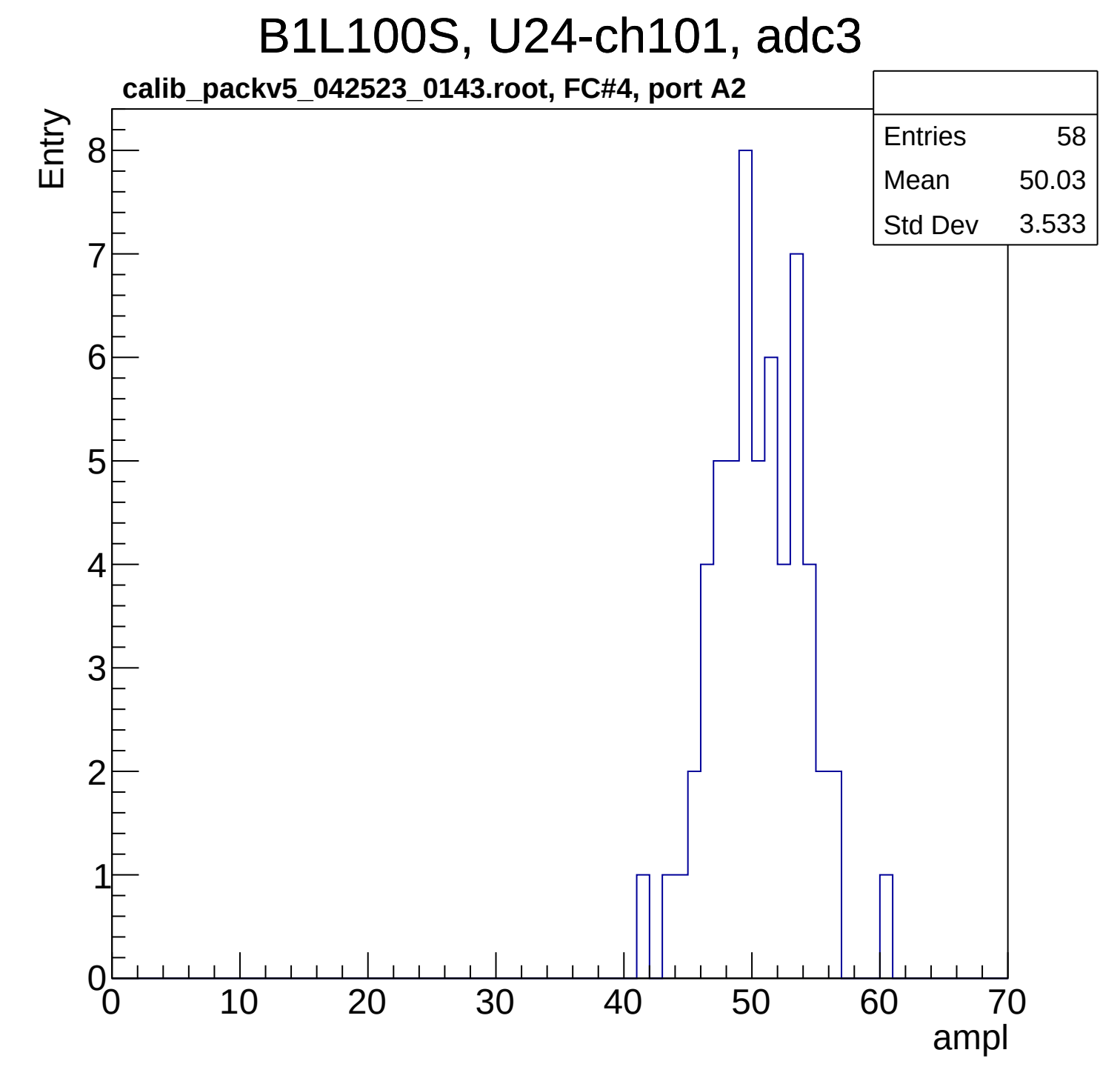
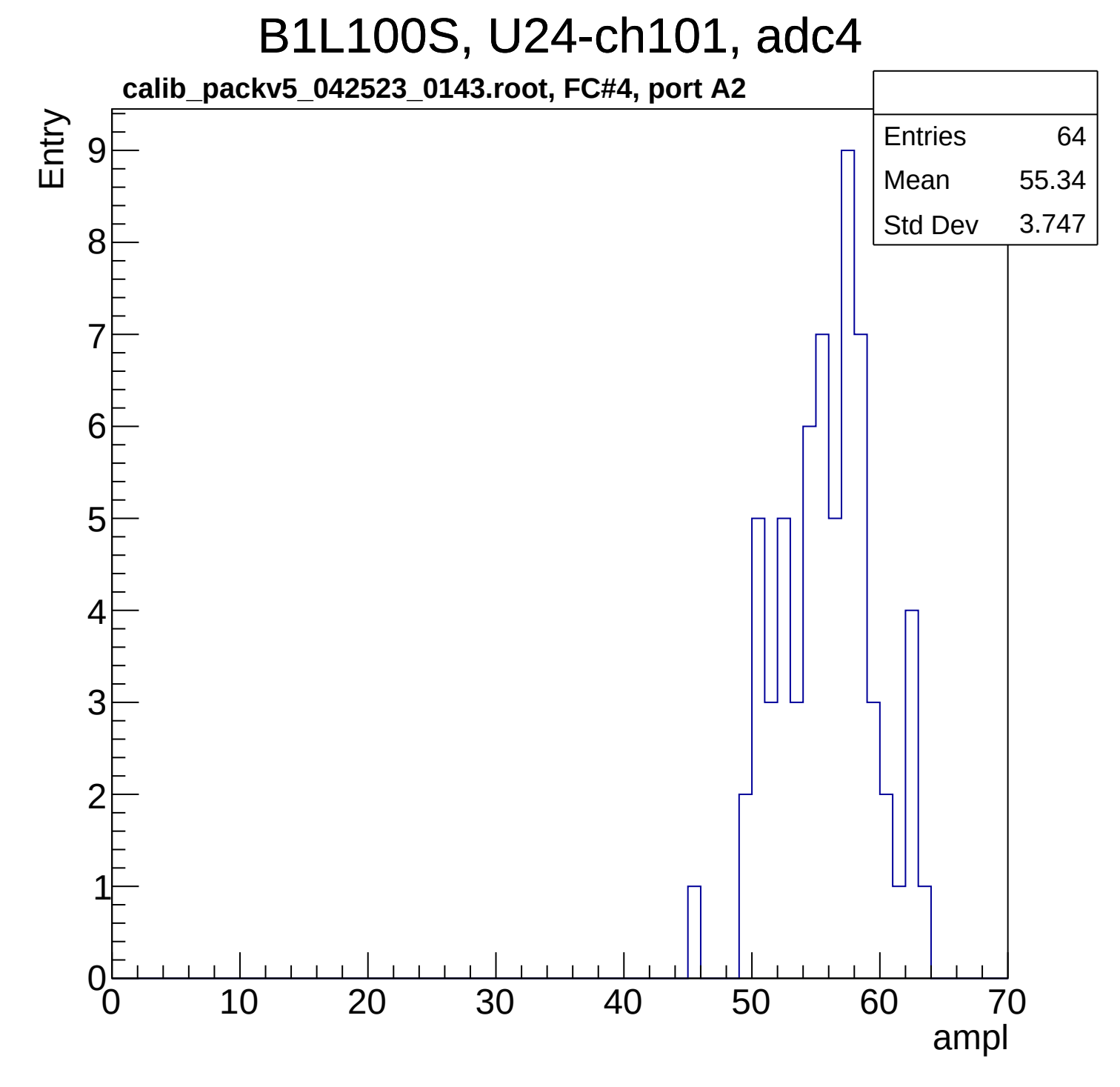

B1L100S, U24-ch101, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 50.03
Std Dev 3.533
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch101, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.34
Std Dev 3.747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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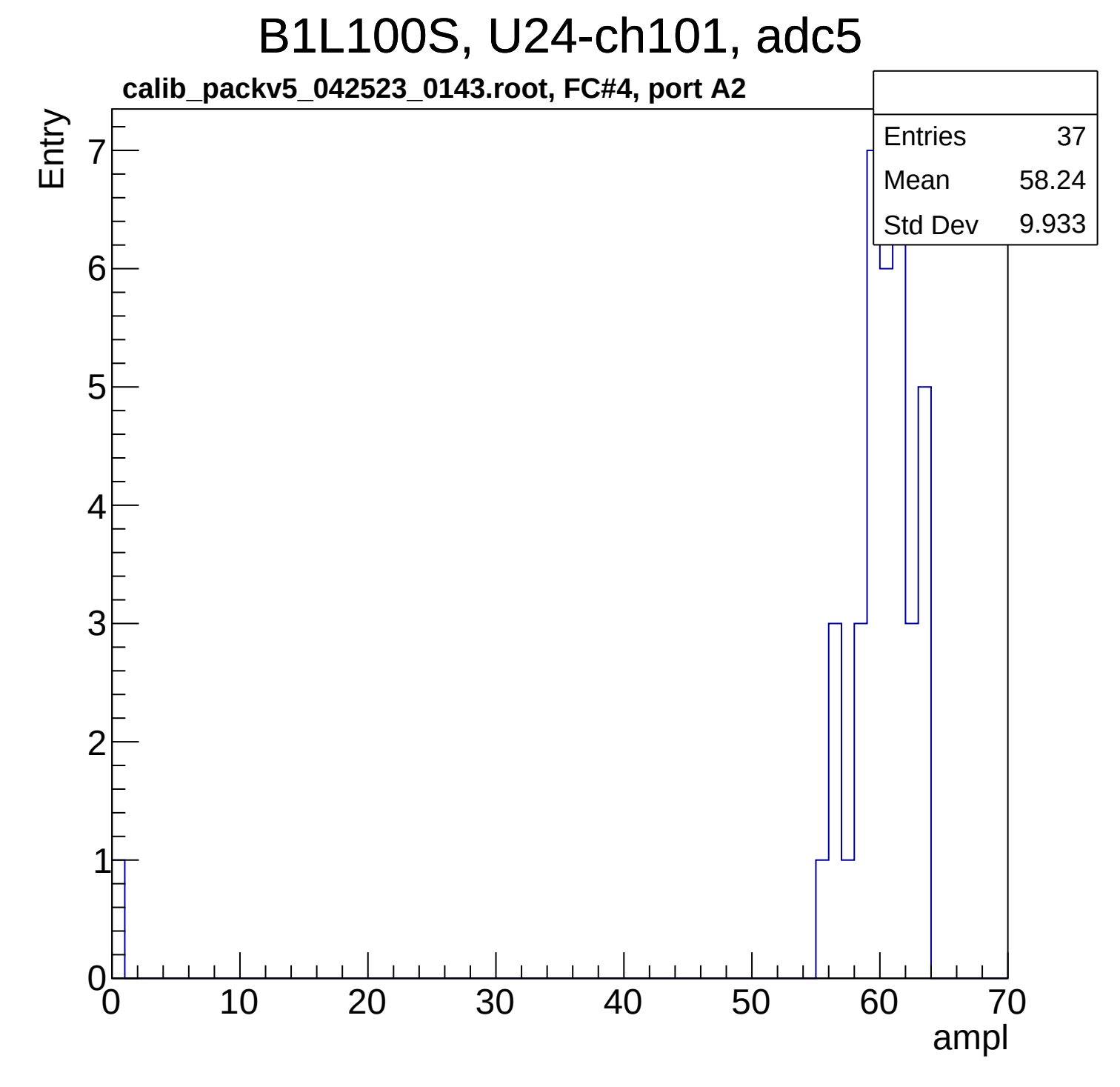
B1L100S, U24-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.24
Std Dev 9.933
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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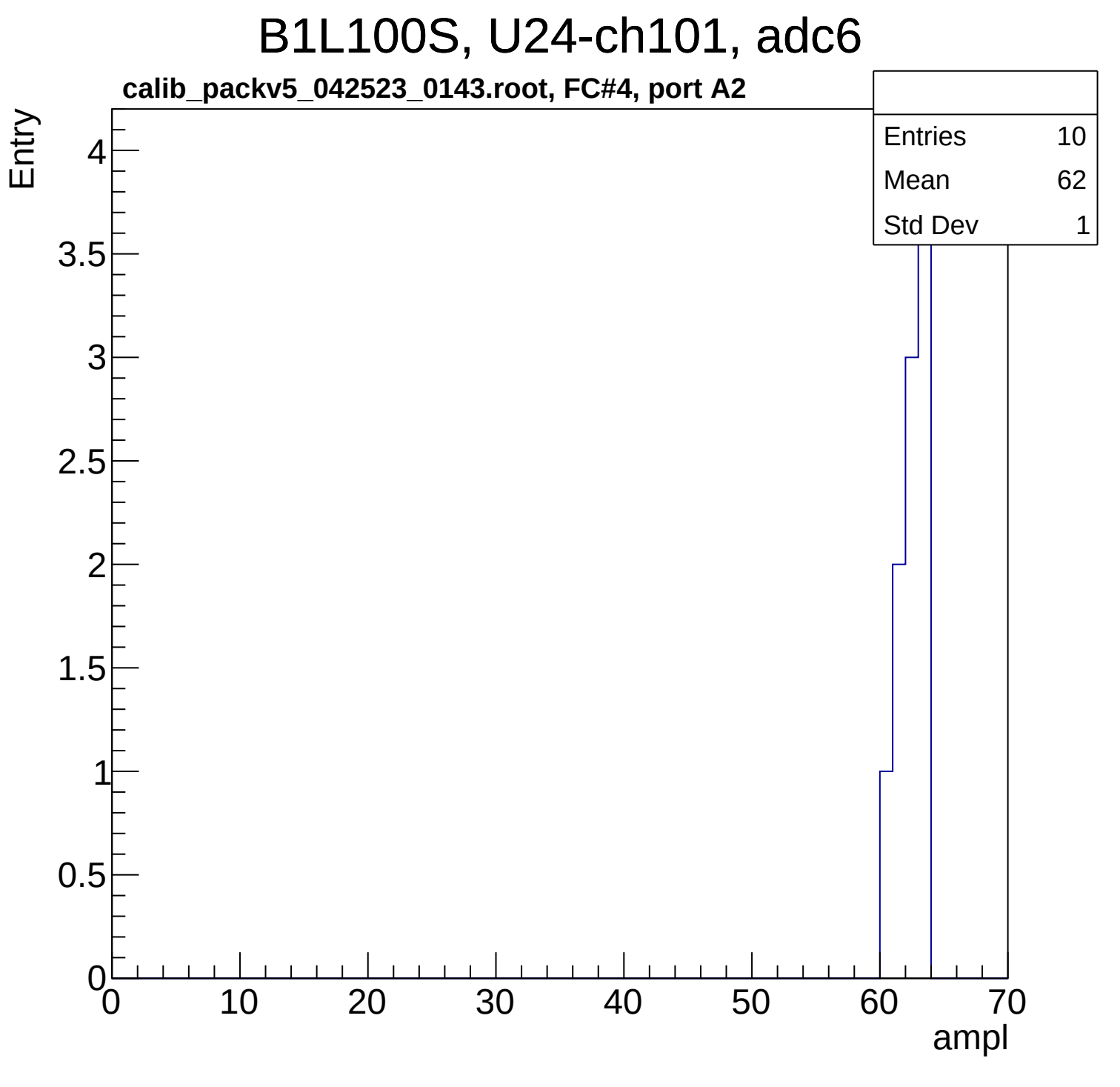
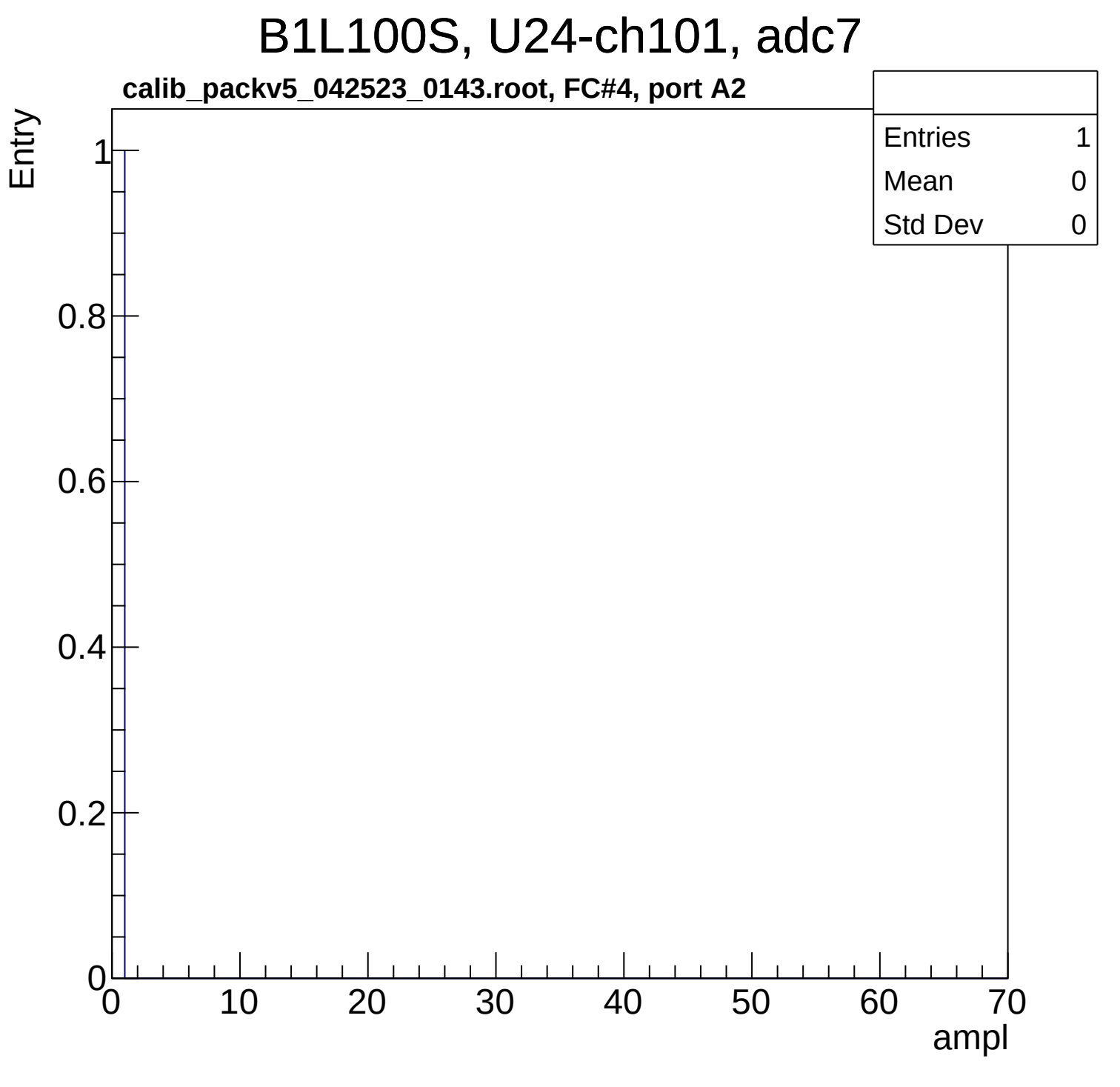

B1L100S, U24-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U24-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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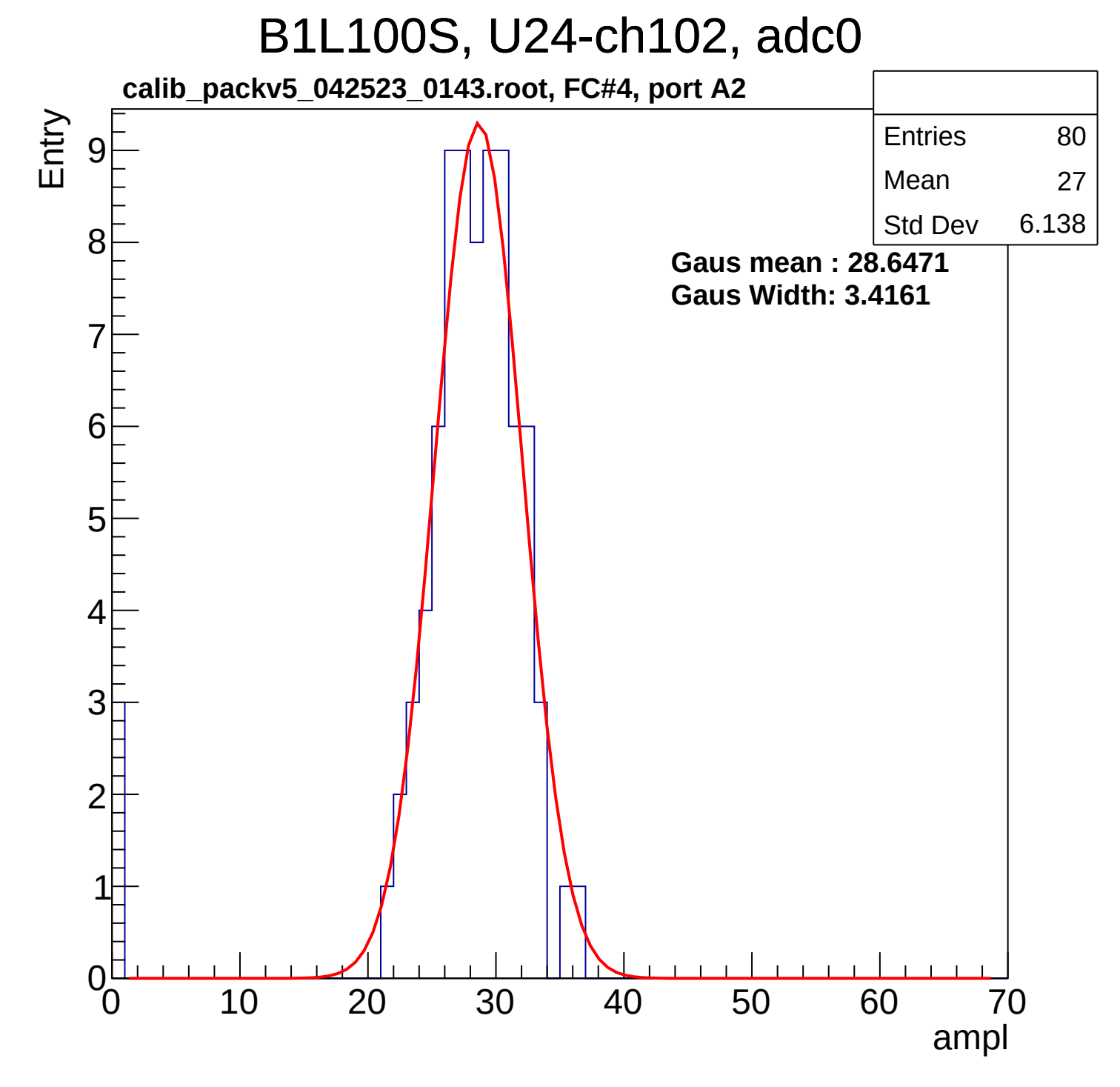
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 27
Std Dev 6.138
Gaus mean : 28.6471
Gaus Width: 3.4161
Entry
ampl
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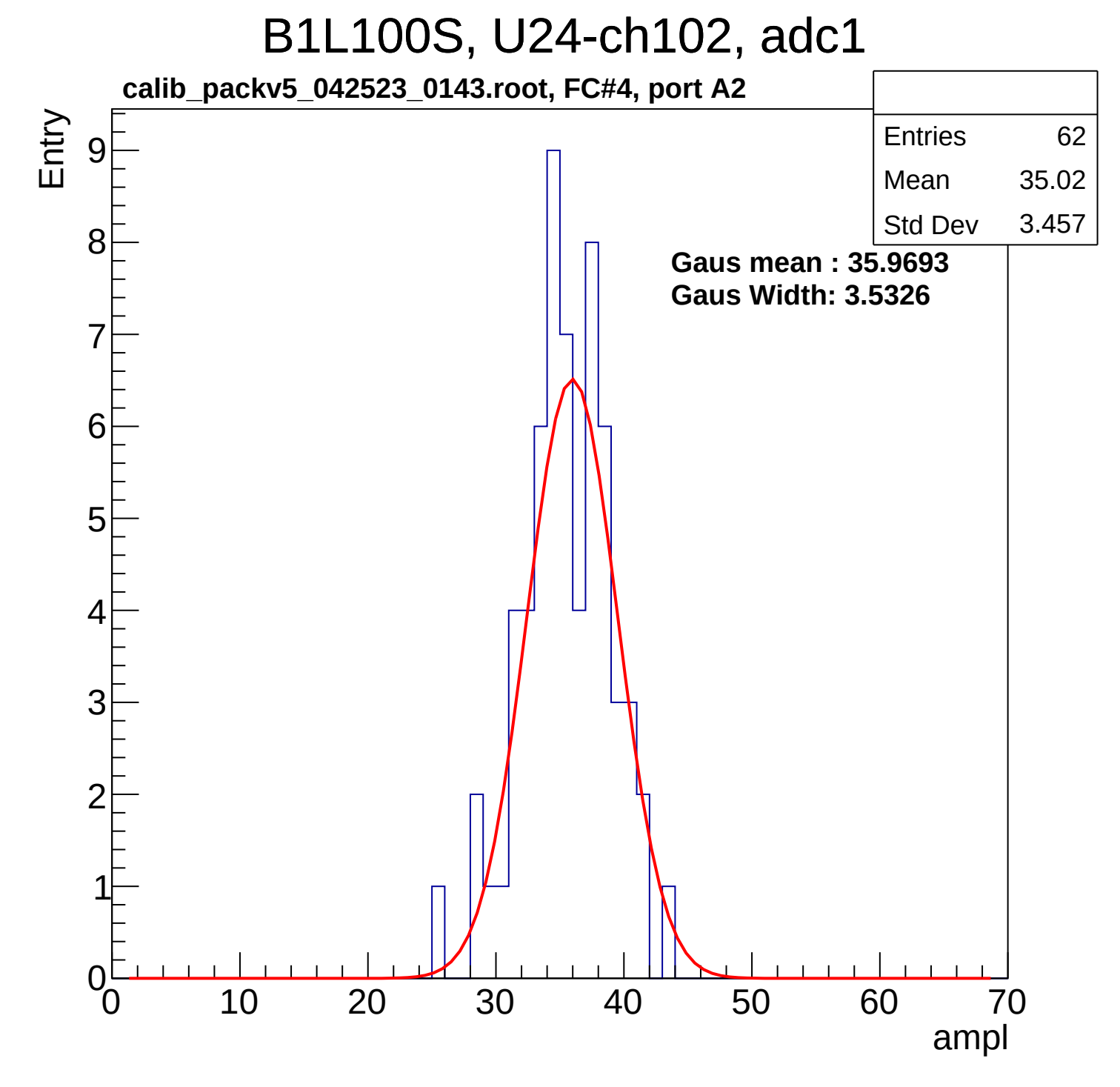
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 35.02
Std Dev 3.457
Gaus mean : 35.9693
Gaus Width: 3.5326
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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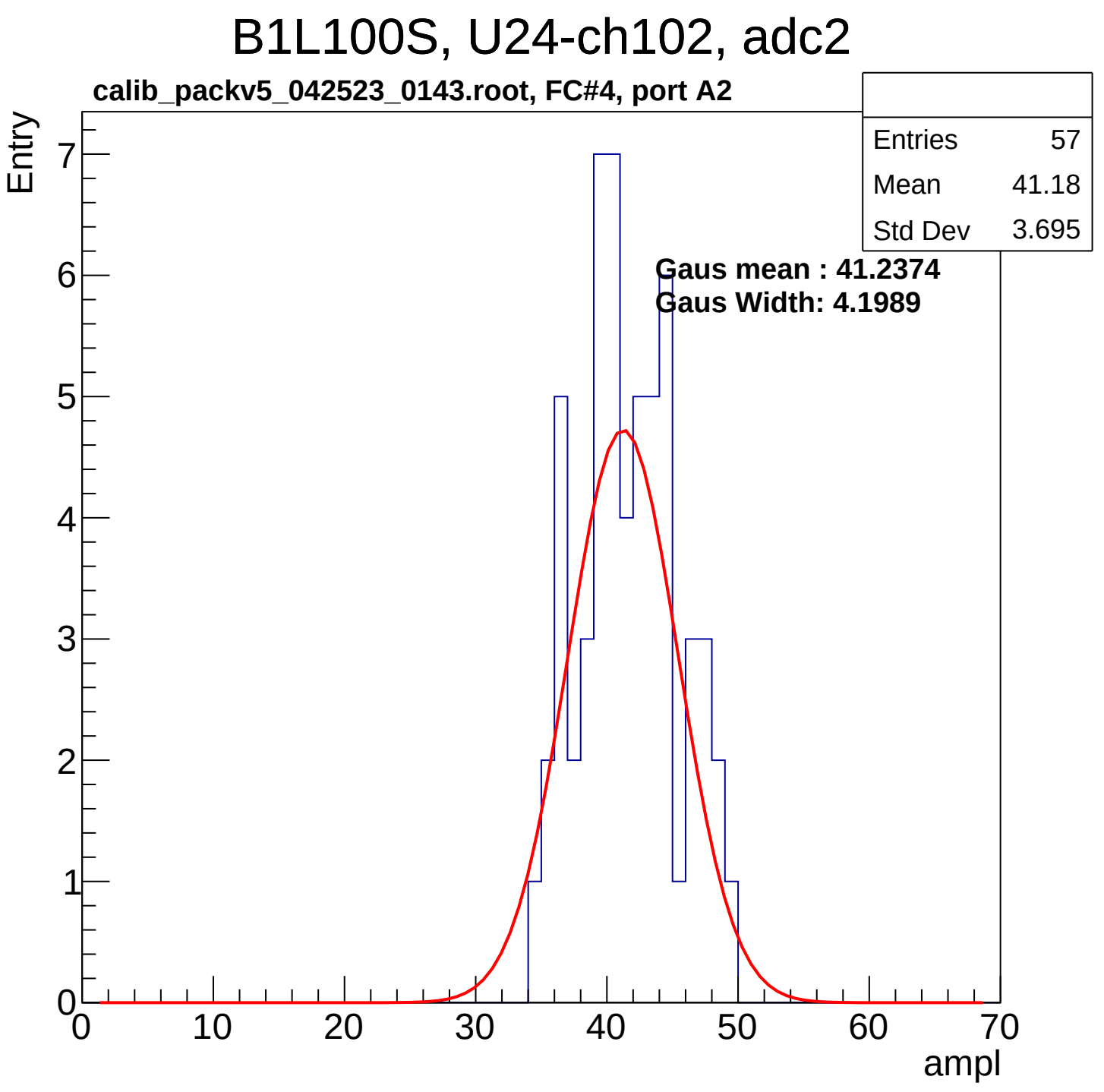
B1L100S, U24-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 41.18
Std Dev 3.695
Gaus mean : 41.2374
Gaus Width: 4.1989
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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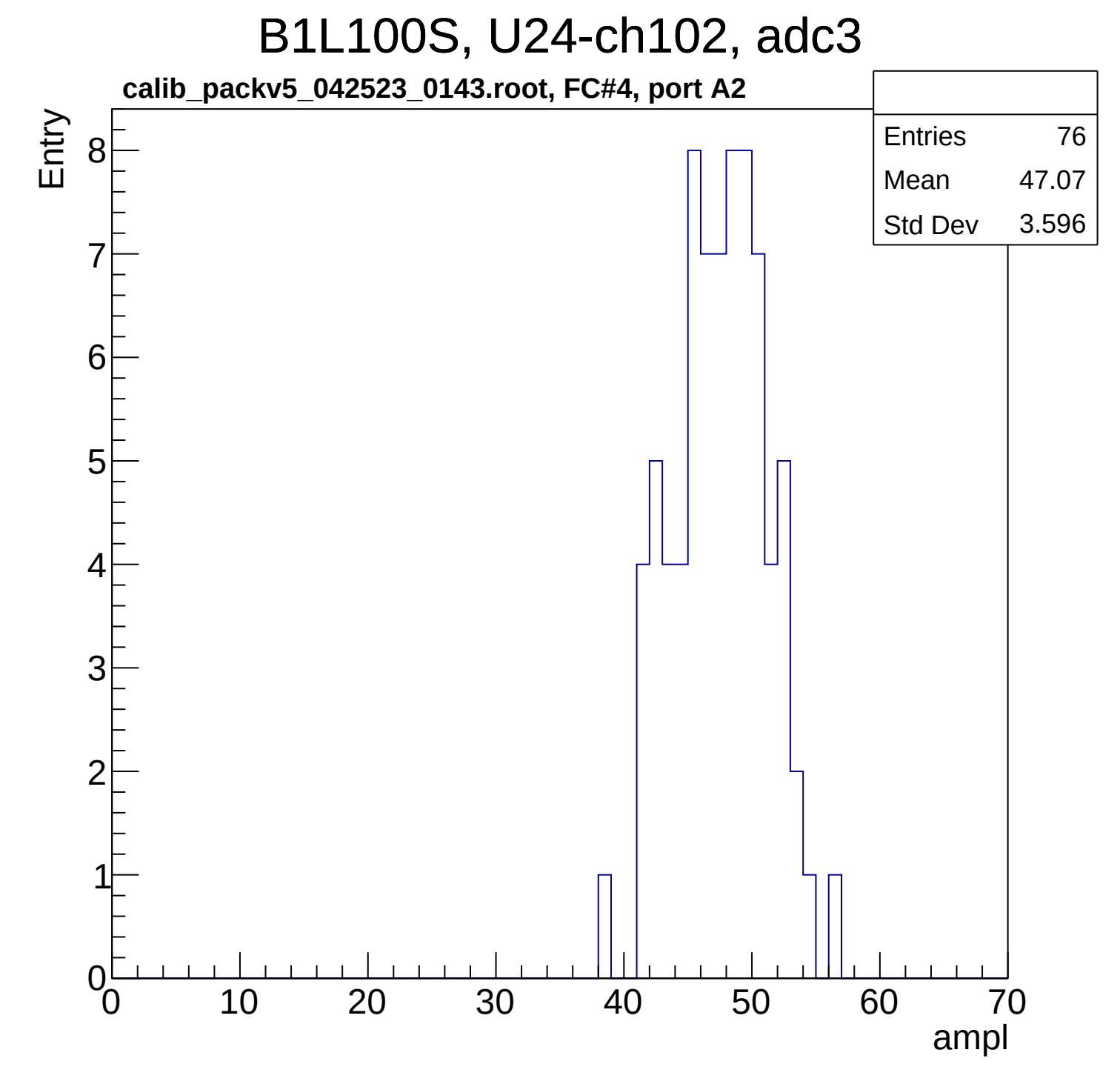
B1L100S, U24-ch102, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 47.07
Std Dev 3.596
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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60
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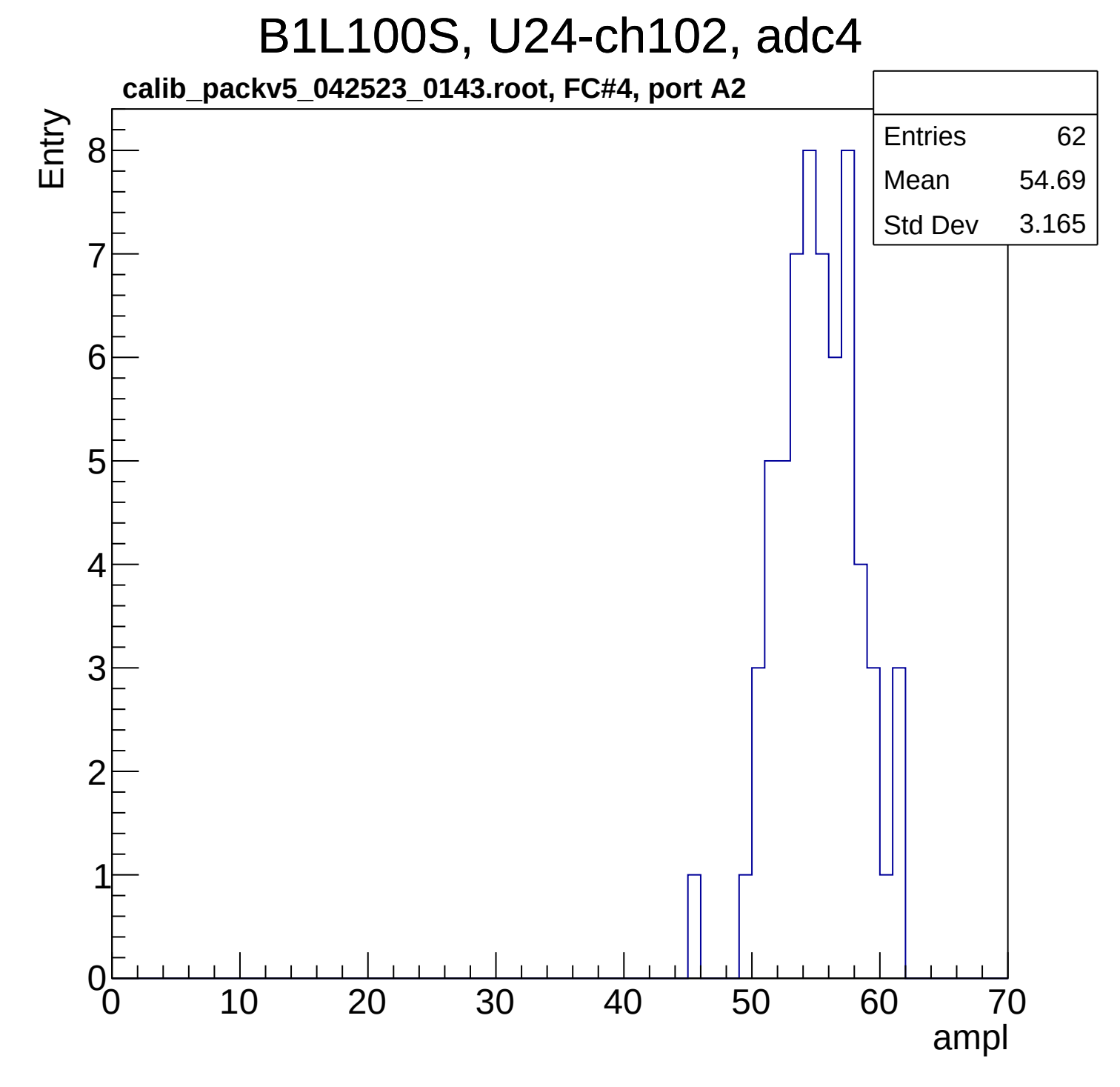
B1L100S, U24-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 54.69
Std Dev 3.165
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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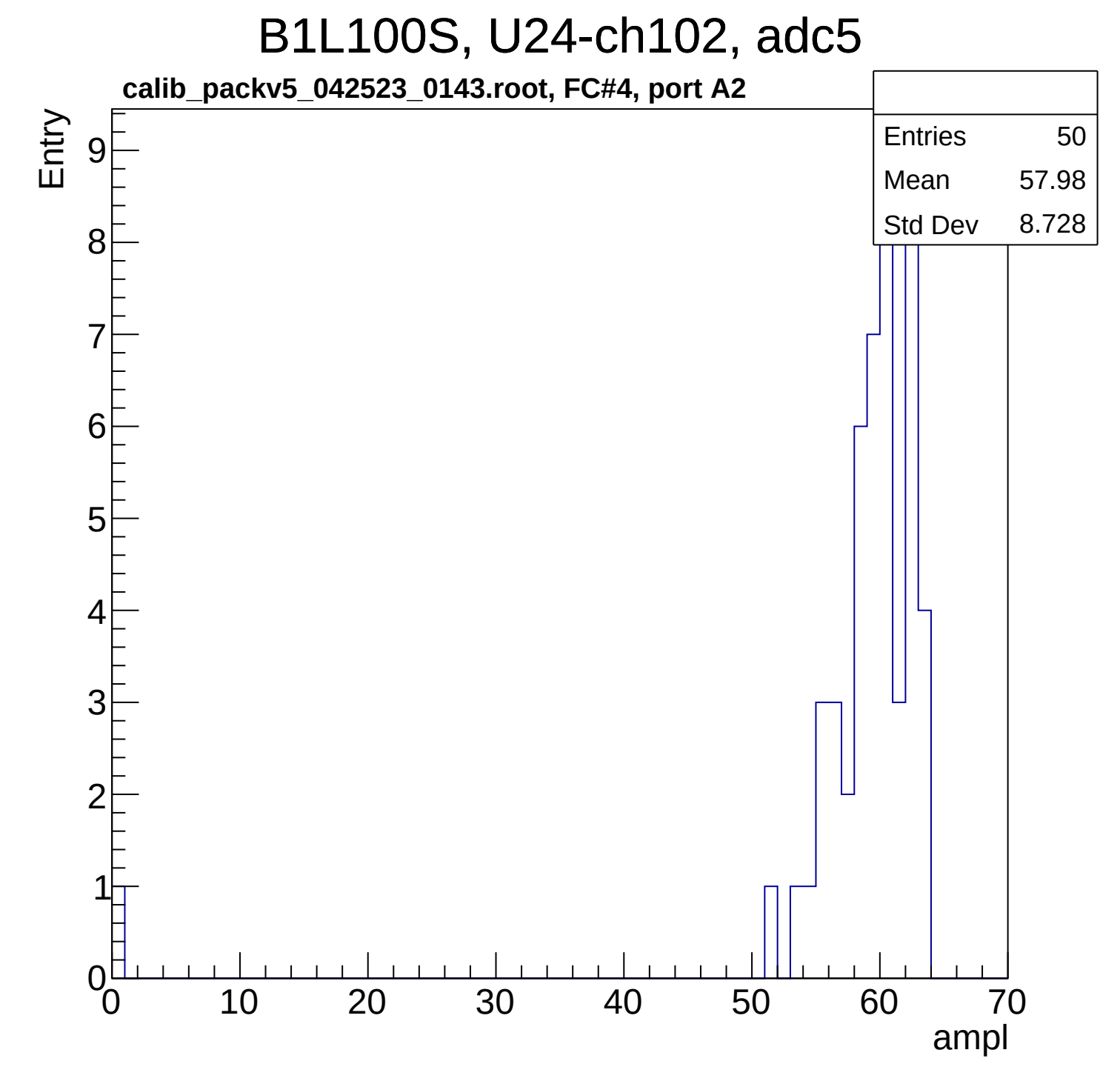
B1L100S, U24-ch102, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.98
Std Dev 8.728
Entry
ampl
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3
4
5
6
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0
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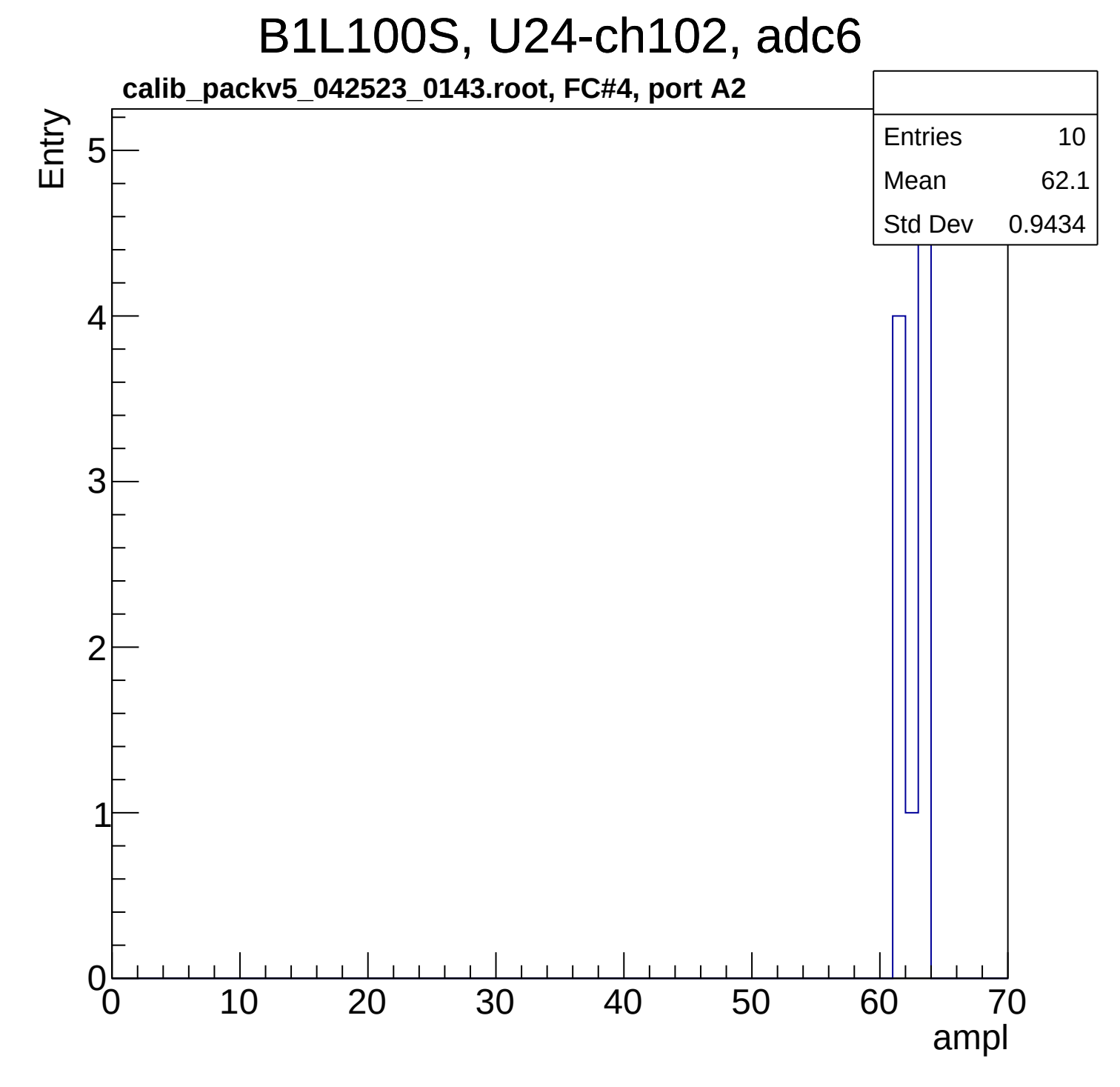
B1L100S, U24-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
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50
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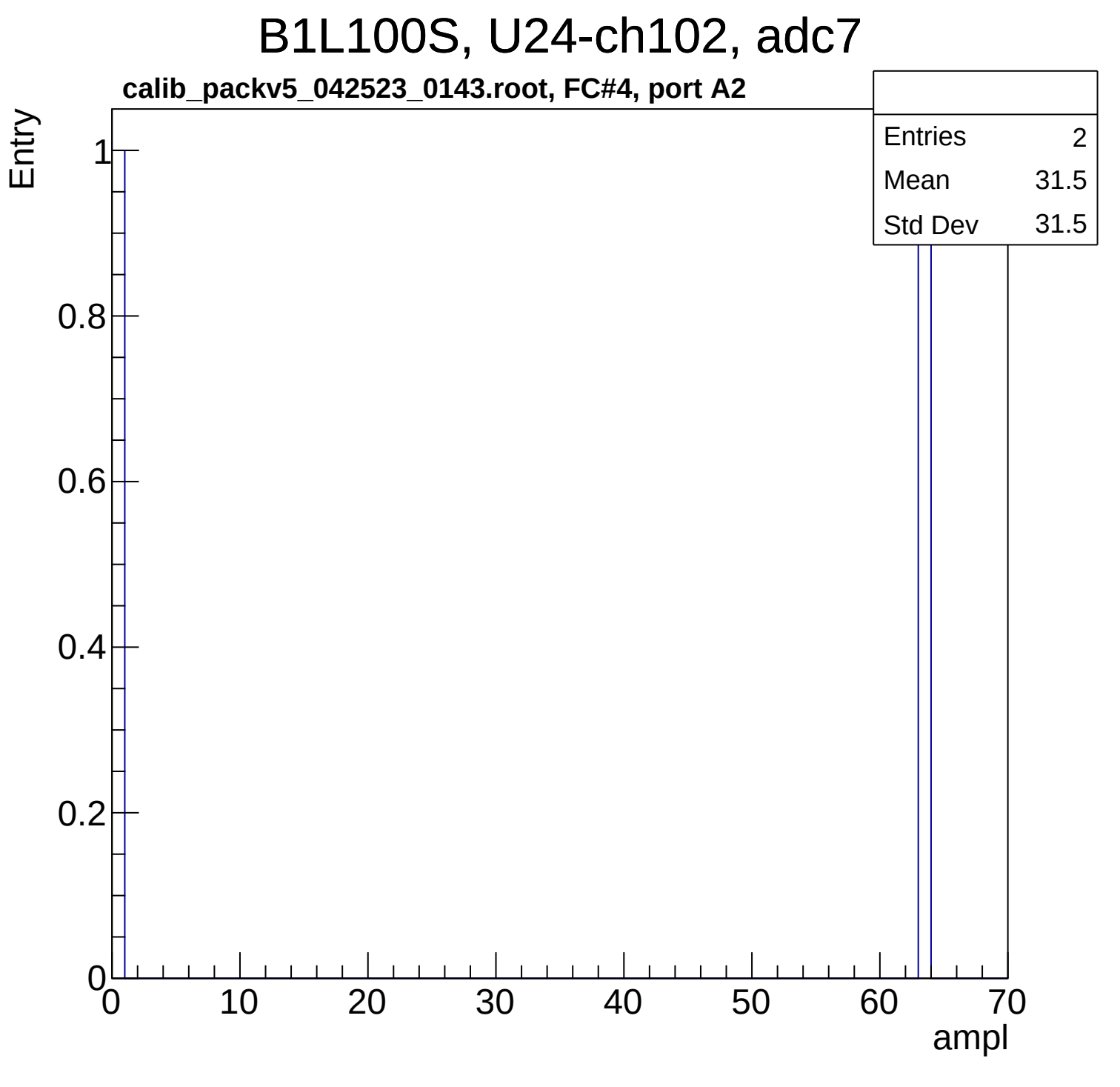
70
ampl


B1L100S, U24-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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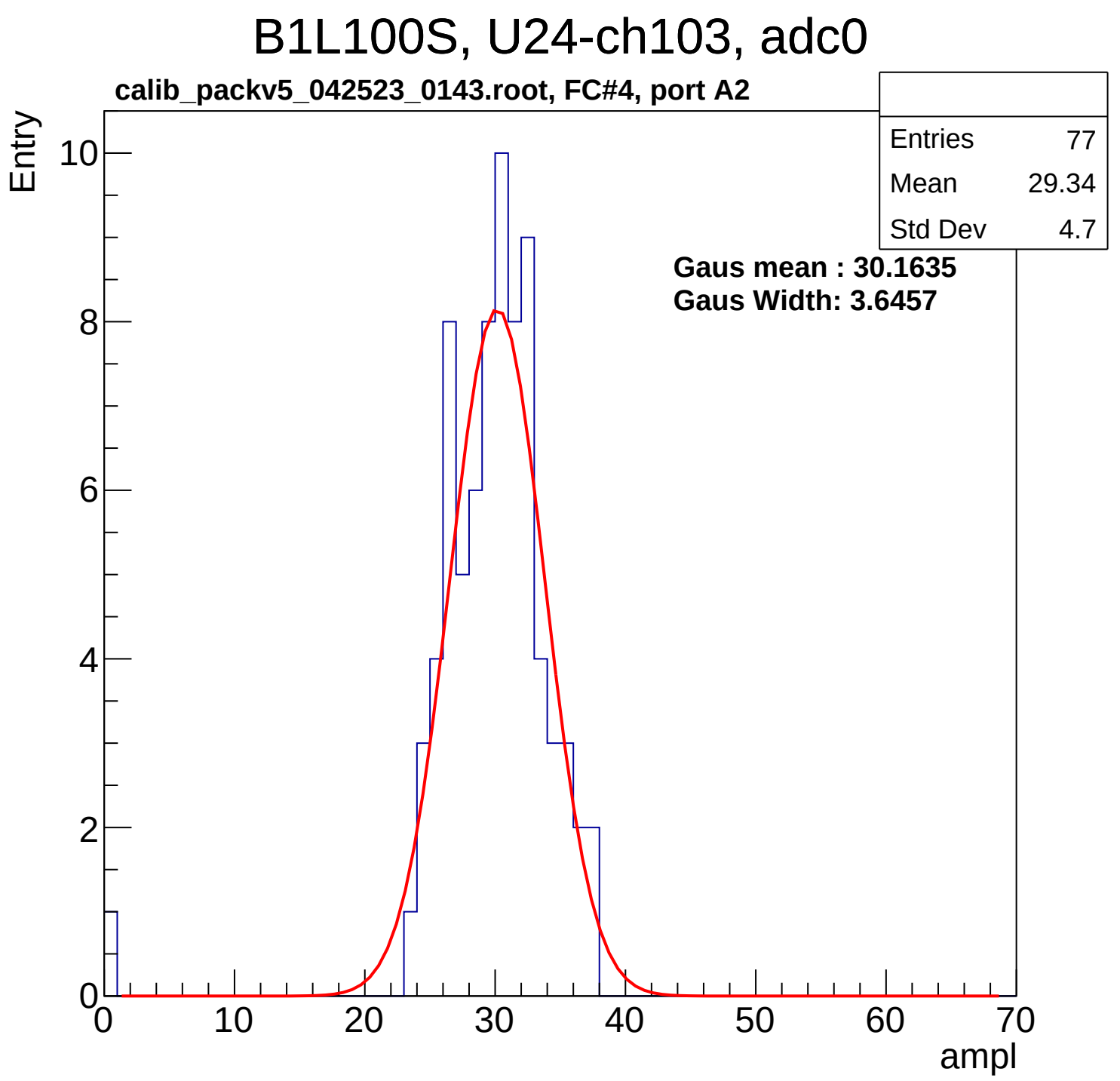
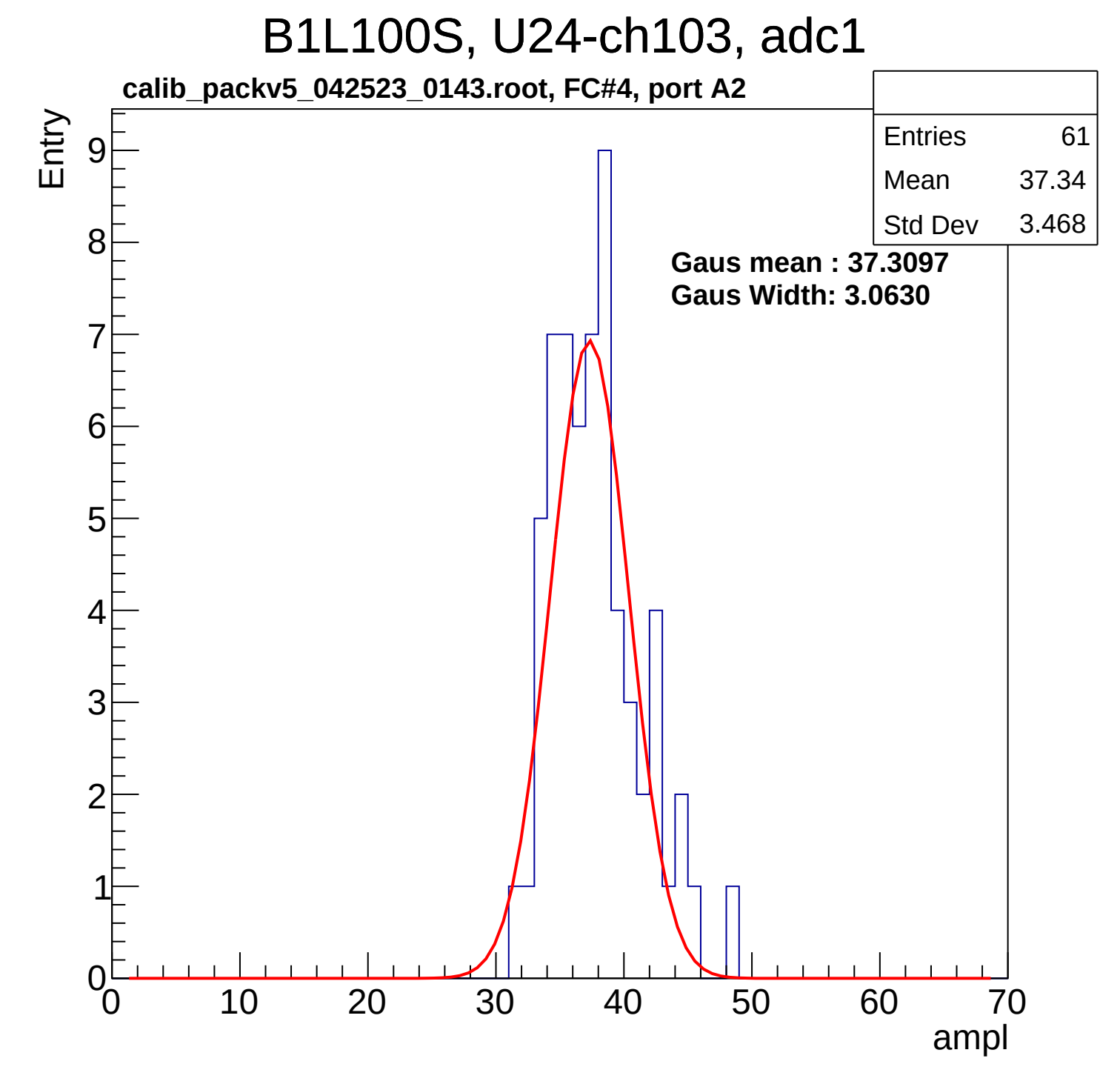
50
60
70
ampl


B1L100S, U24-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.34
Std Dev 4.7
Gaus mean : 30.1635
Gaus Width: 3.6457
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.34
Std Dev 3.468
Gaus mean : 37.3097
Gaus Width: 3.0630
Entry
ampl
0
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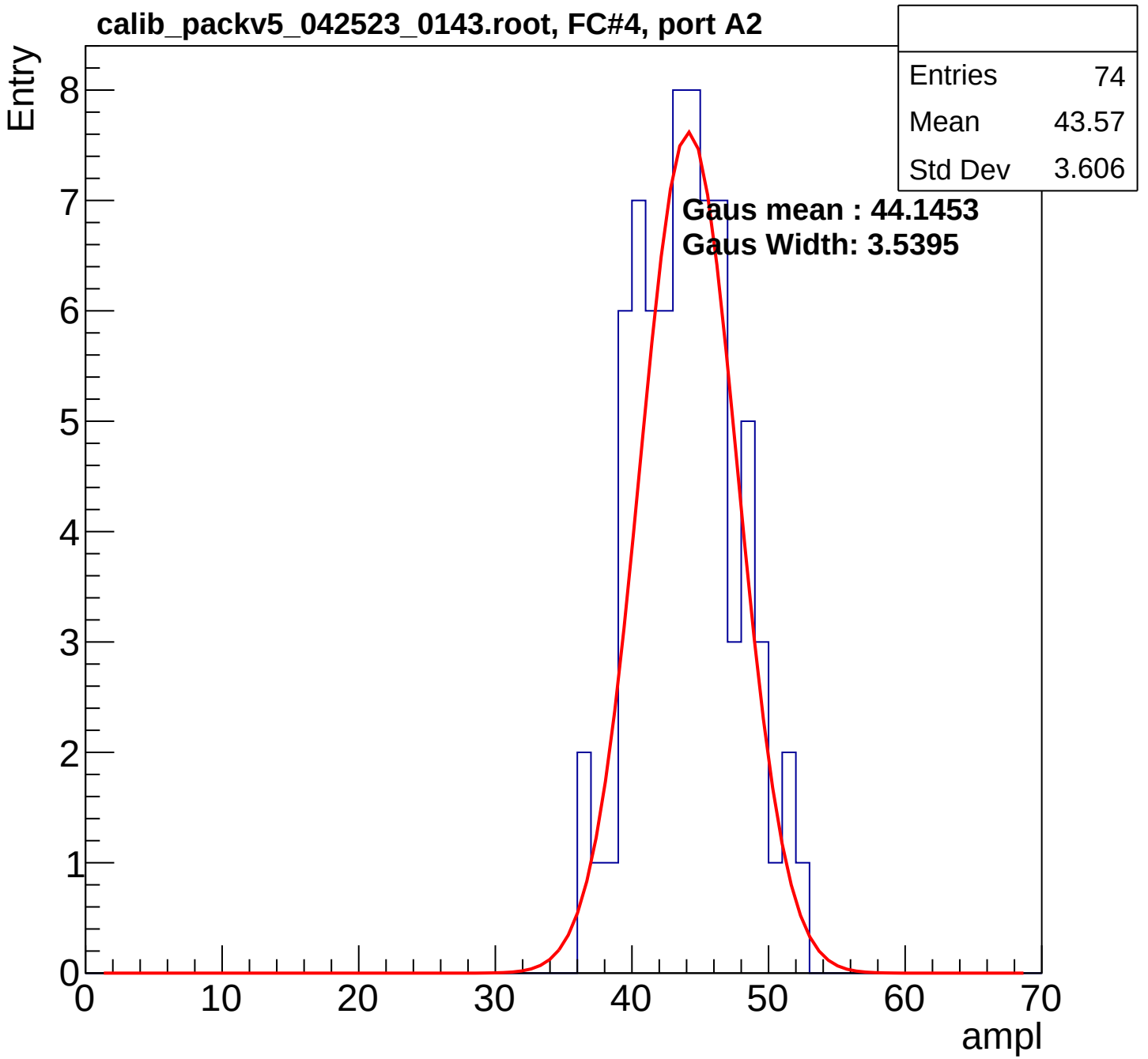
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.57
Std Dev 3.606
Gaus mean : 44.1453
Gaus Width: 3.5395
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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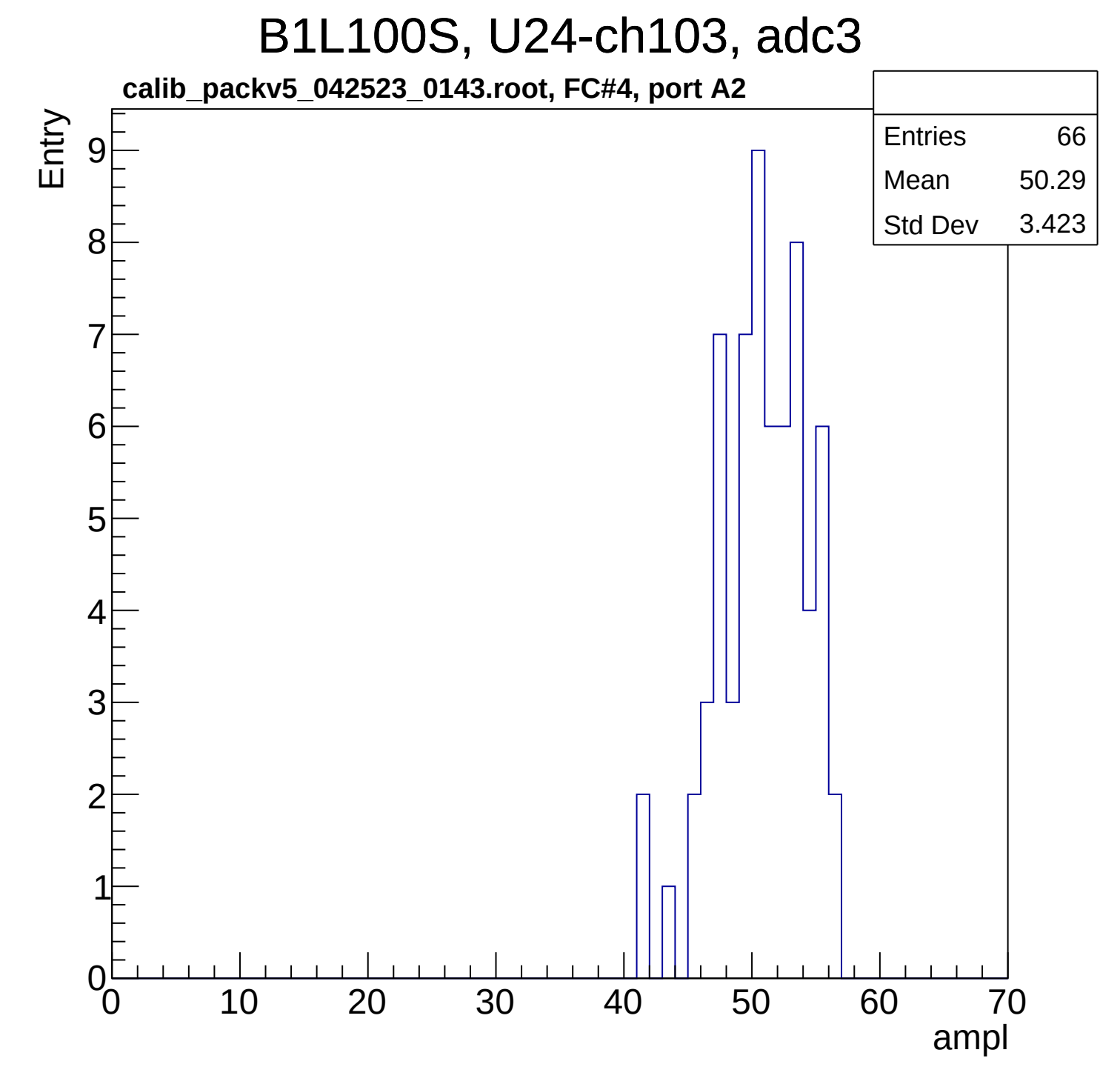
B1L100S, U24-ch103, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.29
Std Dev 3.423
Entry
ampl
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3
4
5
6
7
8
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0
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20
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60
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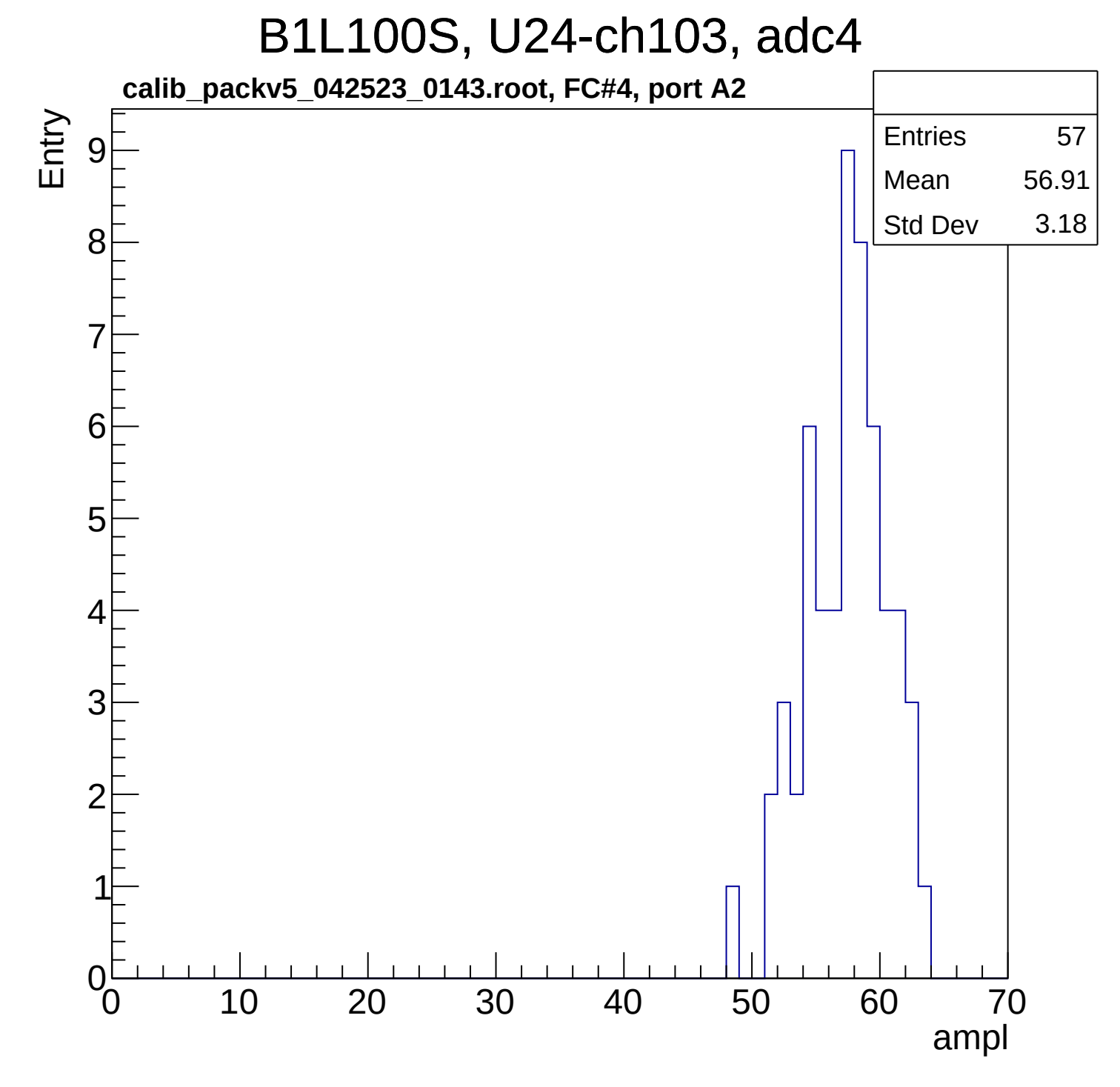
B1L100S, U24-ch103, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.91
Std Dev 3.18
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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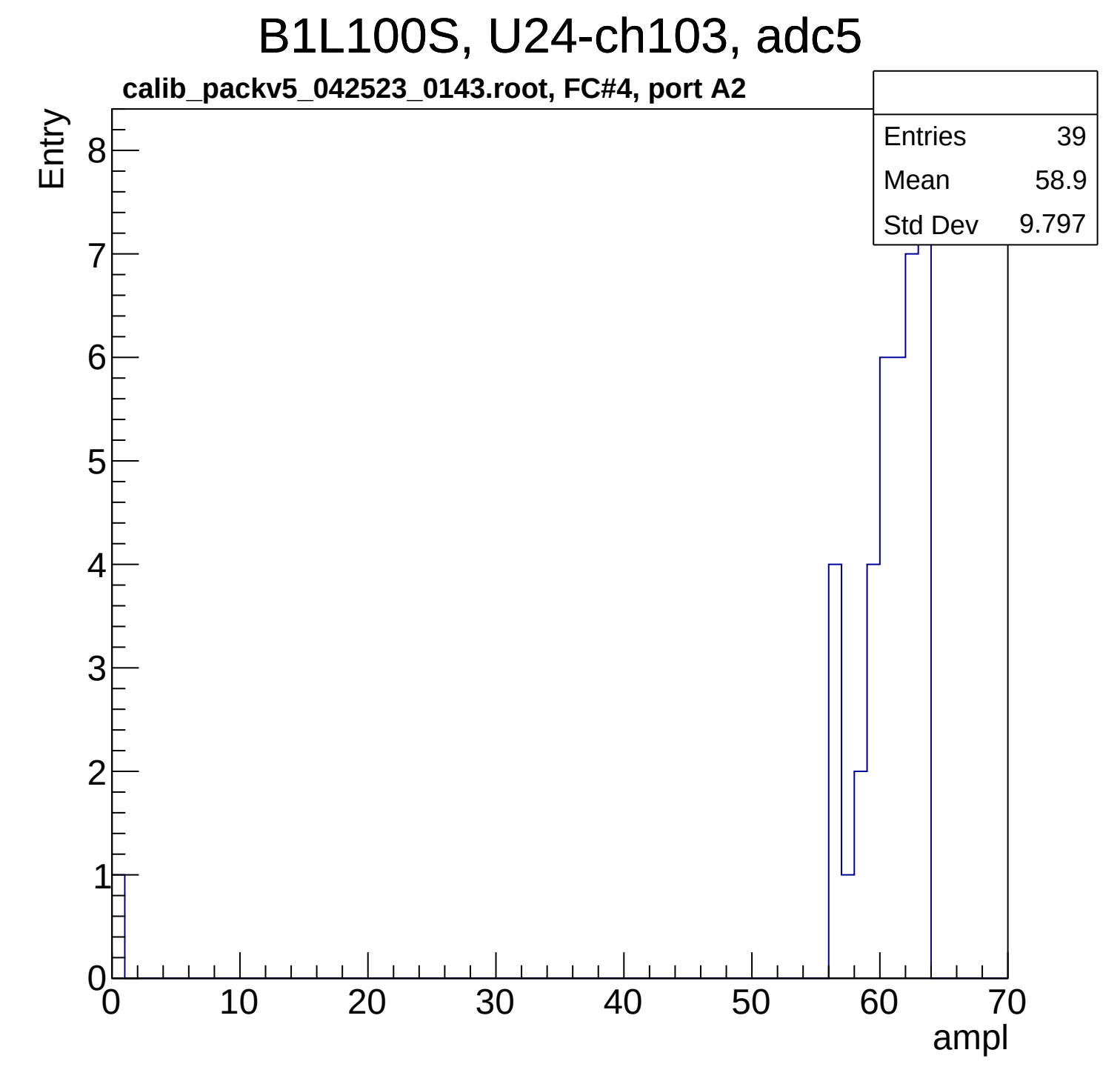
B1L100S, U24-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.9
Std Dev 9.797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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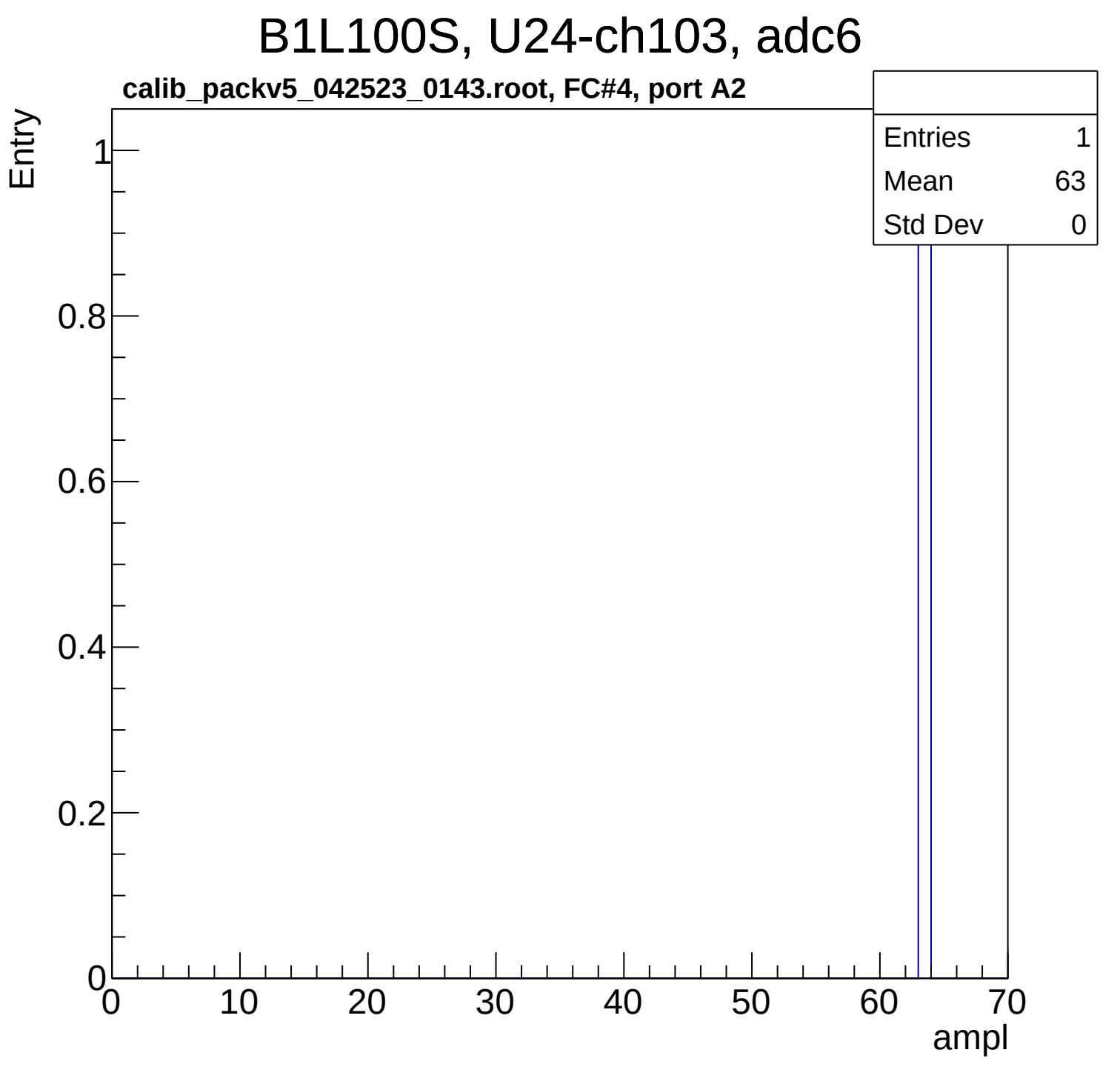
B1L100S, U24-ch103, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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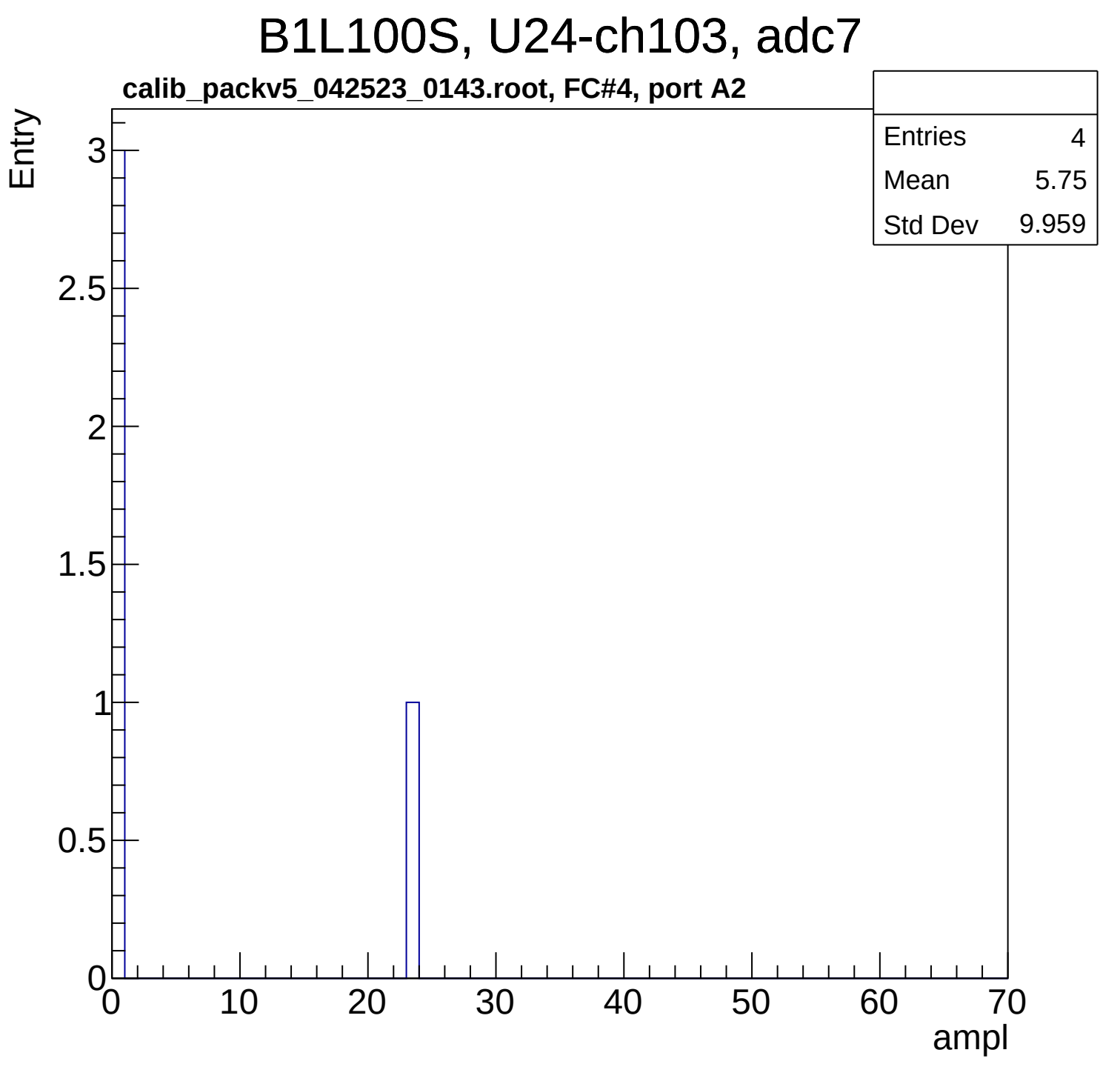
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch103, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
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1.5
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2.5
3
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60
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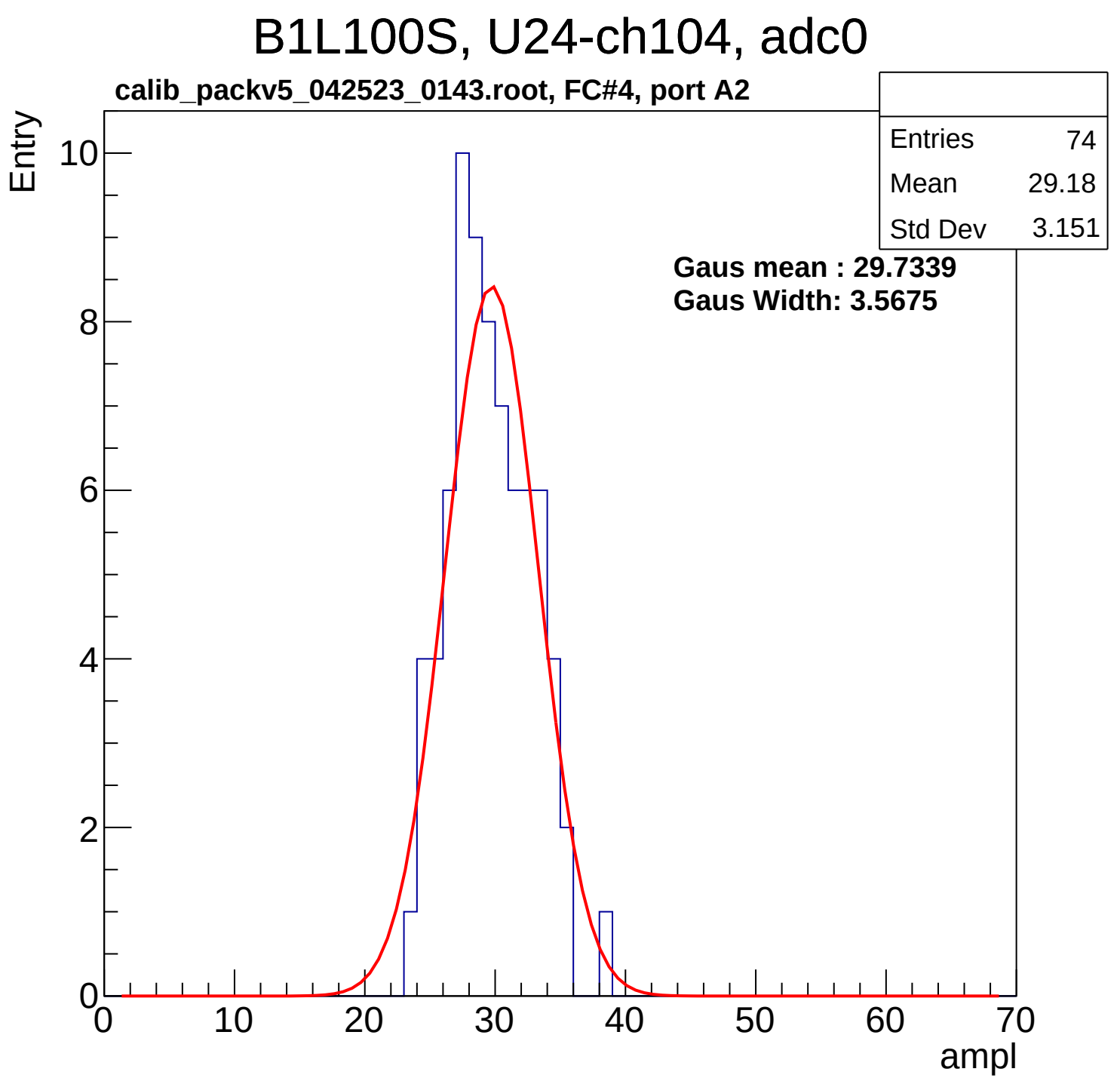
B1L100S, U24-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.18
Std Dev 3.151
Gaus mean : 29.7339
Gaus Width: 3.5675
Entry
ampl
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4
6
8
10
0
10
20
30
40
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60
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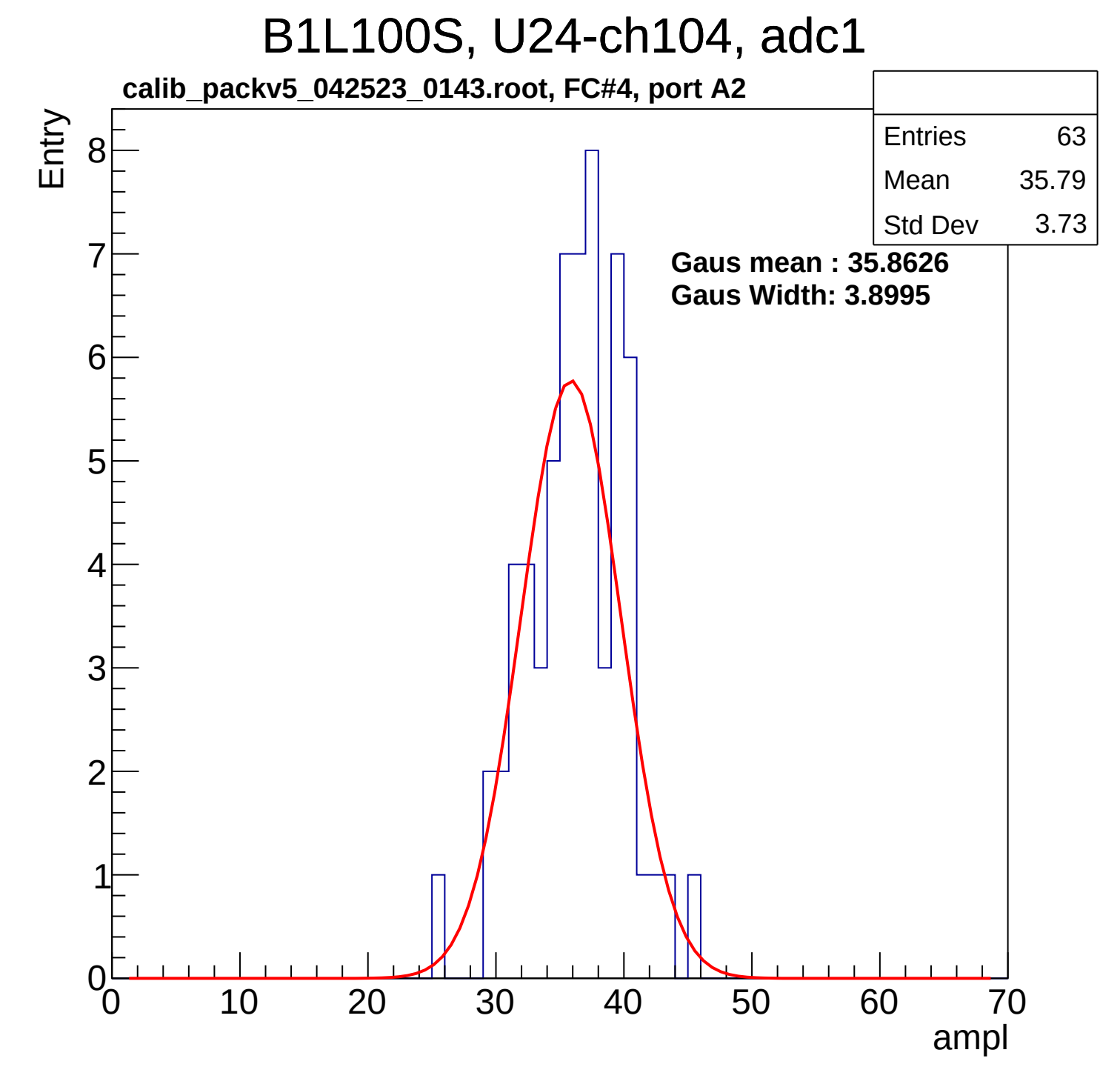
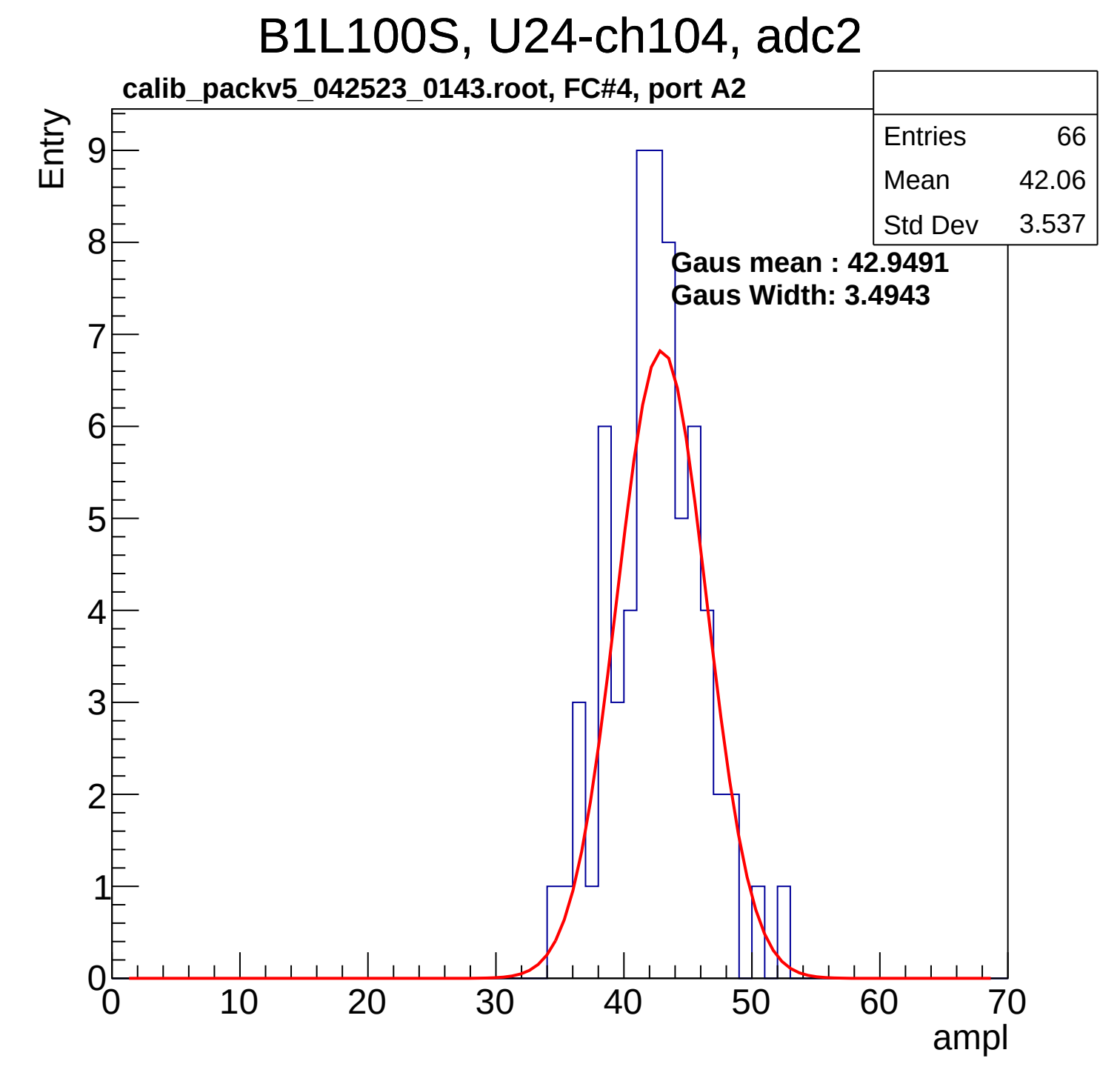
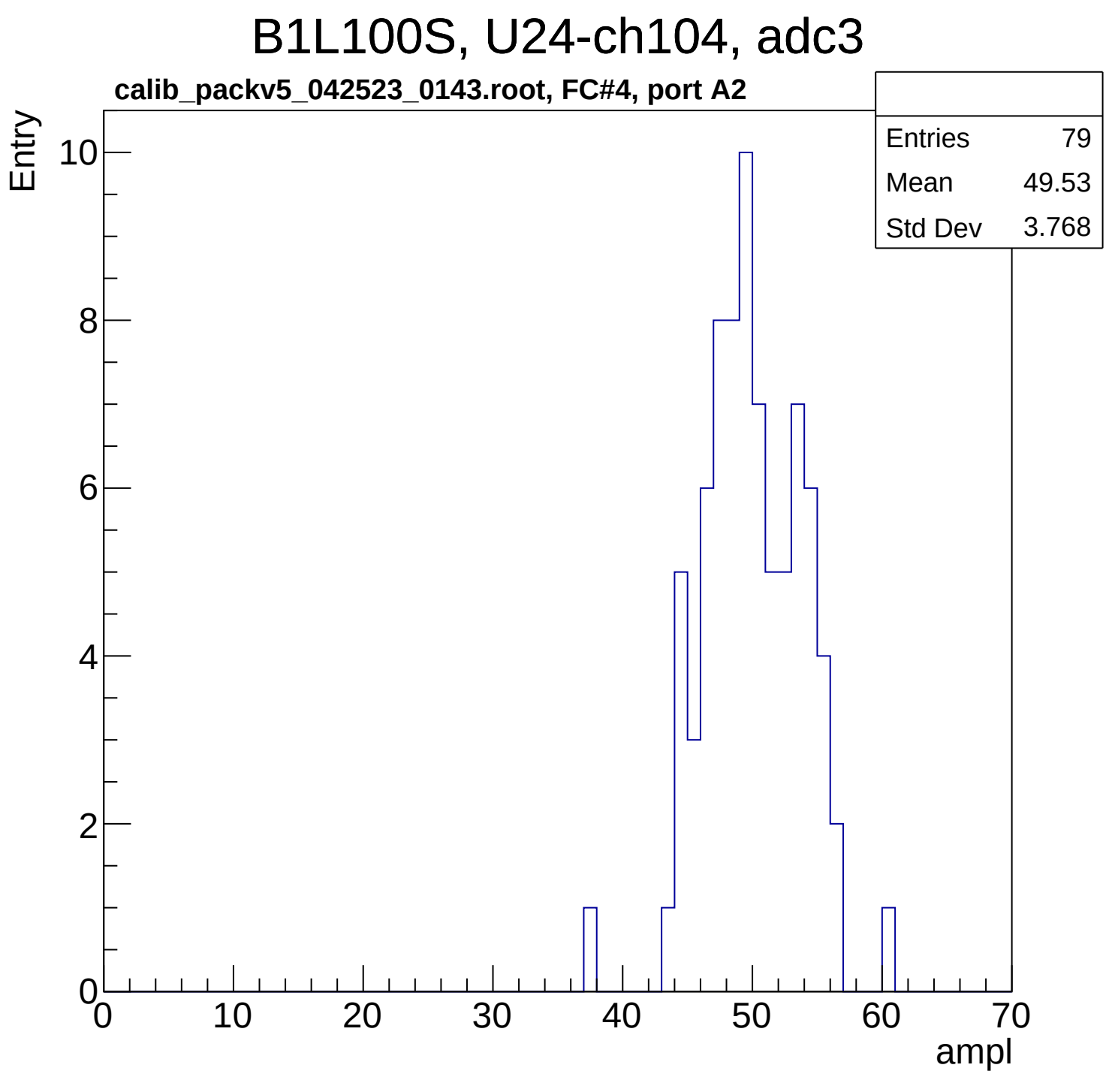

B1L100S, U24-ch104, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.79
Std Dev 3.73
Gaus mean : 35.8626
Gaus Width: 3.8995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch104, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.06
Std Dev 3.537
Gaus mean : 42.9491
Gaus Width: 3.4943
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U24-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 49.53
Std Dev 3.768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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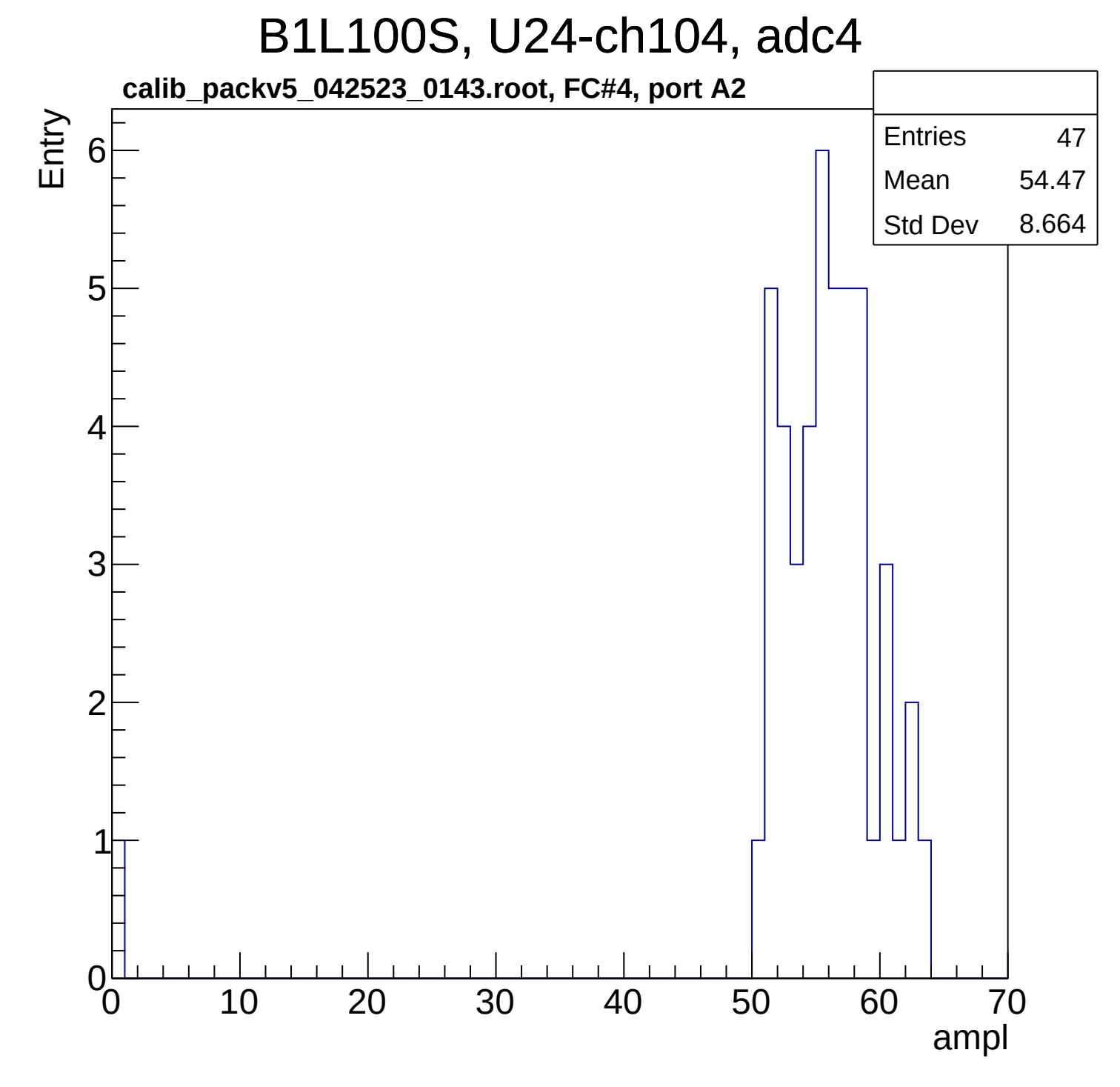
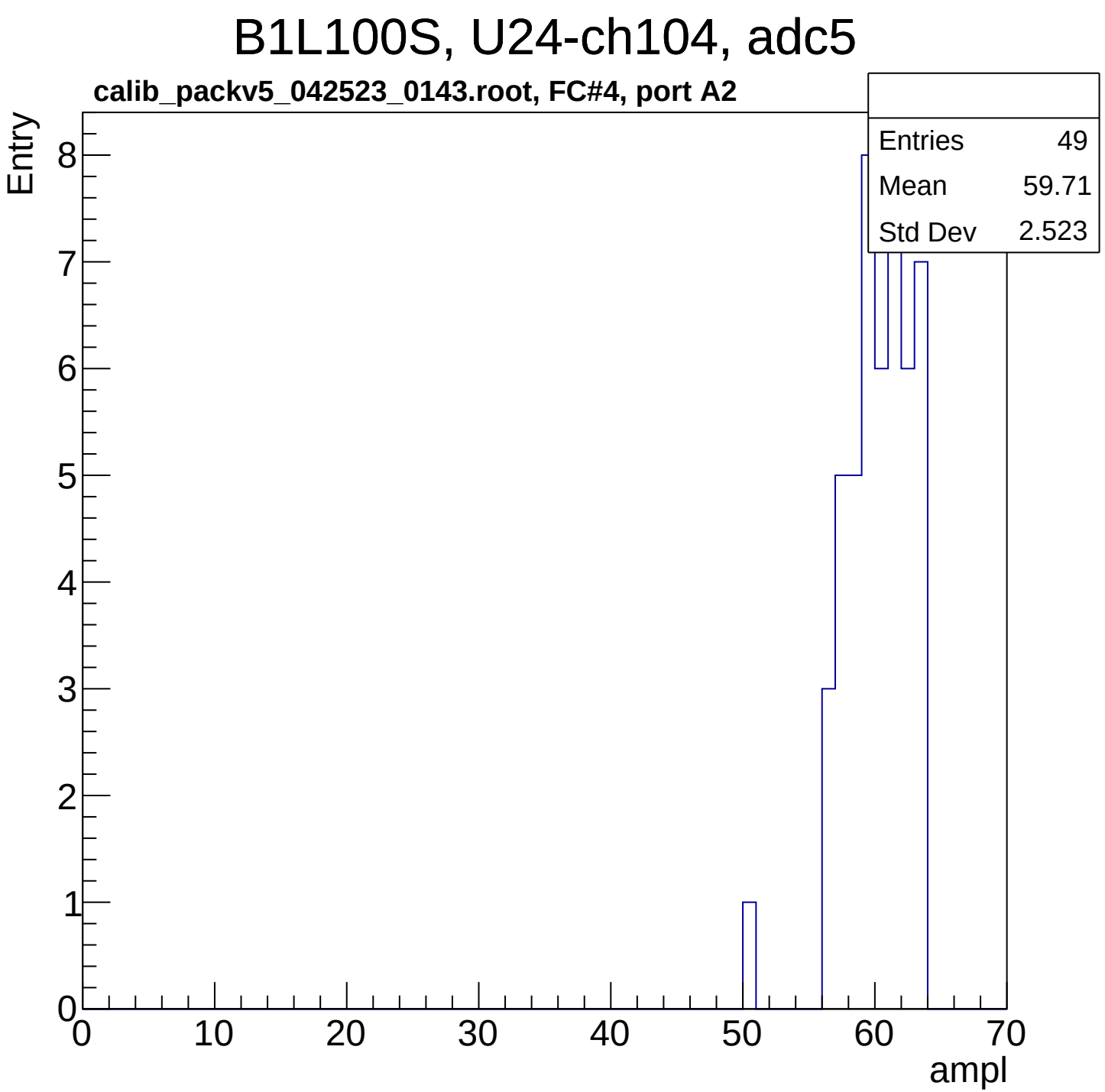

B1L100S, U24-ch104, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 54.47
Std Dev 8.664
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch104, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 59.71
Std Dev 2.523
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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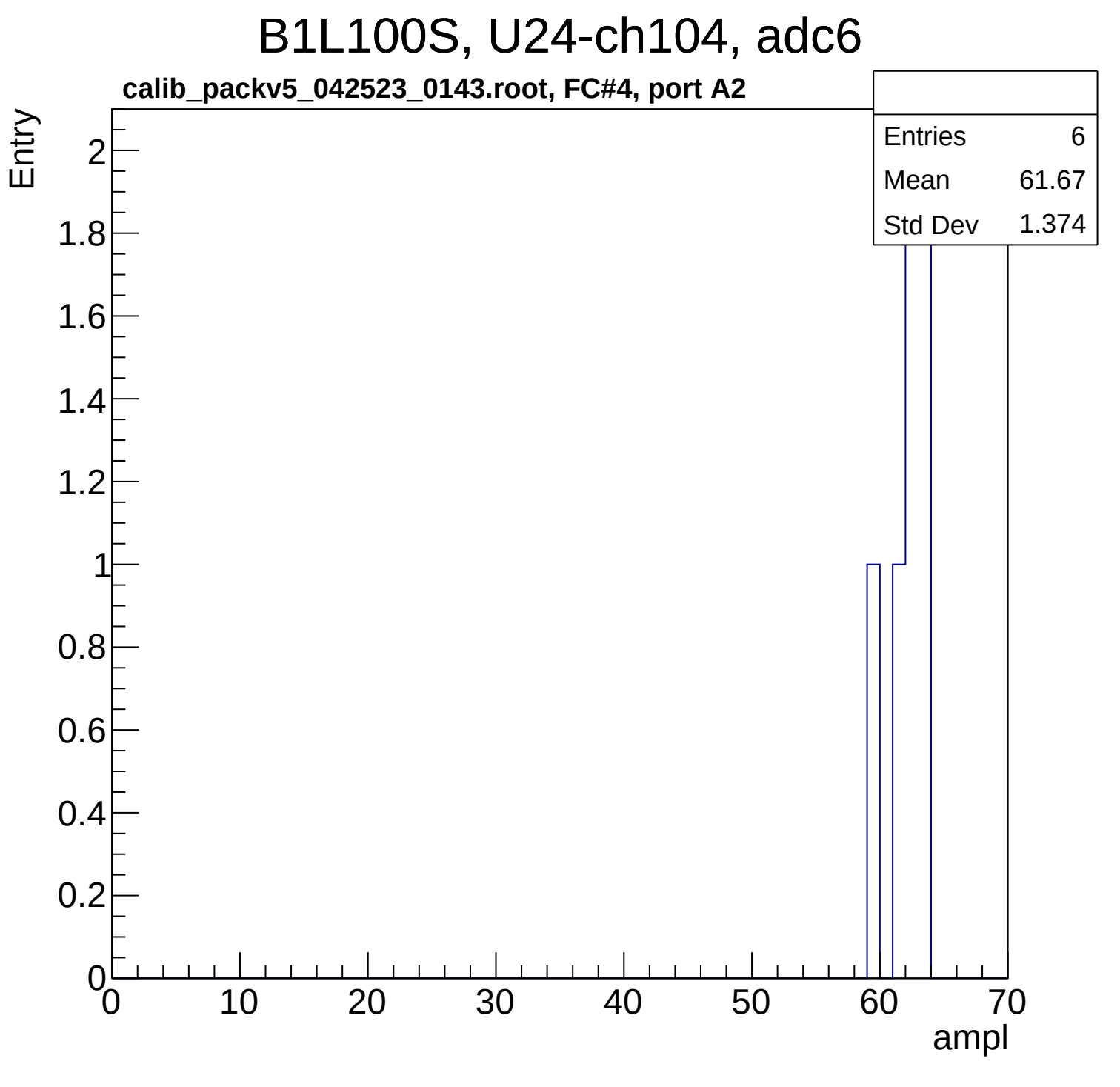
B1L100S, U24-ch104, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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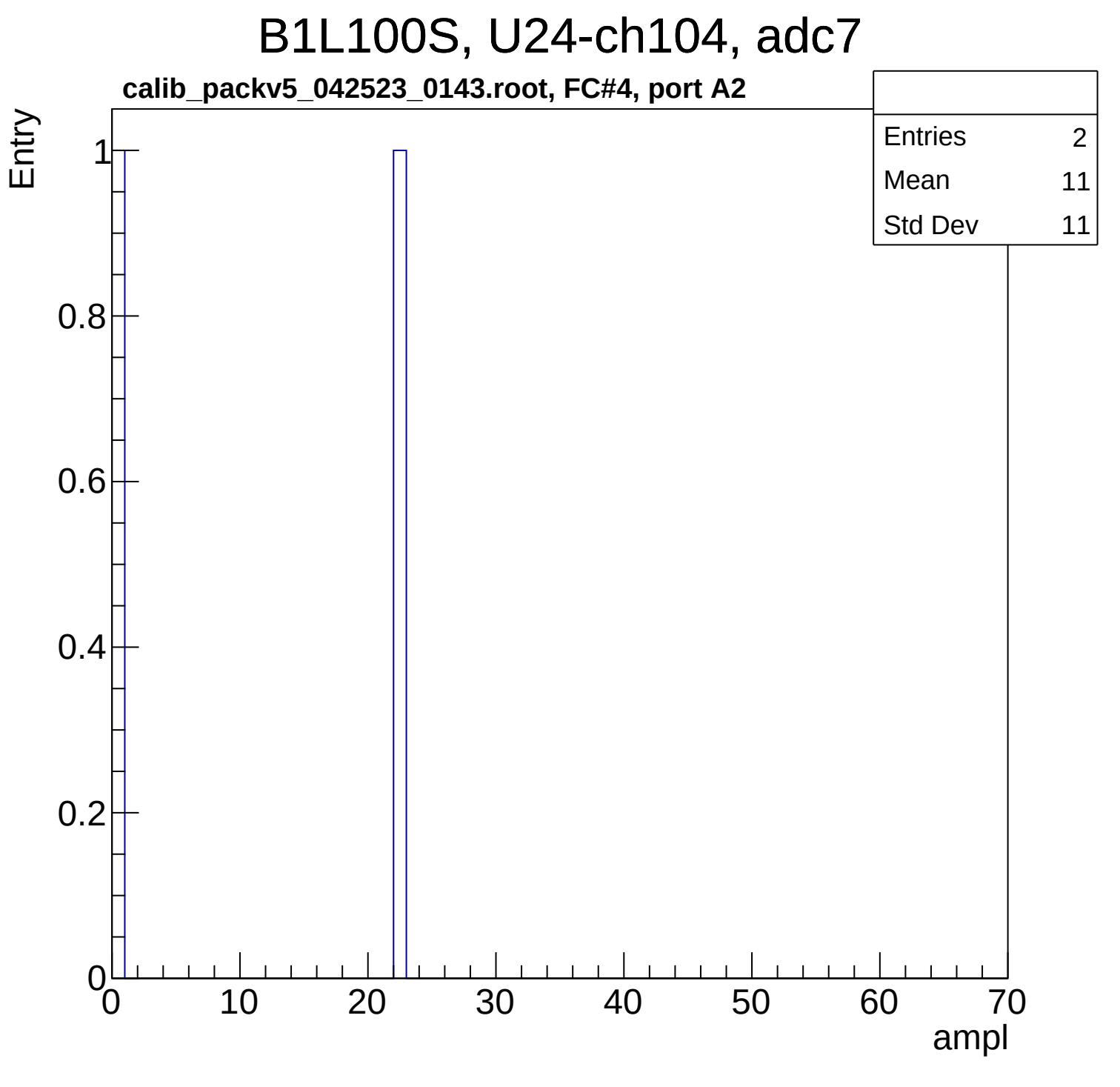
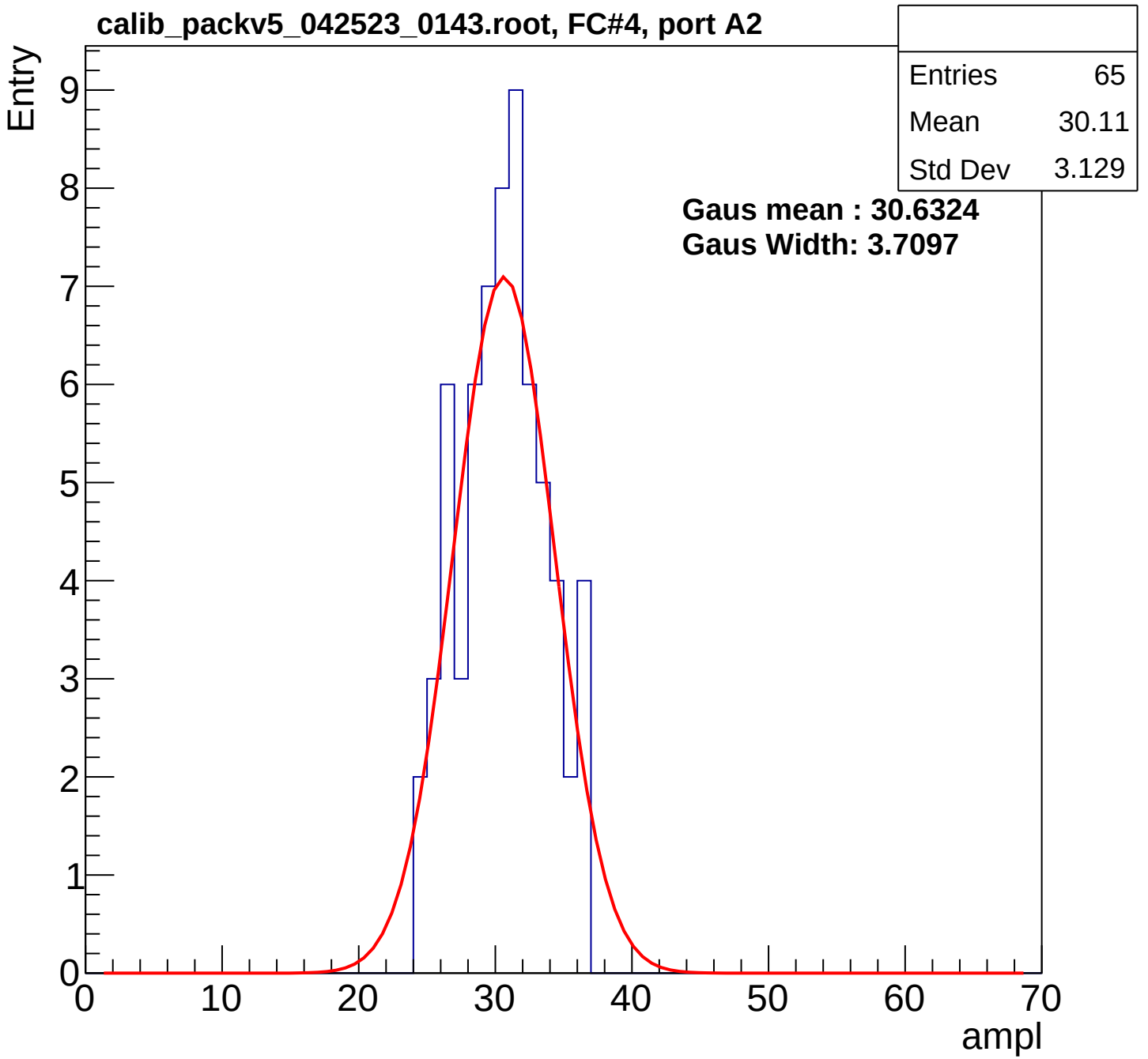
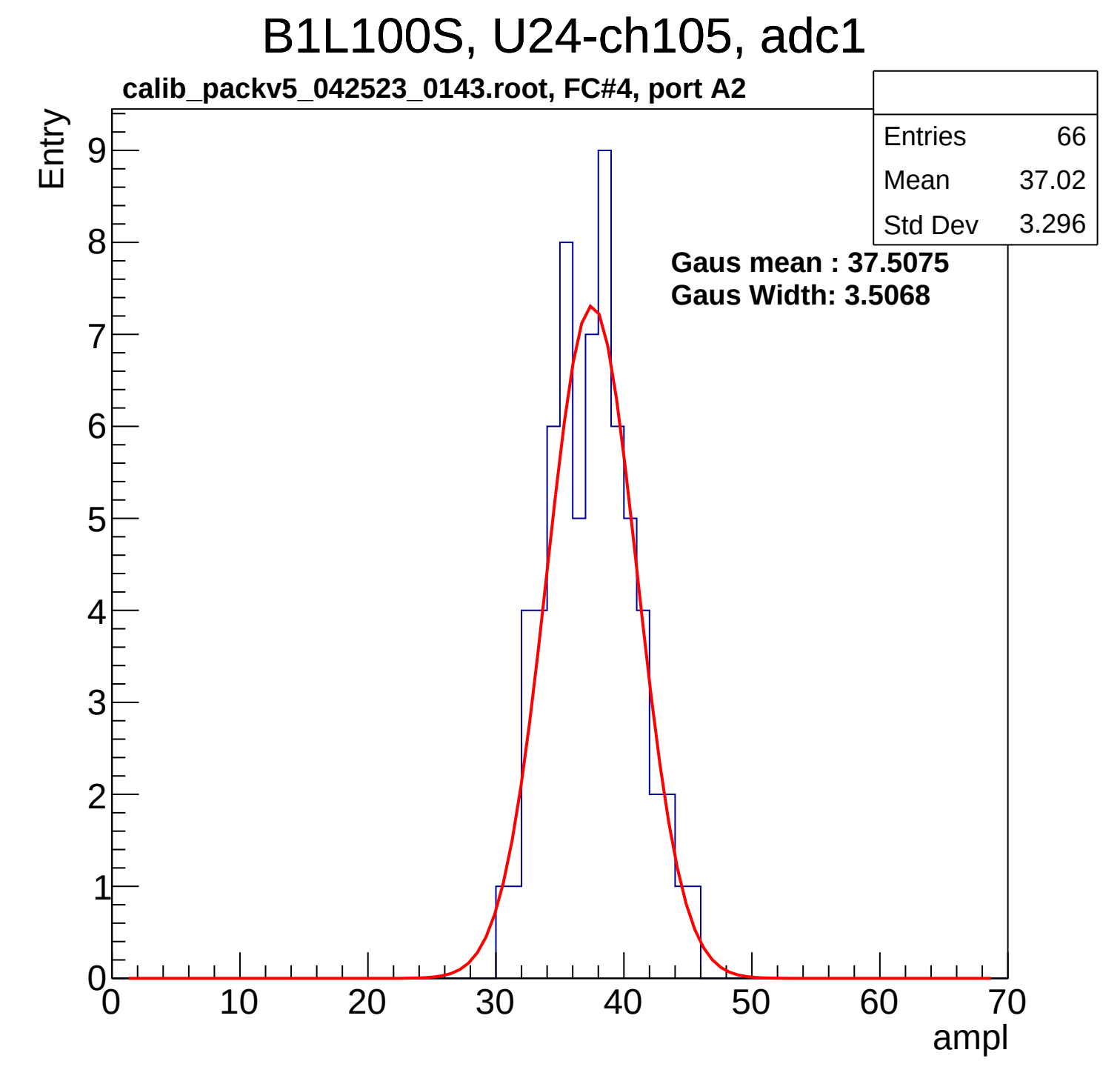

B1L100S, U24-ch104, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.11
Std Dev 3.129
Gaus mean : 30.6324
Gaus Width: 3.7097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 37.02
Std Dev 3.296
Gaus mean : 37.5075
Gaus Width: 3.5068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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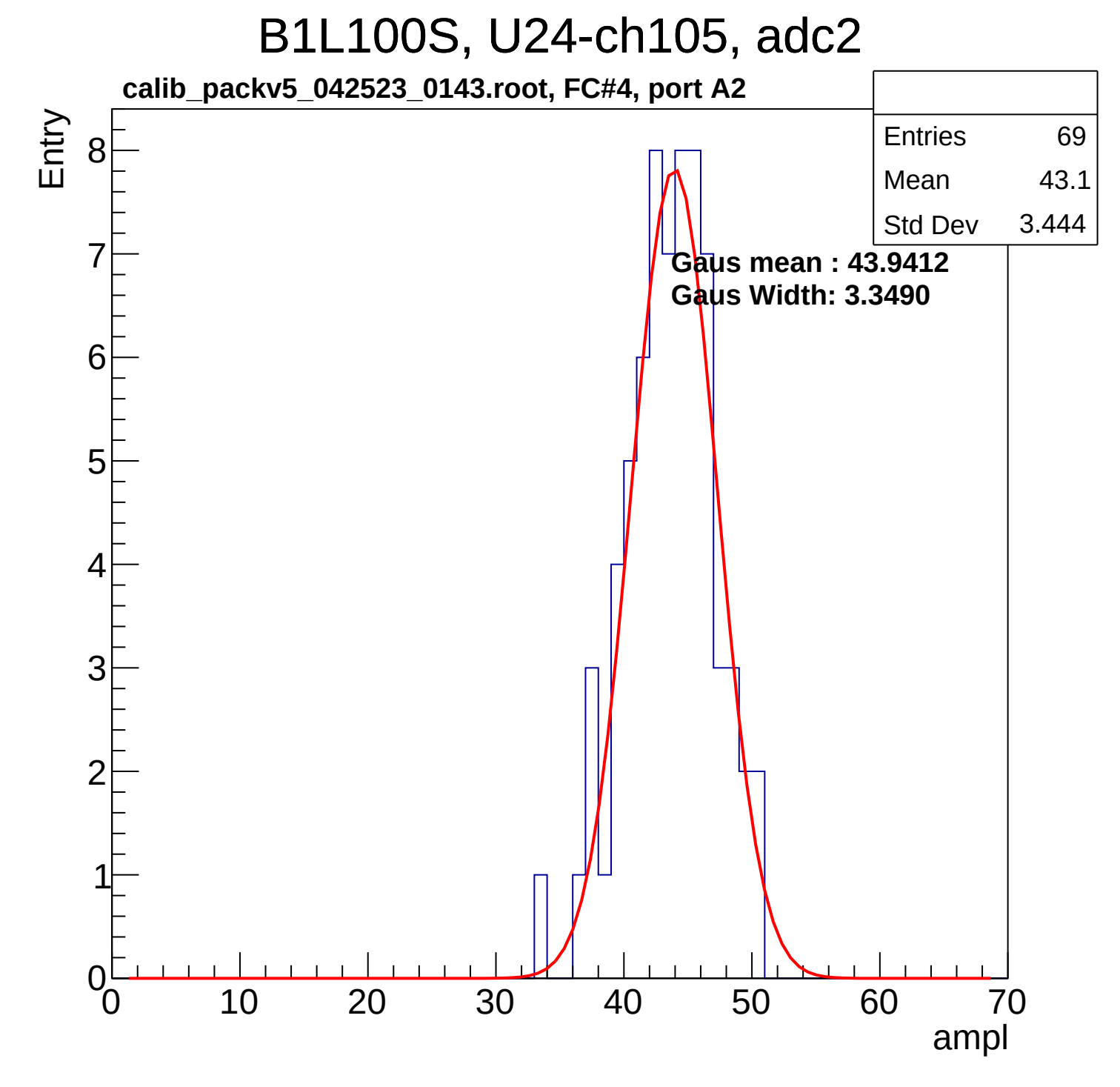
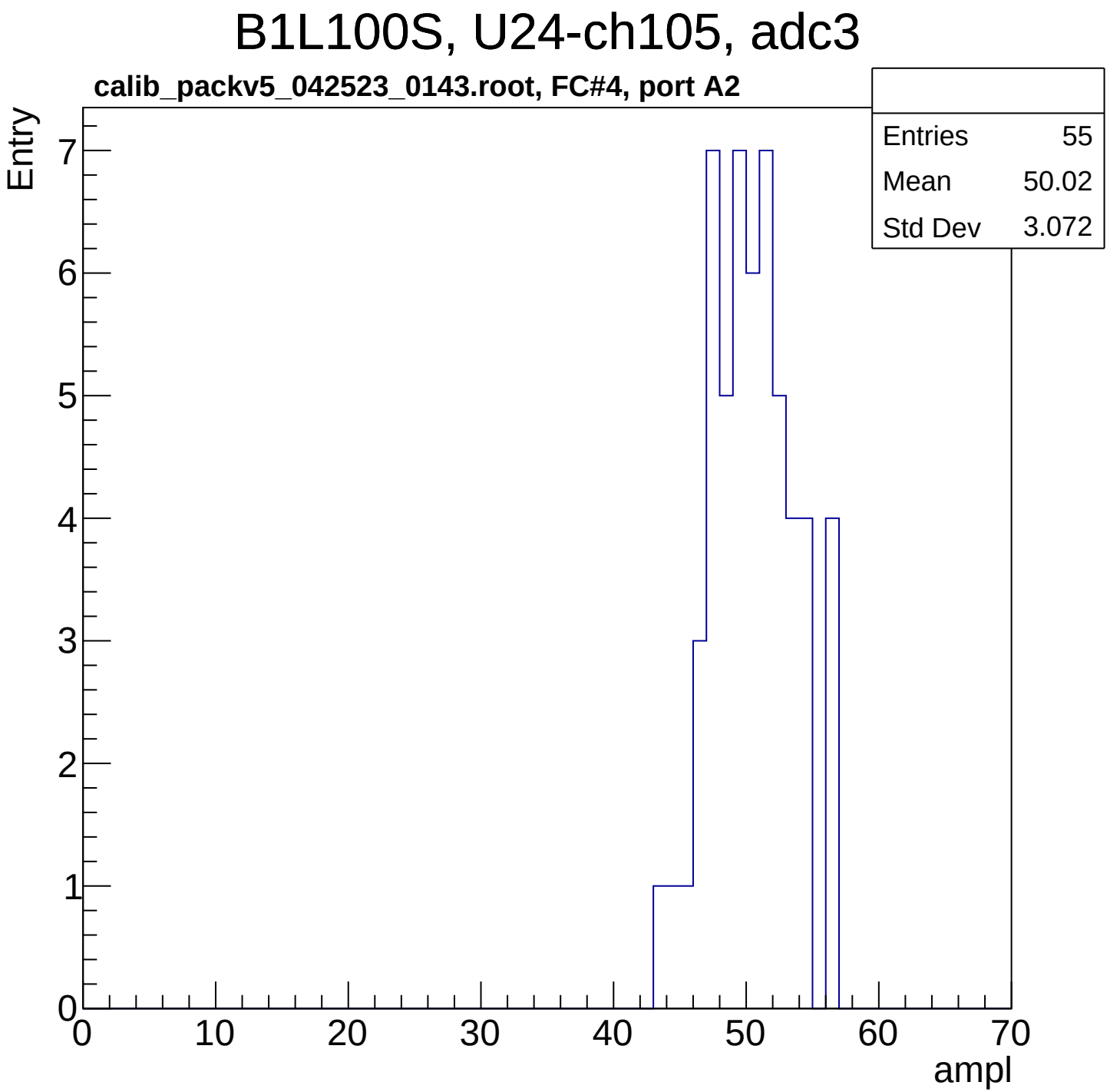
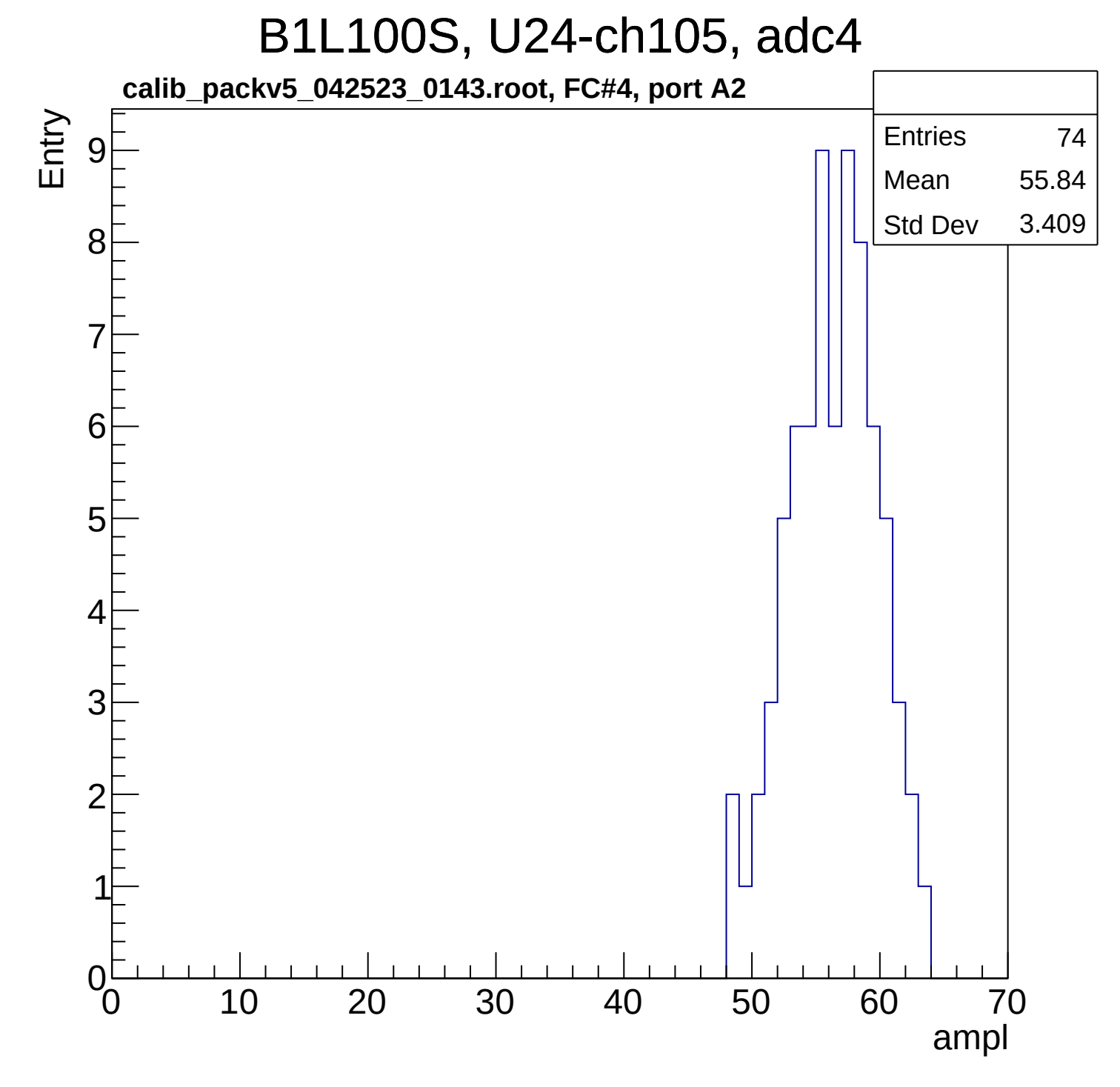
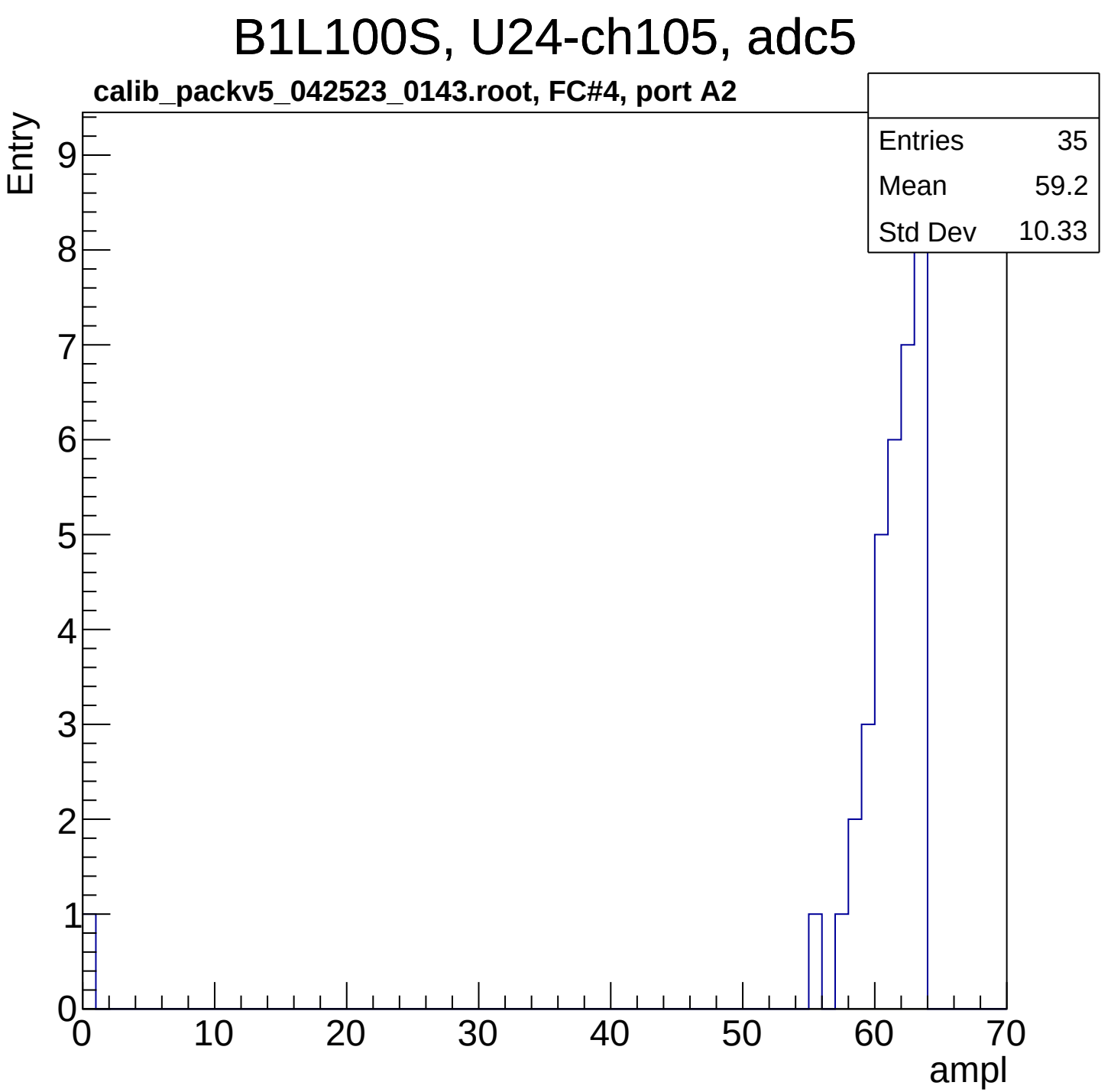
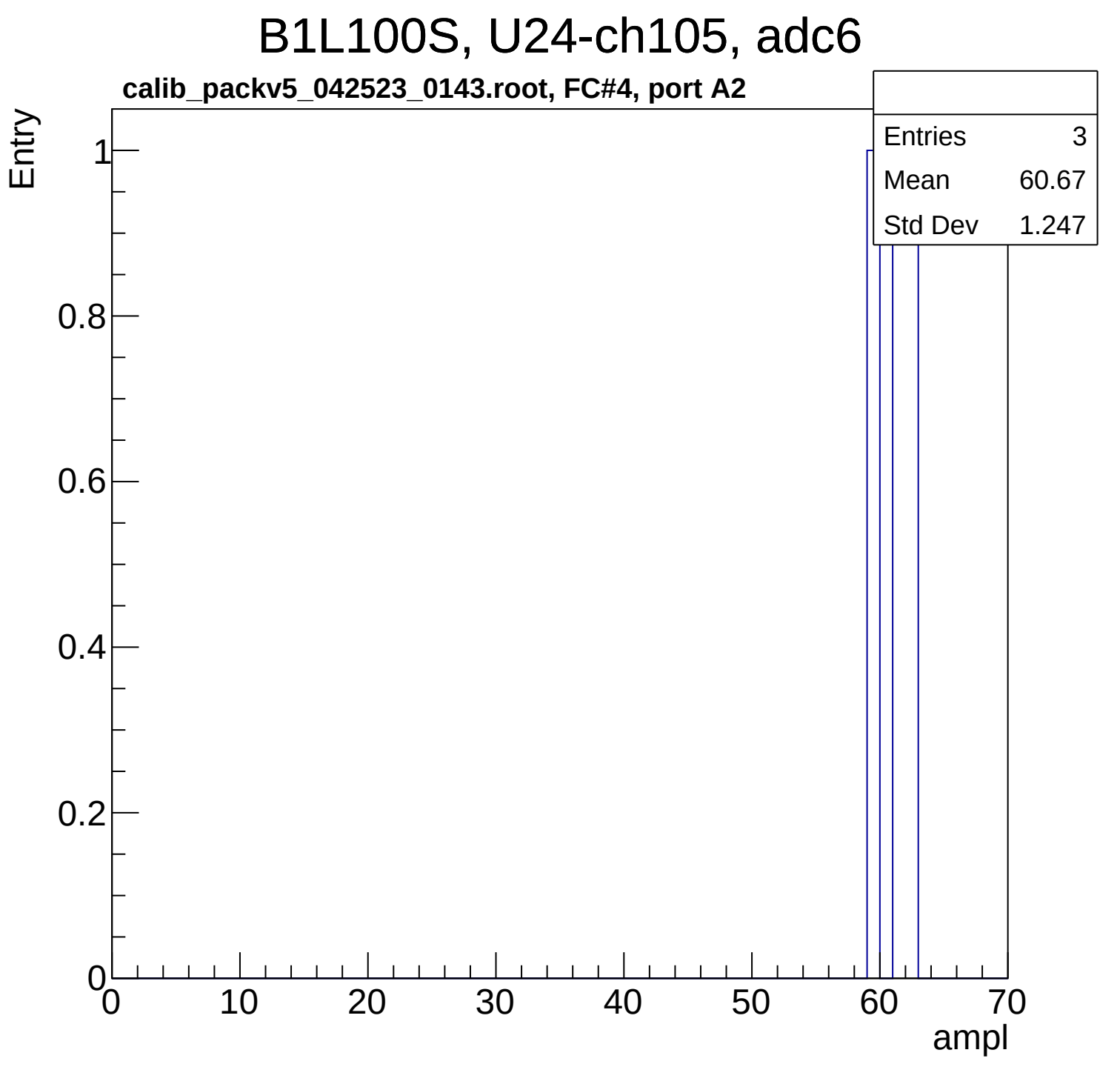
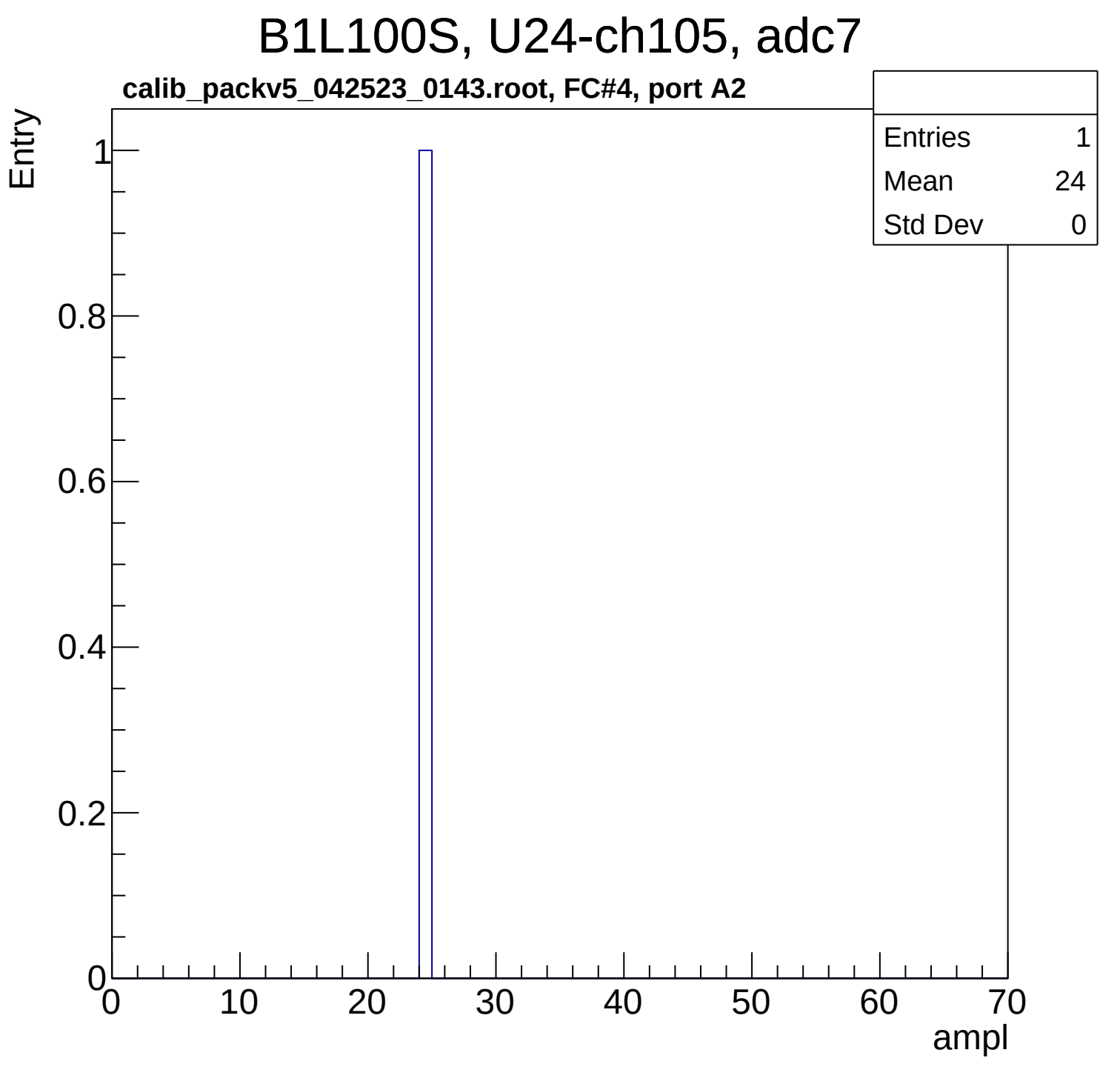

B1L100S, U24-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.1
Std Dev 3.444
Gaus mean : 43.9412
Gaus Width: 3.3490
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

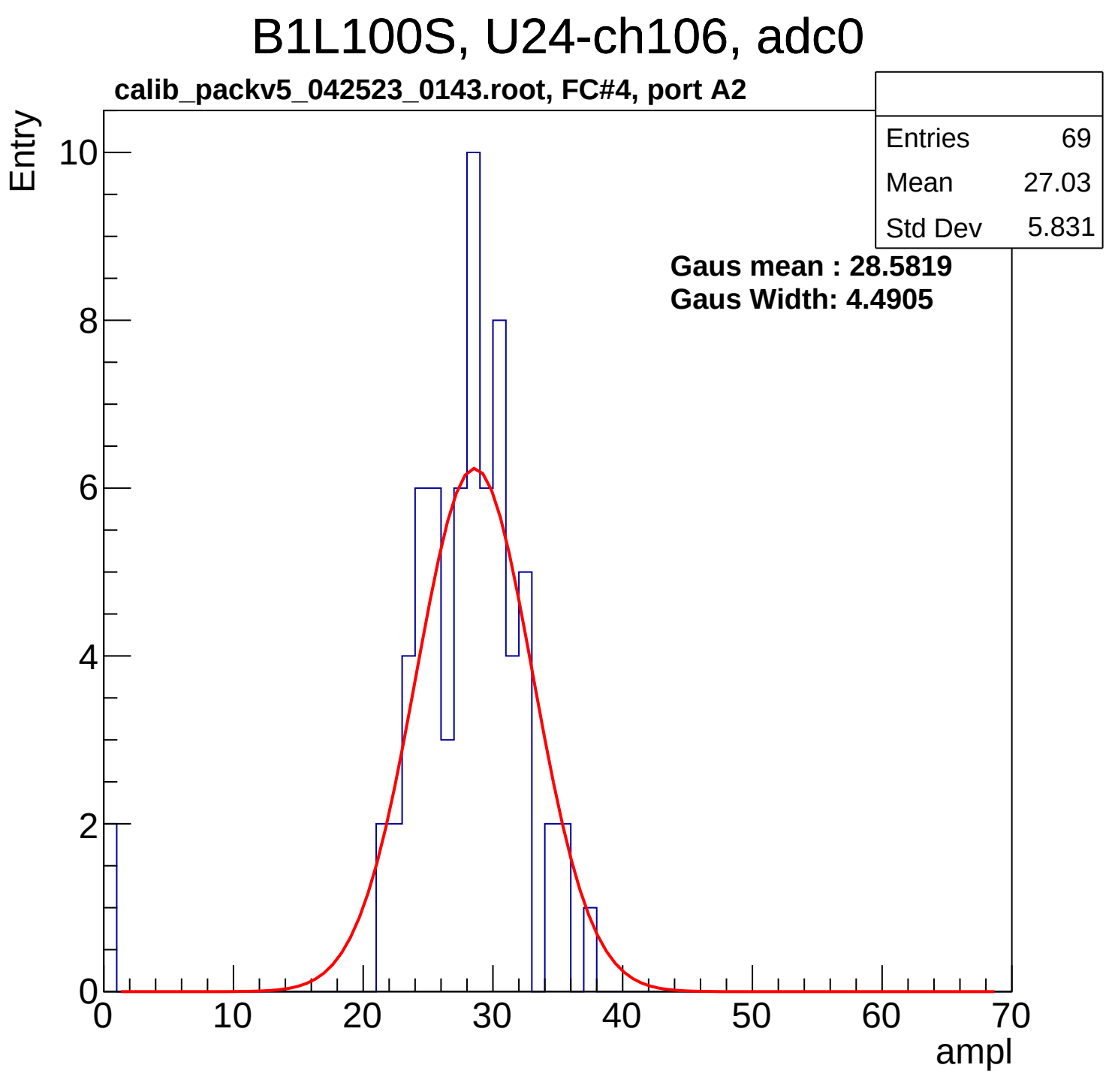

B1L100S, U24-ch105, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.02
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

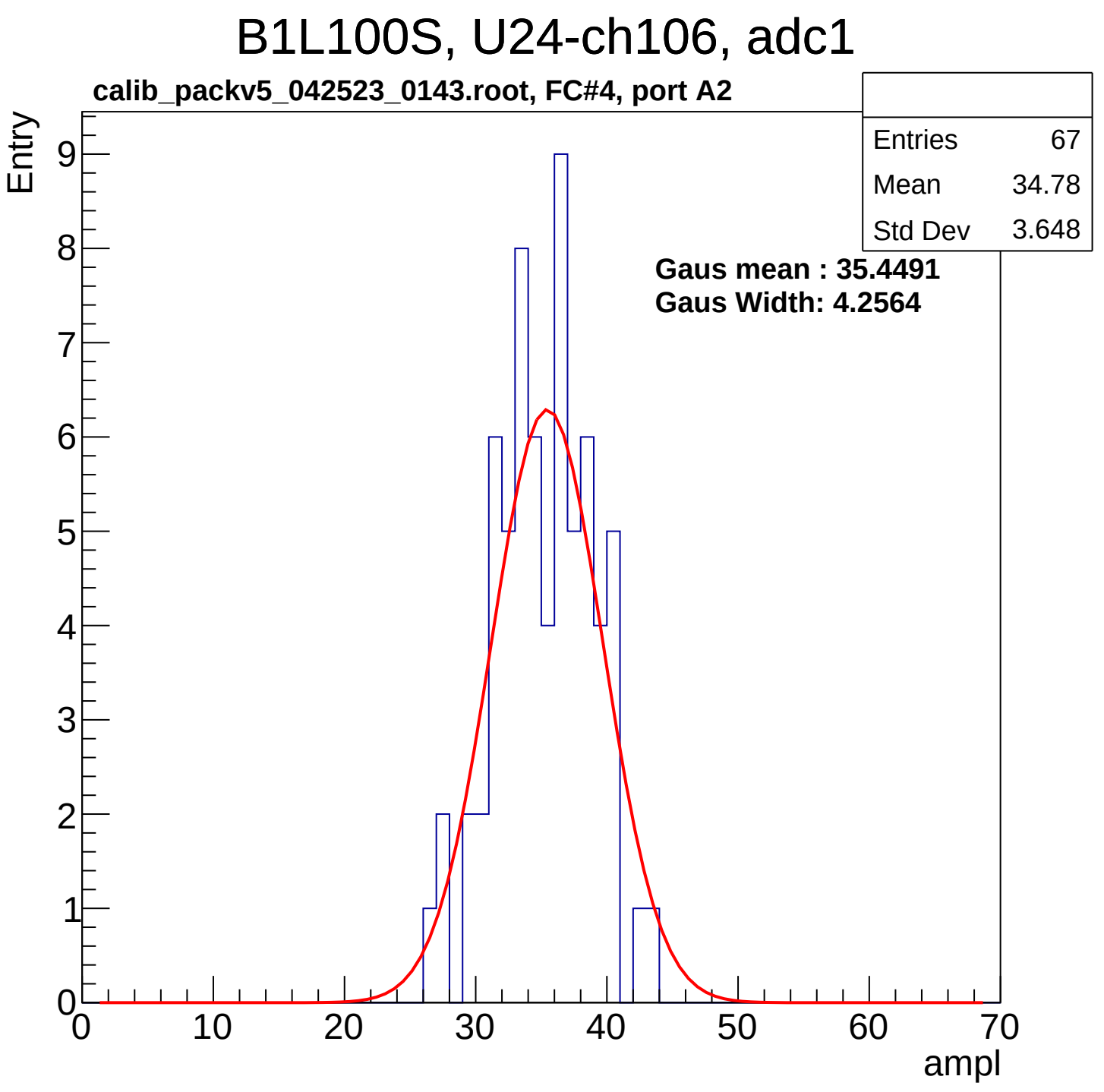

B1L100S, U24-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 55.84
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

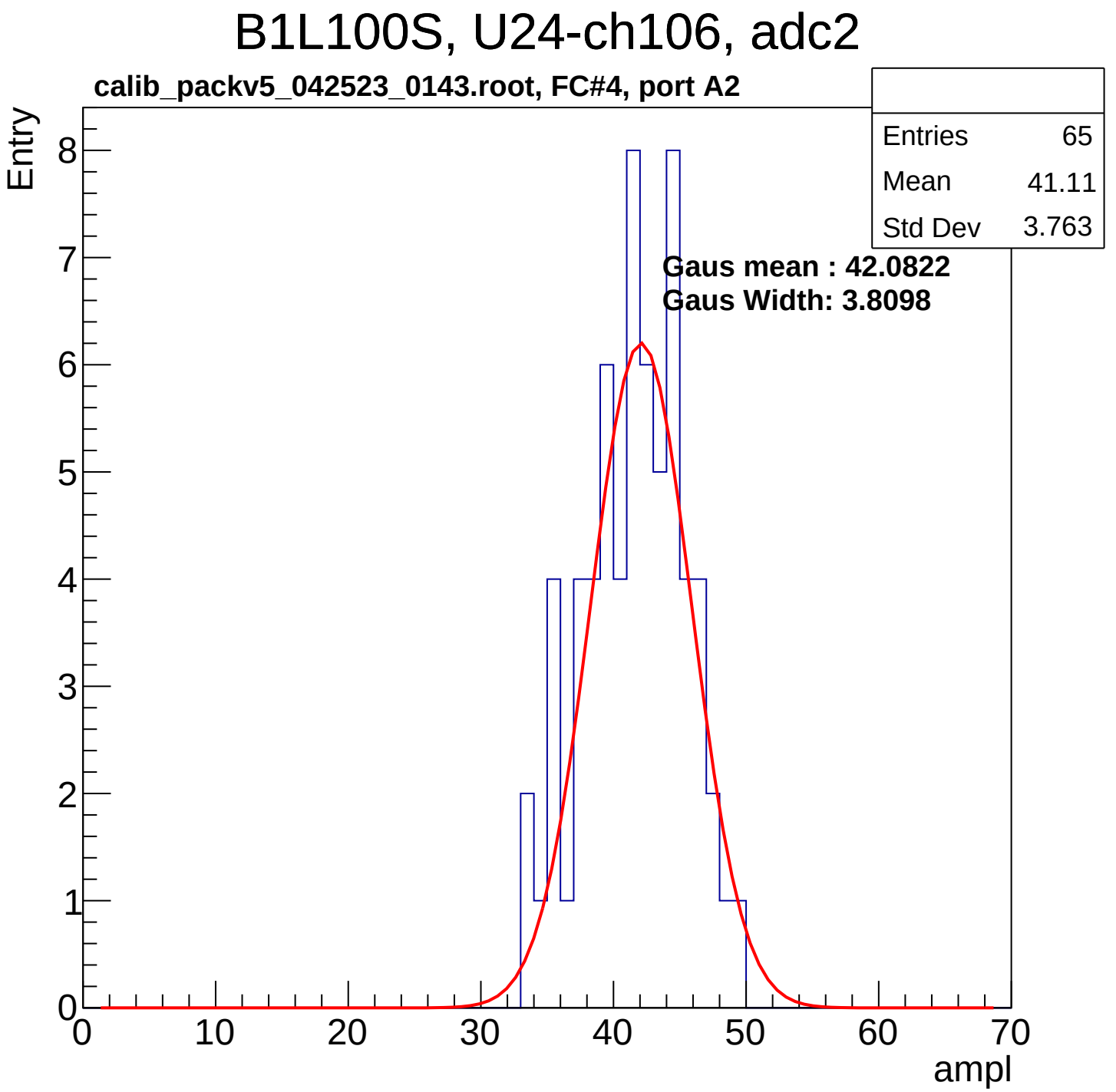

B1L100S, U24-ch105, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.2
Std Dev 10.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

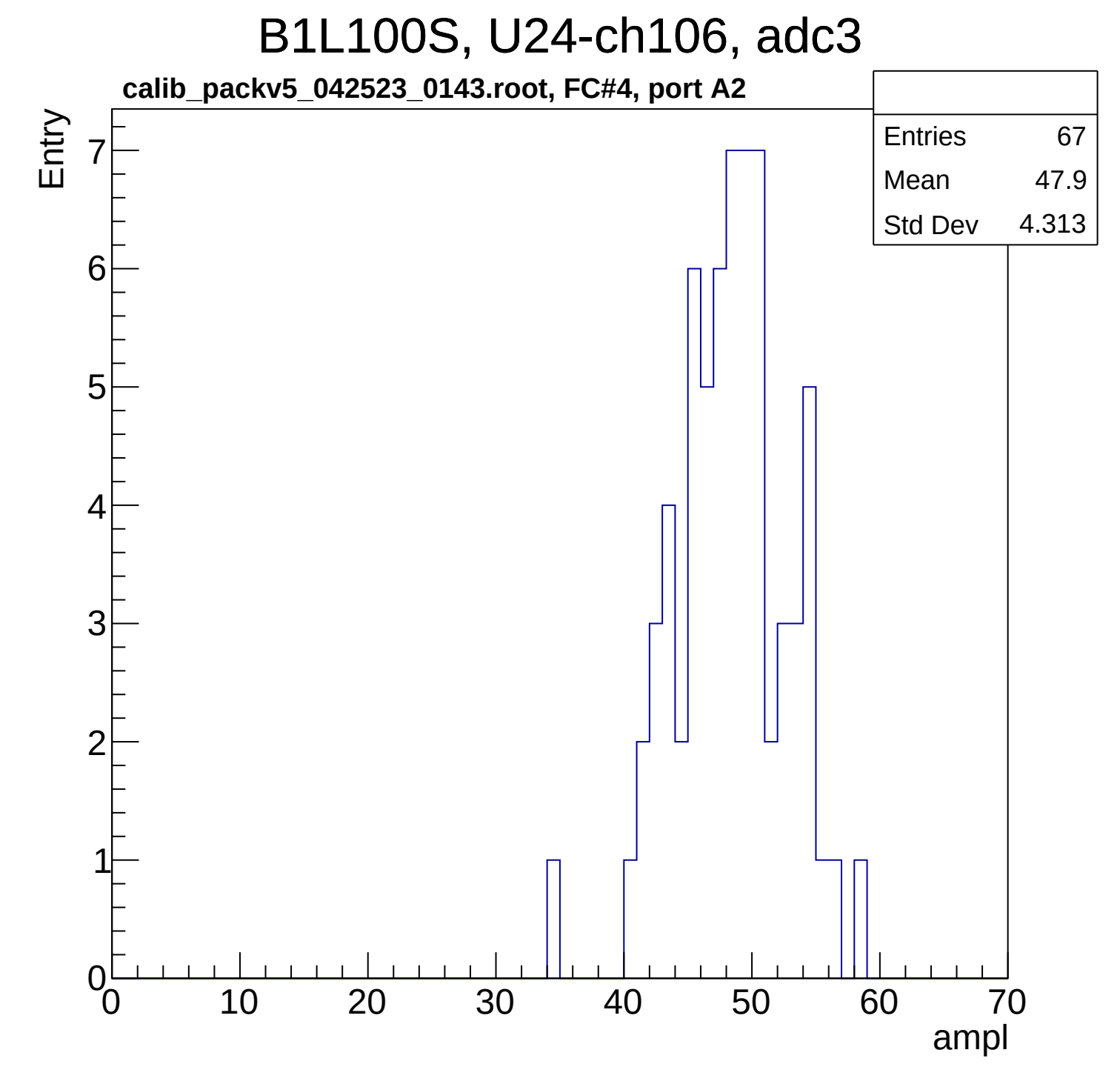

B1L100S, U24-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

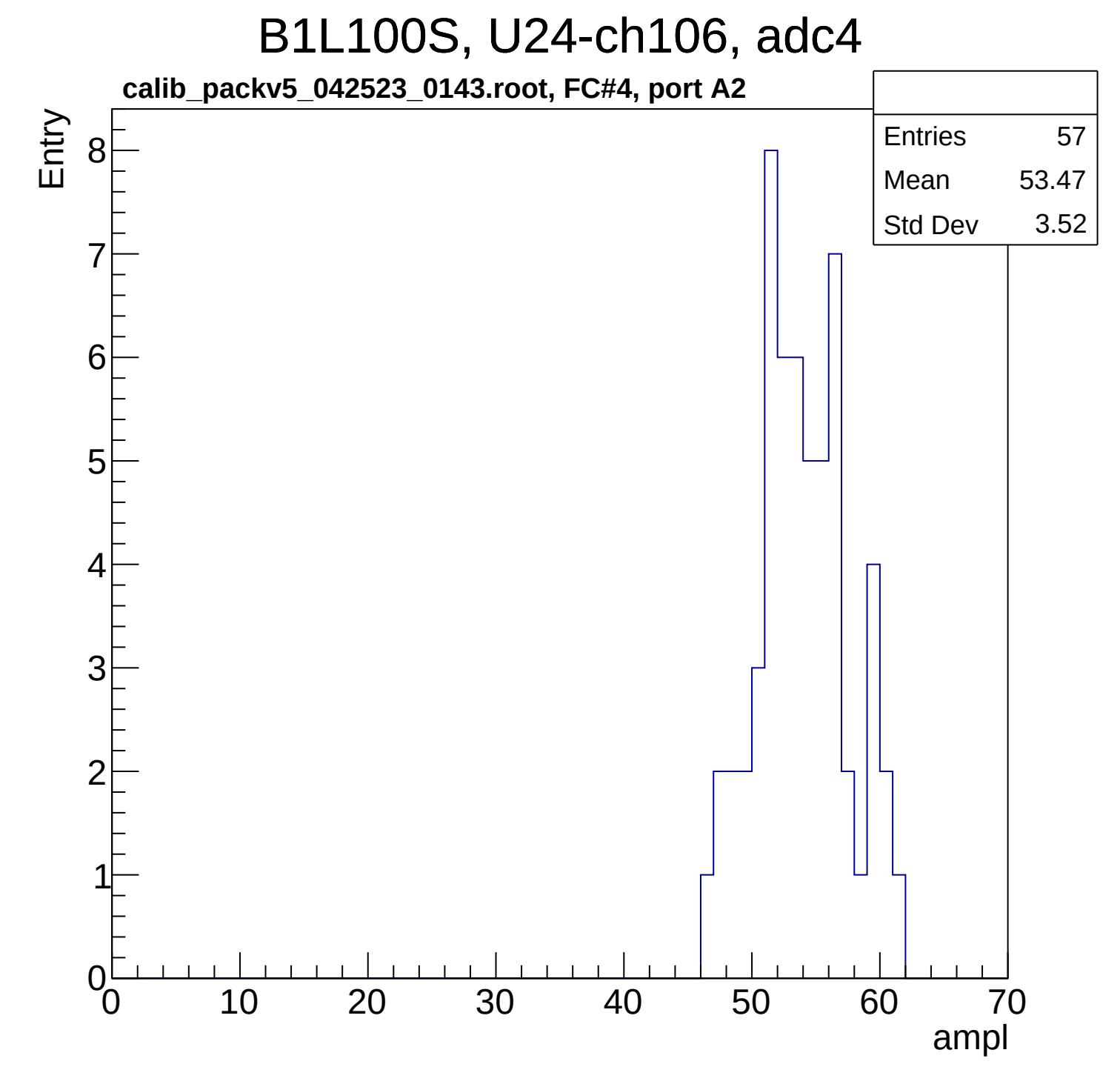

B1L100S, U24-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 27.03
Std Dev 5.831
Gaus mean : 28.5819
Gaus Width: 4.4905
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch106, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 34.78
Std Dev 3.648
Gaus mean : 35.4491
Gaus Width: 4.2564
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 41.11
Std Dev 3.763
Gaus mean : 42.0822
Gaus Width: 3.8098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch106, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 47.9
Std Dev 4.313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch106, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 53.47
Std Dev 3.52
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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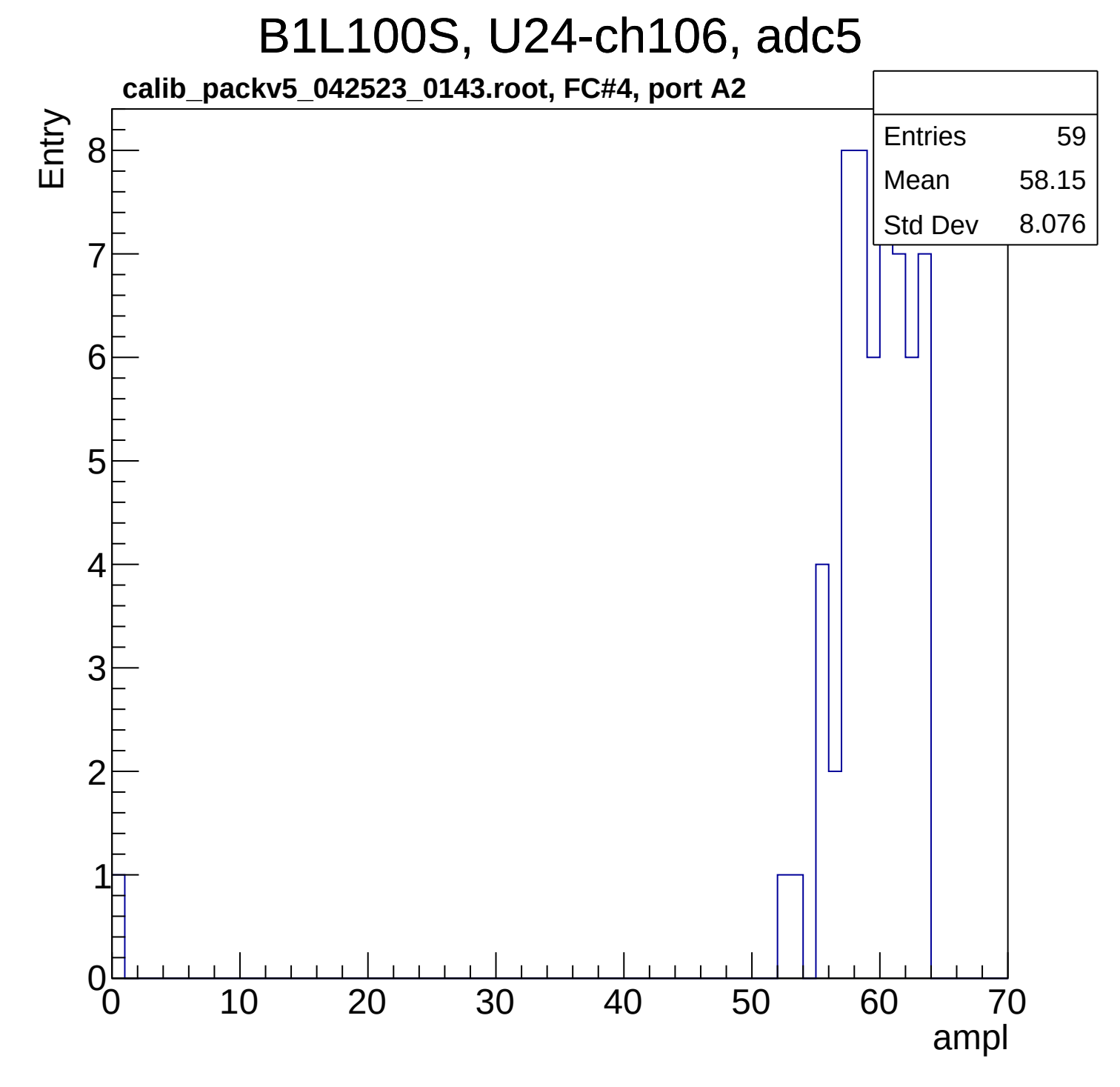
B1L100S, U24-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.15
Std Dev 8.076
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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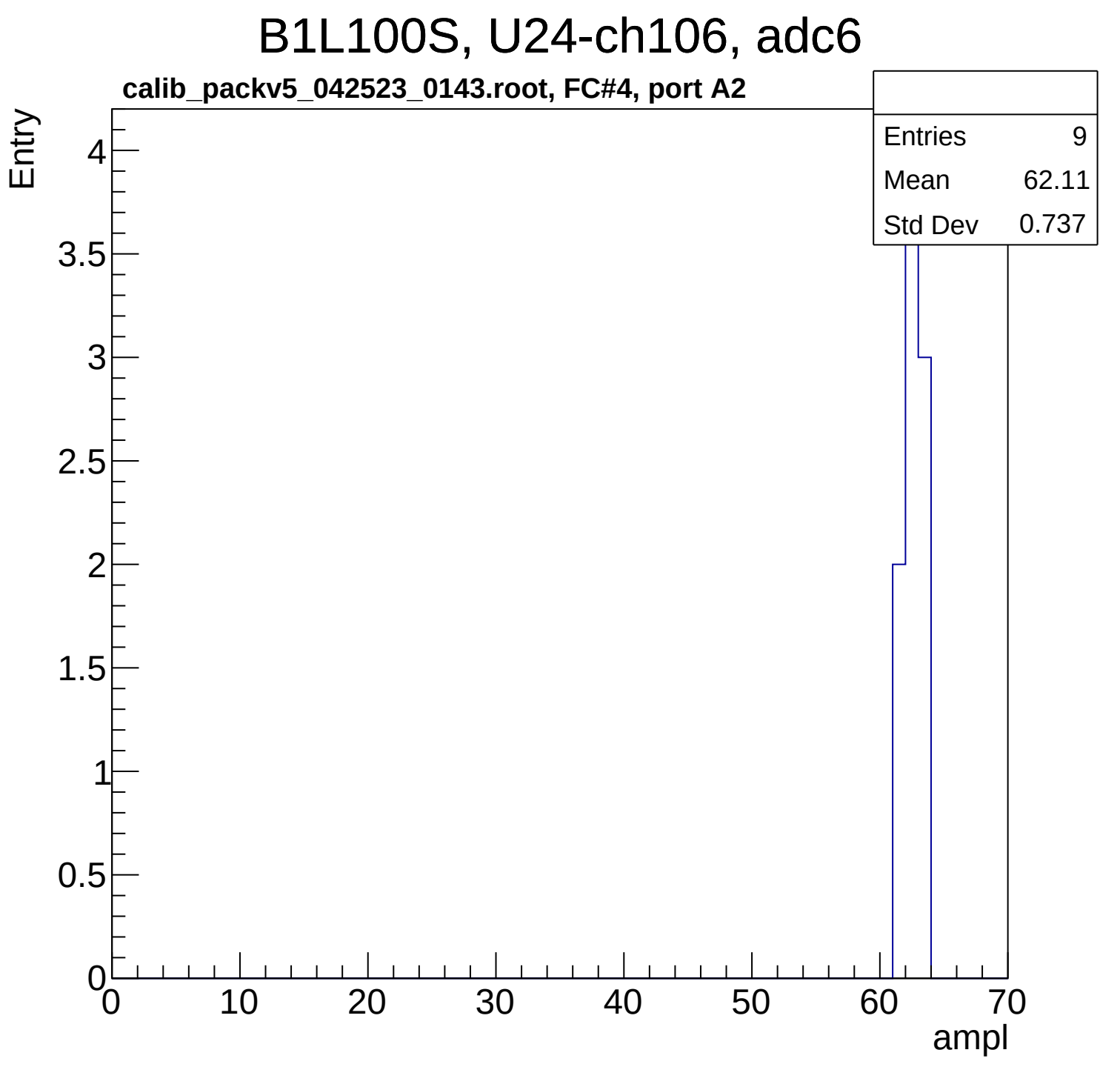
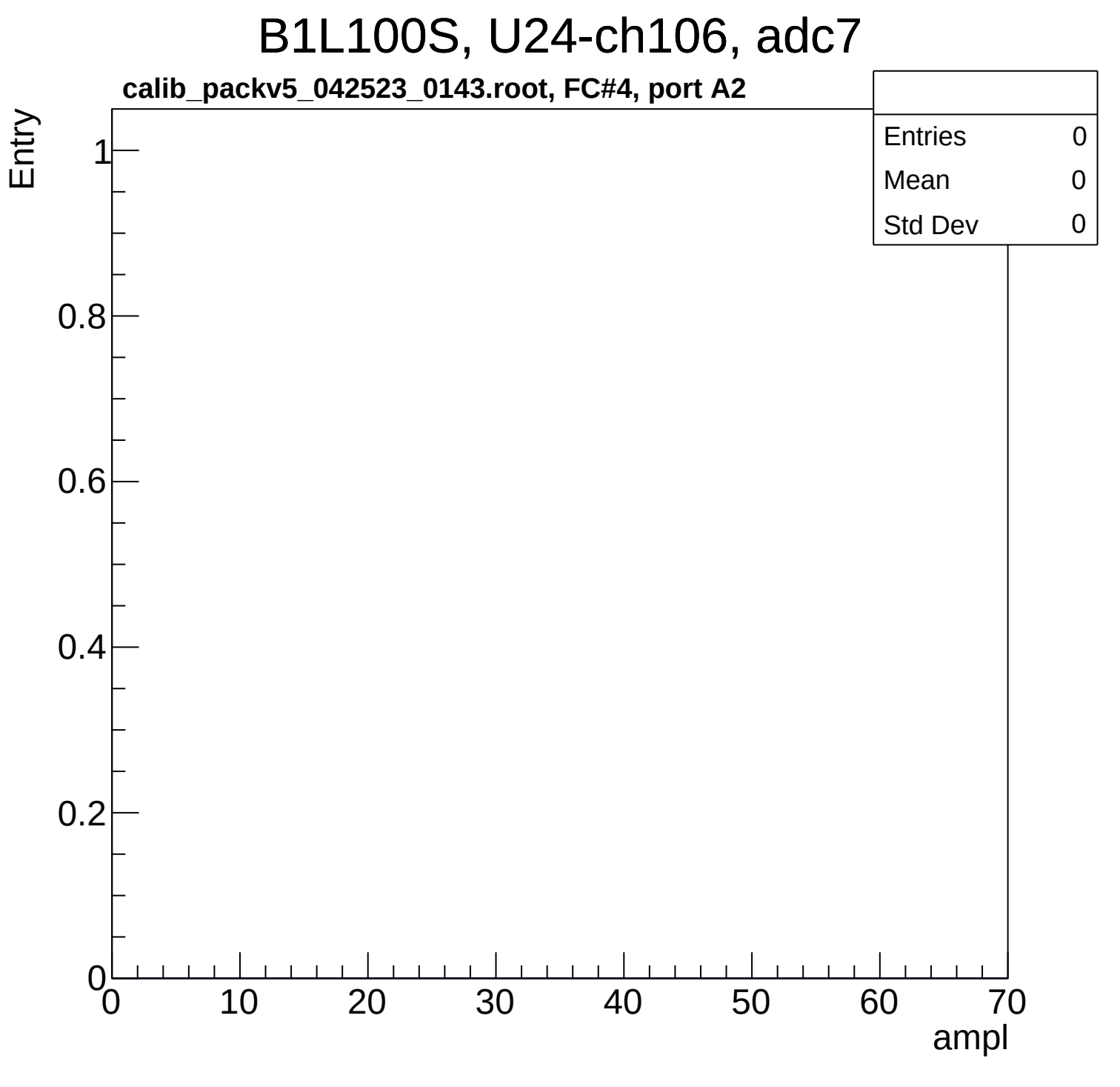
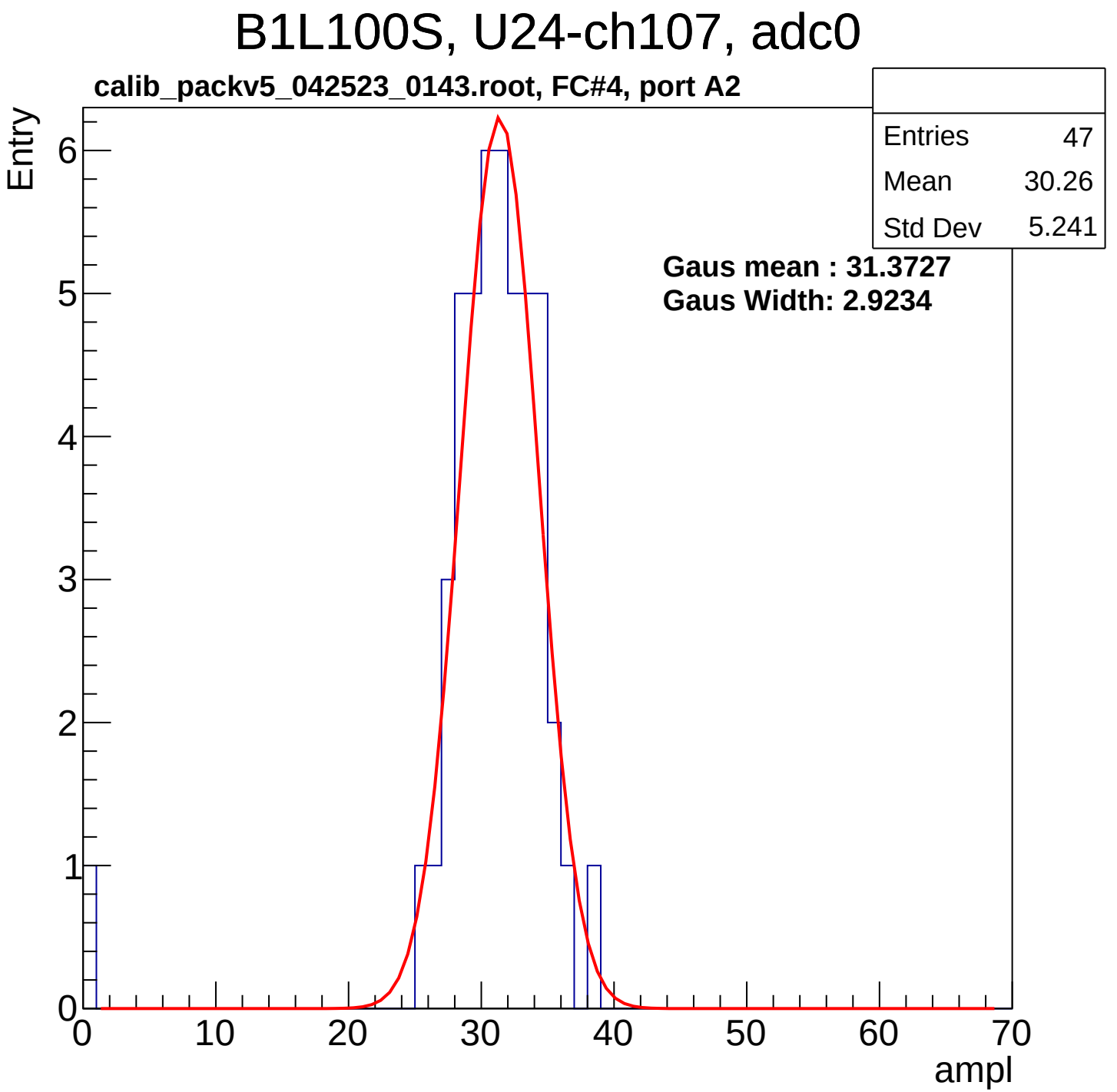
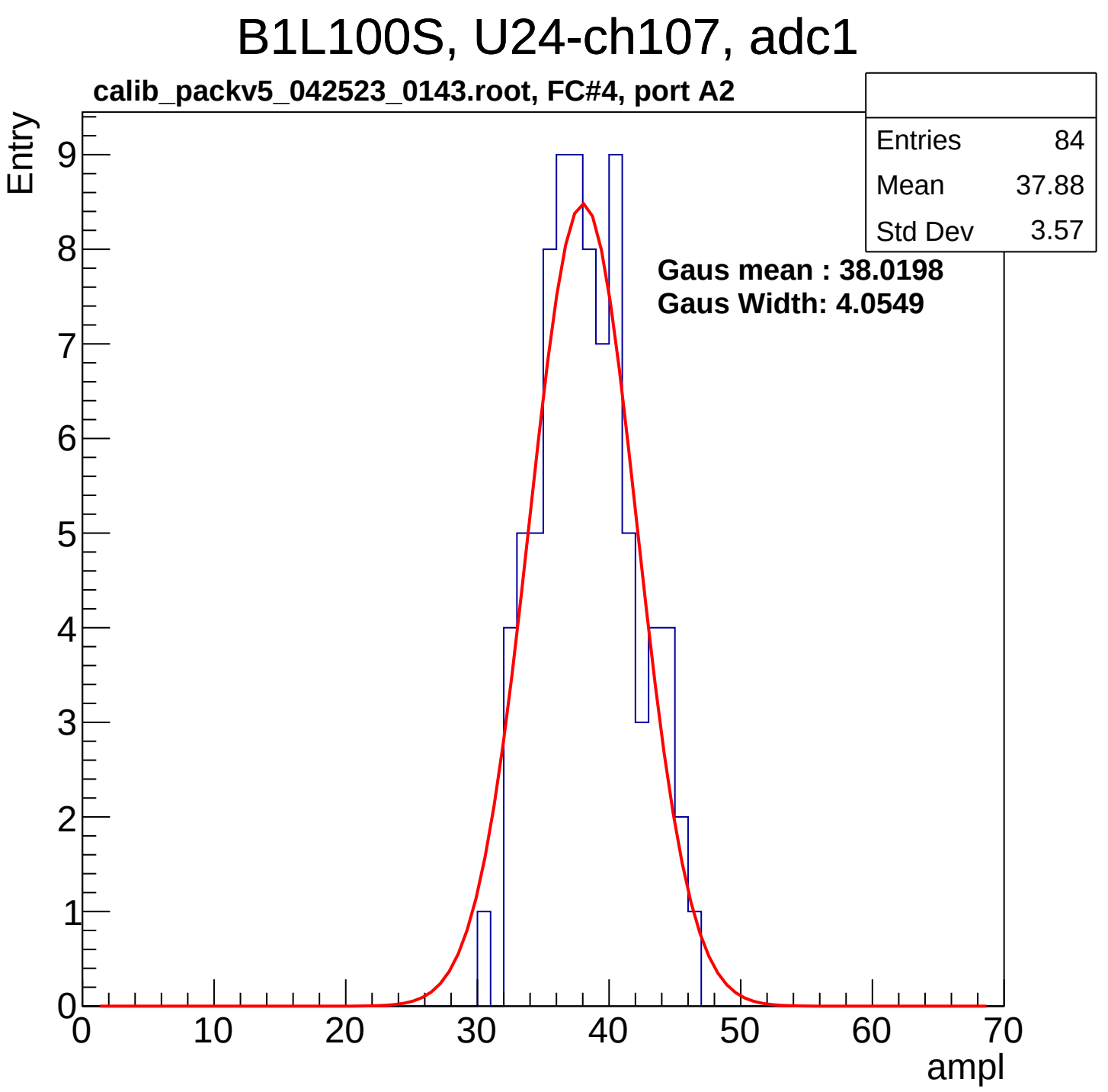
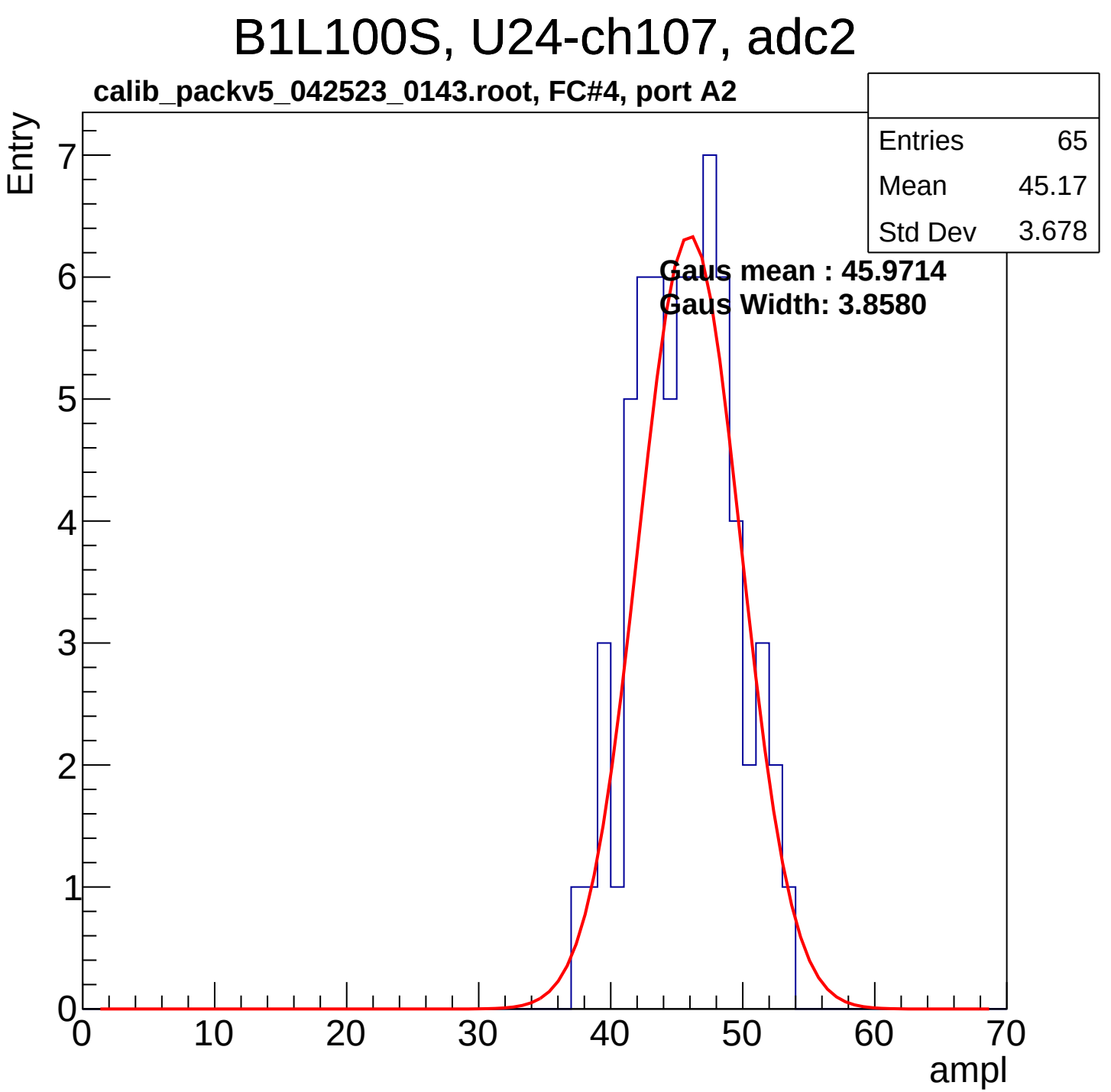
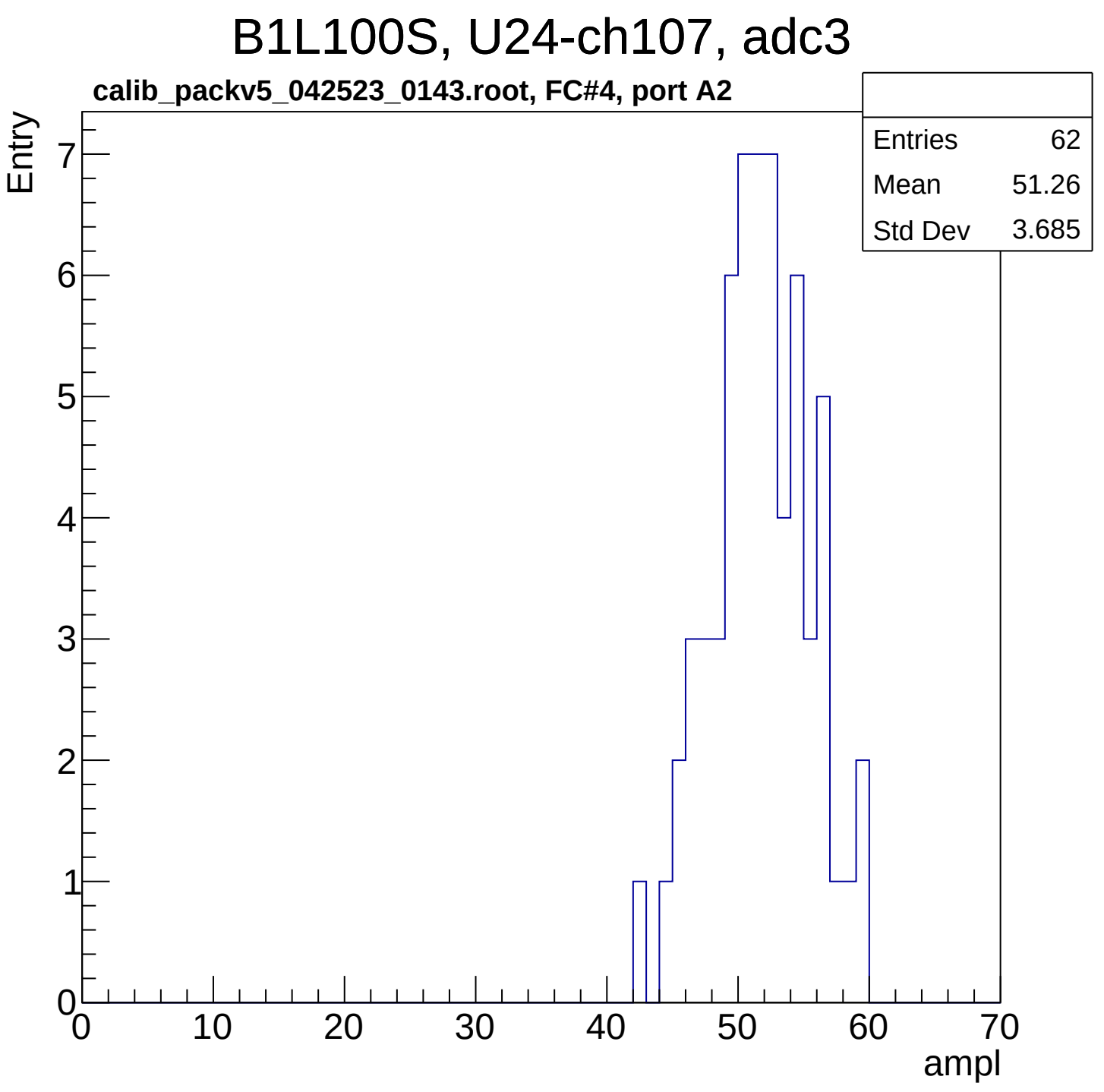

B1L100S, U24-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62.11
Std Dev 0.737
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

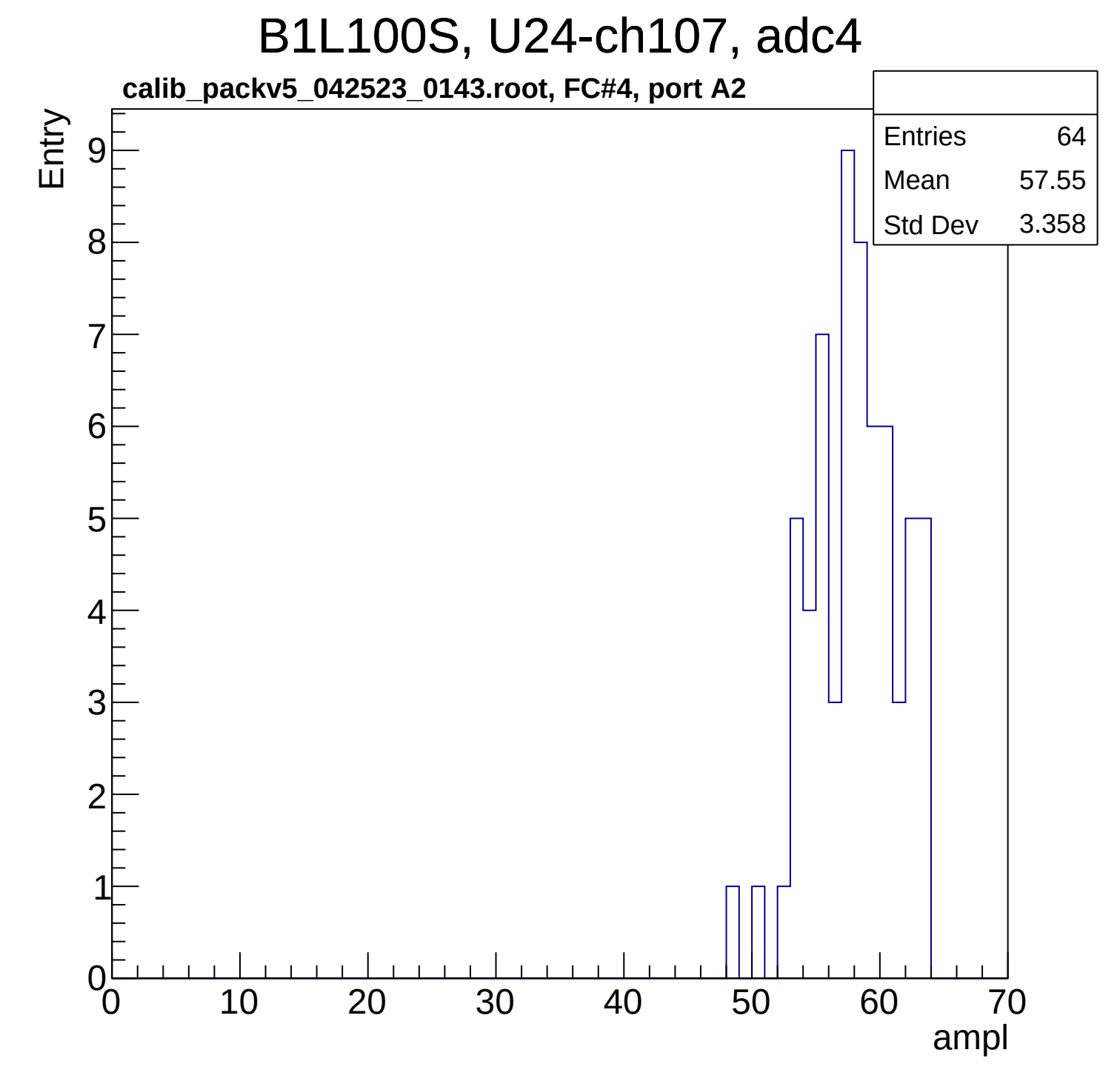

B1L100S, U24-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch107, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 30.26
Std Dev 5.241
Gaus mean : 31.3727
Gaus Width: 2.9234
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.88
Std Dev 3.57
Gaus mean : 38.0198
Gaus Width: 4.0549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 45.17
Std Dev 3.678
Gaus mean : 45.9714
Gaus Width: 3.8580
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.26
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.55
Std Dev 3.358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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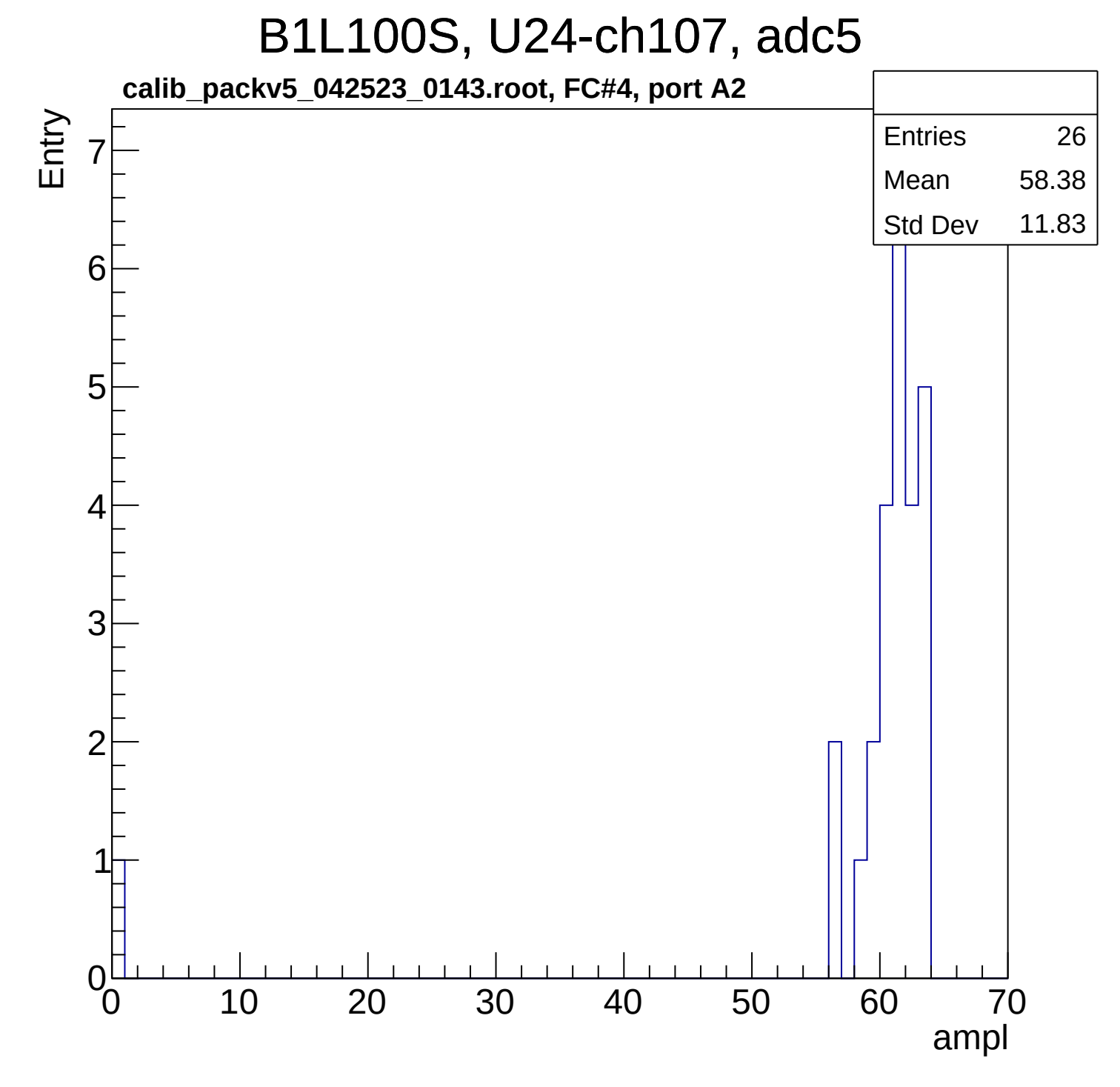
40
50
60
70


B1L100S, U24-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.38
Std Dev 11.83
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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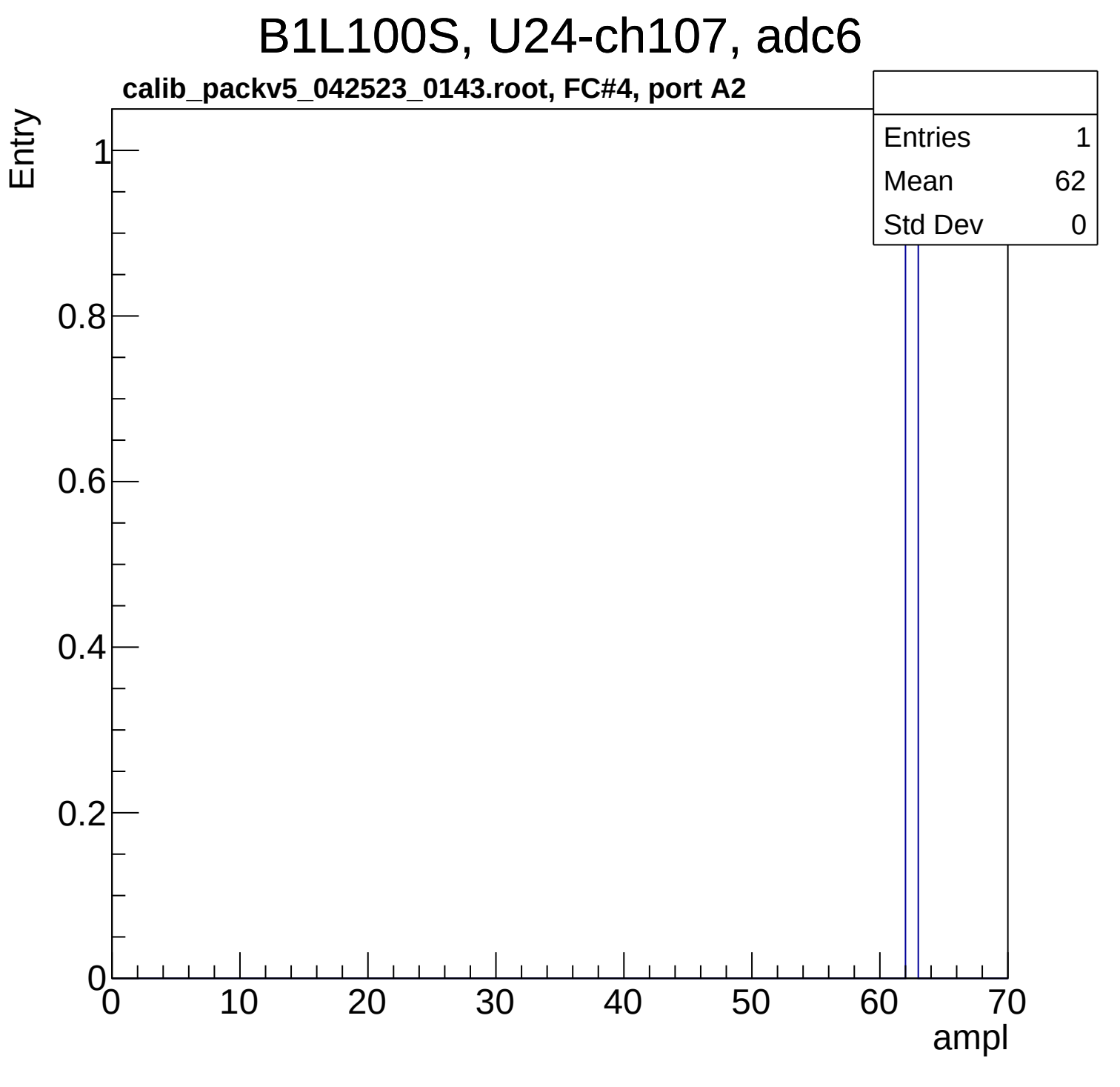
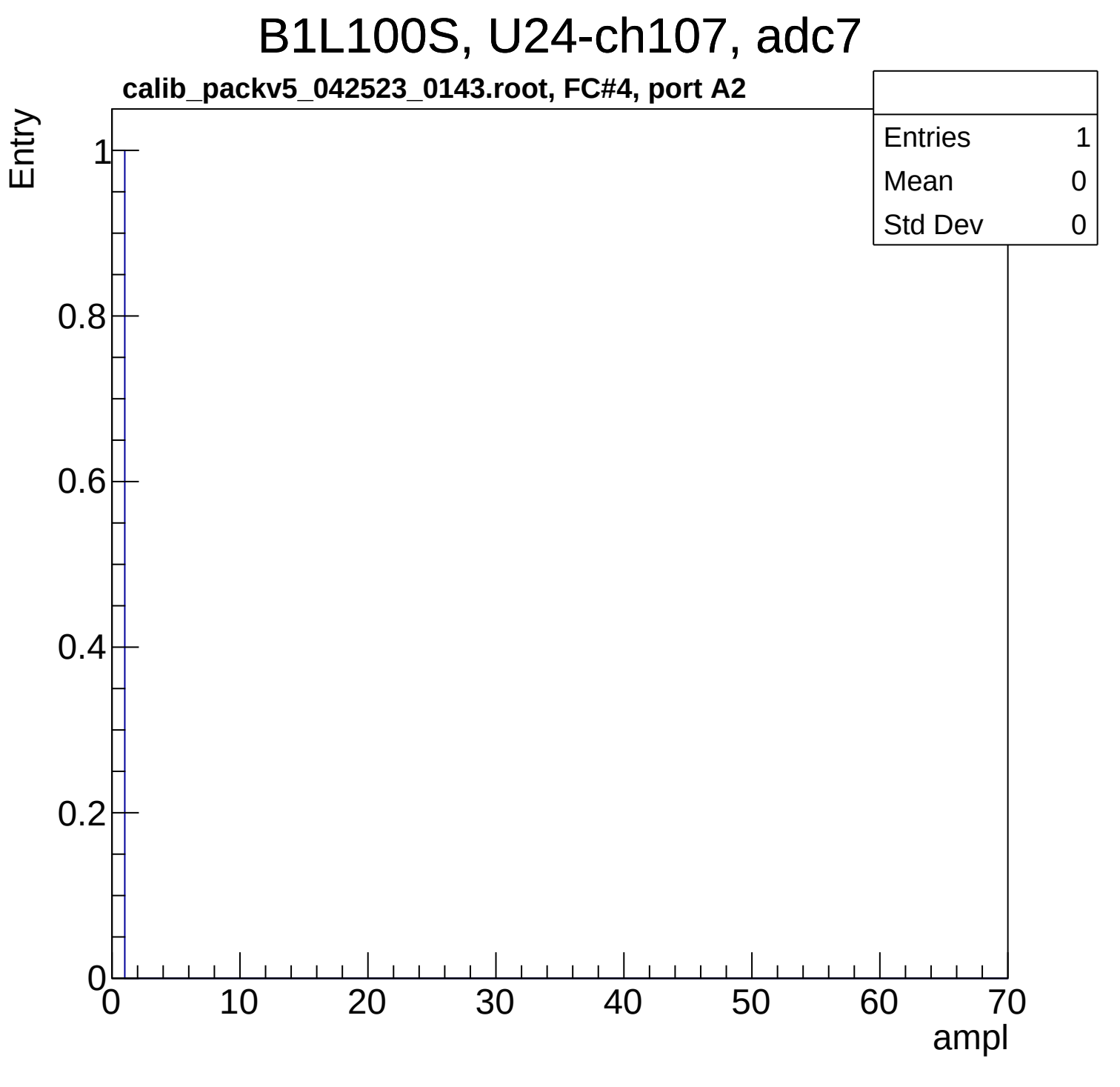

B1L100S, U24-ch107, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch107, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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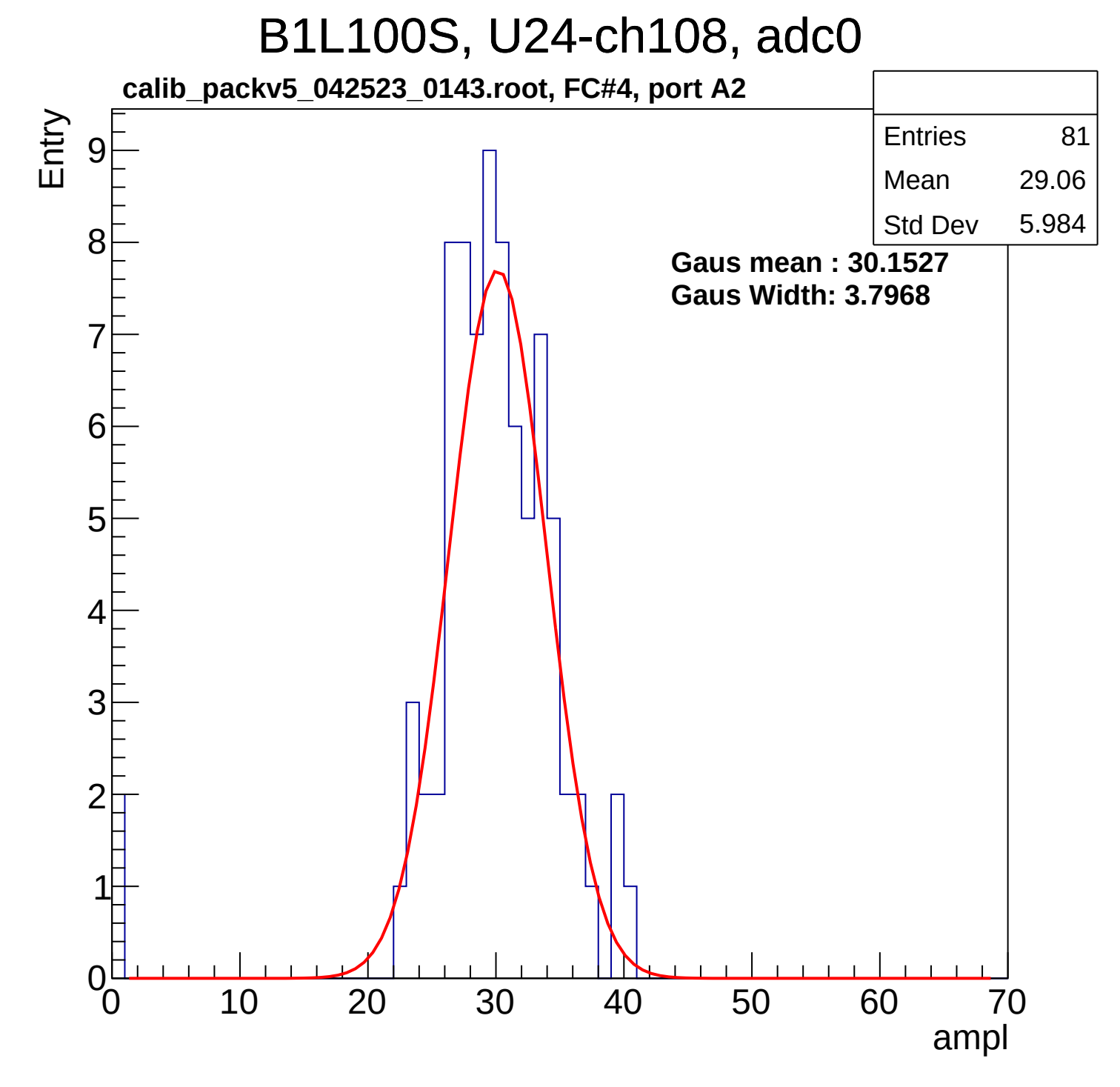
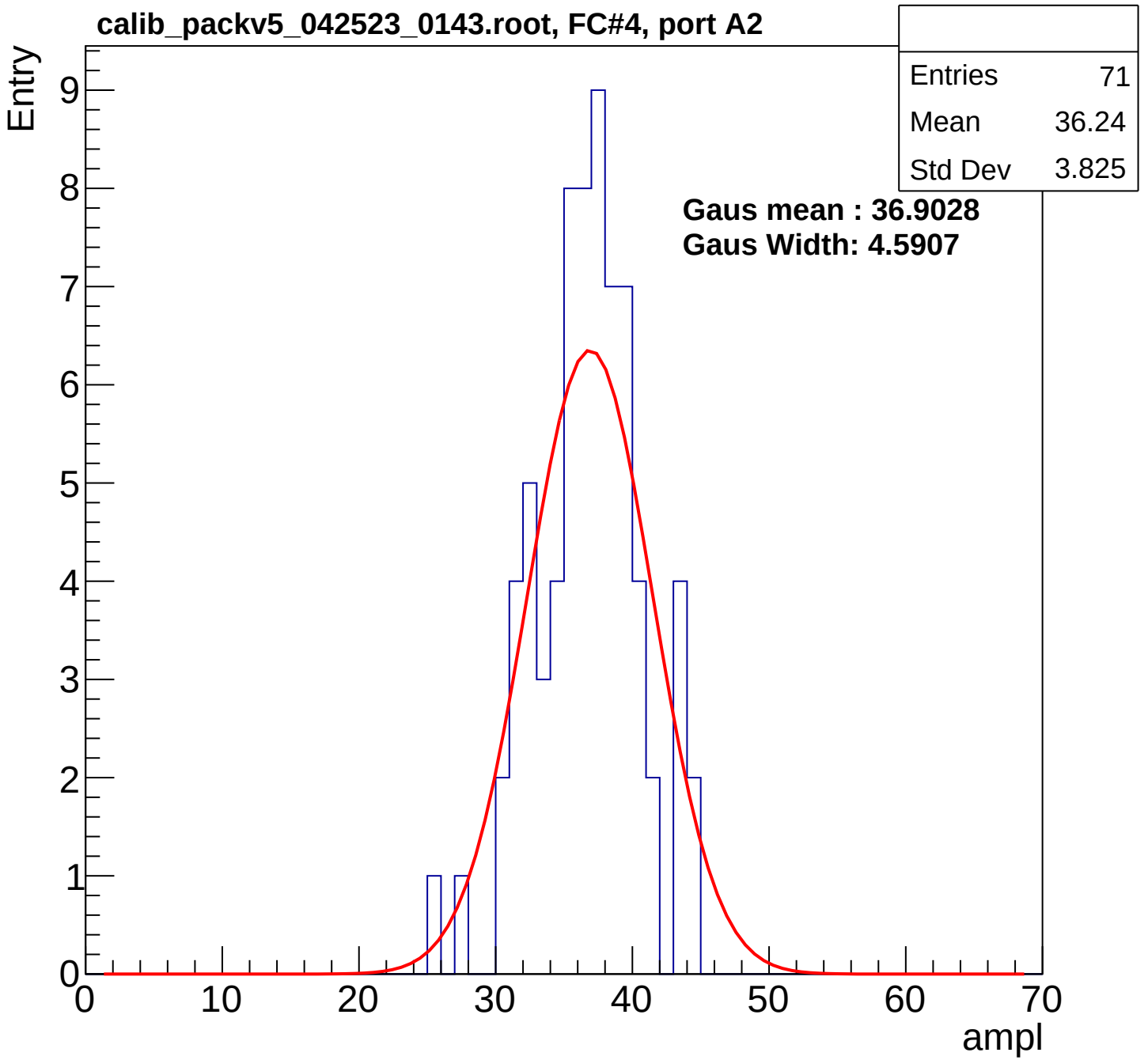
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.06
Std Dev 5.984
Gaus mean : 30.1527
Gaus Width: 3.7968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.24
Std Dev 3.825
Gaus mean : 36.9028
Gaus Width: 4.5907
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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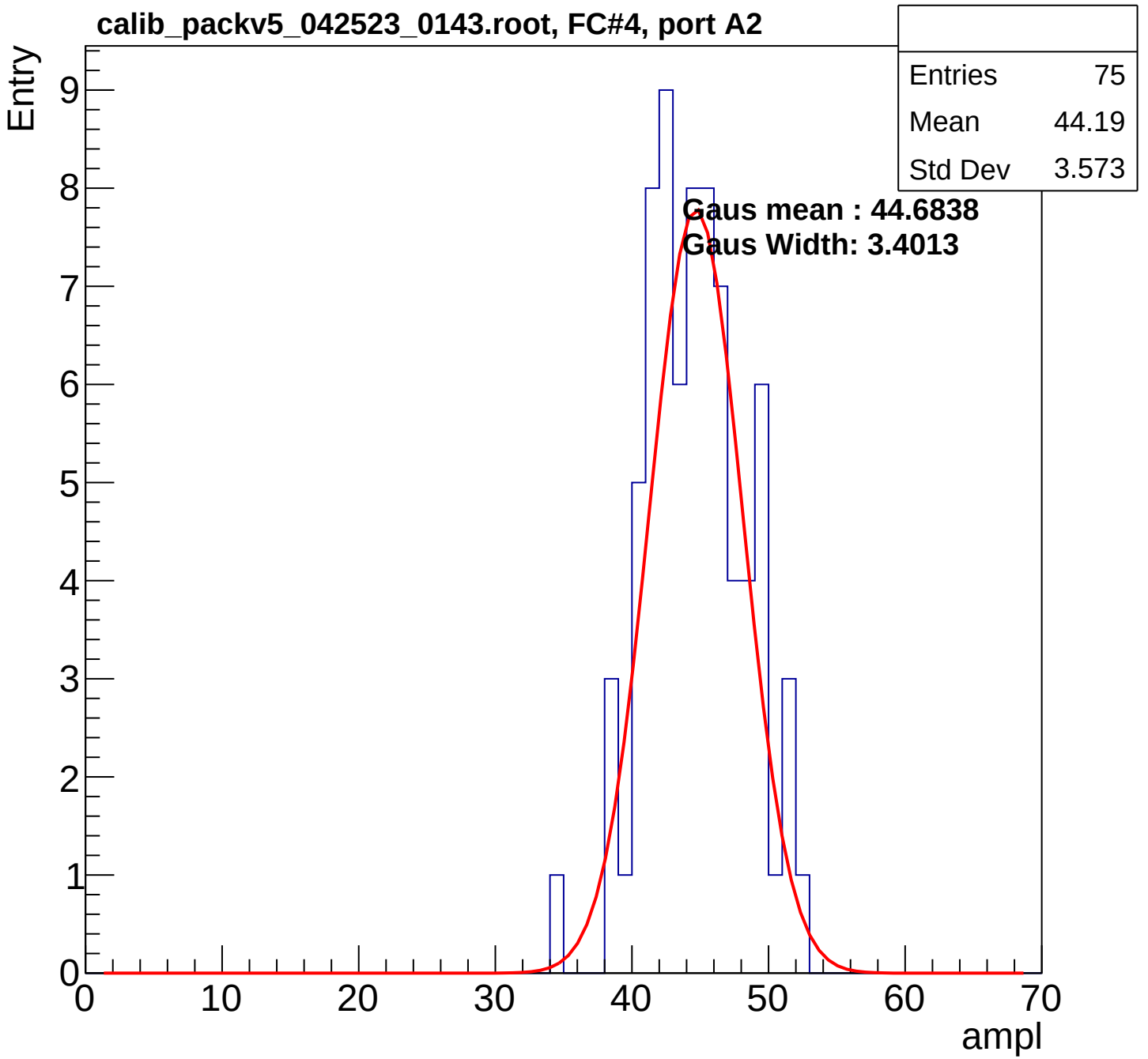
B1L100S, U24-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.19
Std Dev 3.573
Gaus mean : 44.6838
Gaus Width: 3.4013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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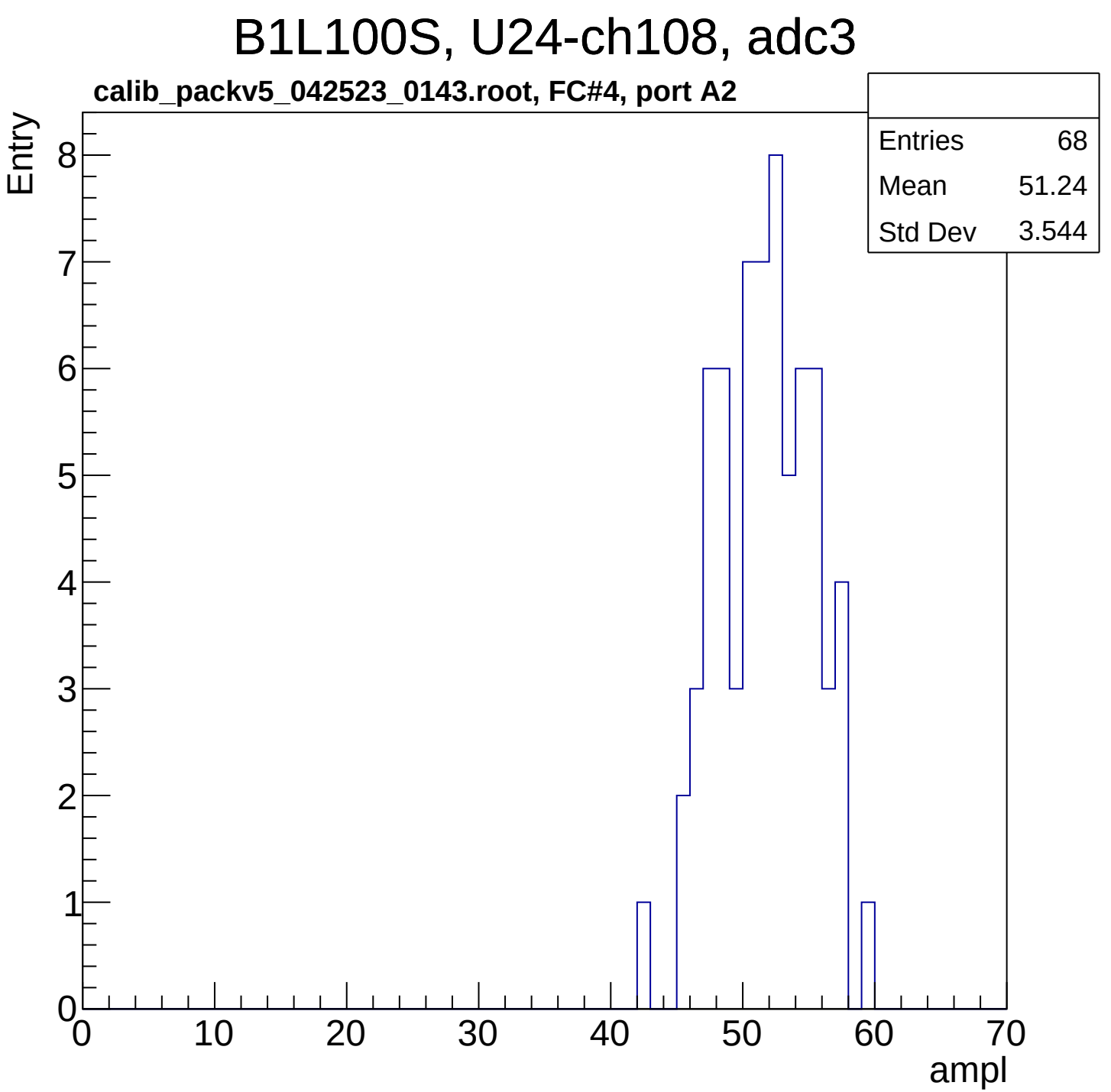
B1L100S, U24-ch108, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 51.24
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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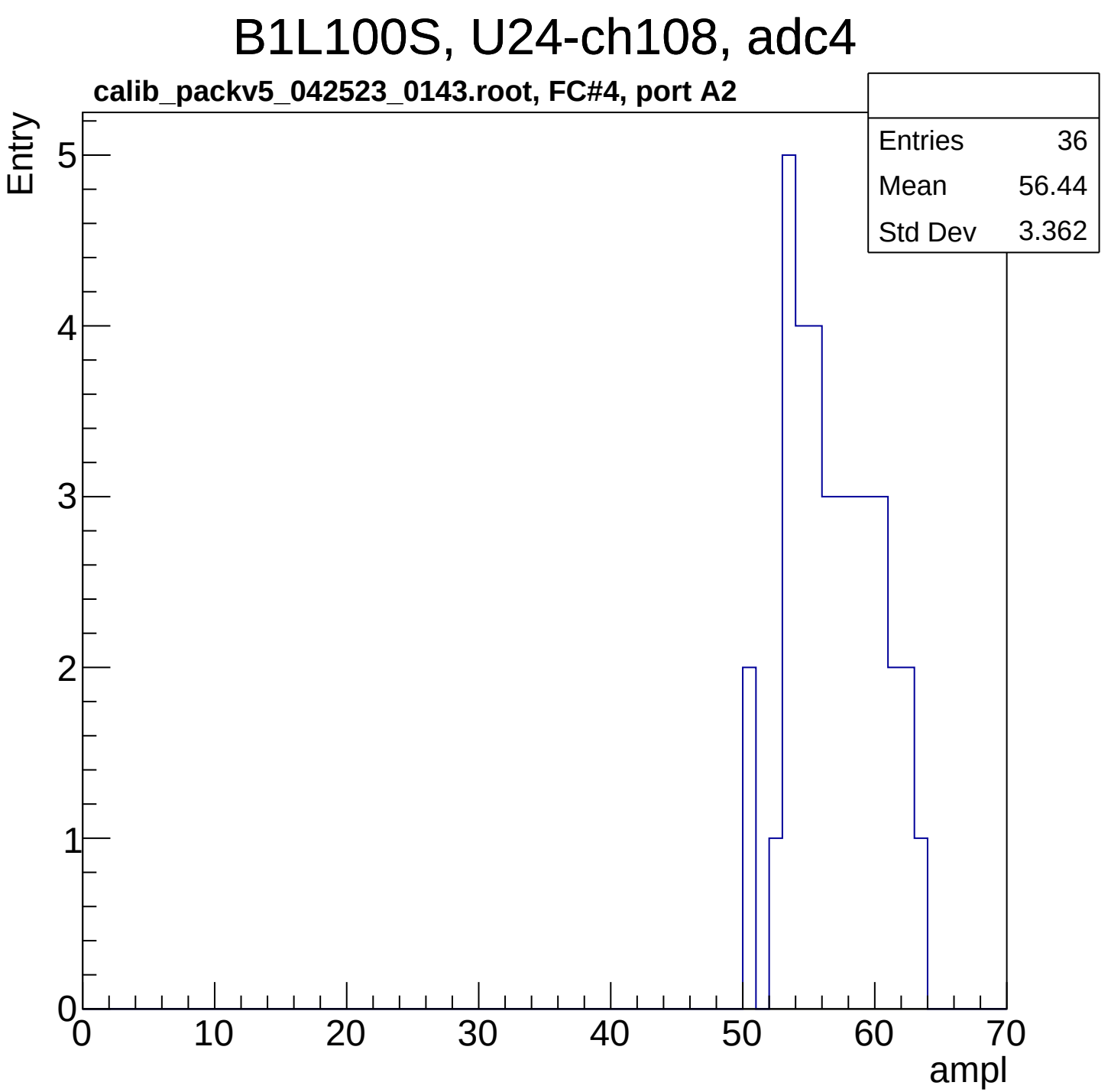
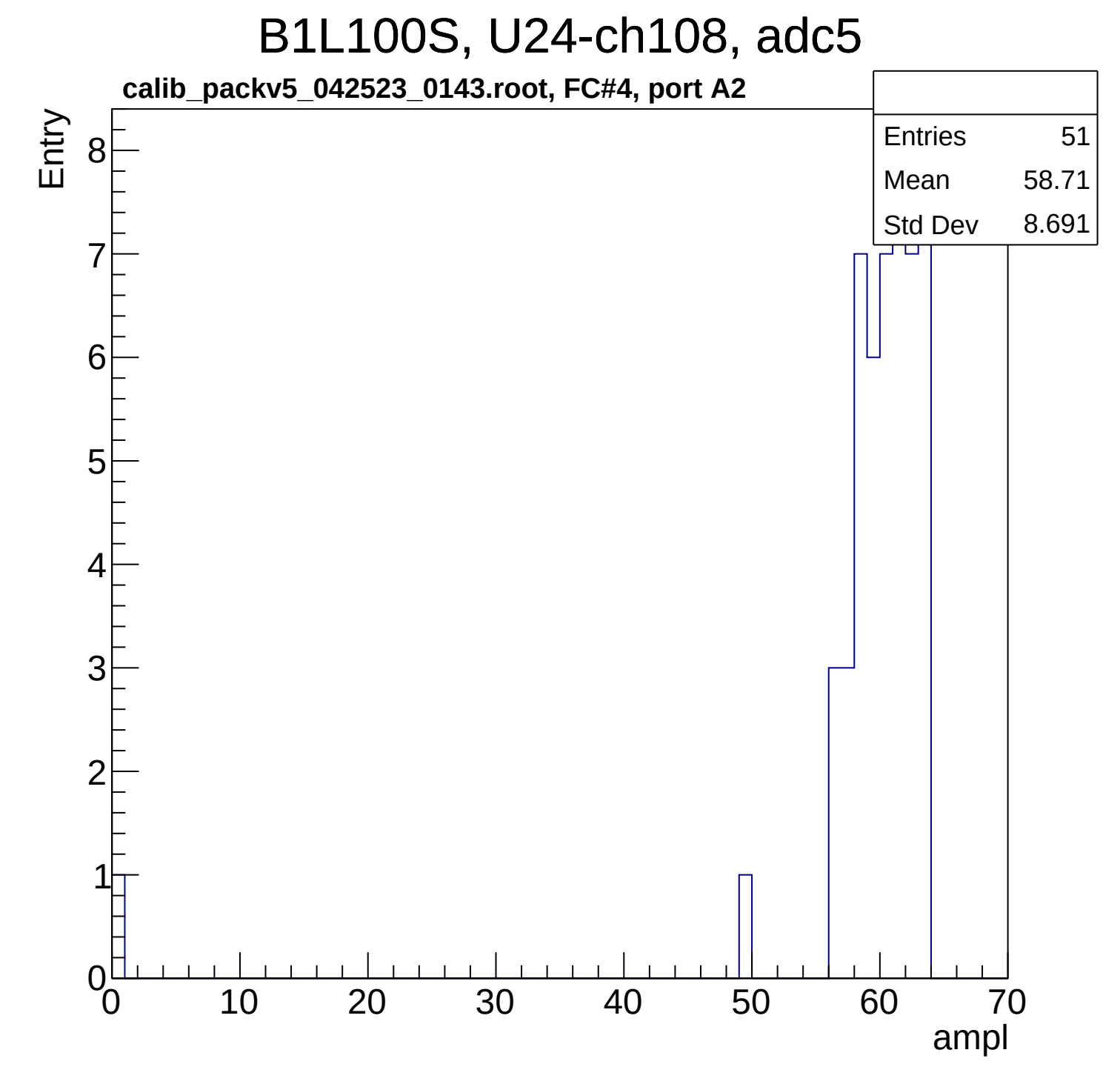
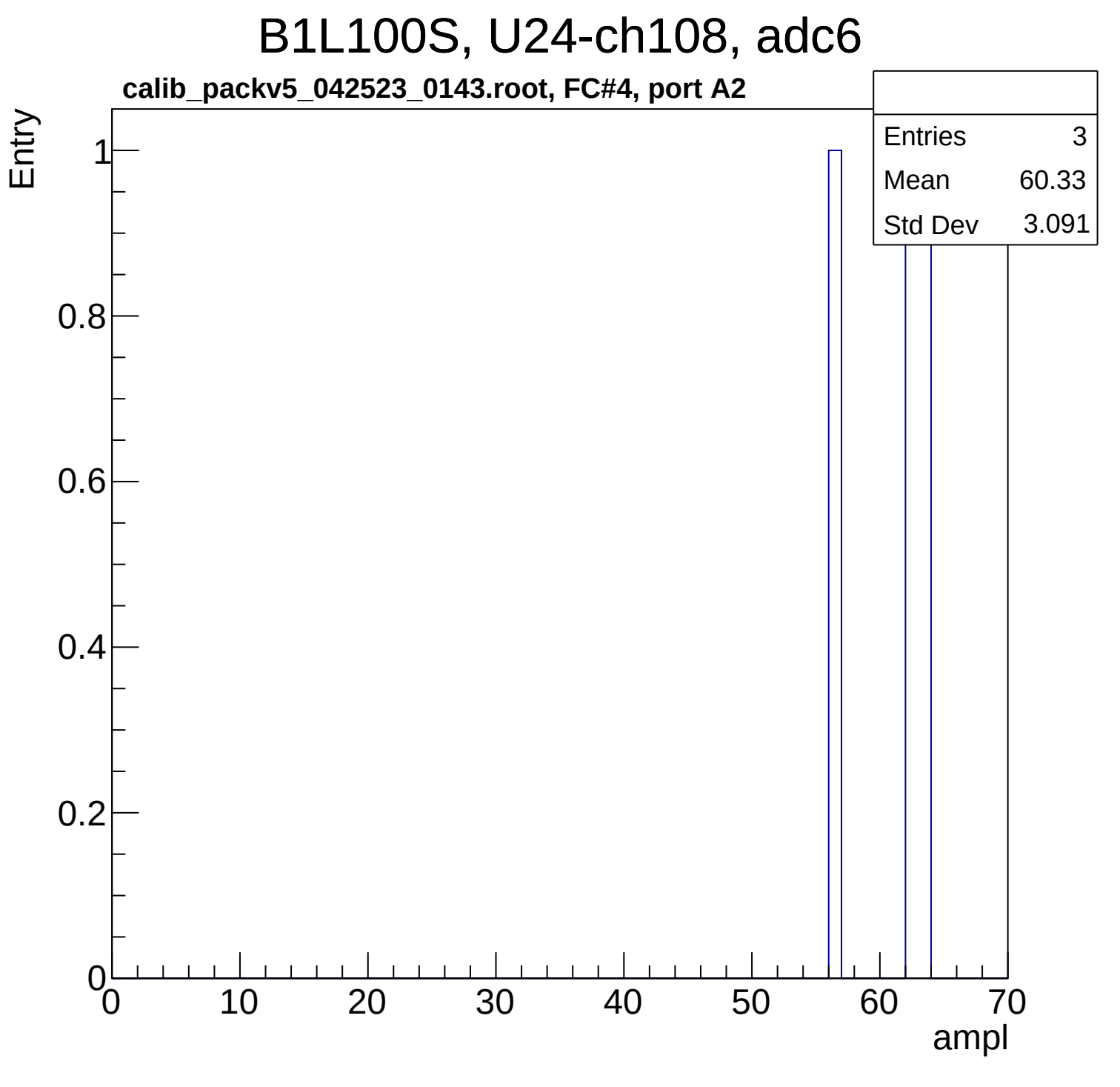
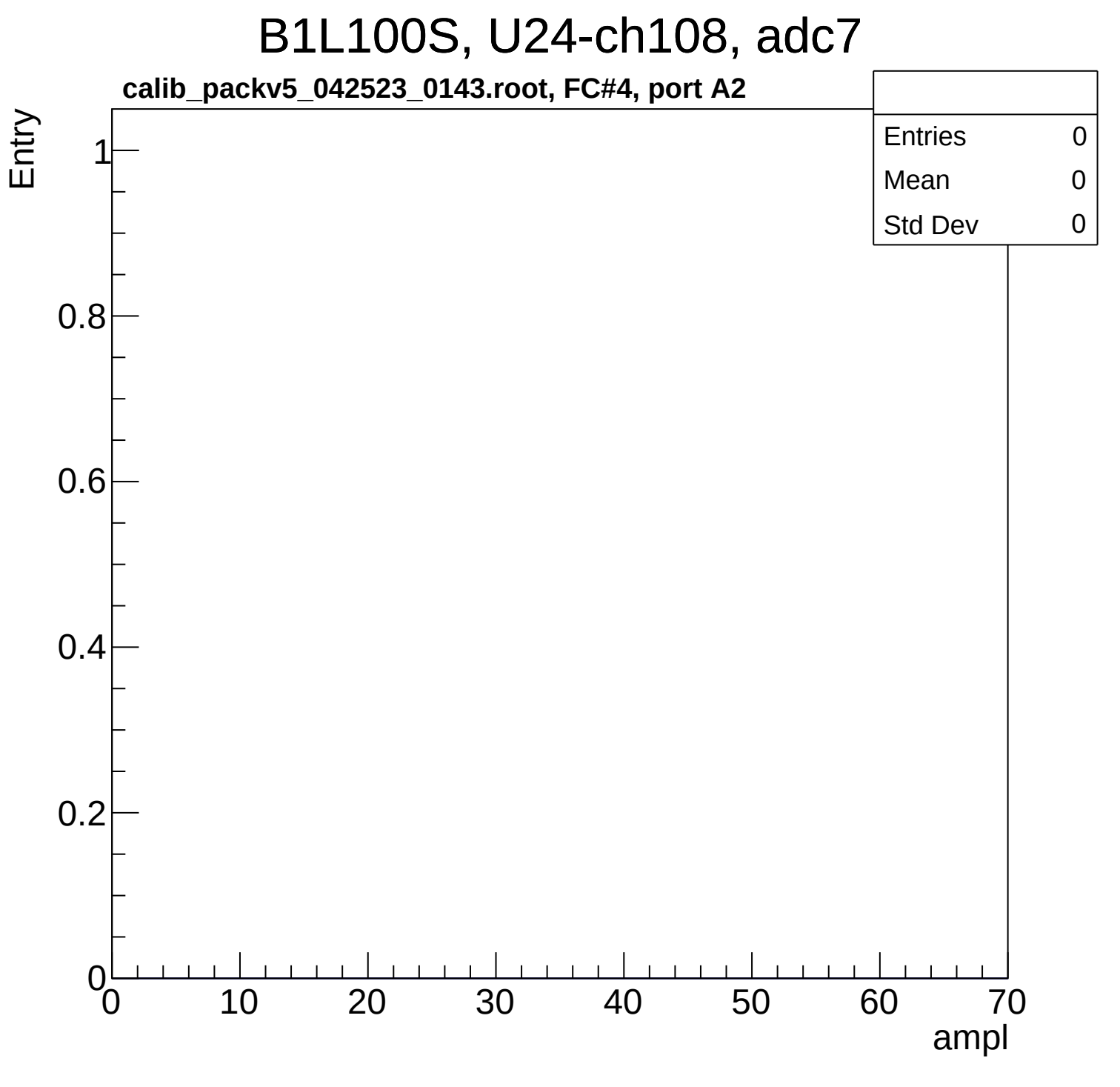
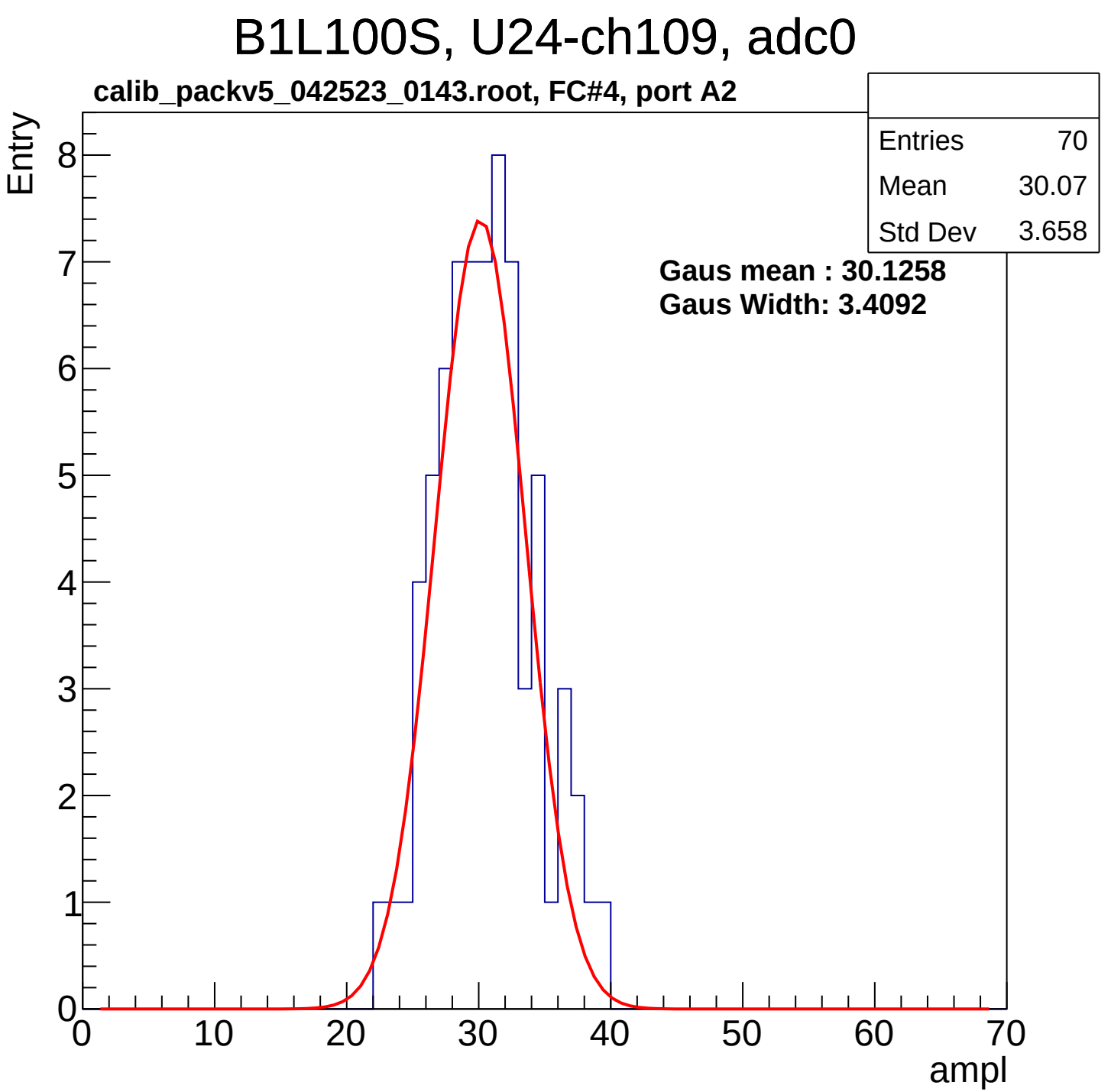
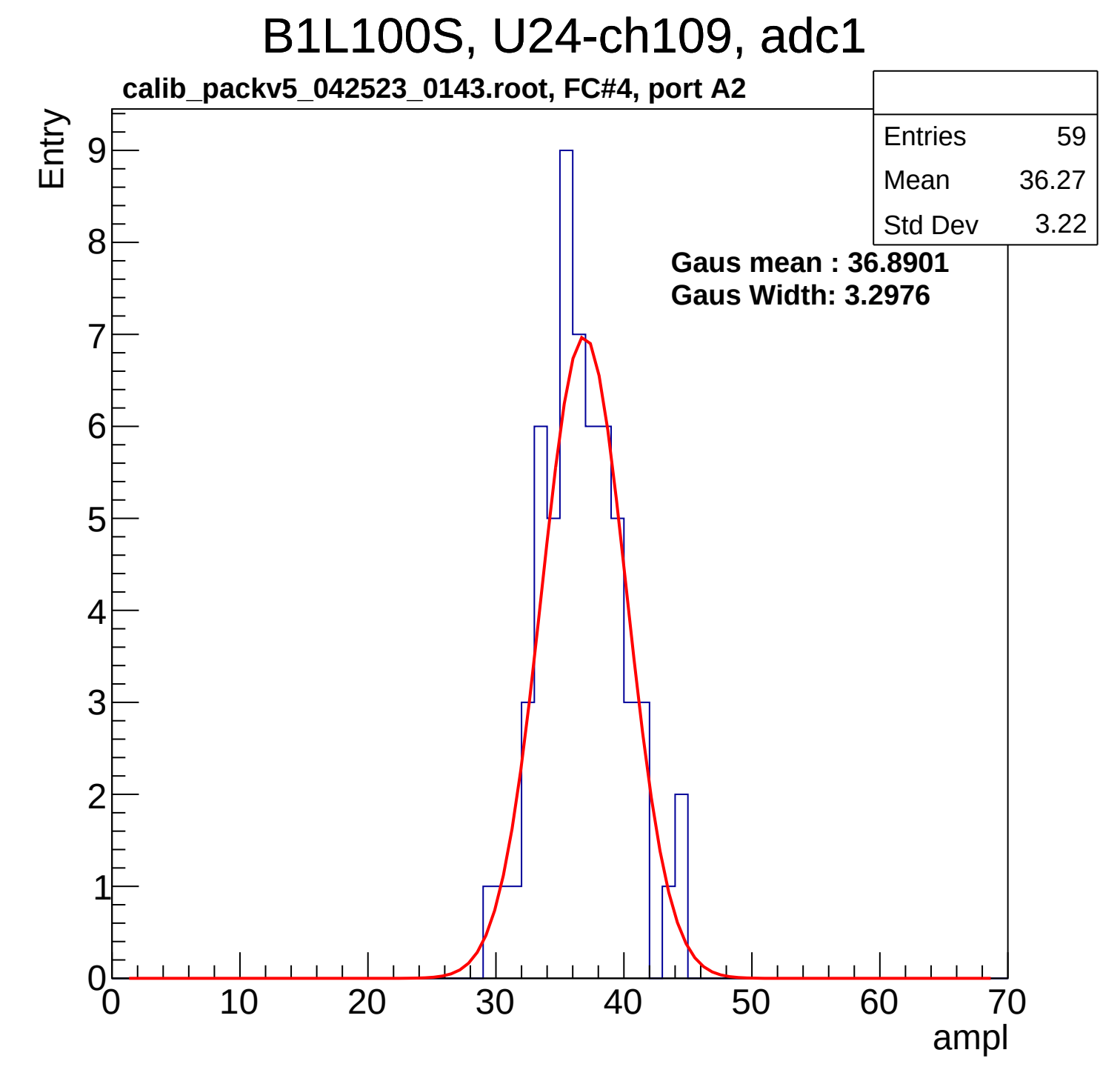

B1L100S, U24-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 56.44
Std Dev 3.362
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

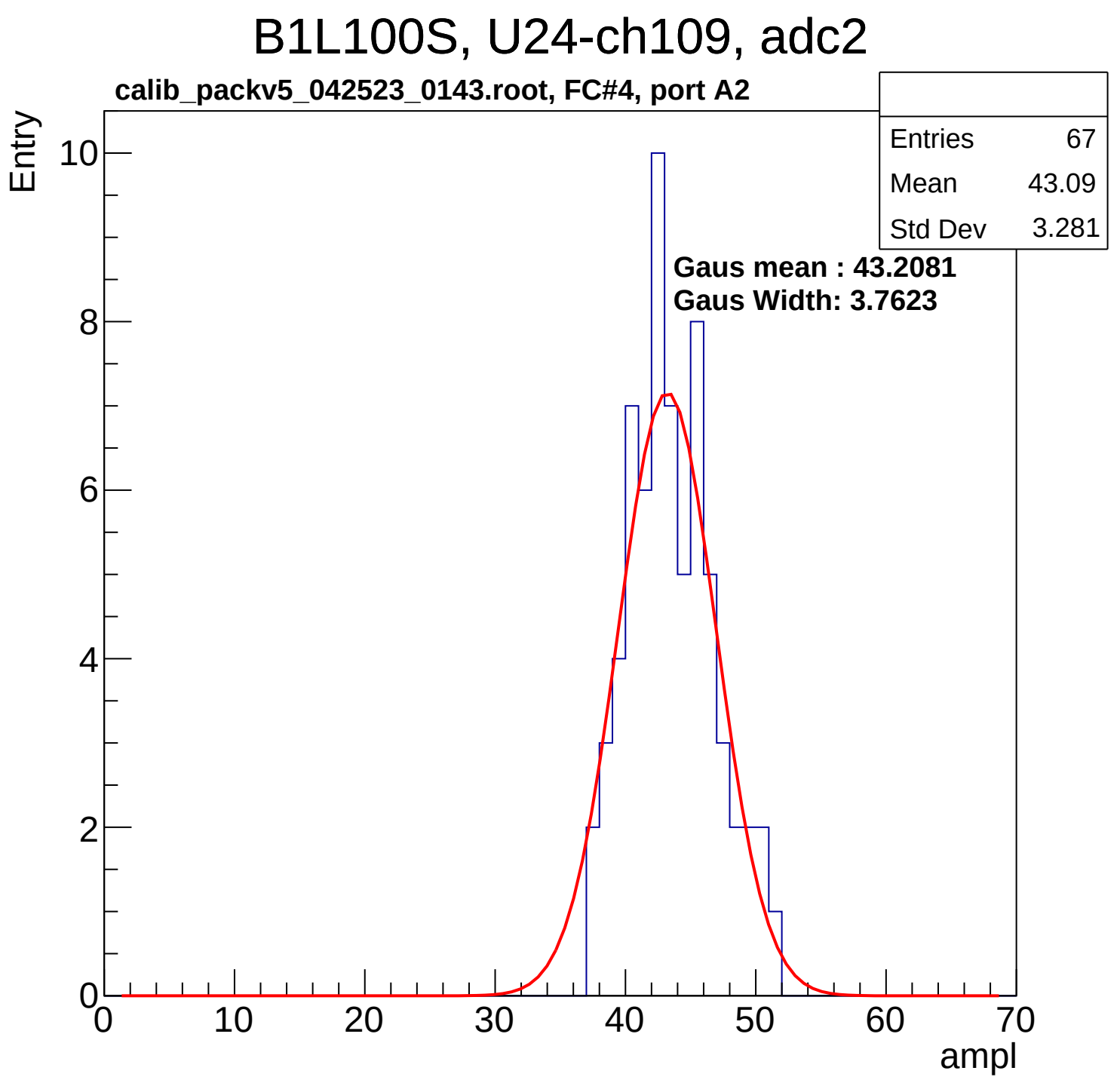

B1L100S, U24-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.71
Std Dev 8.691
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

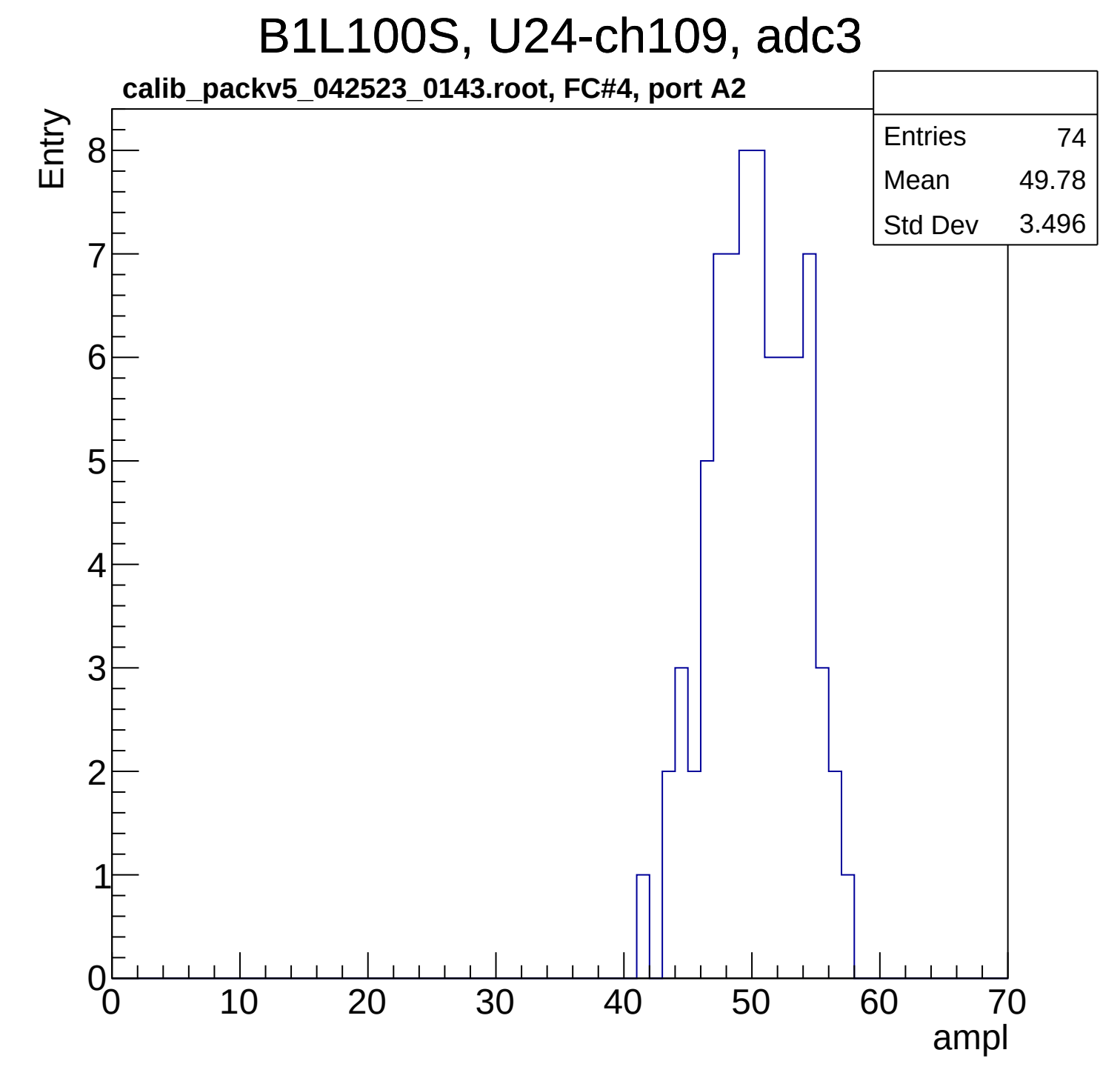

B1L100S, U24-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.33
Std Dev 3.091
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch108, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 30.07
Std Dev 3.658
Gaus mean : 30.1258
Gaus Width: 3.4092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.27
Std Dev 3.22
Gaus mean : 36.8901
Gaus Width: 3.2976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43.09
Std Dev 3.281
Gaus mean : 43.2081
Gaus Width: 3.7623
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.78
Std Dev 3.496
Entry
ampl
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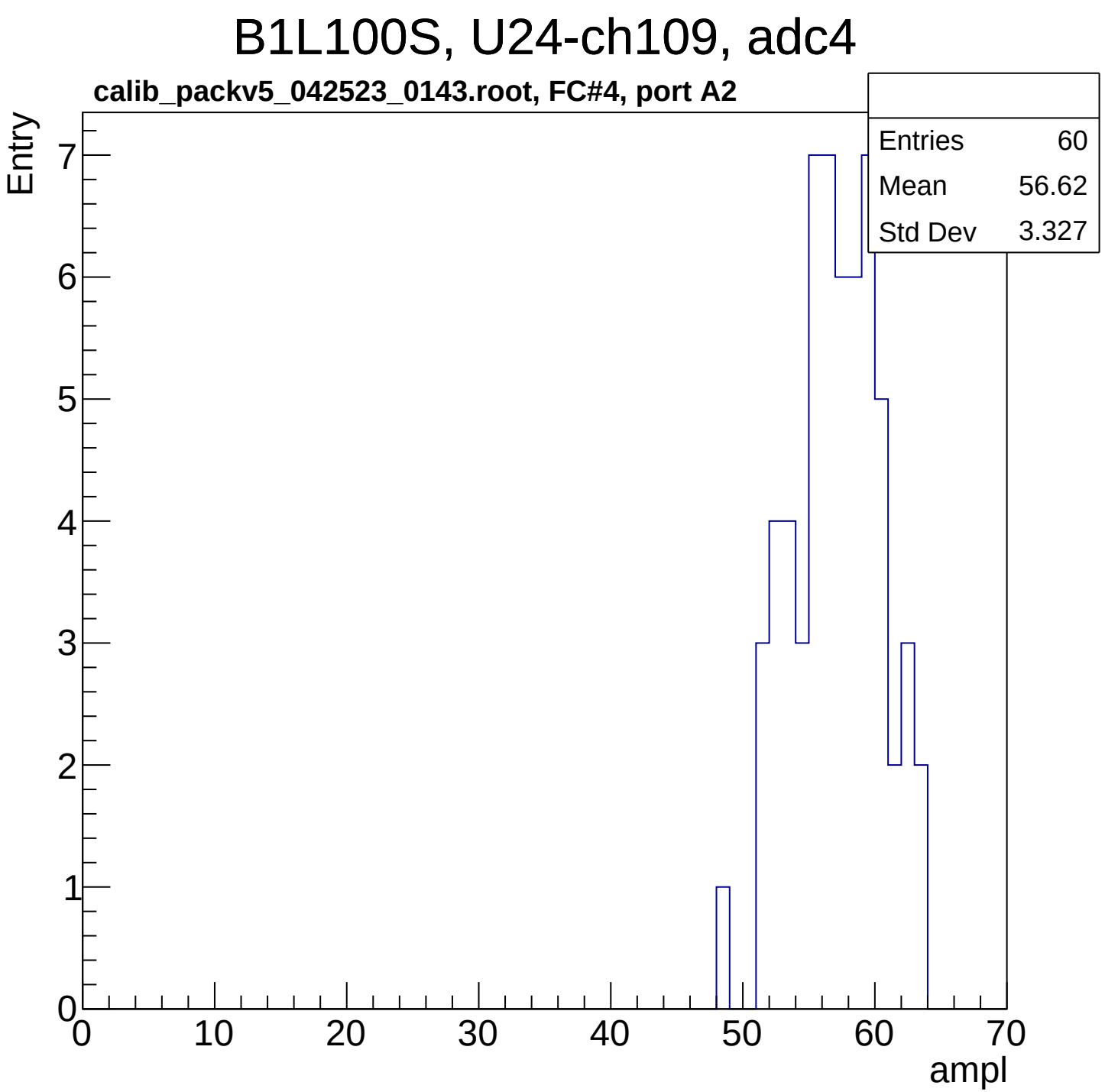
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.62
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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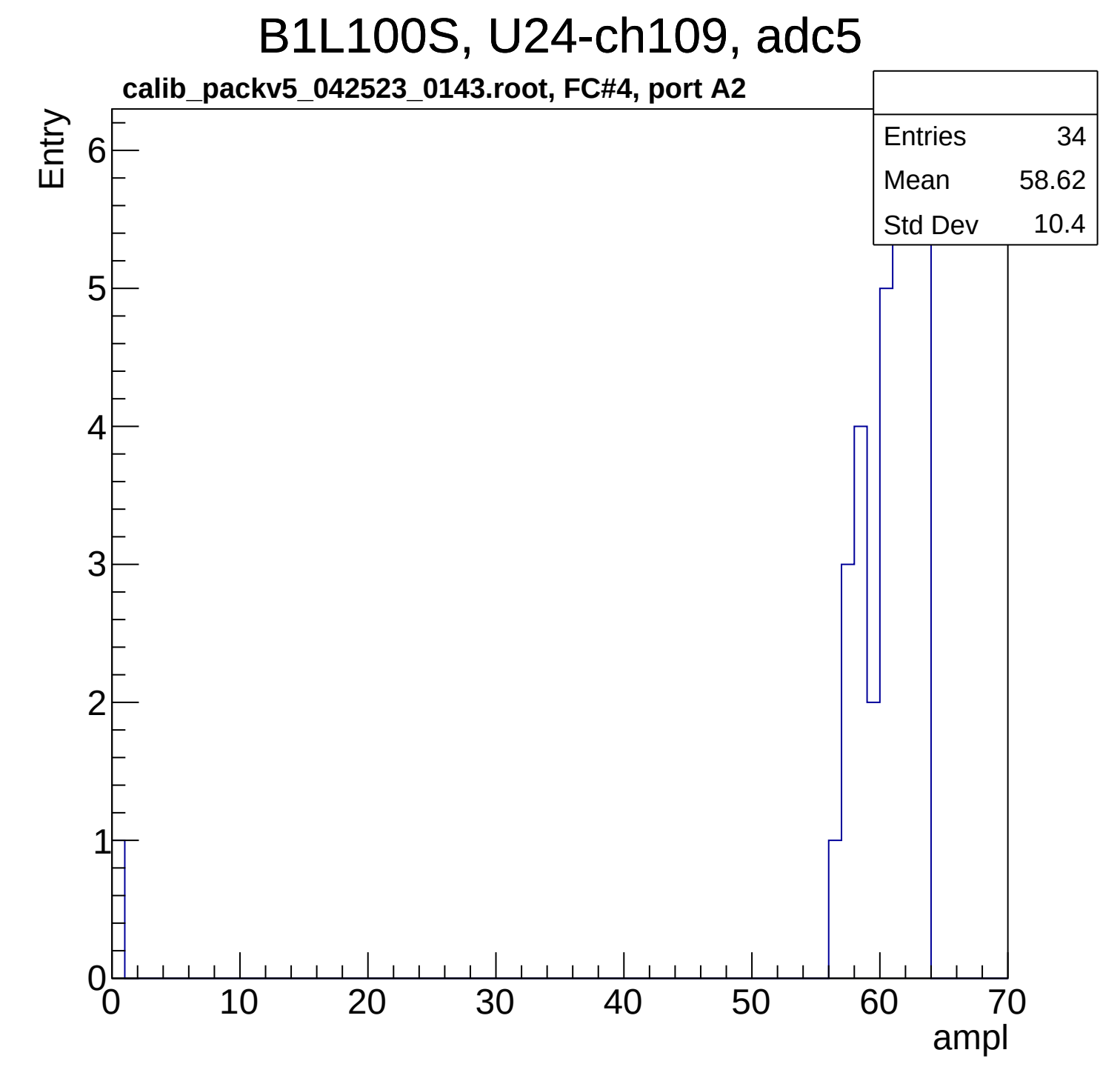
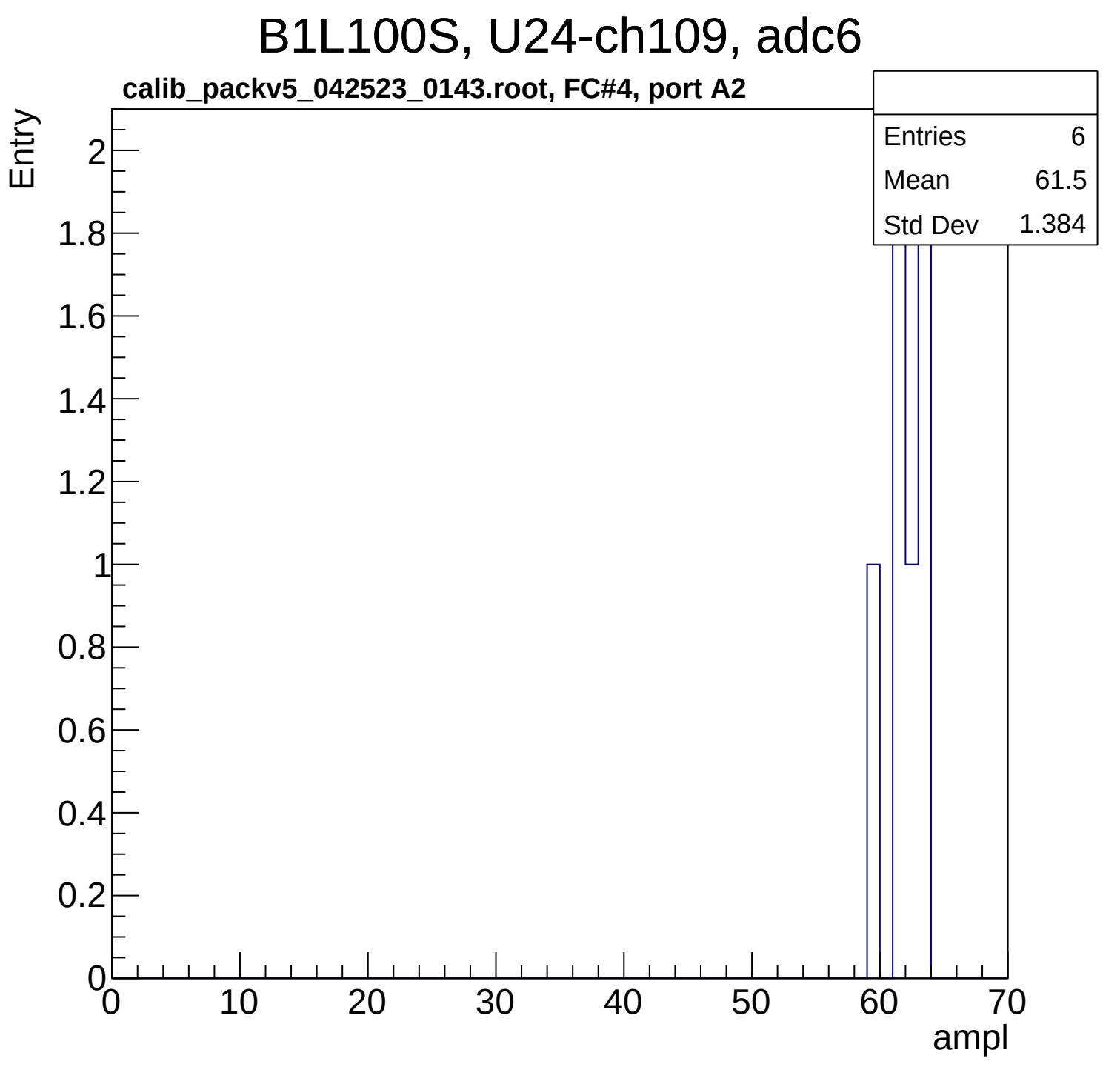
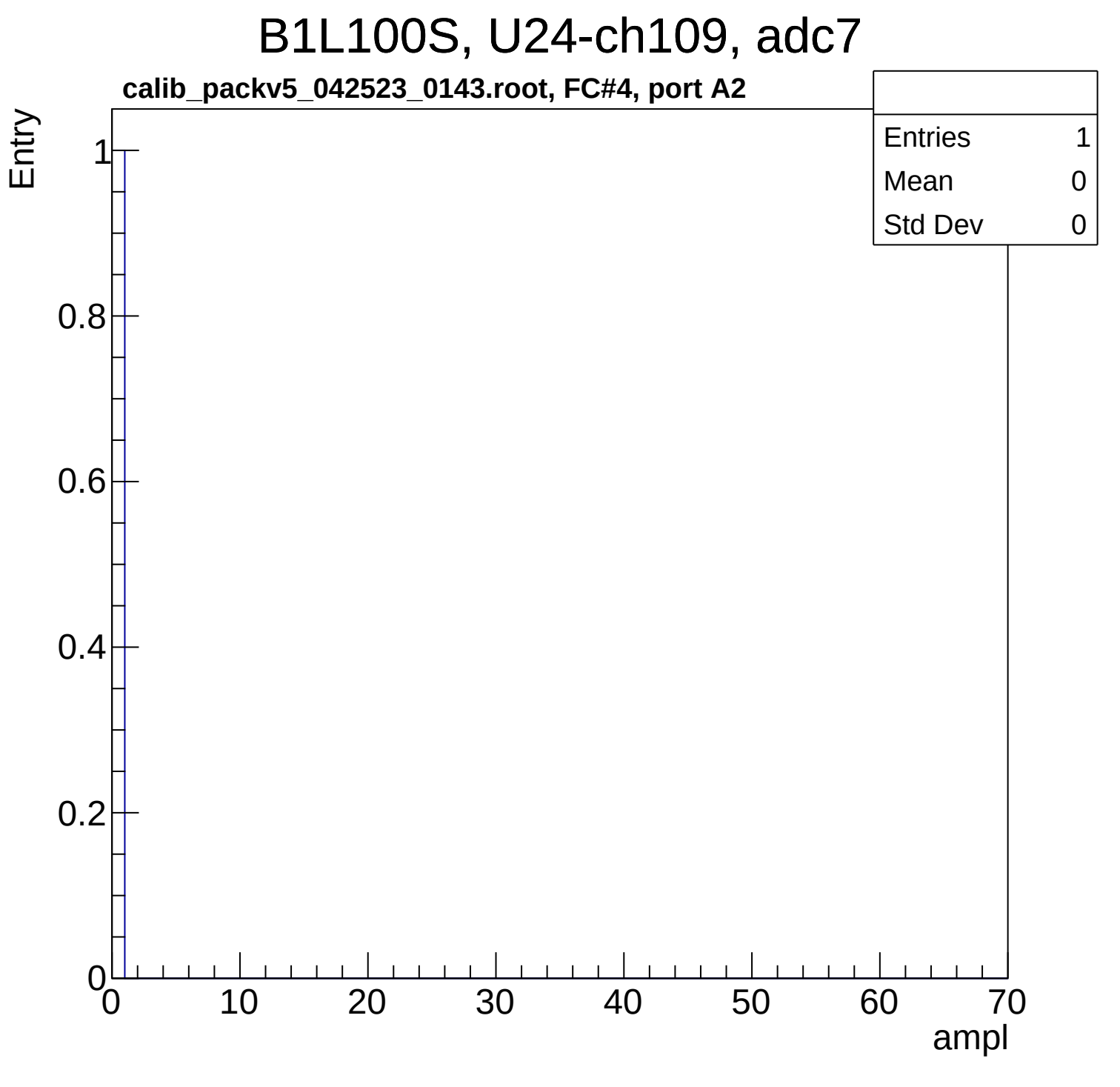
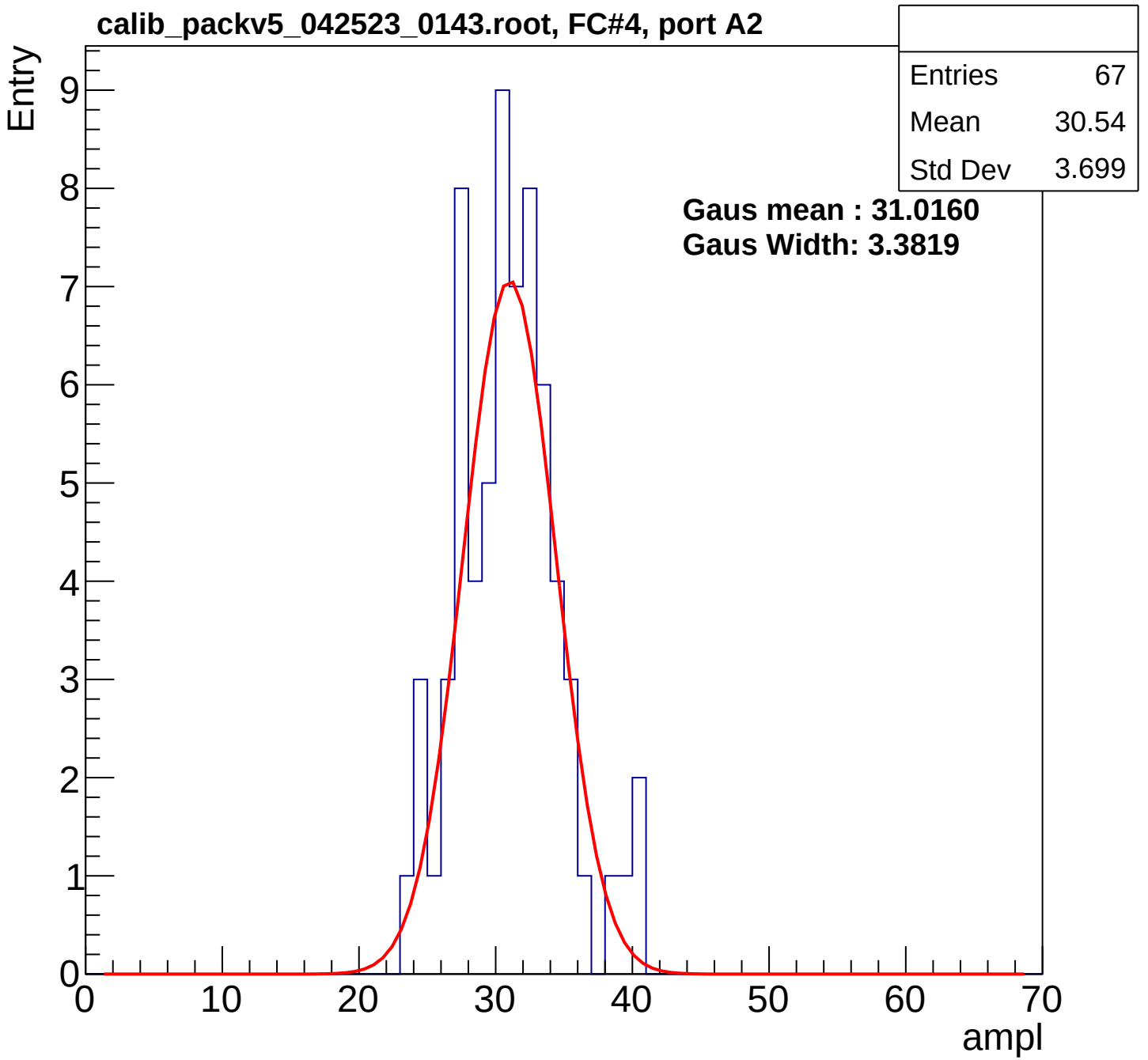
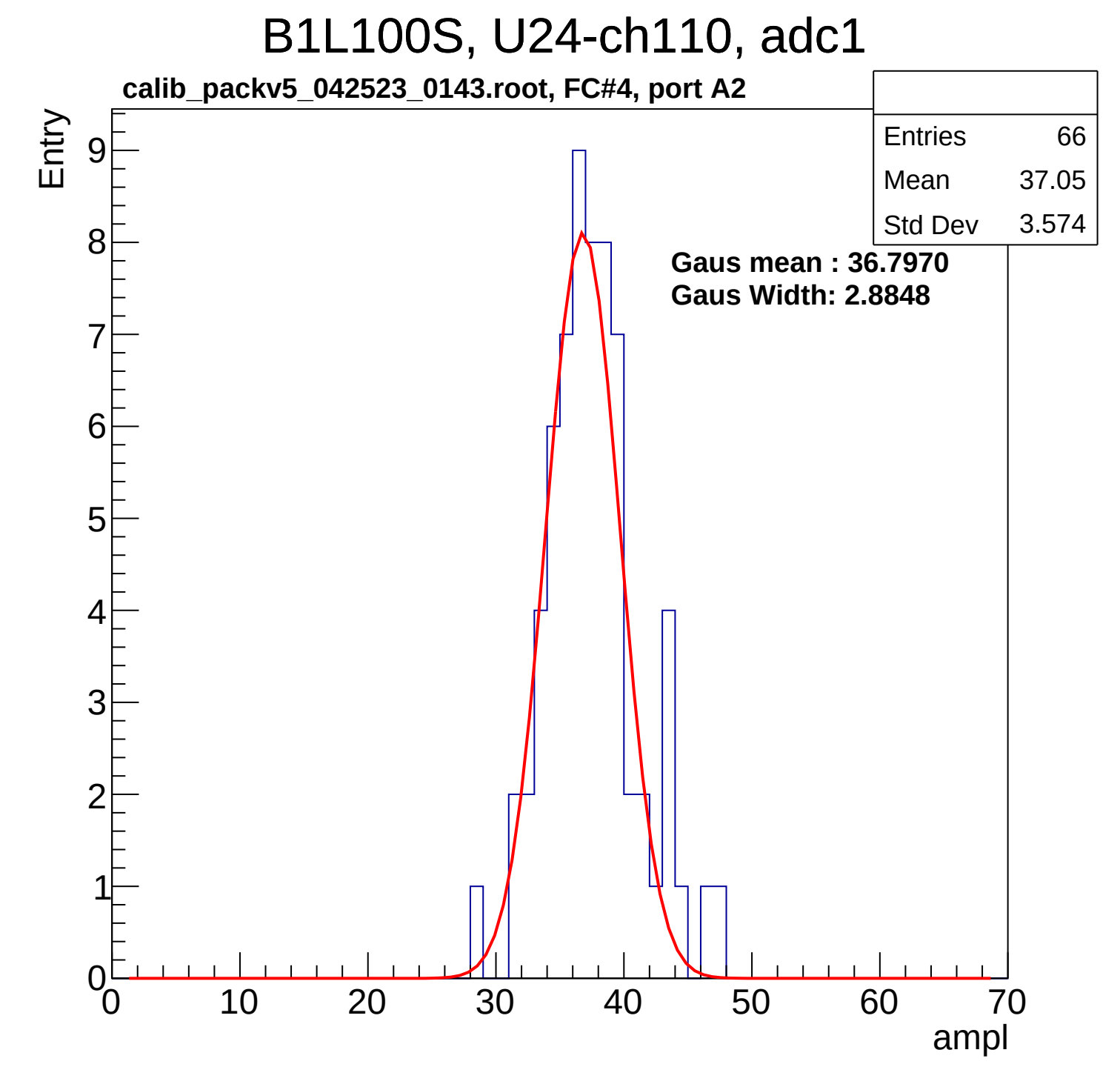
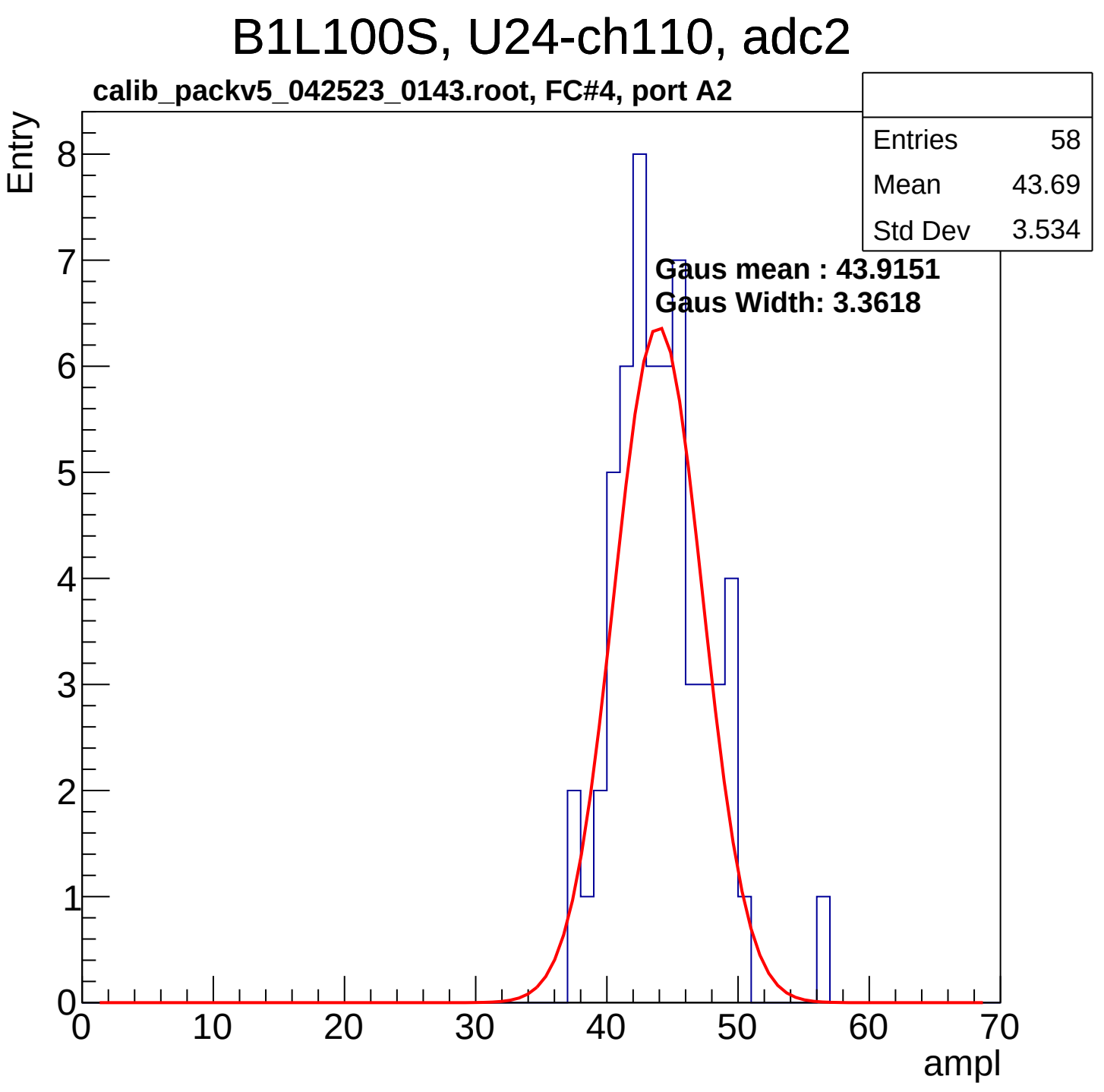

B1L100S, U24-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.62
Std Dev 10.4
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.5
Std Dev 1.384
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch109, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.54
Std Dev 3.699
Gaus mean : 31.0160
Gaus Width: 3.3819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 37.05
Std Dev 3.574
Gaus mean : 36.7970
Gaus Width: 2.8848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.69
Std Dev 3.534
Gaus mean : 43.9151
Gaus Width: 3.3618
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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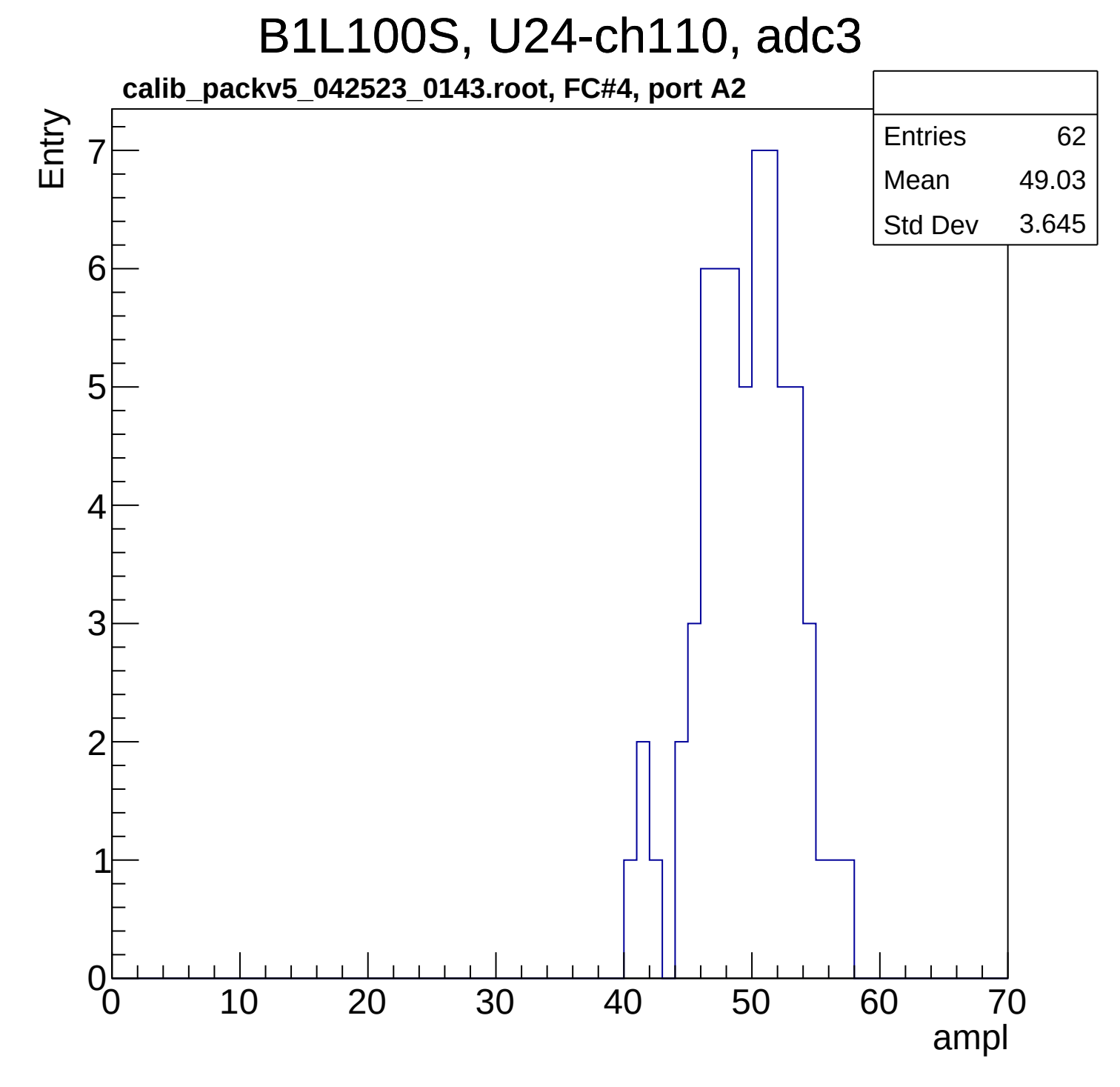
B1L100S, U24-ch110, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 49.03
Std Dev 3.645
Entry
ampl
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2
3
4
5
6
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0
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40
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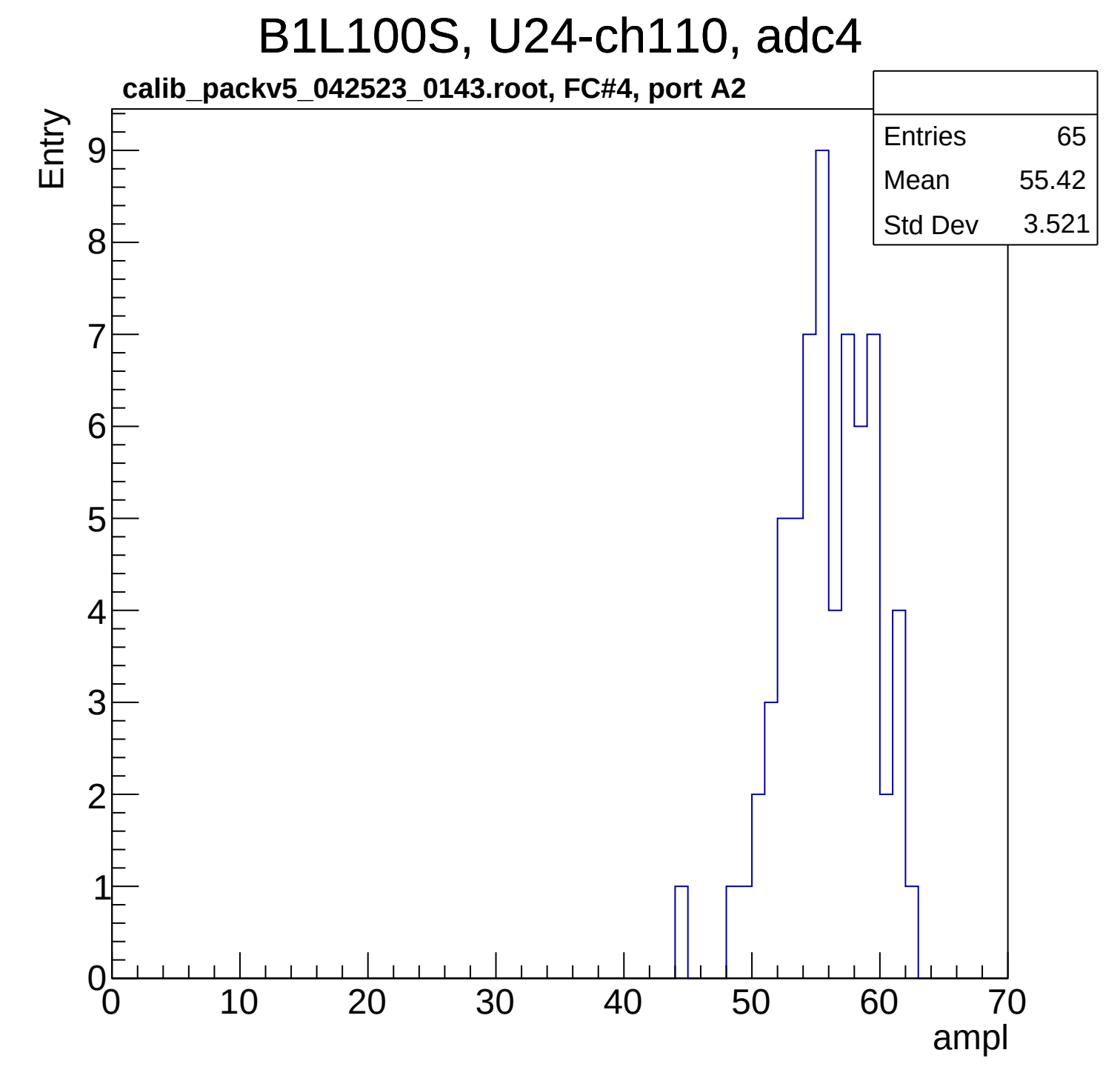
60
70


B1L100S, U24-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.42
Std Dev 3.521
Entry
ampl
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3
4
5
6
7
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0
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60
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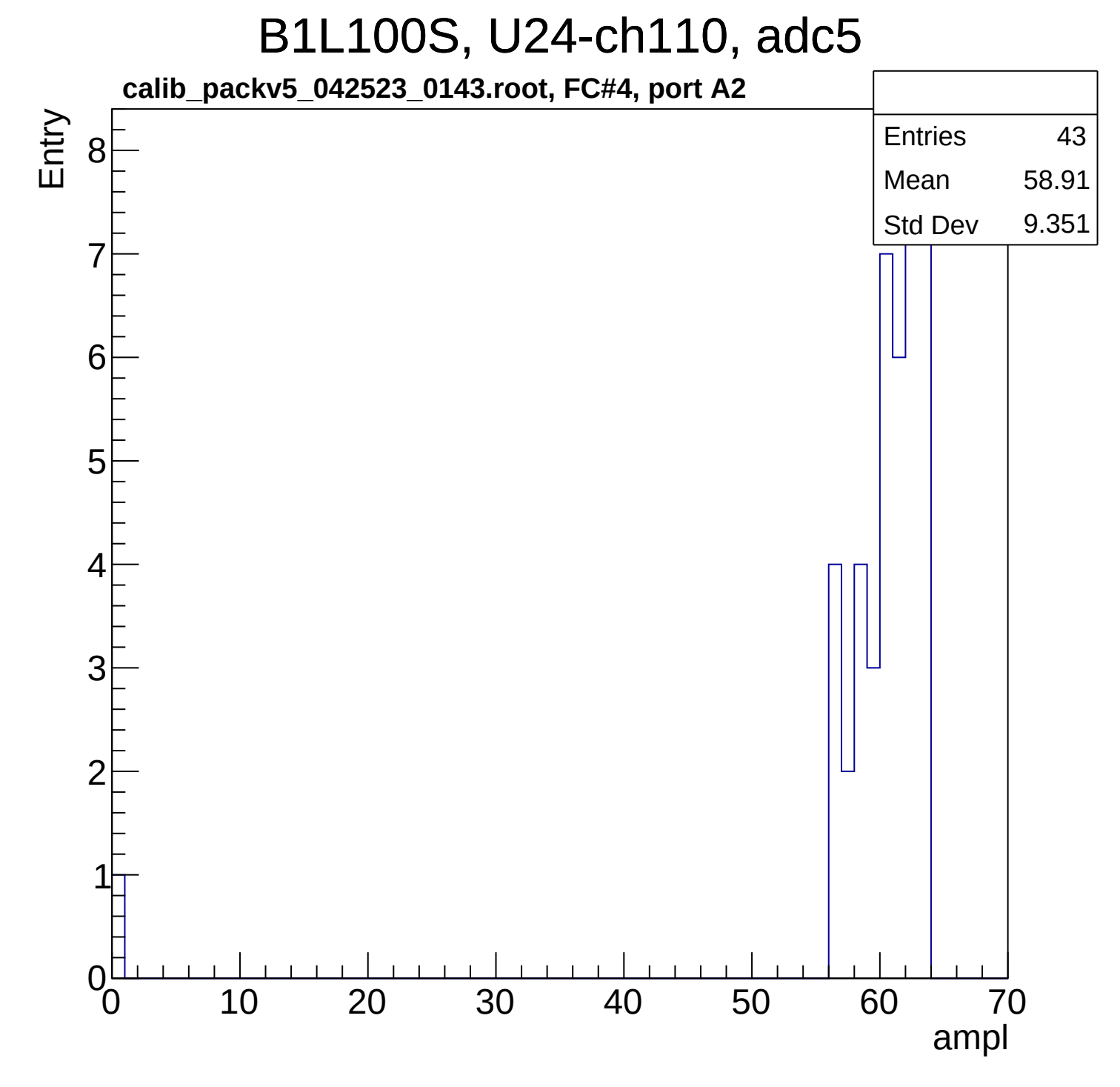
B1L100S, U24-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.91
Std Dev 9.351
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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60
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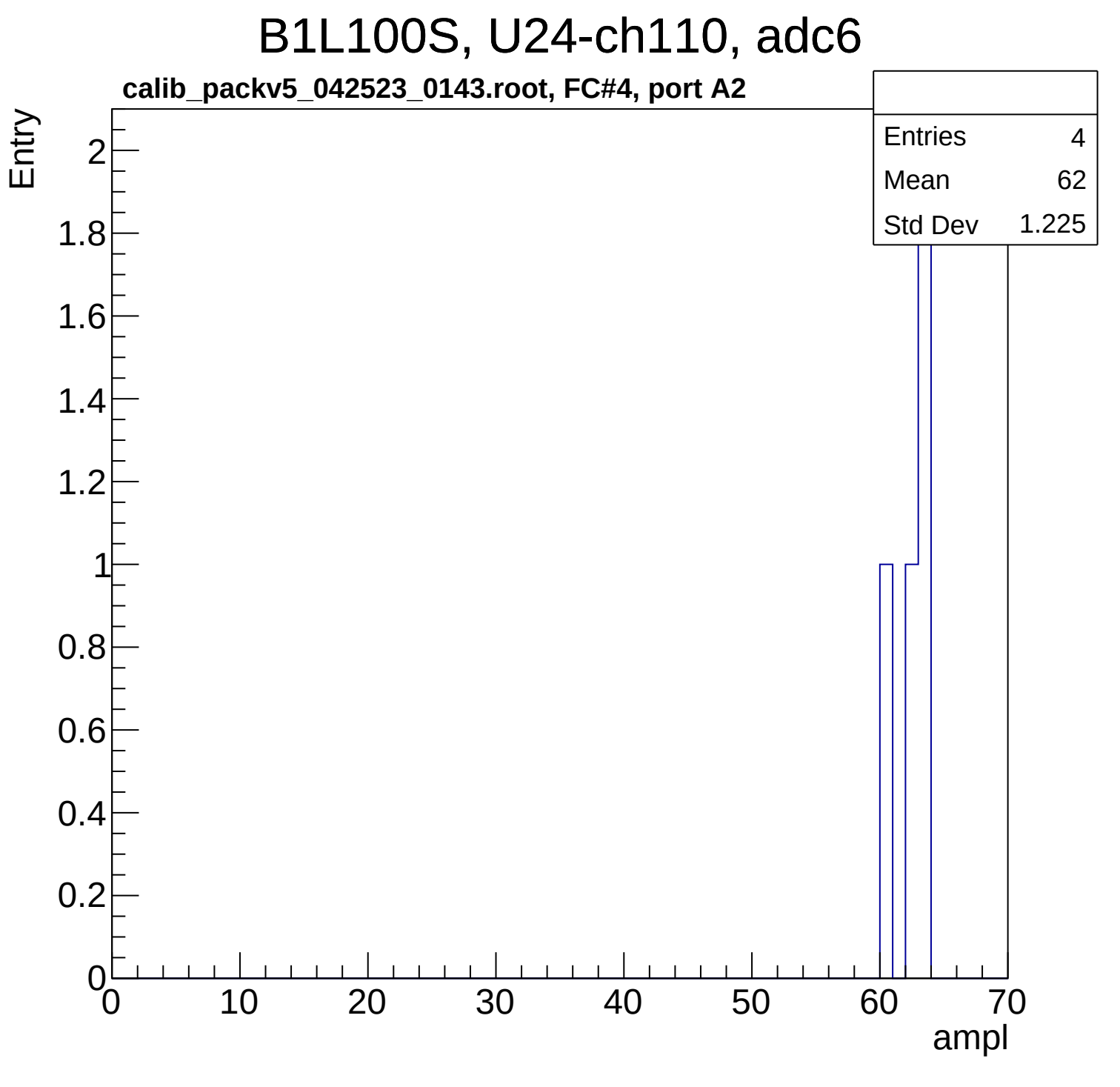
B1L100S, U24-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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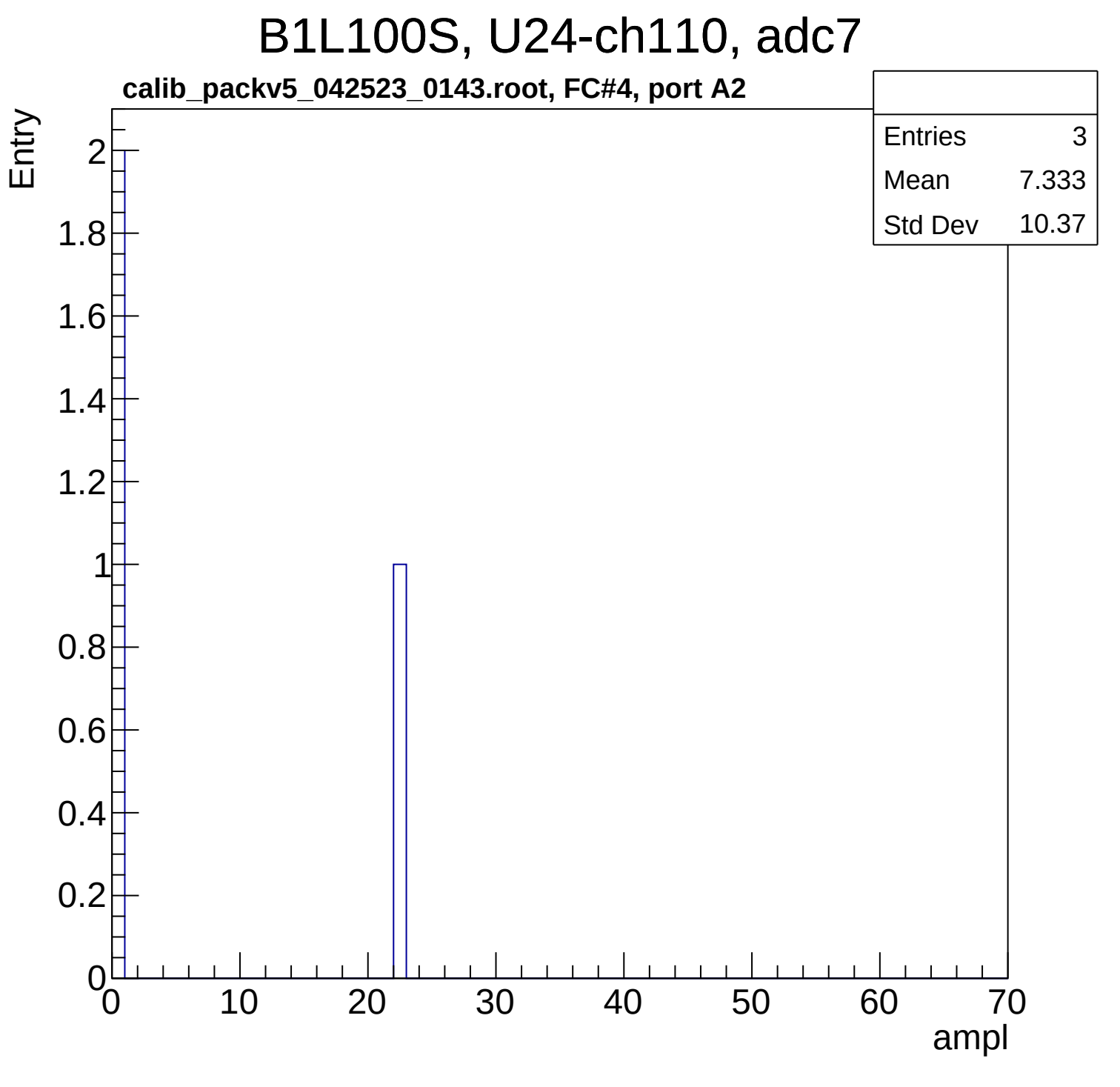
B1L100S, U24-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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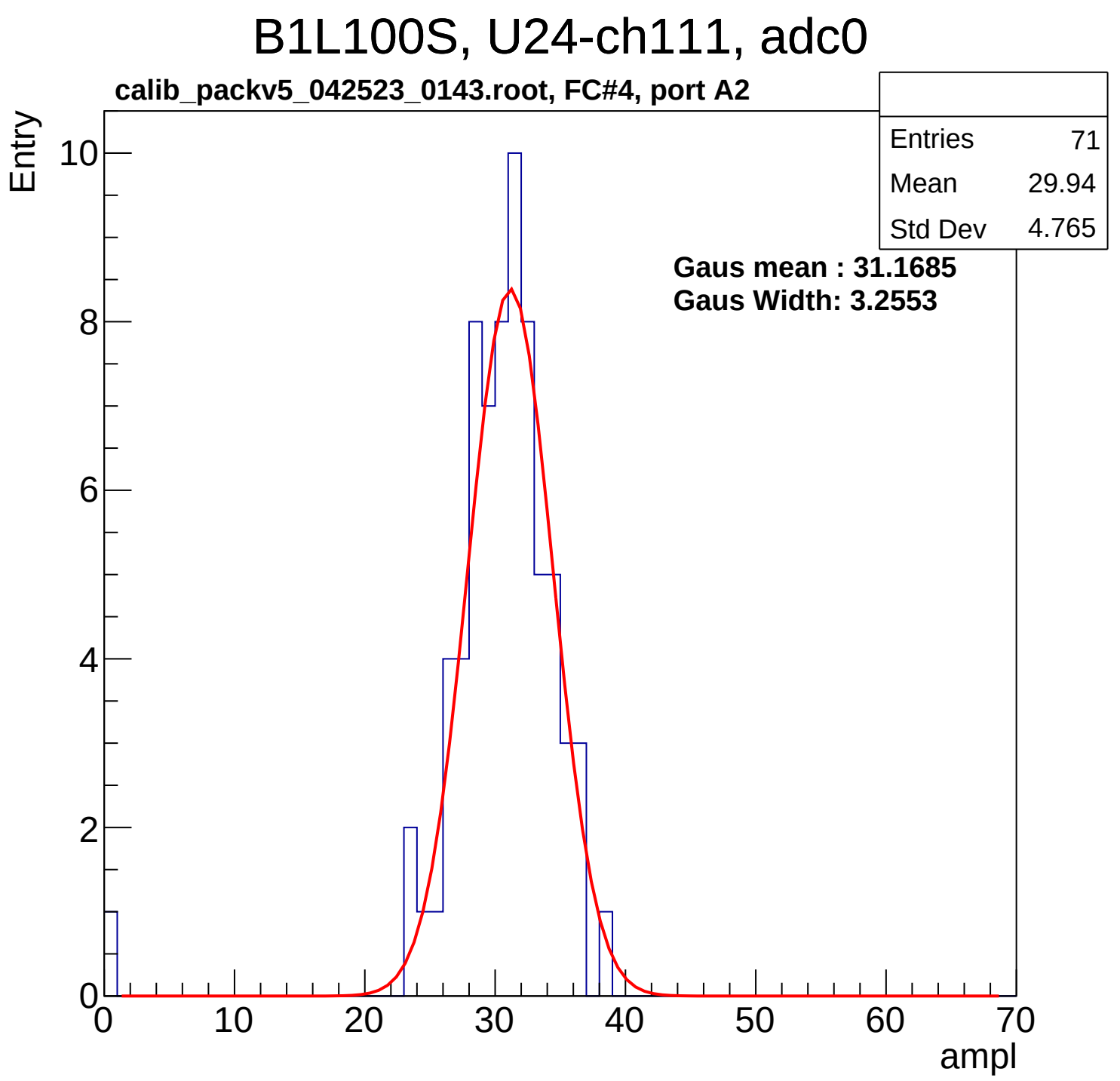
B1L100S, U24-ch111, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.94
Std Dev 4.765
Gaus mean : 31.1685
Gaus Width: 3.2553
Entry
ampl
0
2
4
6
8
10
0
10
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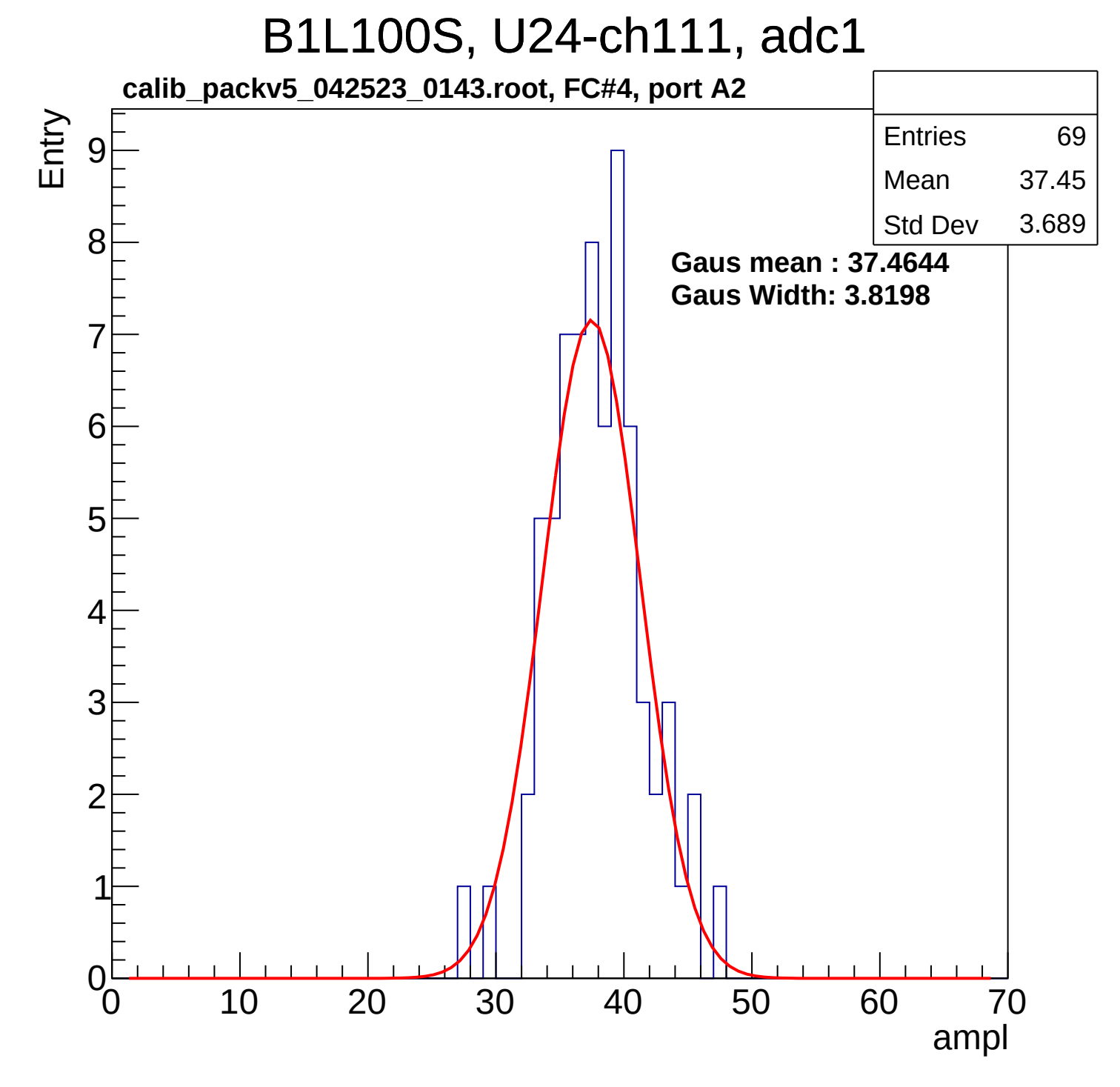
40
50
60
70


B1L100S, U24-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.45
Std Dev 3.689
Gaus mean : 37.4644
Gaus Width: 3.8198
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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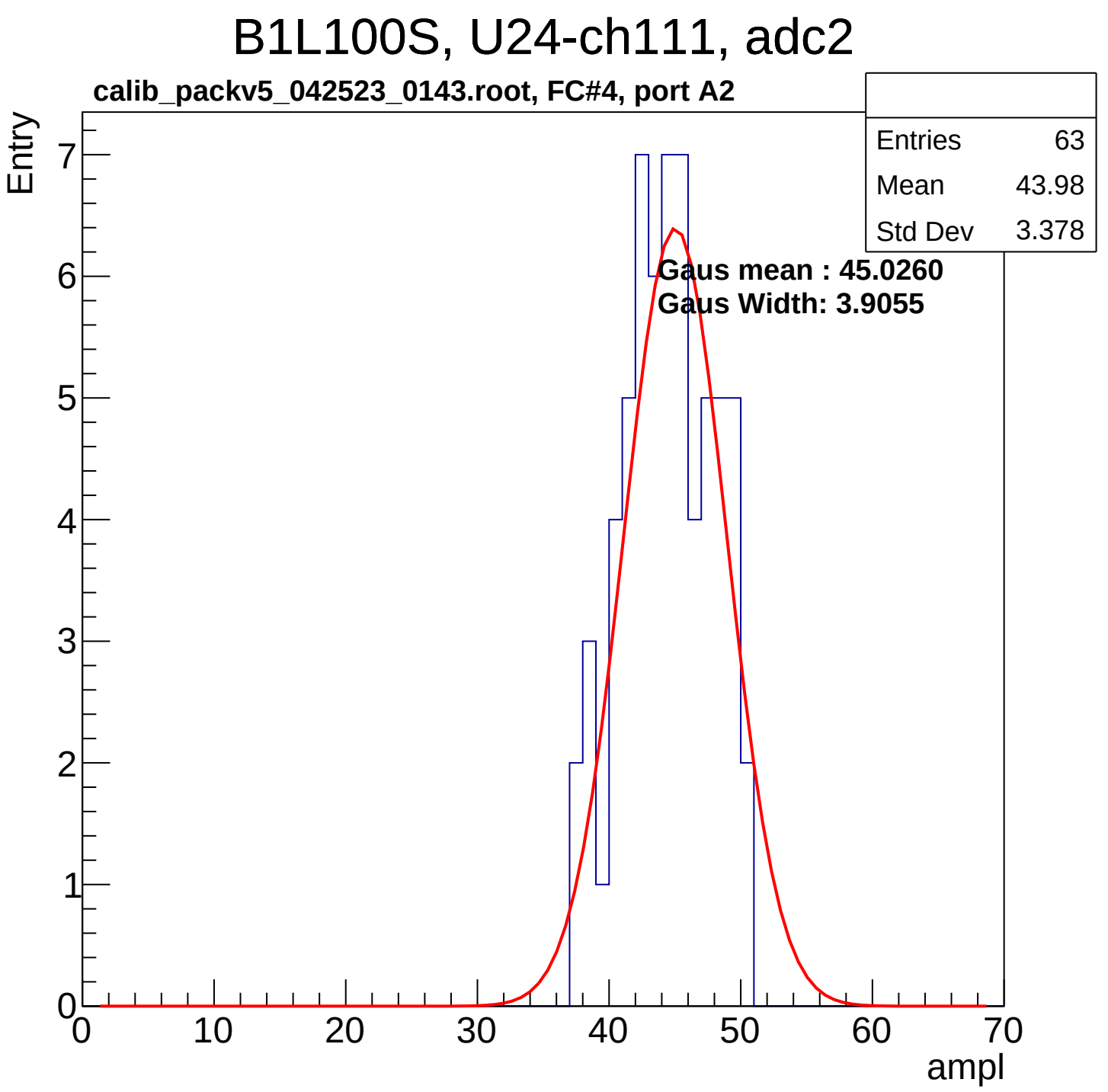
B1L100S, U24-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.98
Std Dev 3.378
Gaus mean : 45.0260
Gaus Width: 3.9055
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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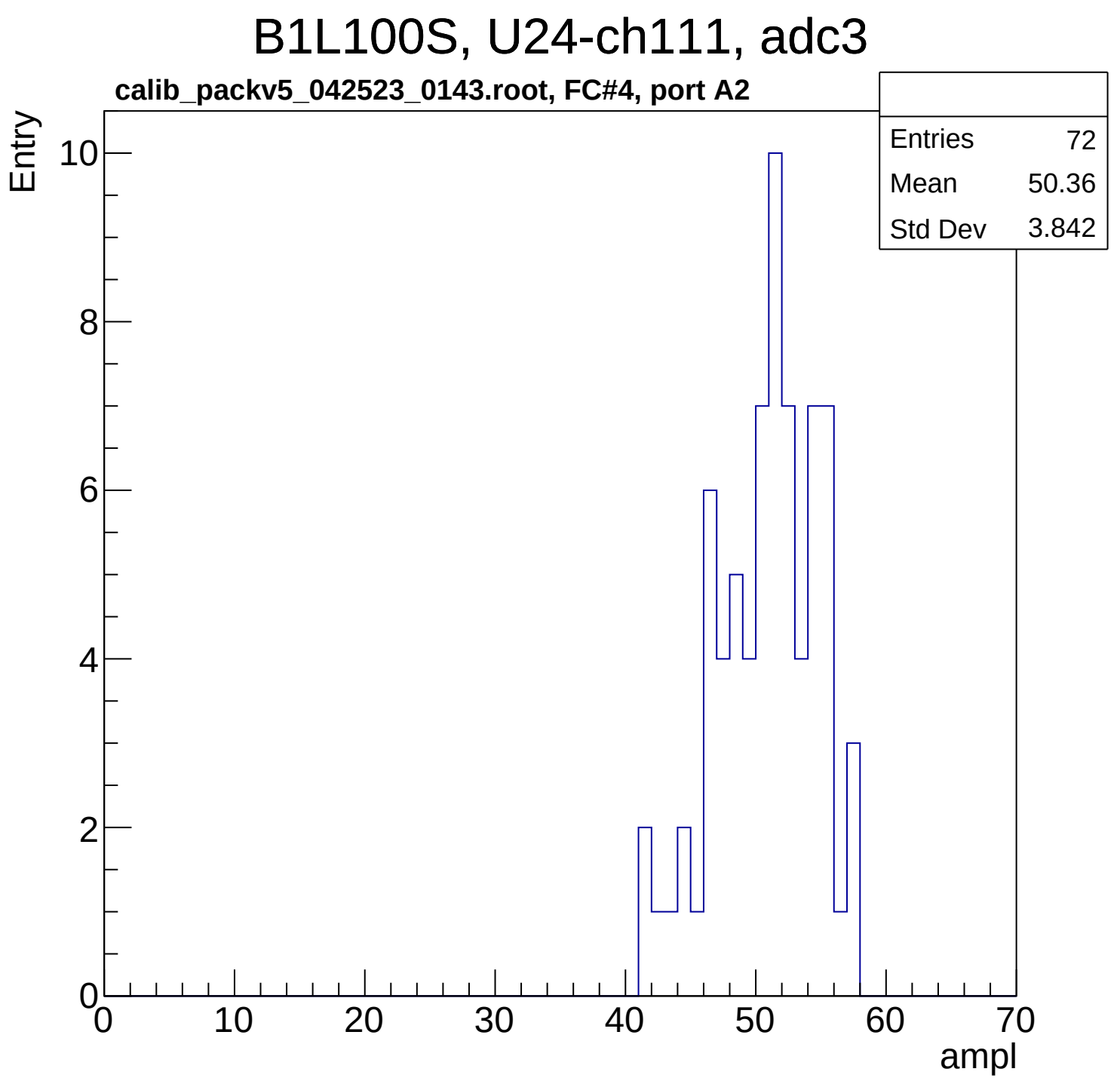
B1L100S, U24-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.36
Std Dev 3.842
Entry
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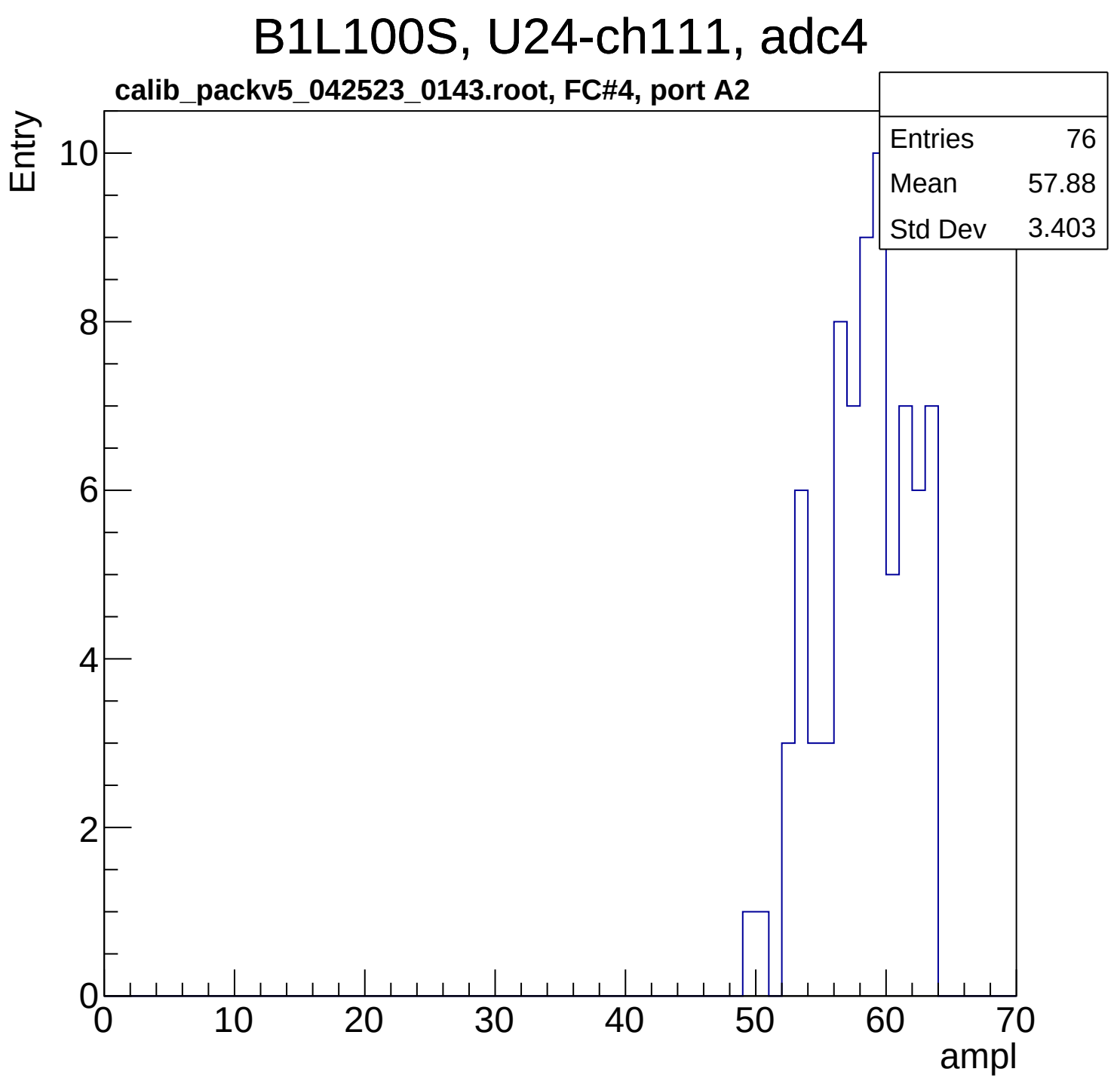
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch111, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 57.88
Std Dev 3.403
Entry
ampl
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4
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8
10
0
10
20
30
40
50
60
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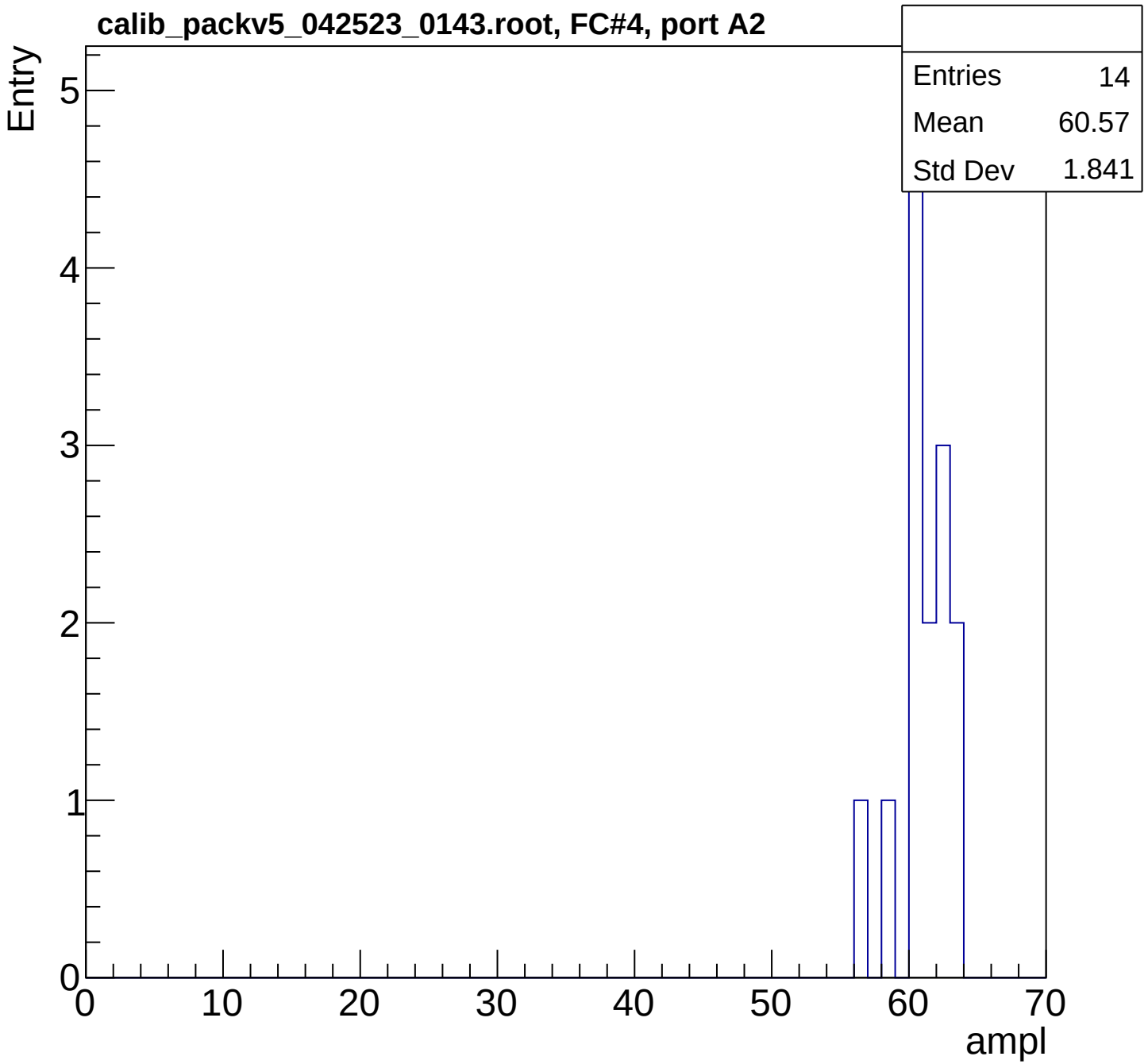
B1L100S, U24-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 60.57
Std Dev 1.841
Entry
ampl
0
1
2
3
4
5
0
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40
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60
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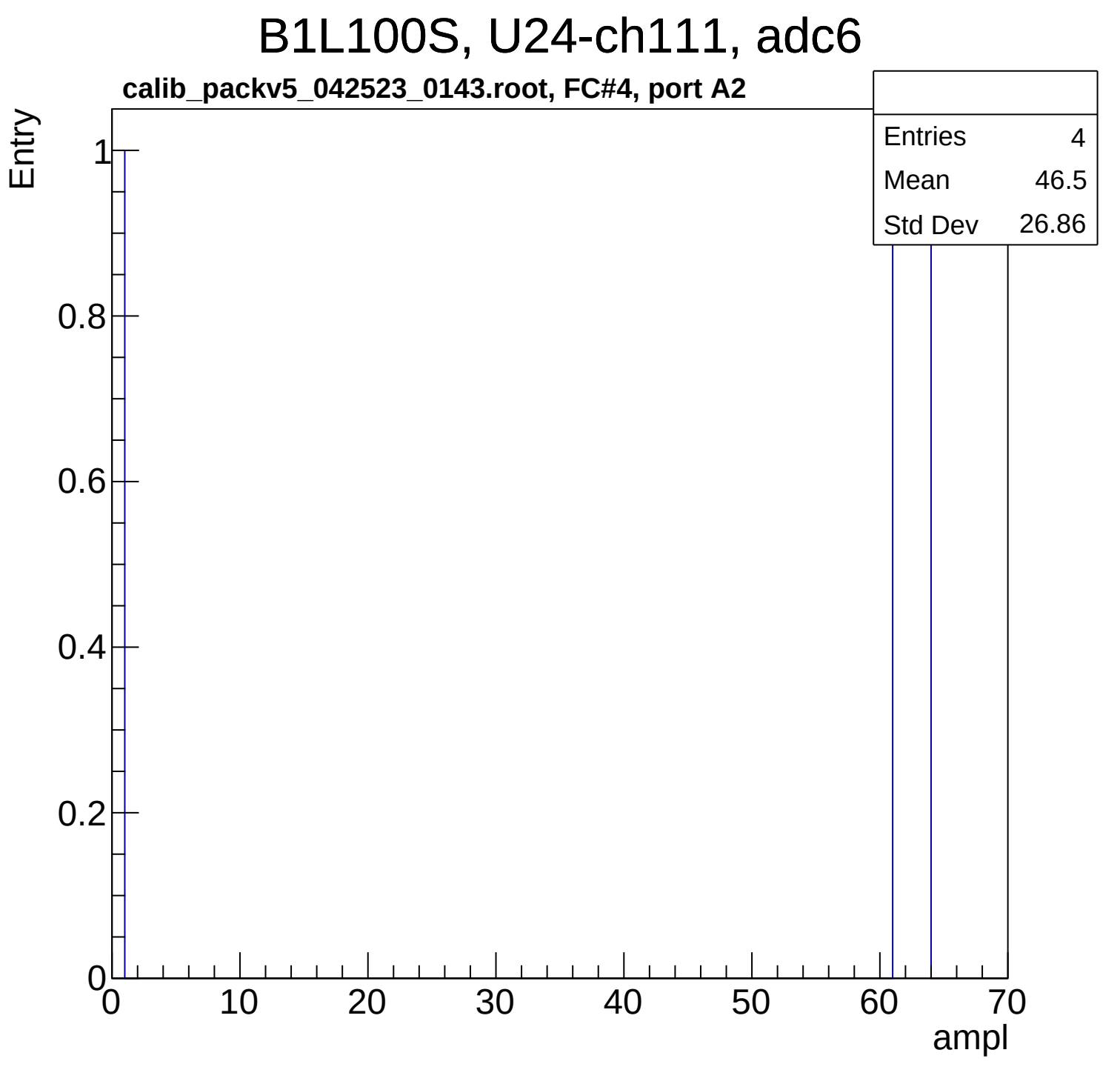
B1L100S, U24-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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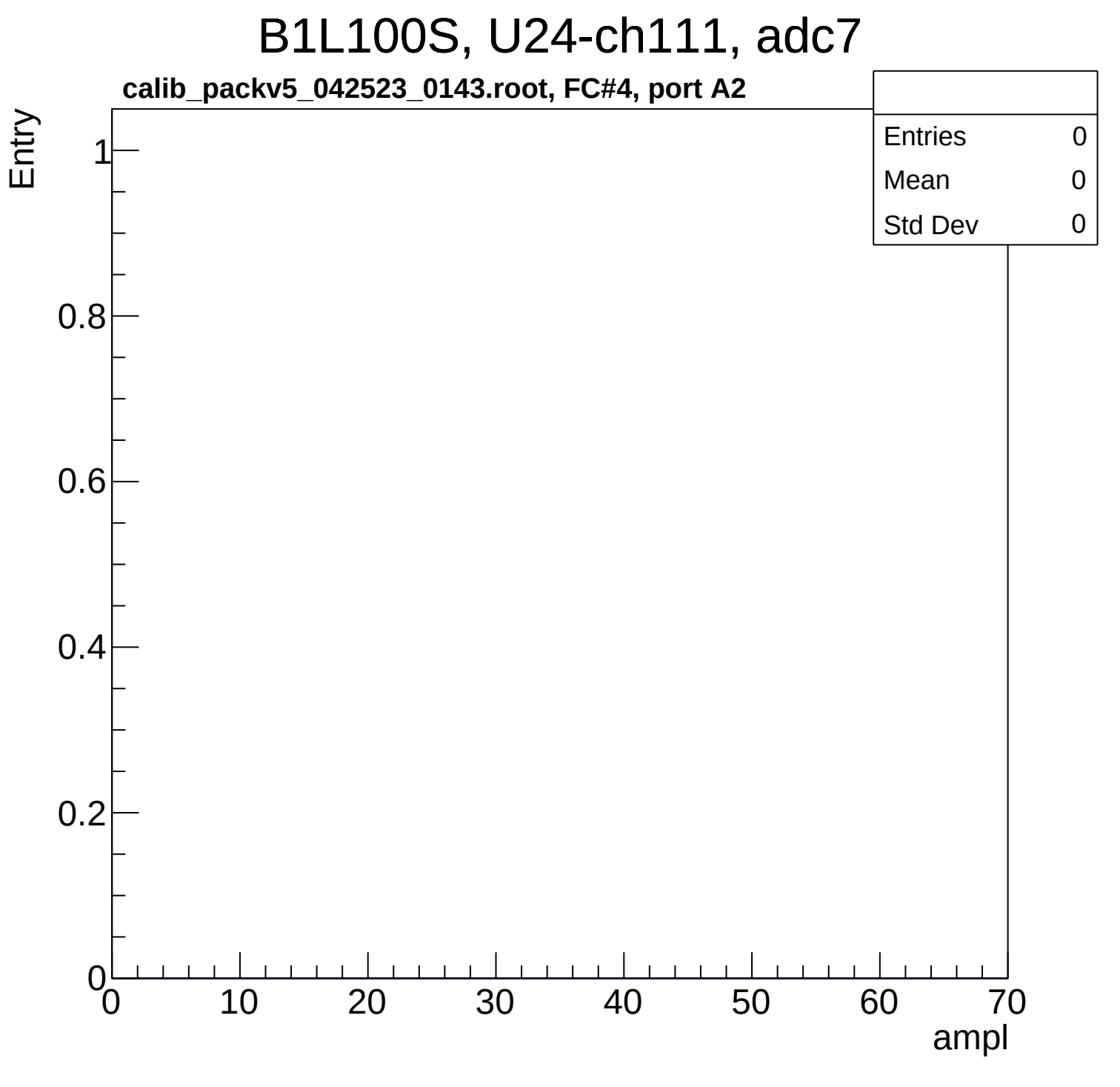
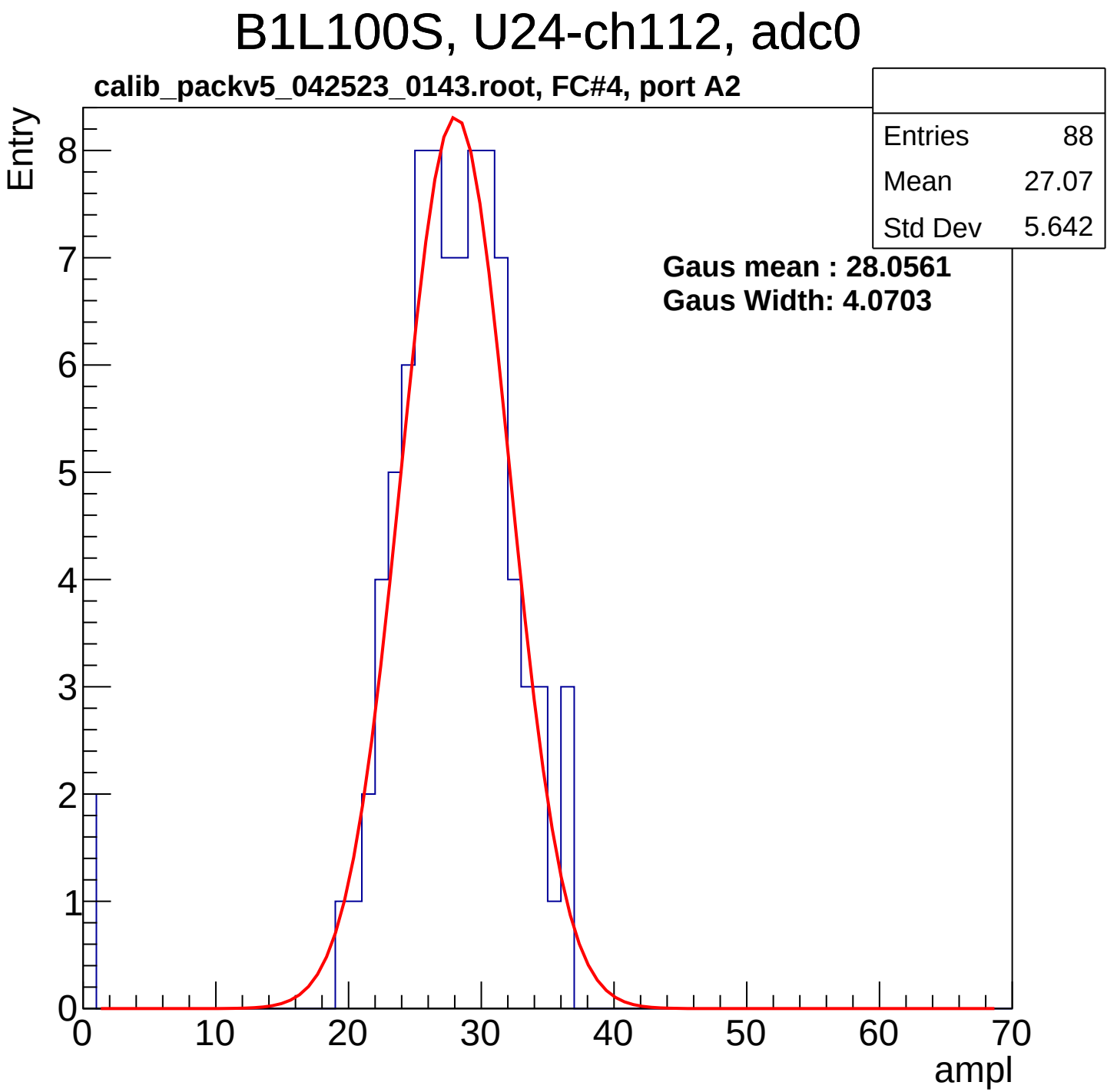
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch111, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 27.07
Std Dev 5.642
Gaus mean : 28.0561
Gaus Width: 4.0703
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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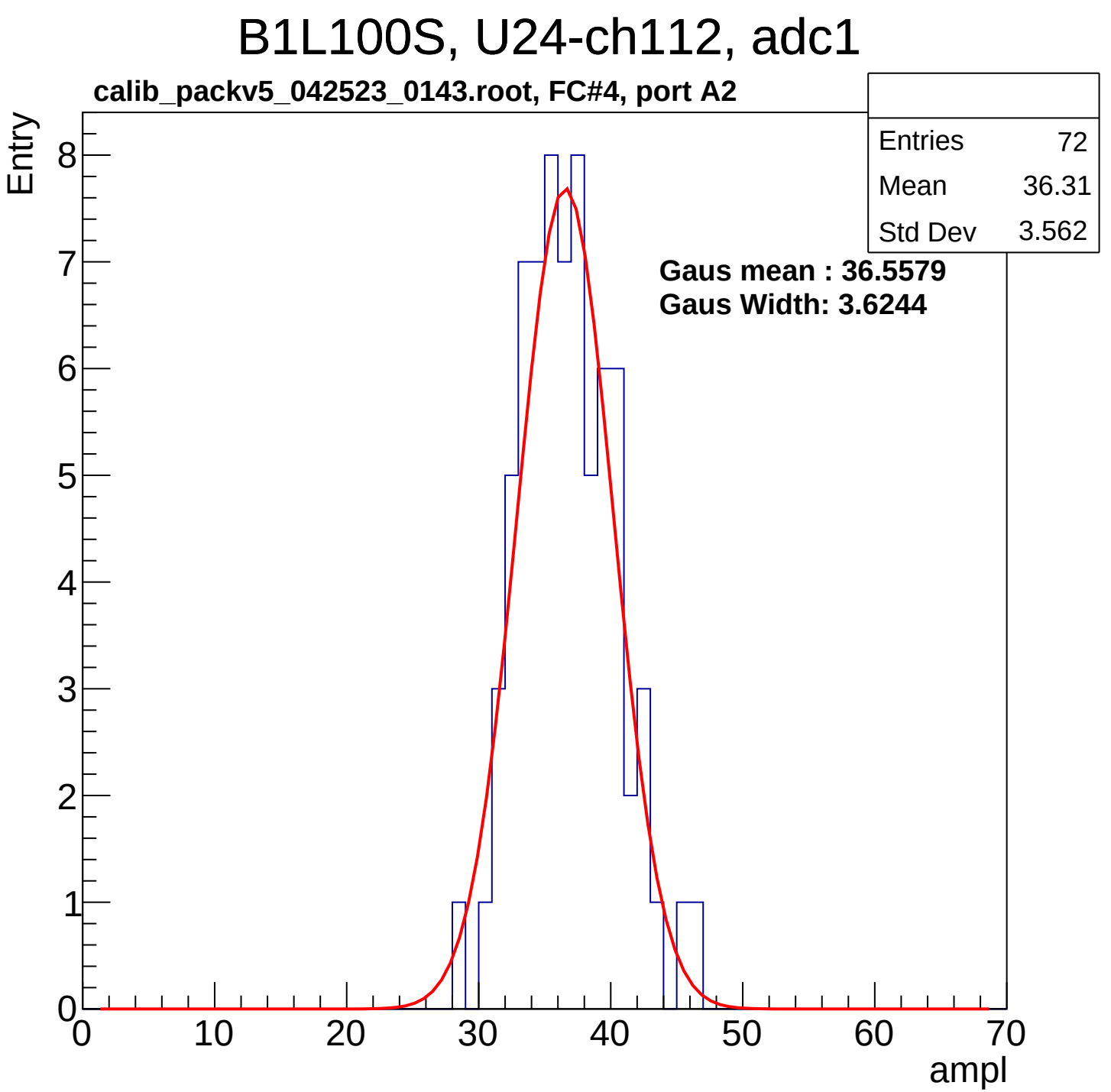
B1L100S, U24-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.31
Std Dev 3.562
Gaus mean : 36.5579
Gaus Width: 3.6244
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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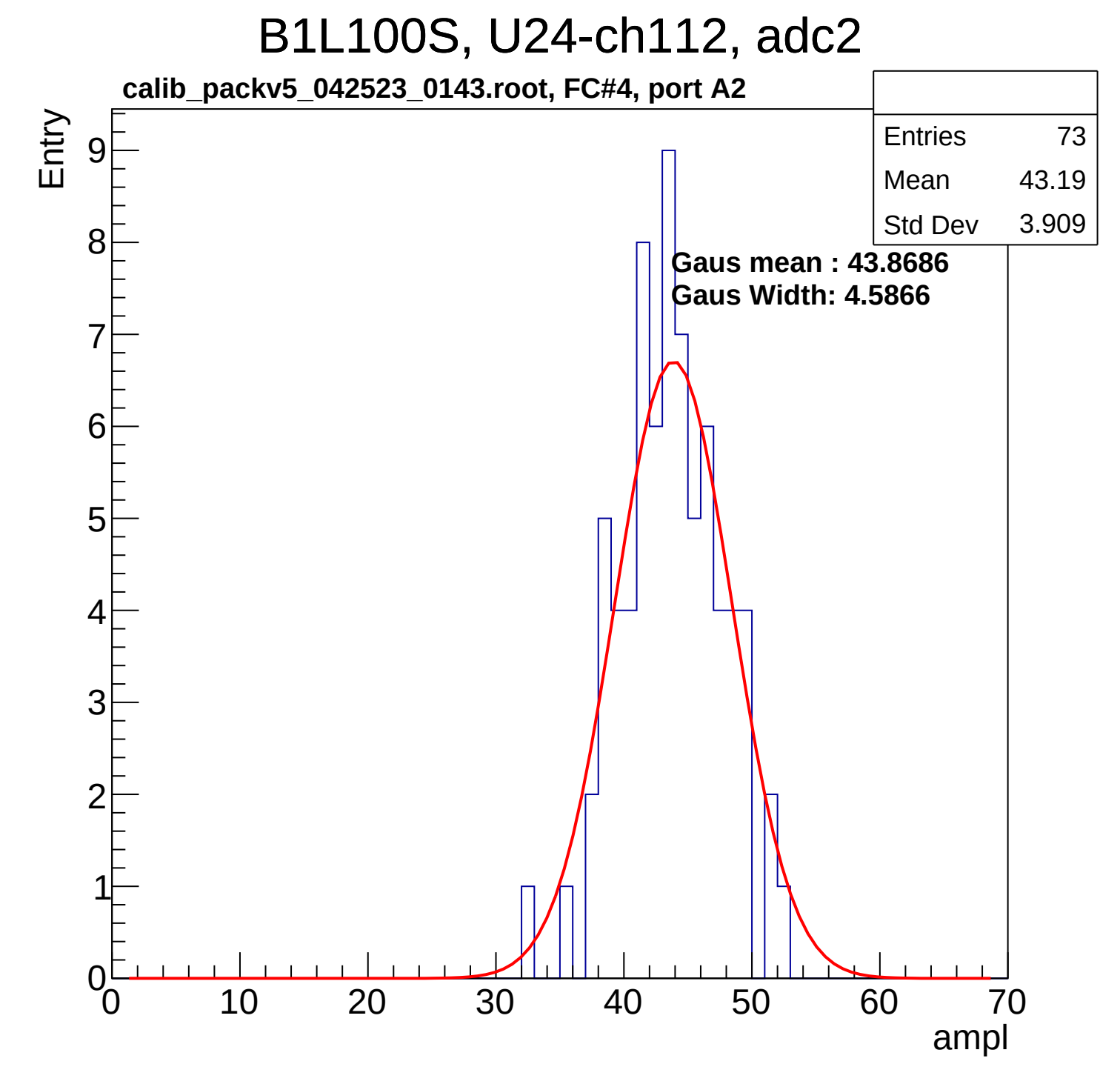
B1L100S, U24-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.19
Std Dev 3.909
Gaus mean : 43.8686
Gaus Width: 4.5866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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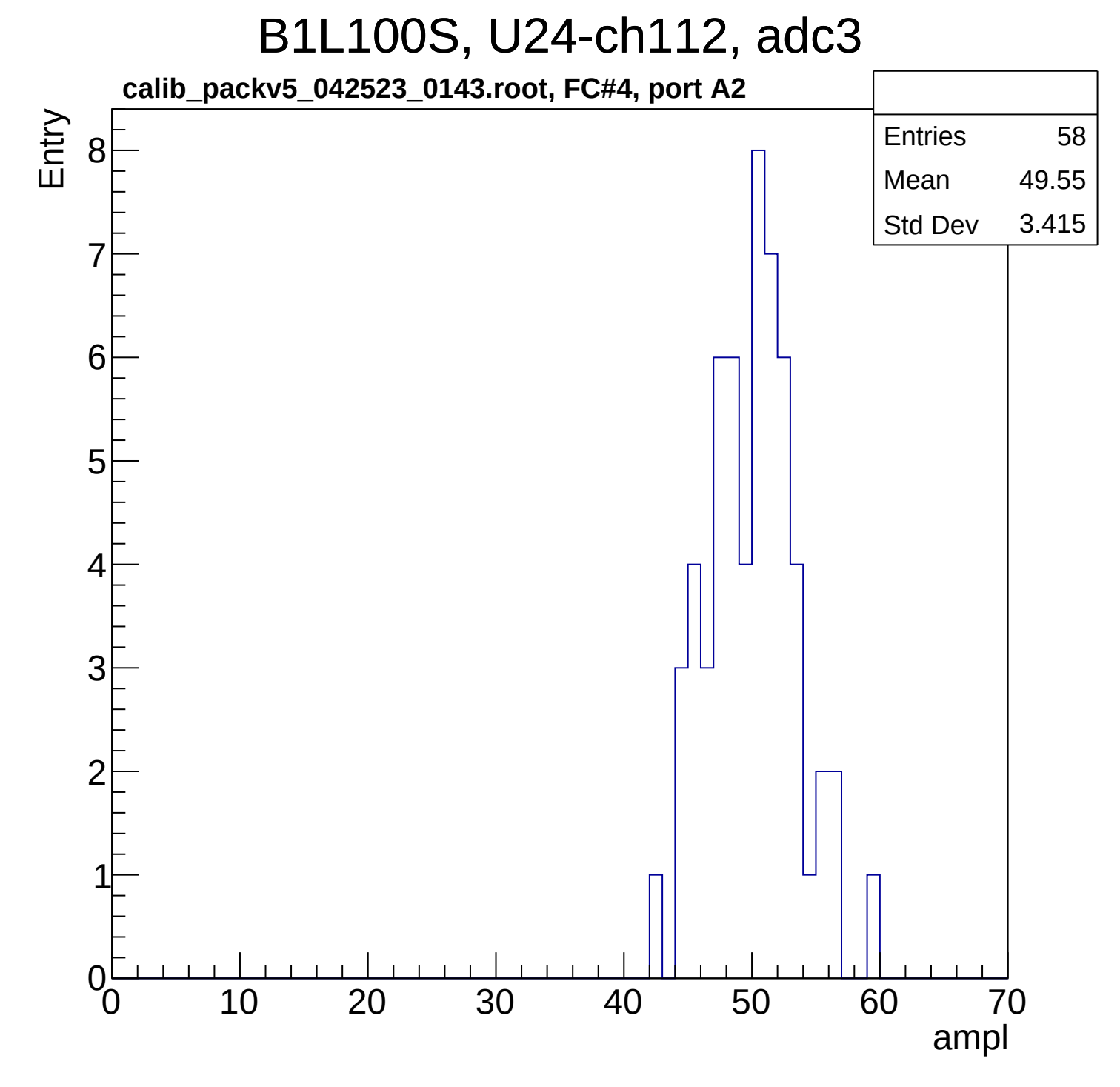
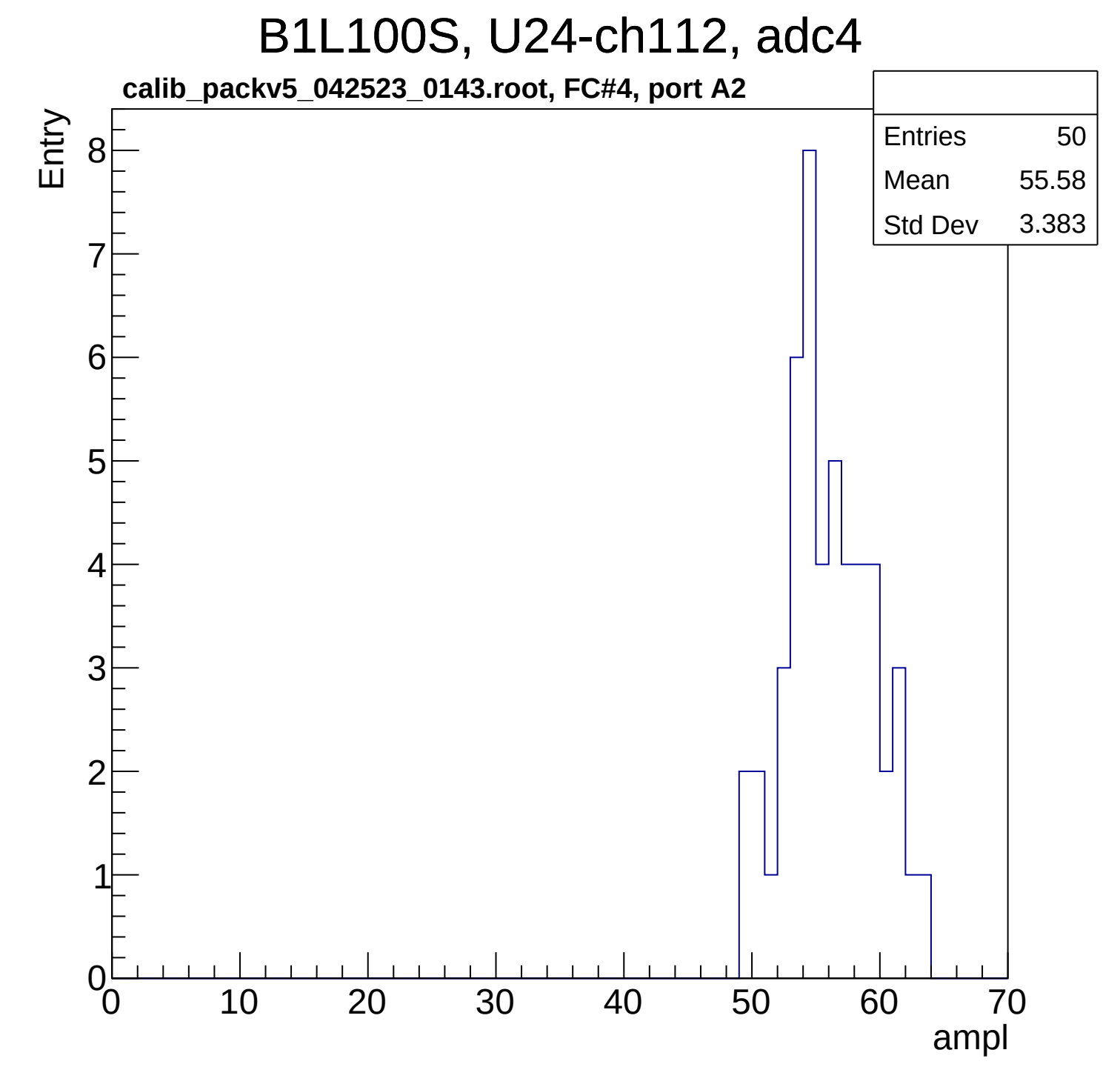

B1L100S, U24-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.55
Std Dev 3.415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.58
Std Dev 3.383
Entry
ampl
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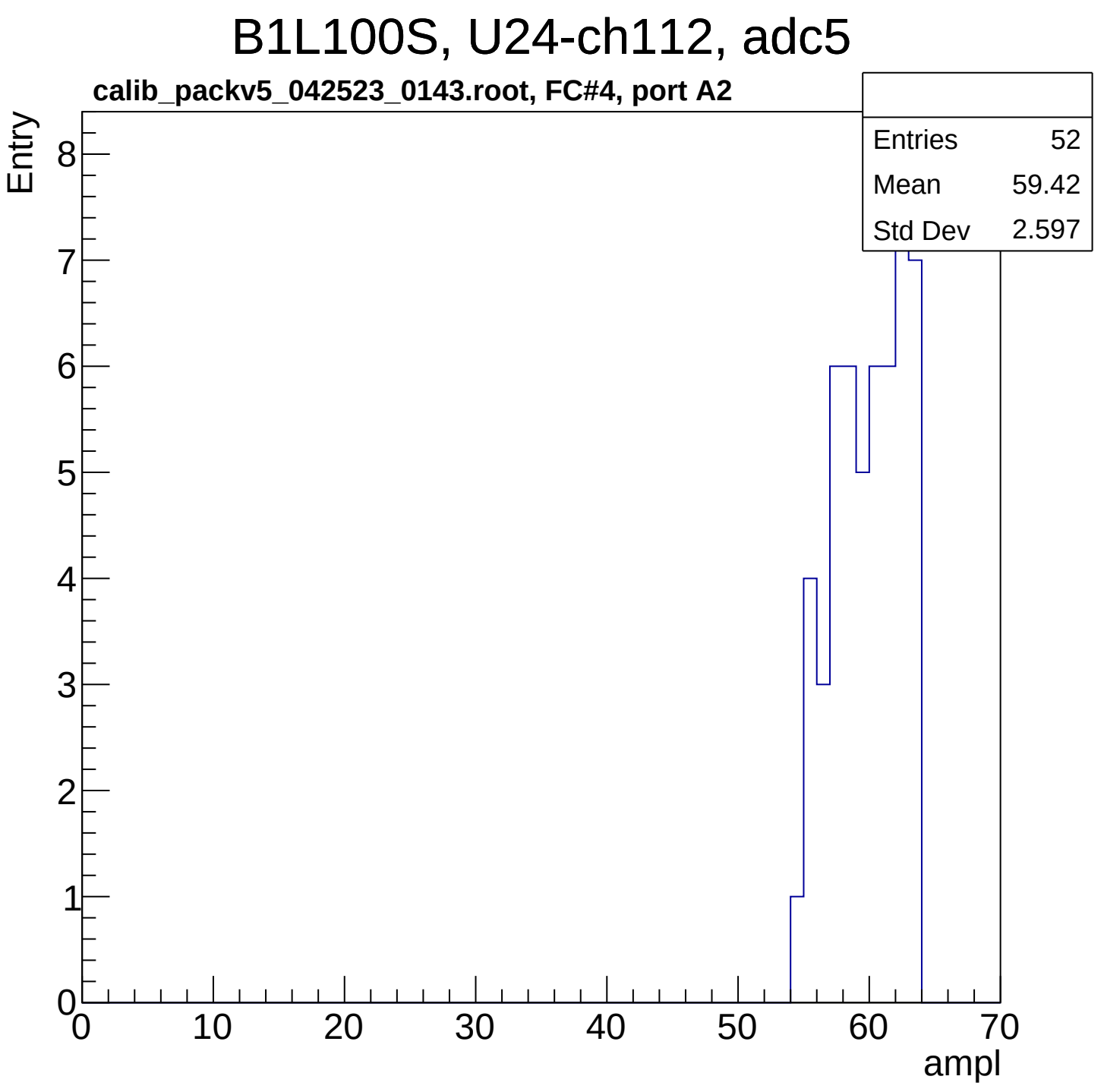
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch112, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 59.42
Std Dev 2.597
Entry
ampl
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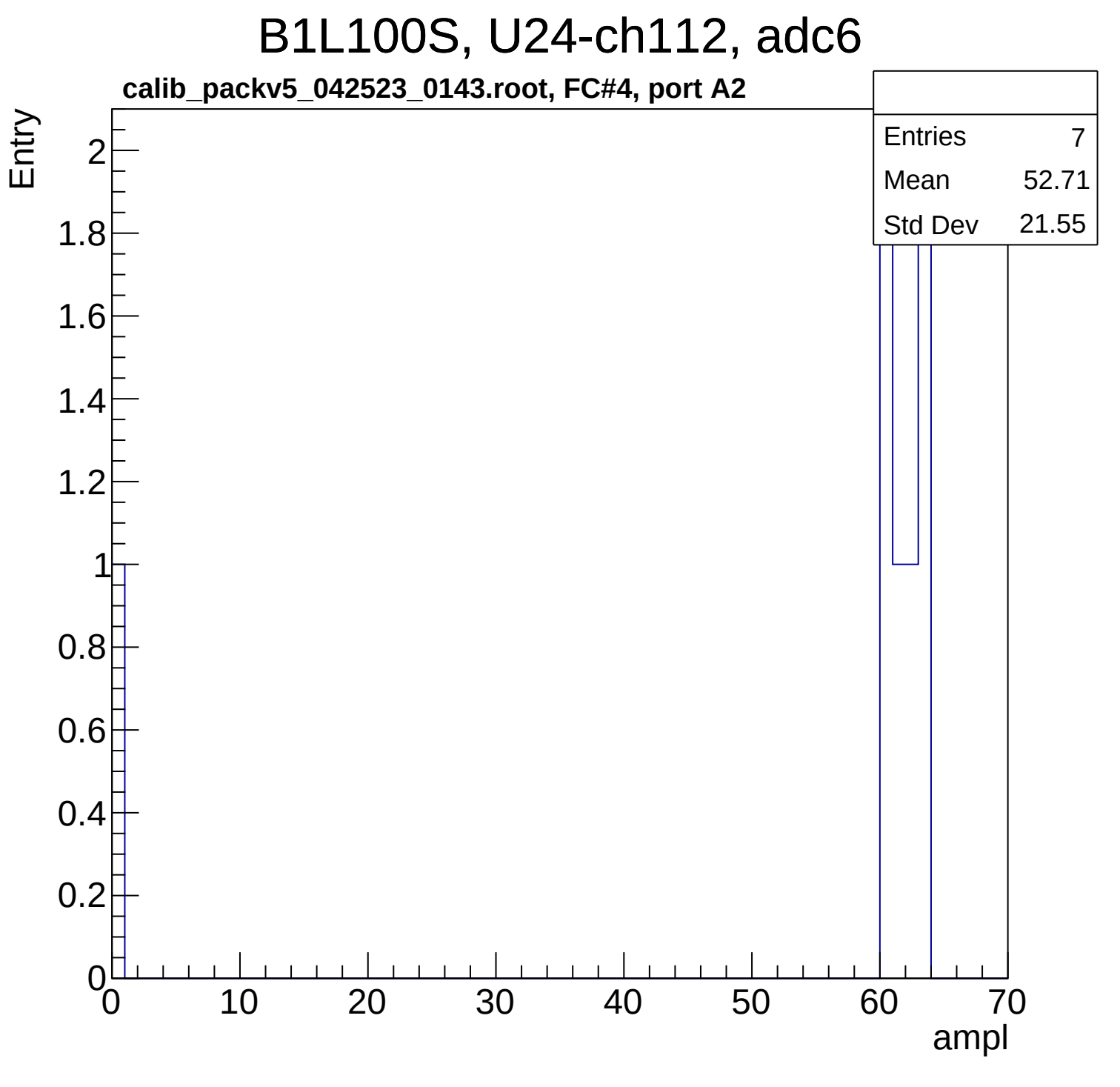
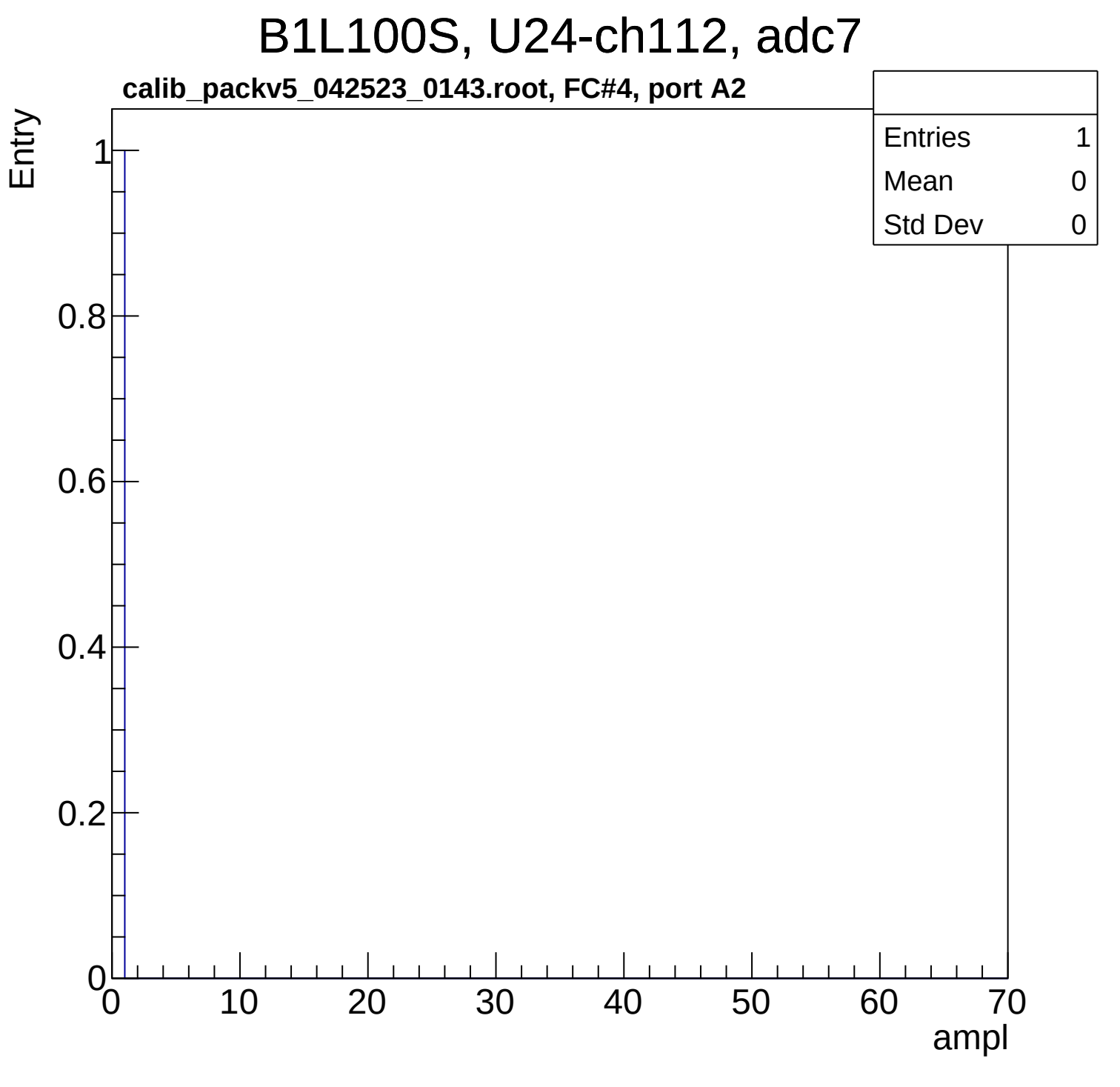
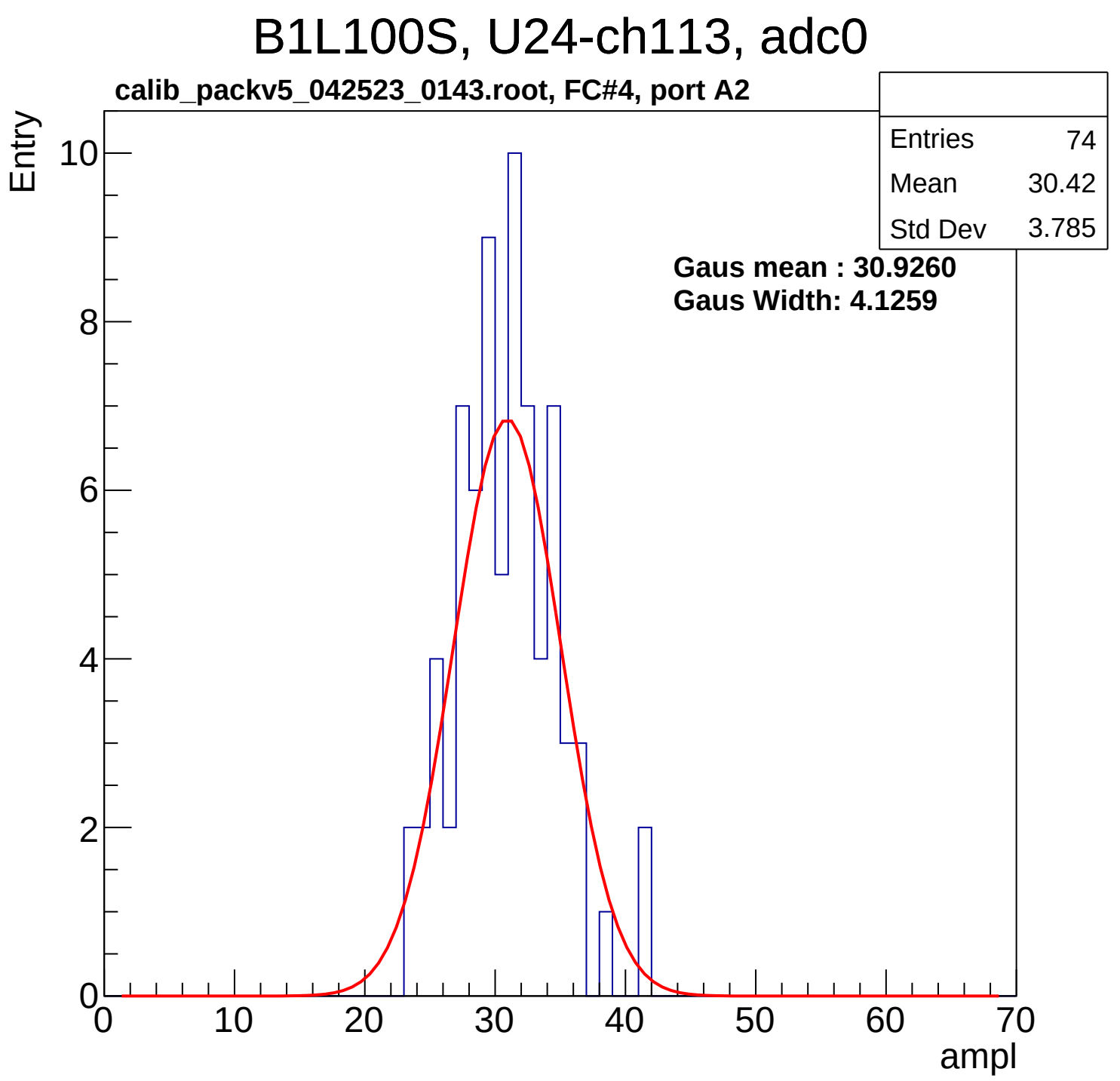
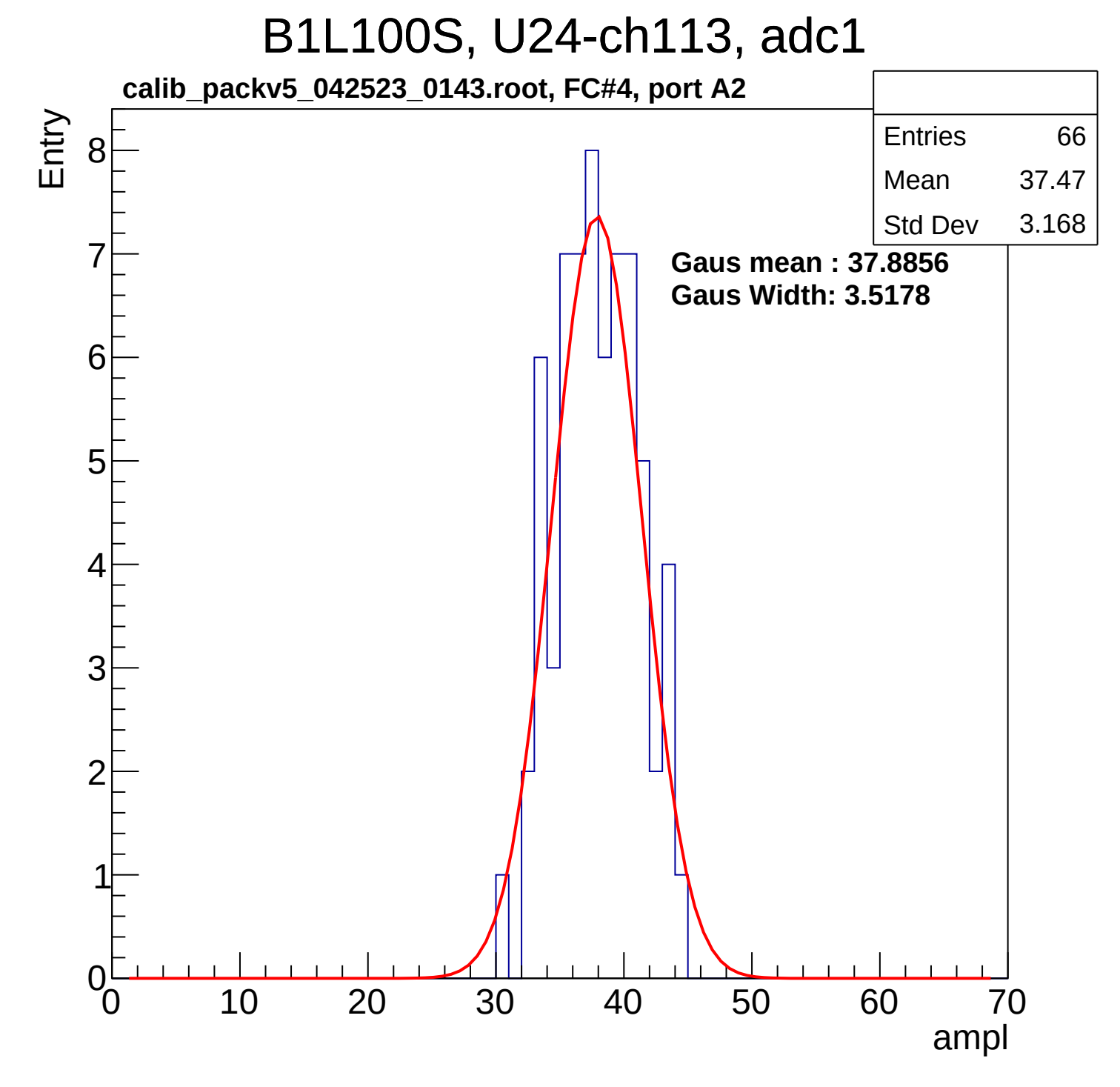
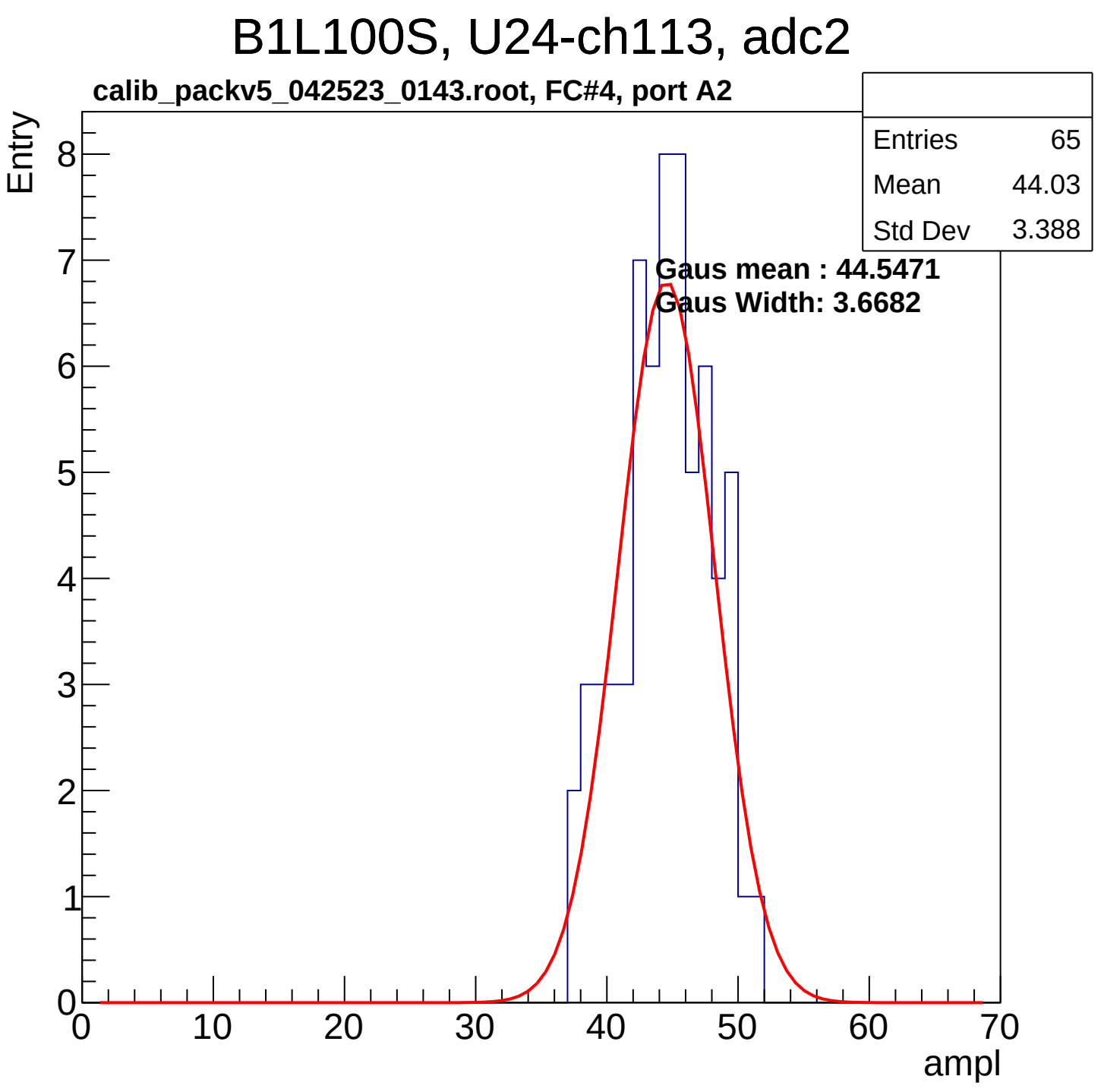
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch112, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 52.71
Std Dev 21.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch112, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 30.42
Std Dev 3.785
Gaus mean : 30.9260
Gaus Width: 4.1259
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 37.47
Std Dev 3.168
Gaus mean : 37.8856
Gaus Width: 3.5178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.03
Std Dev 3.388
Gaus mean : 44.5471
Gaus Width: 3.6682
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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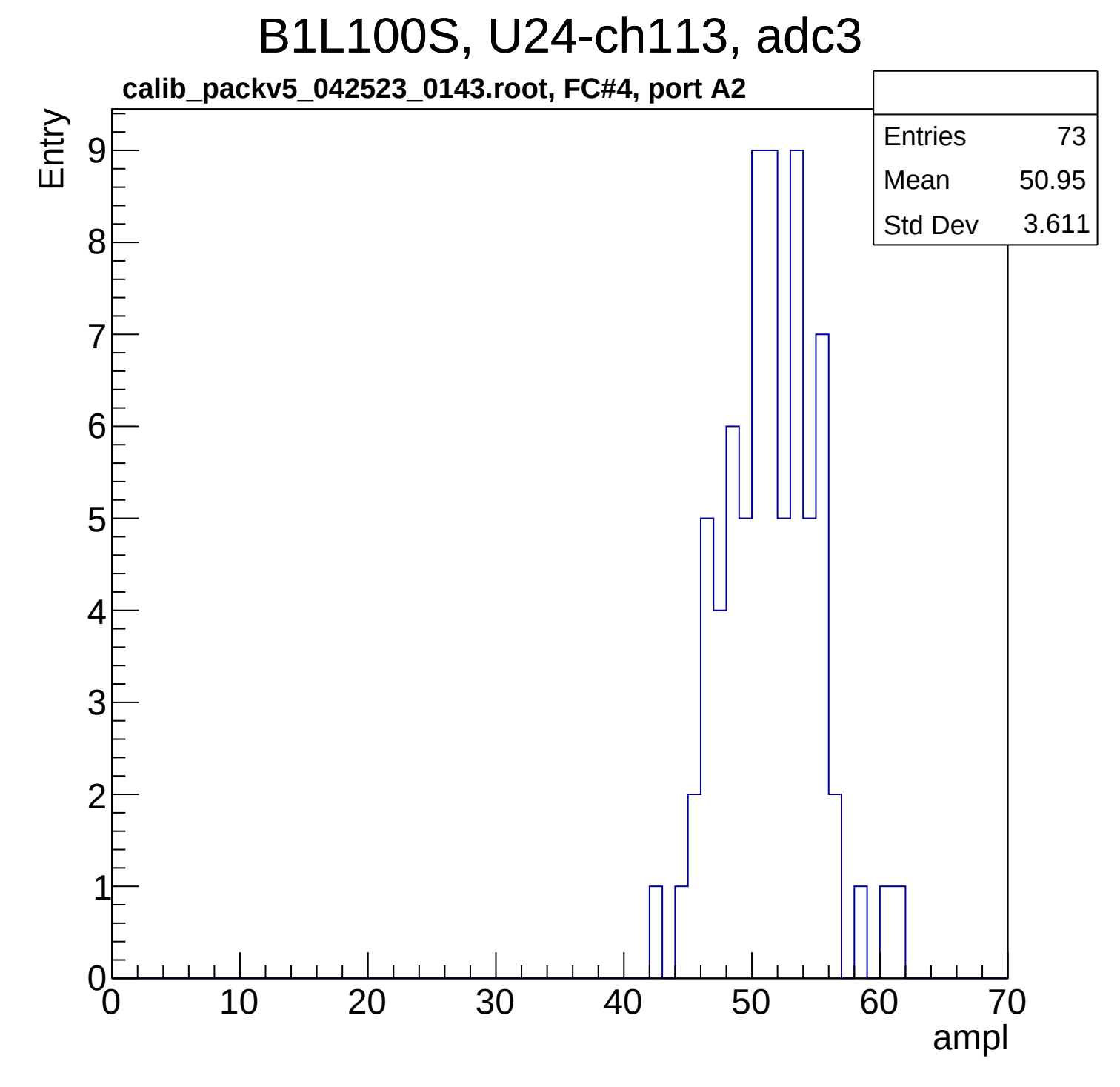
B1L100S, U24-ch113, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 50.95
Std Dev 3.611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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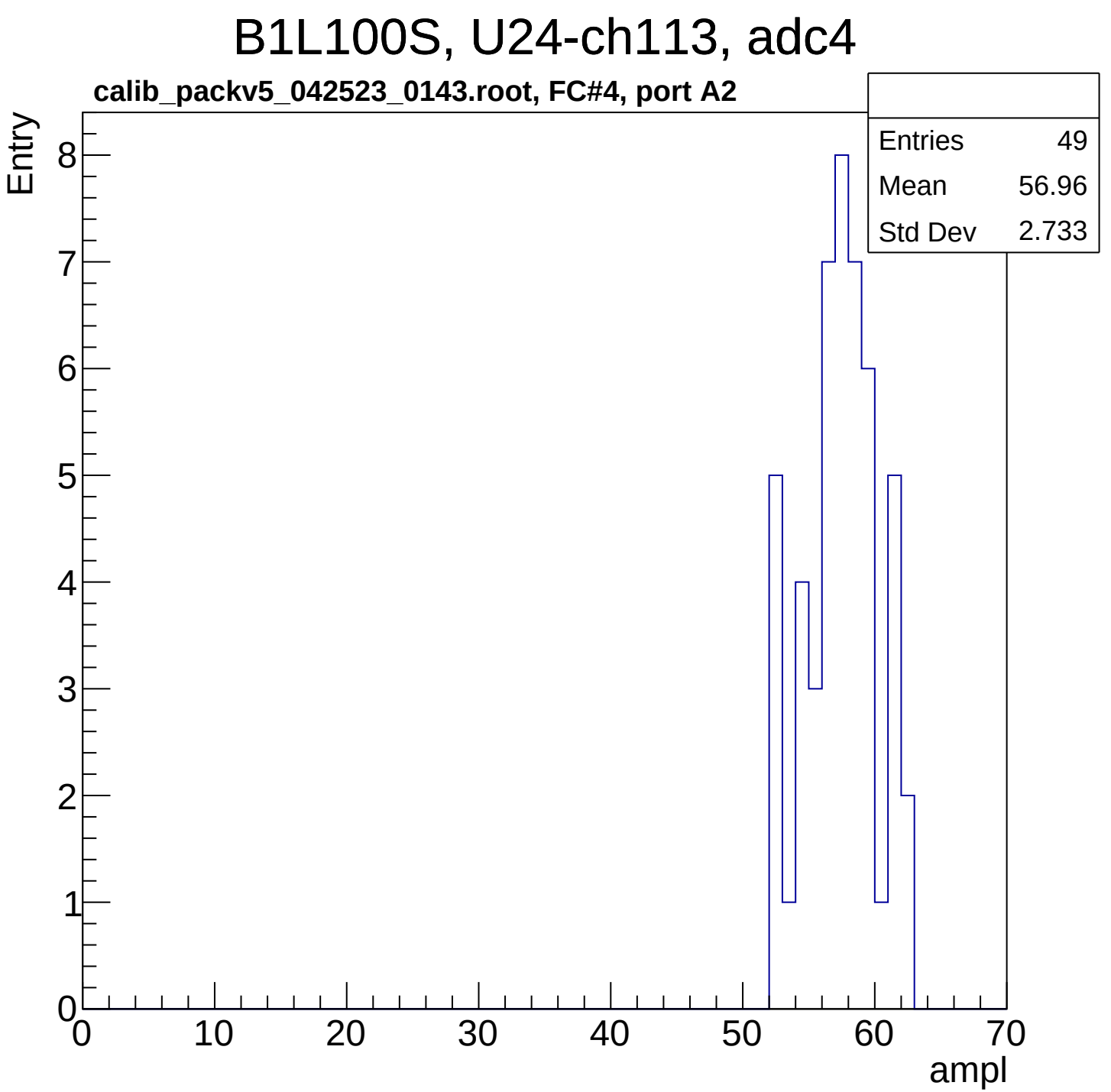
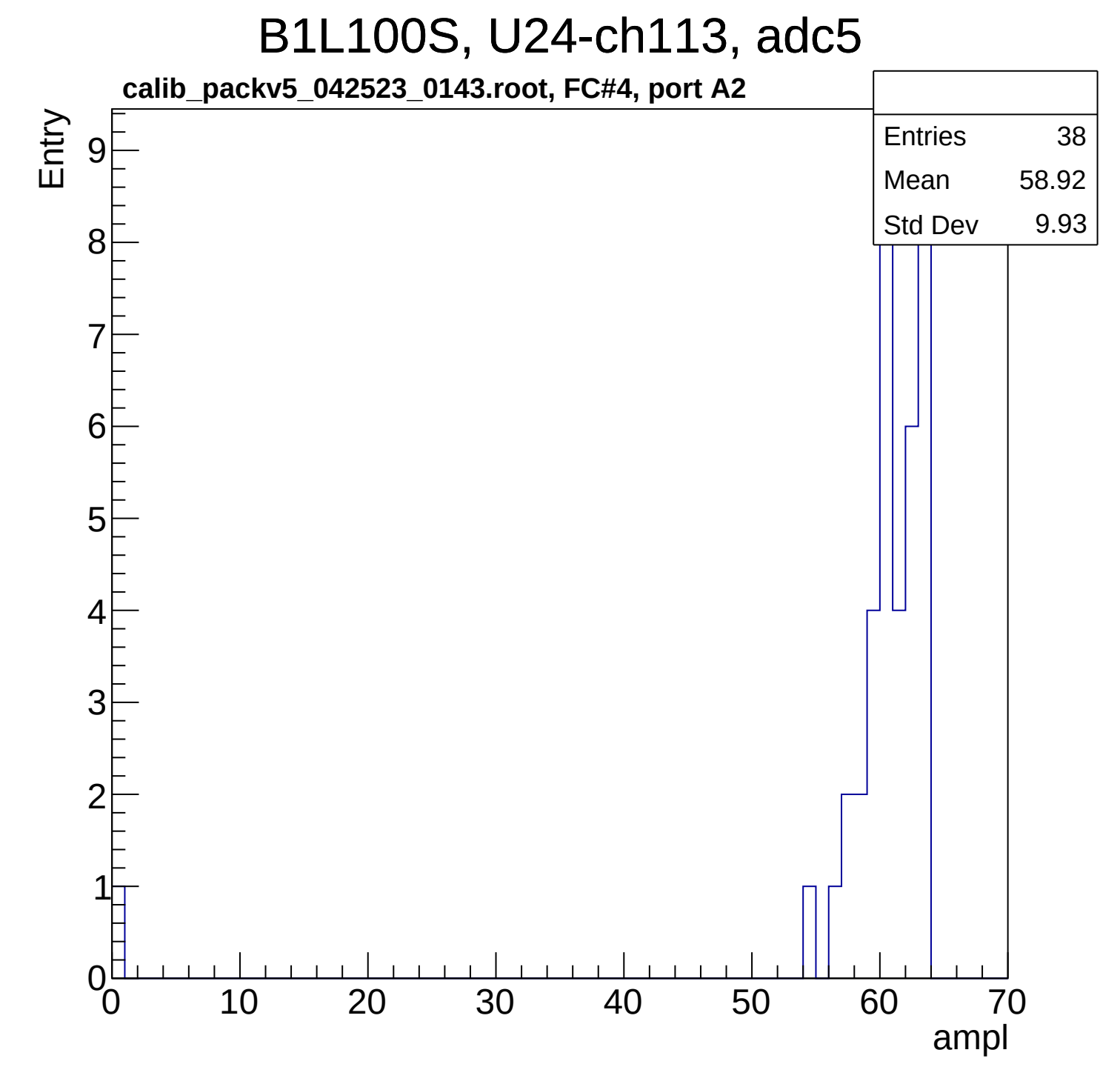

B1L100S, U24-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.96
Std Dev 2.733
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.92
Std Dev 9.93
Entry
ampl
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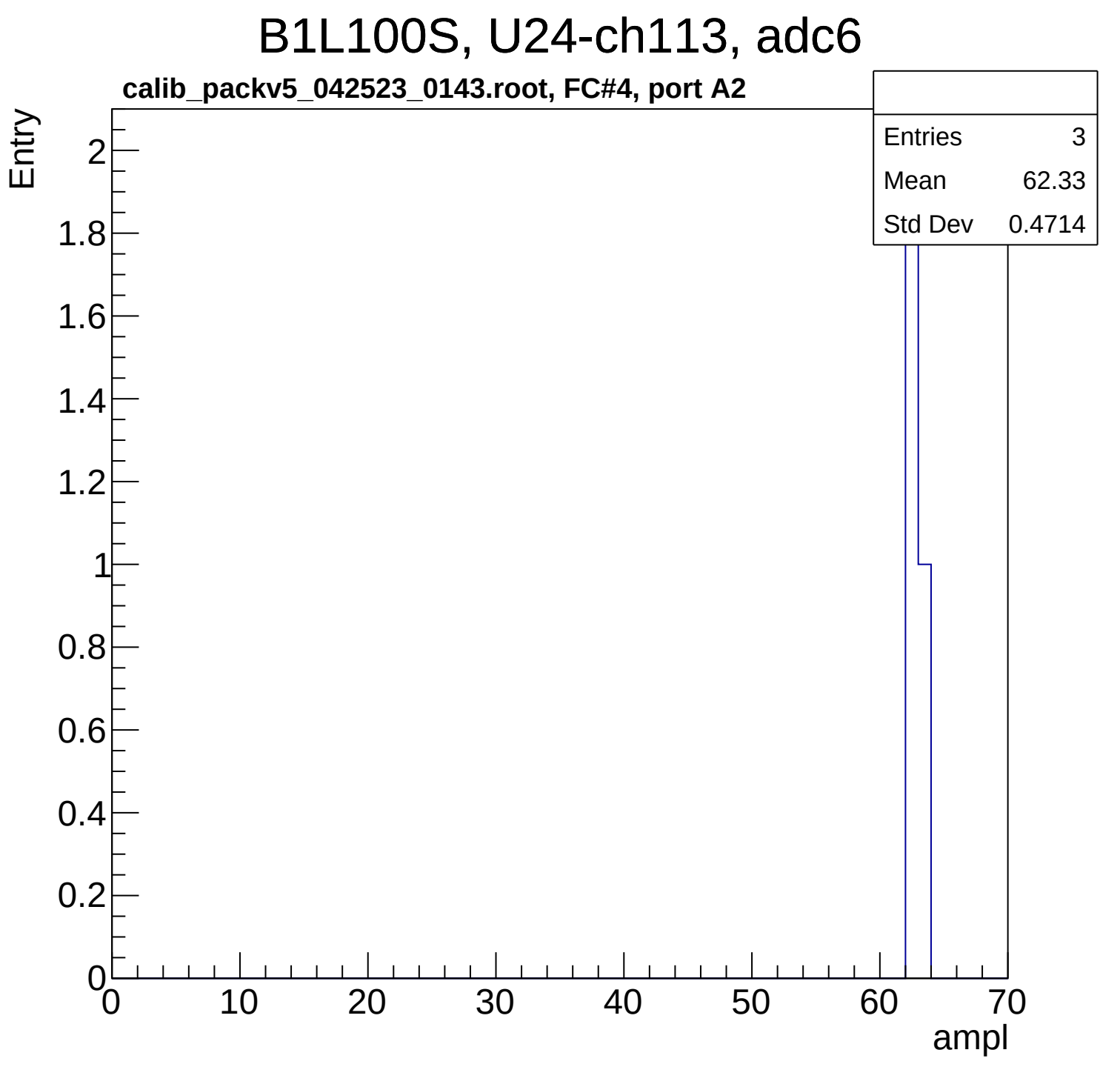
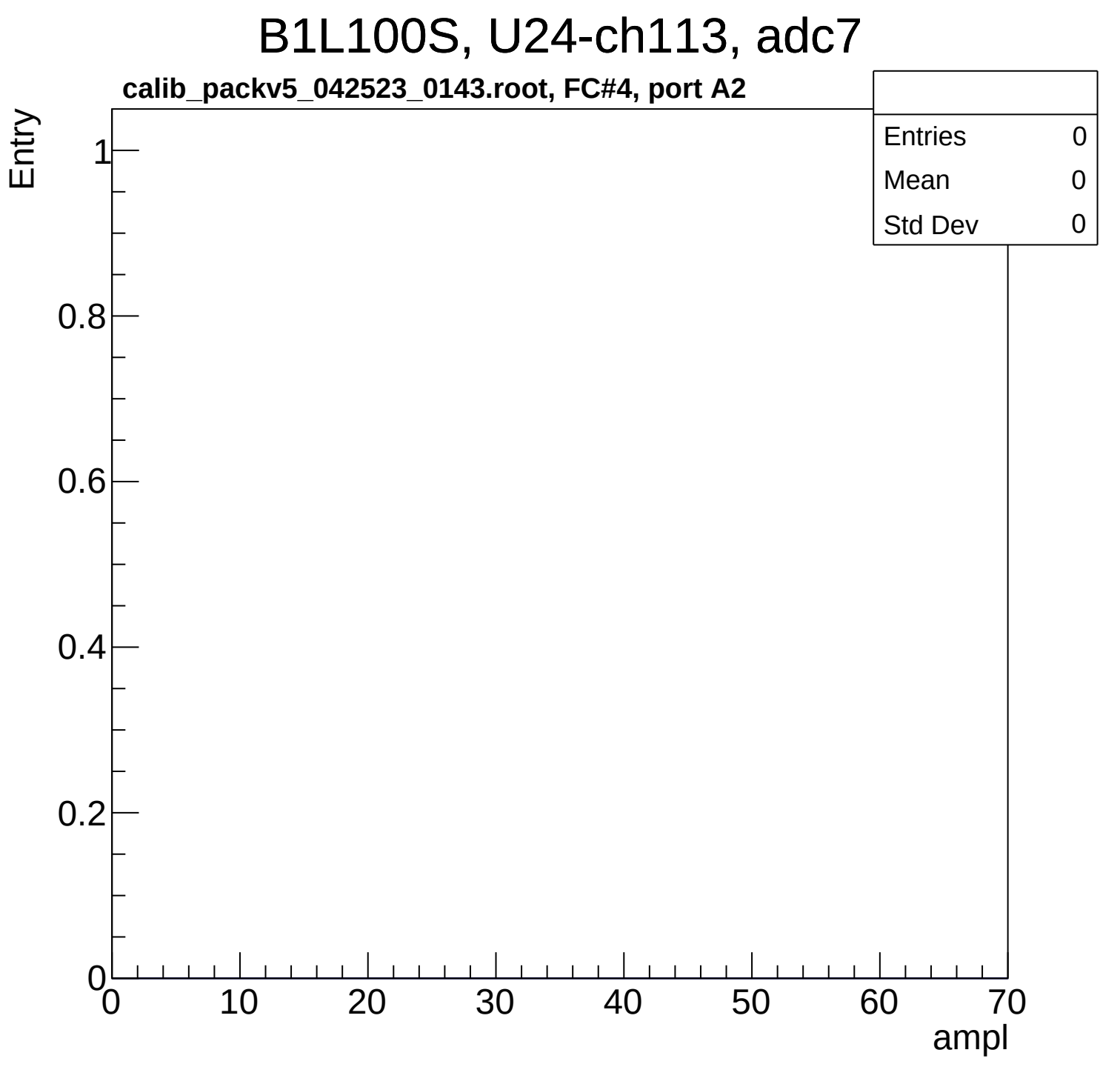
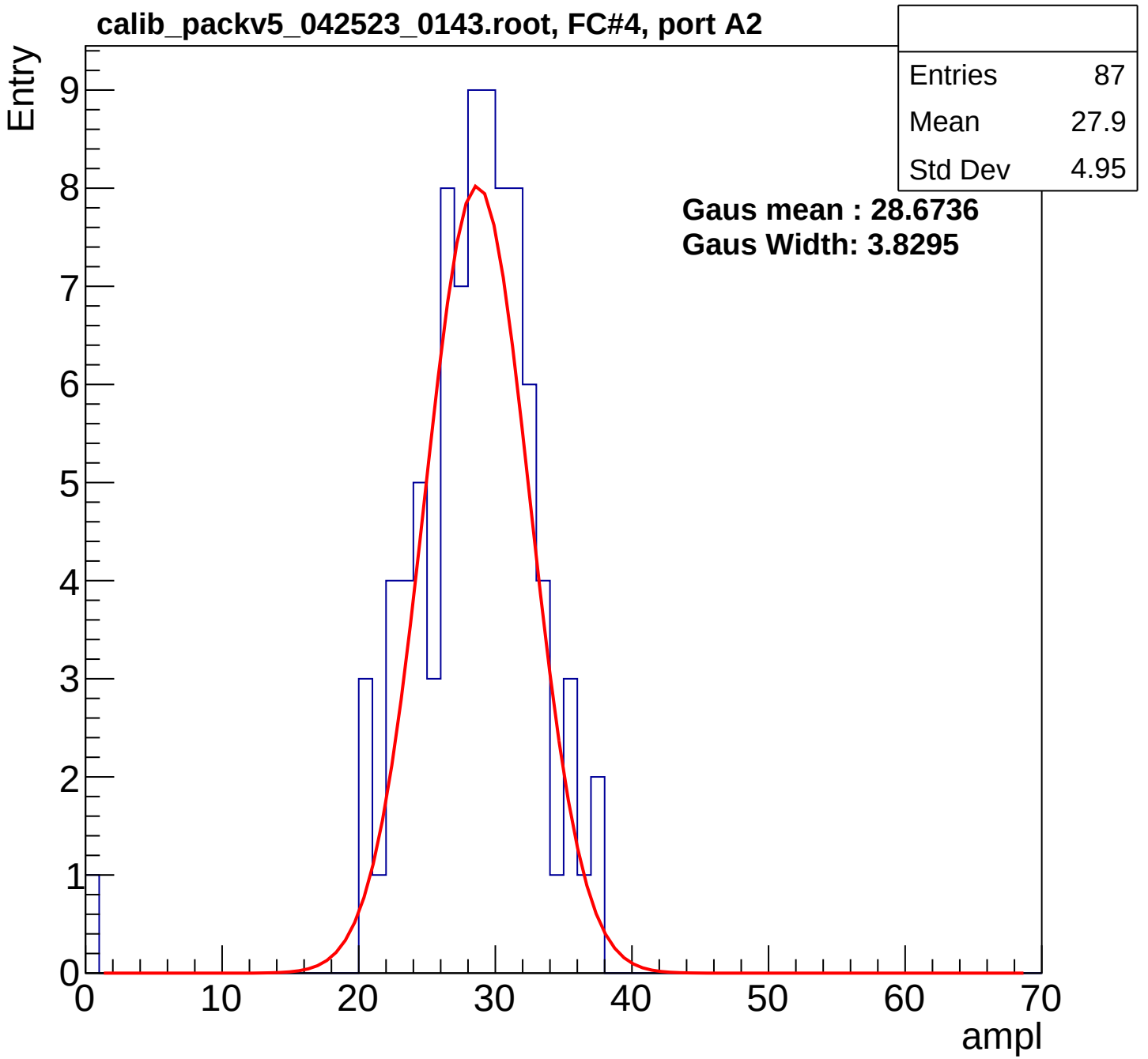
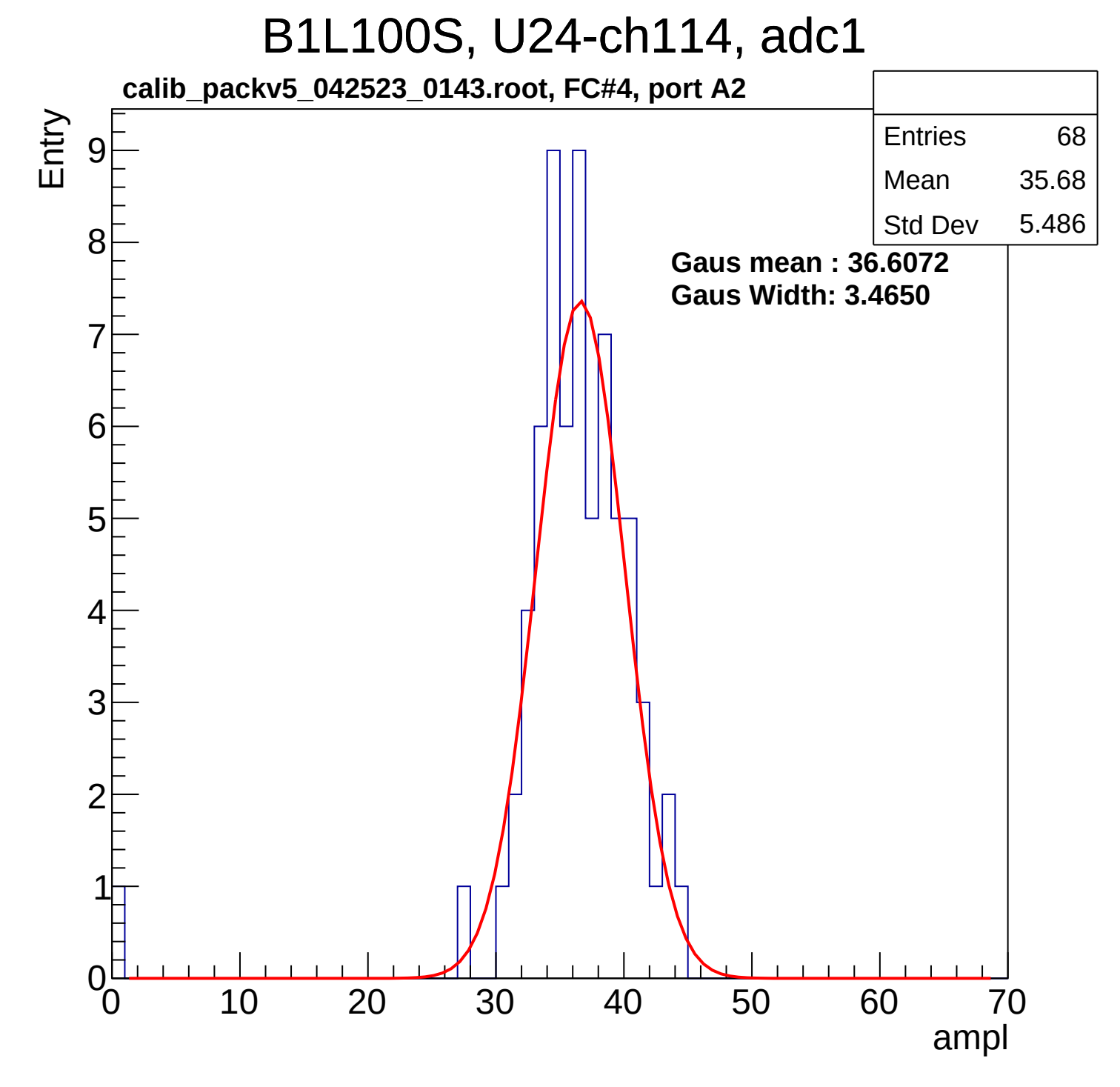
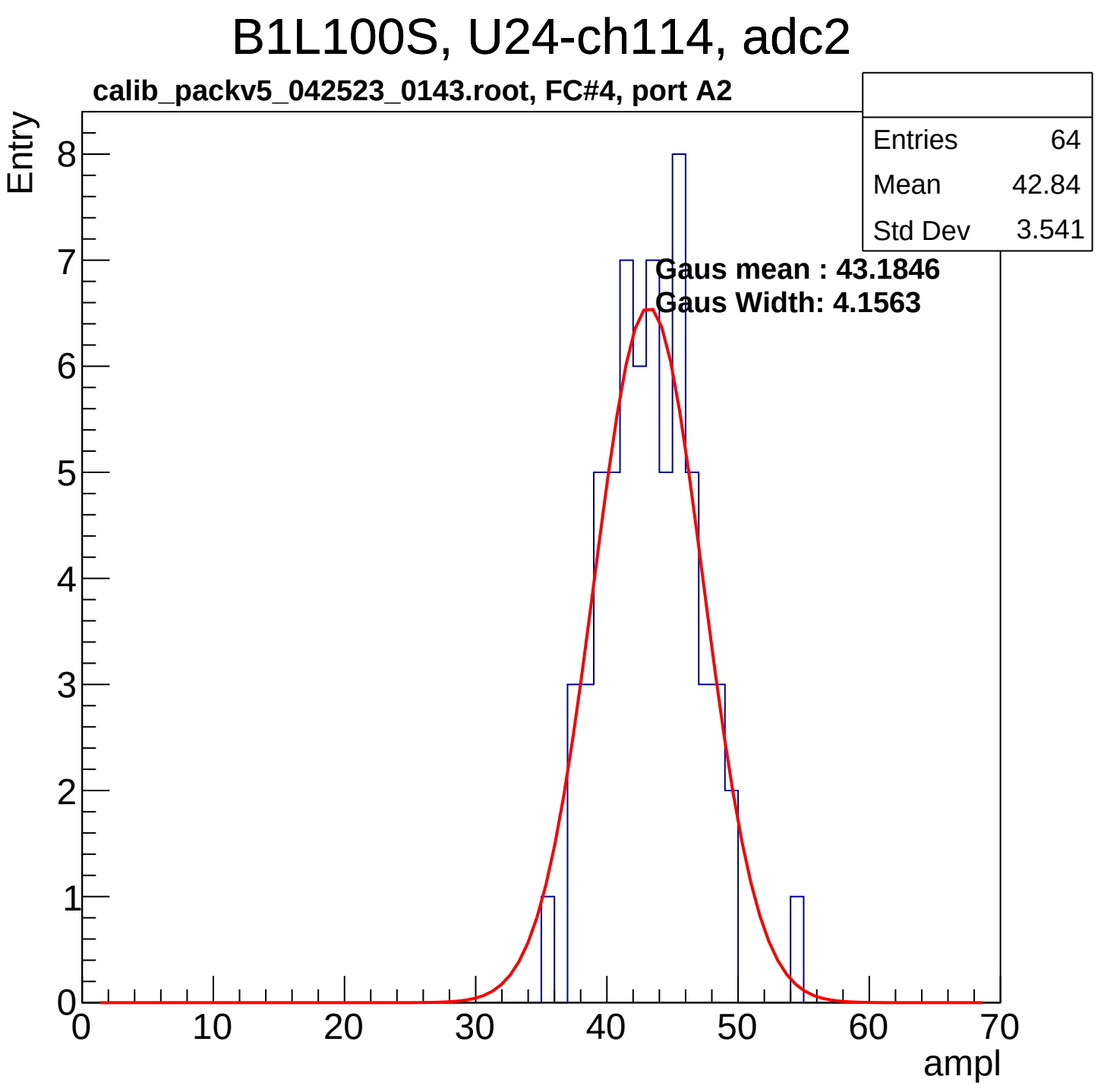
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch113, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch114, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 27.9
Std Dev 4.95
Gaus mean : 28.6736
Gaus Width: 3.8295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 35.68
Std Dev 5.486
Gaus mean : 36.6072
Gaus Width: 3.4650
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.84
Std Dev 3.541
Gaus mean : 43.1846
Gaus Width: 4.1563
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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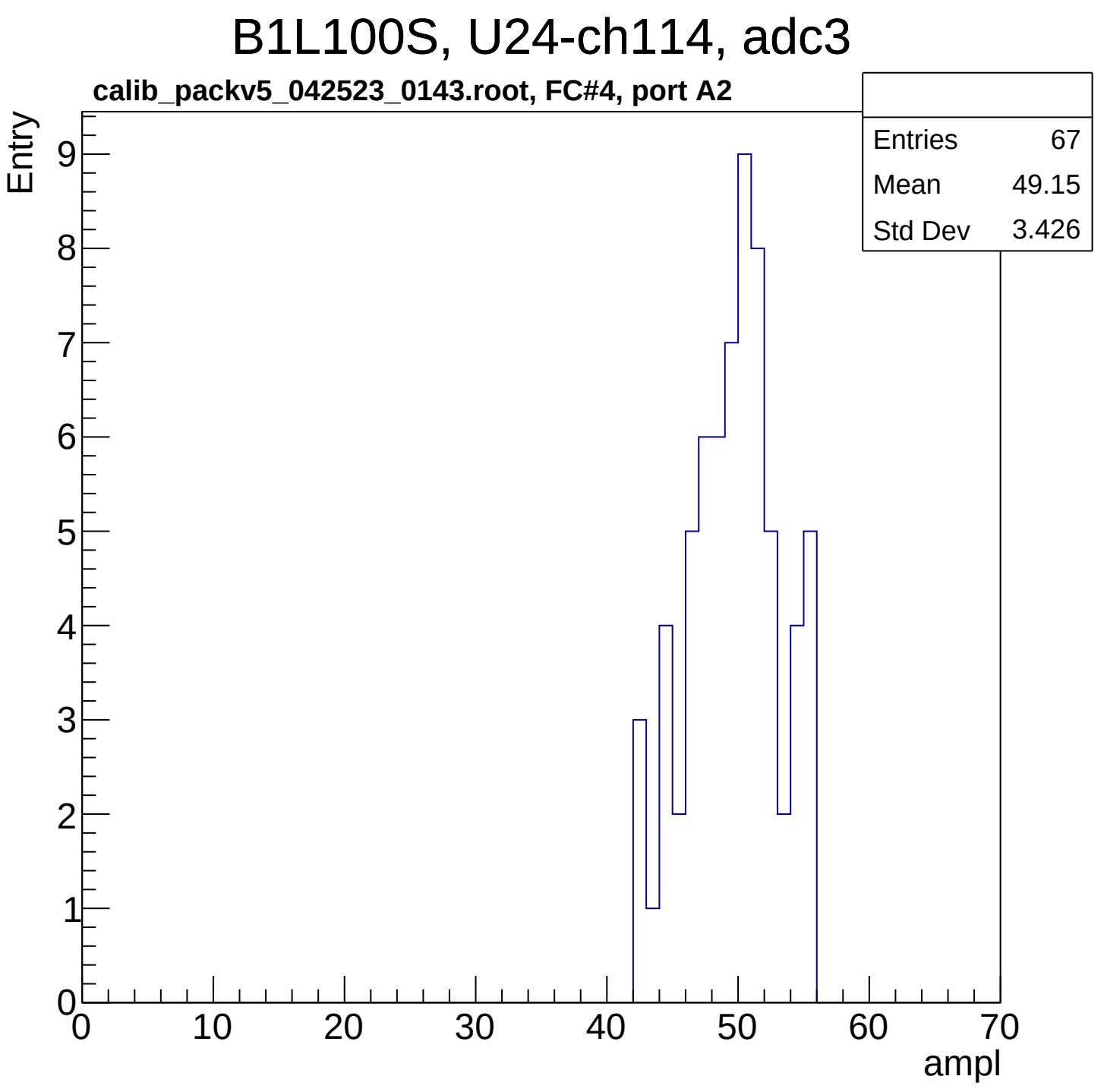
B1L100S, U24-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.15
Std Dev 3.426
Entry
ampl
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3
4
5
6
7
8
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0
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60
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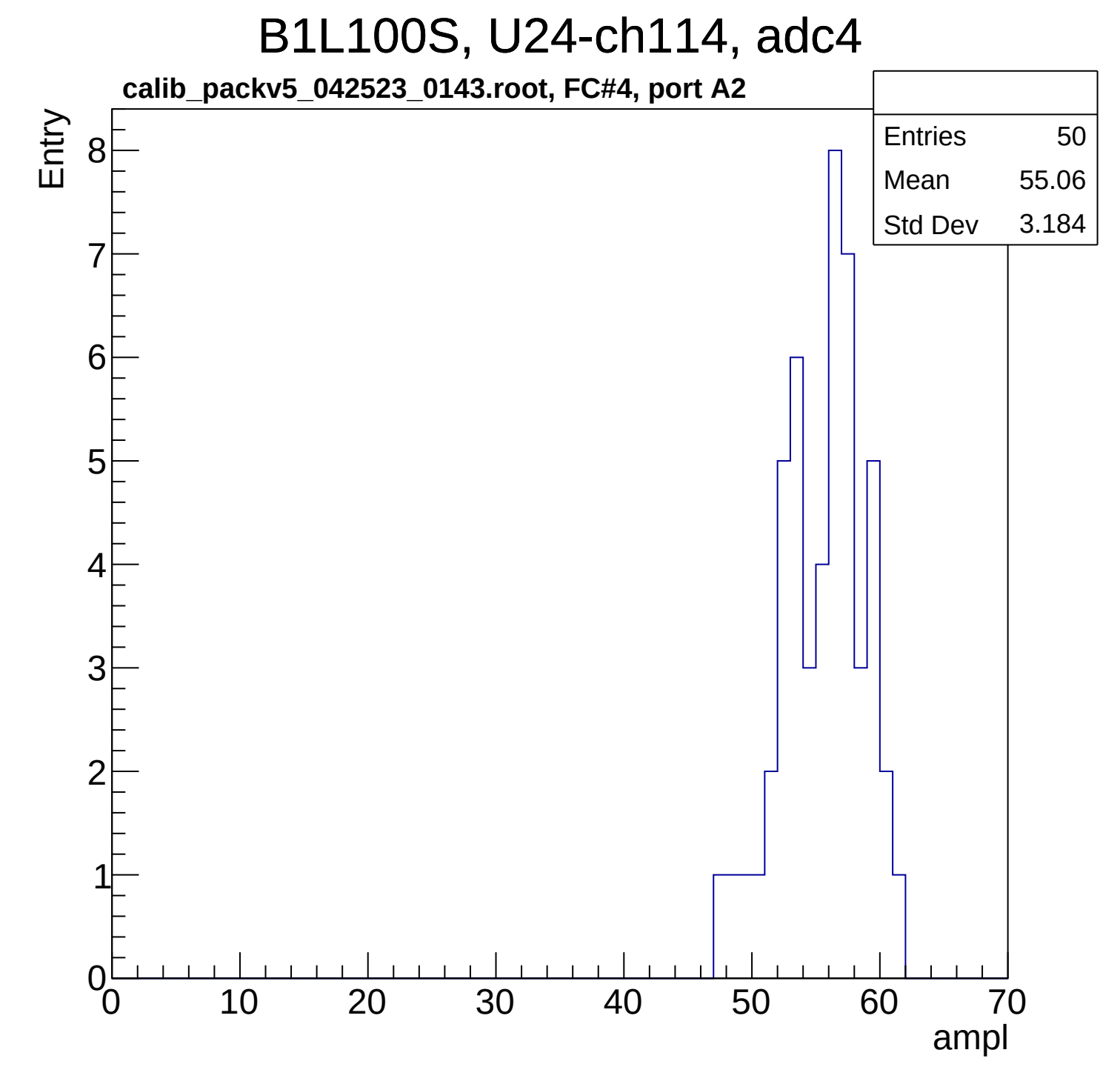
B1L100S, U24-ch114, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.06
Std Dev 3.184
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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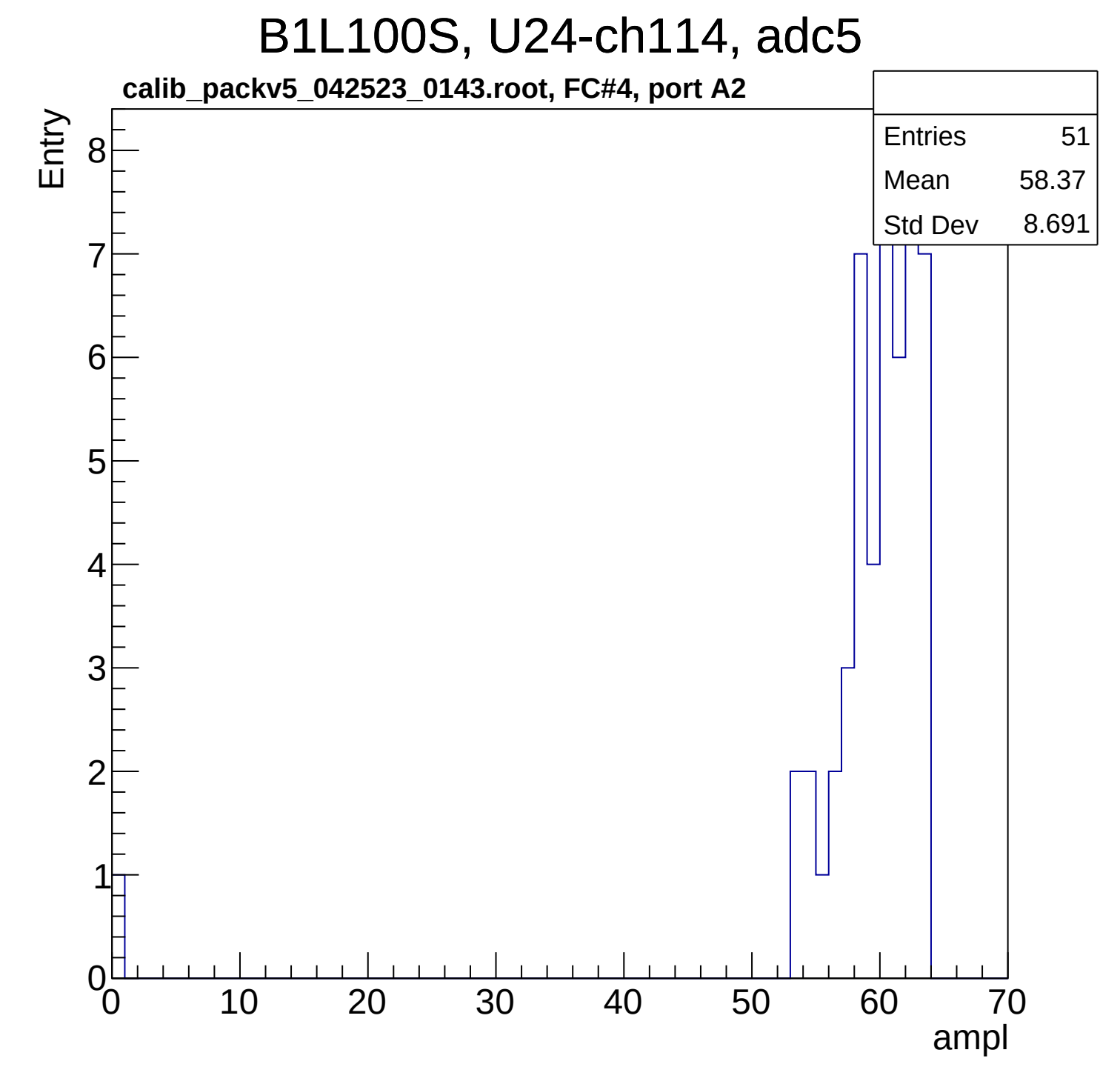
B1L100S, U24-ch114, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.37
Std Dev 8.691
Entry
ampl
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7
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0
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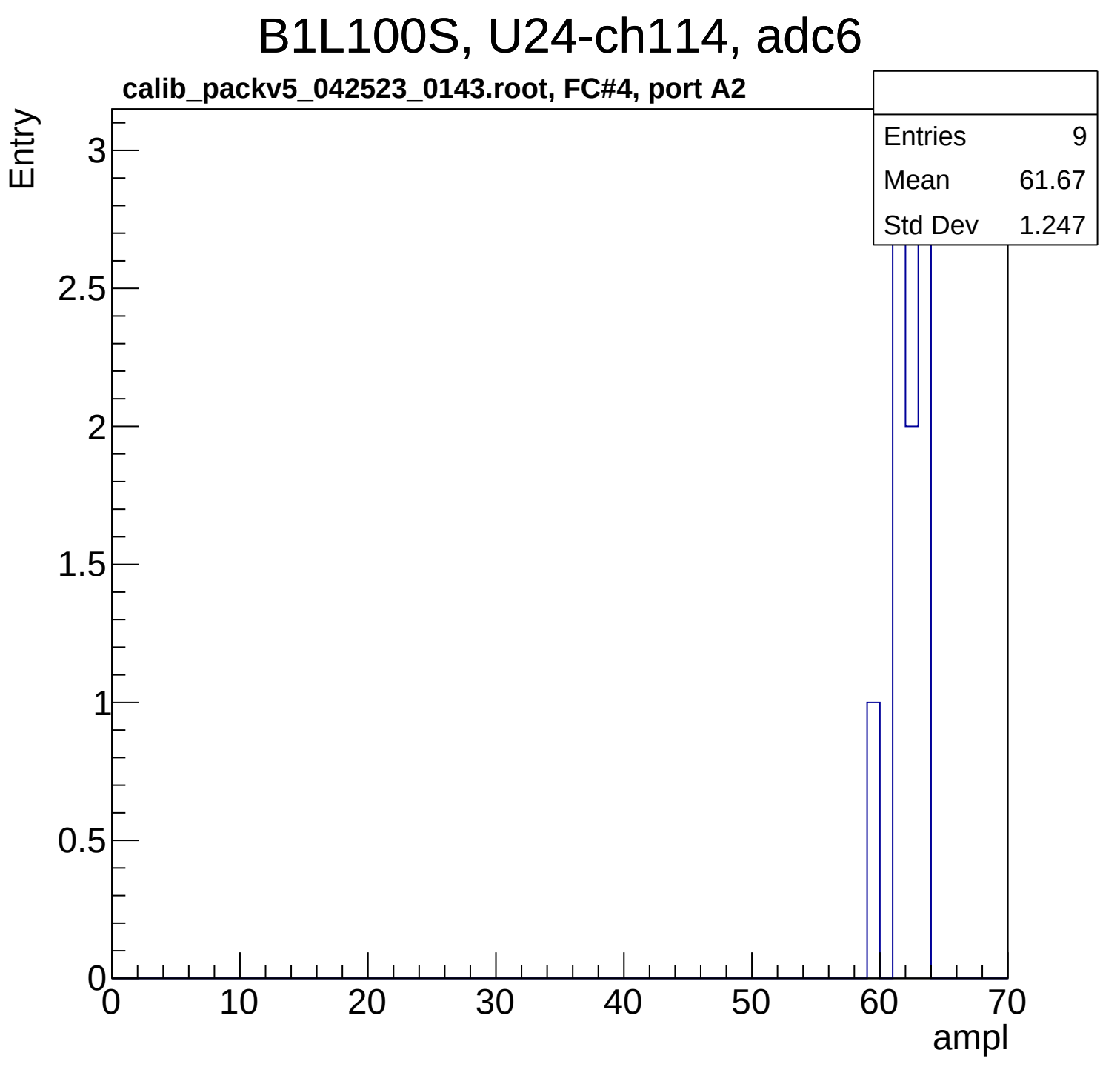
B1L100S, U24-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.67
Std Dev 1.247
Entry
ampl
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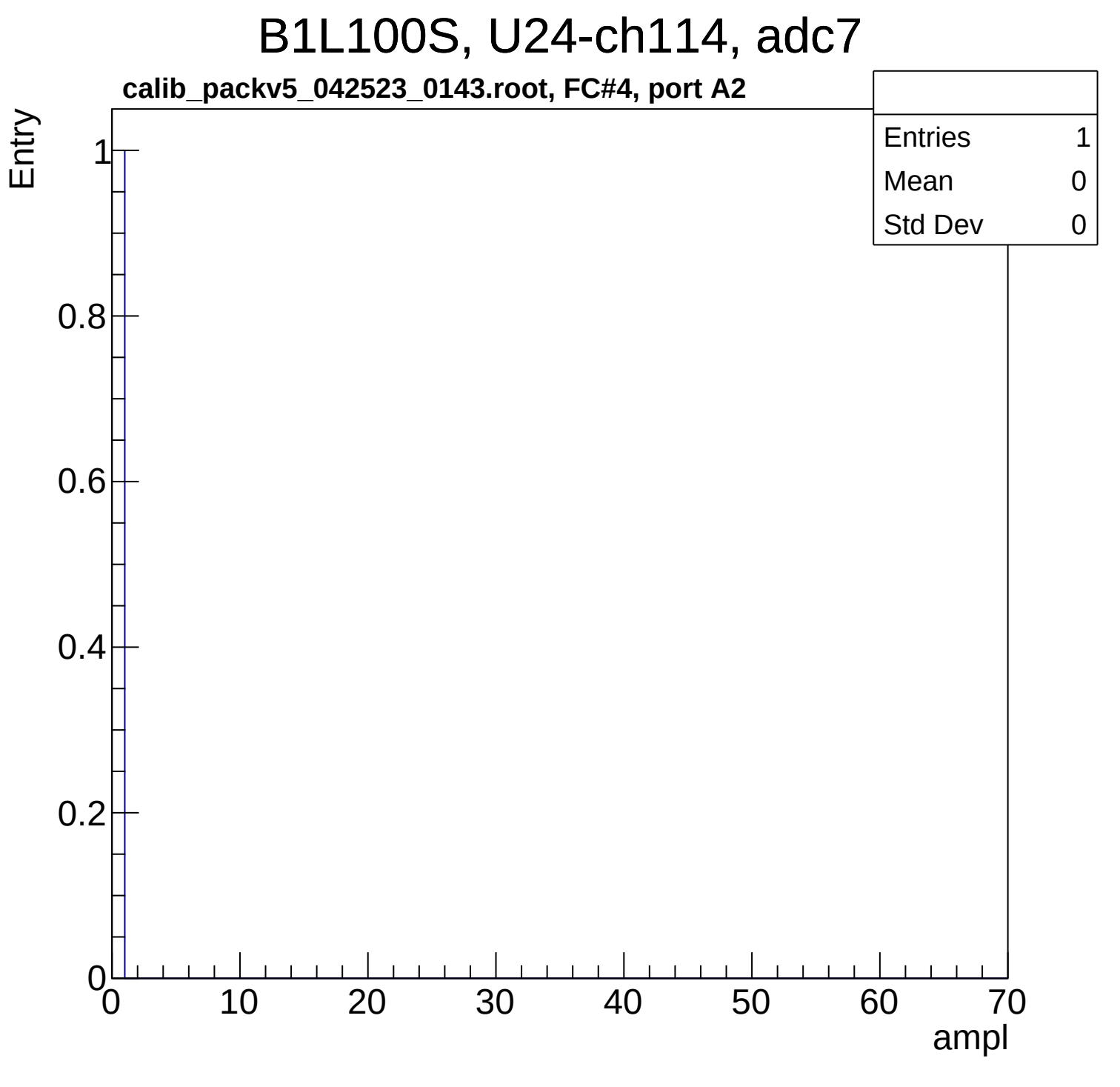
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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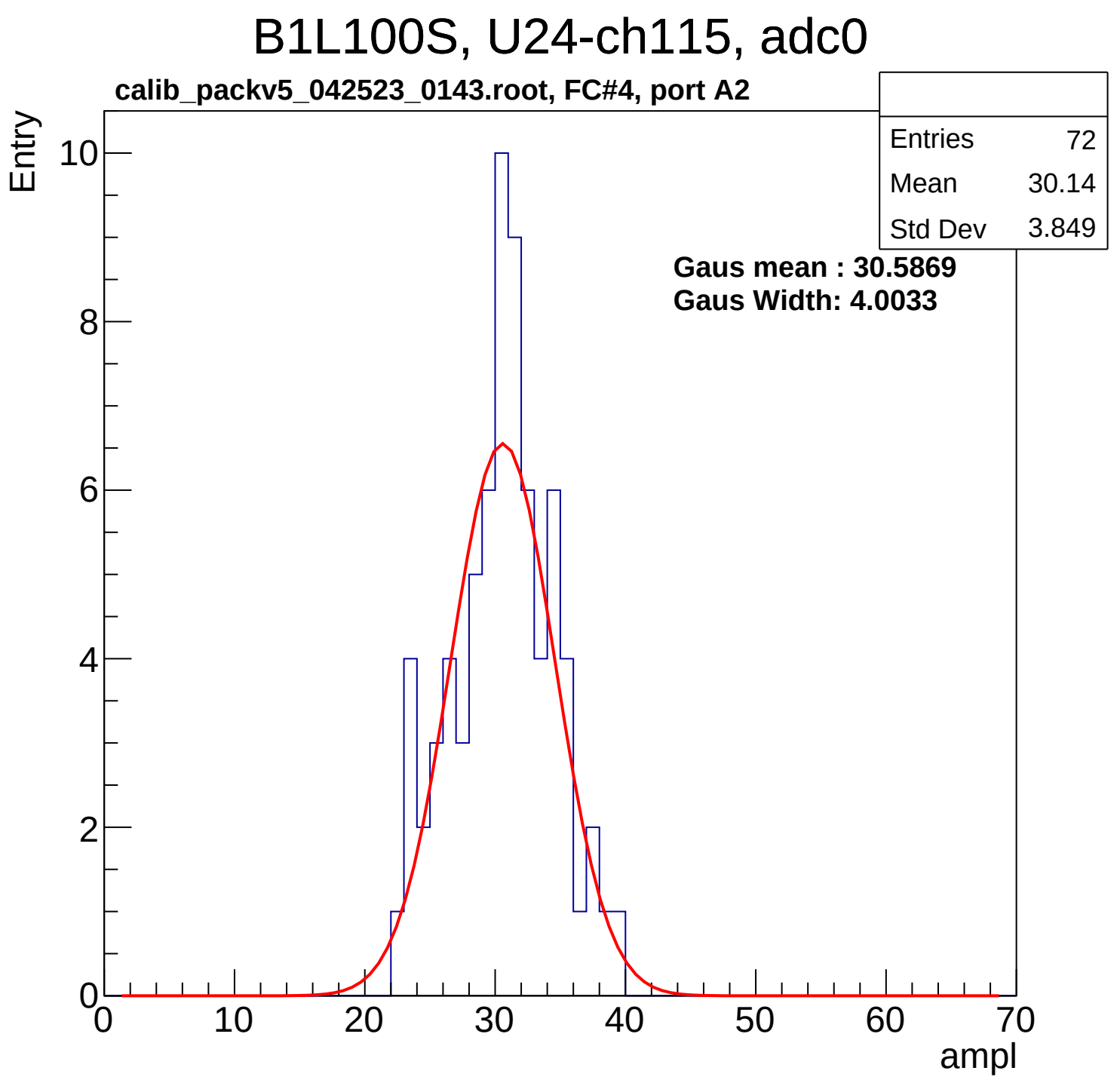
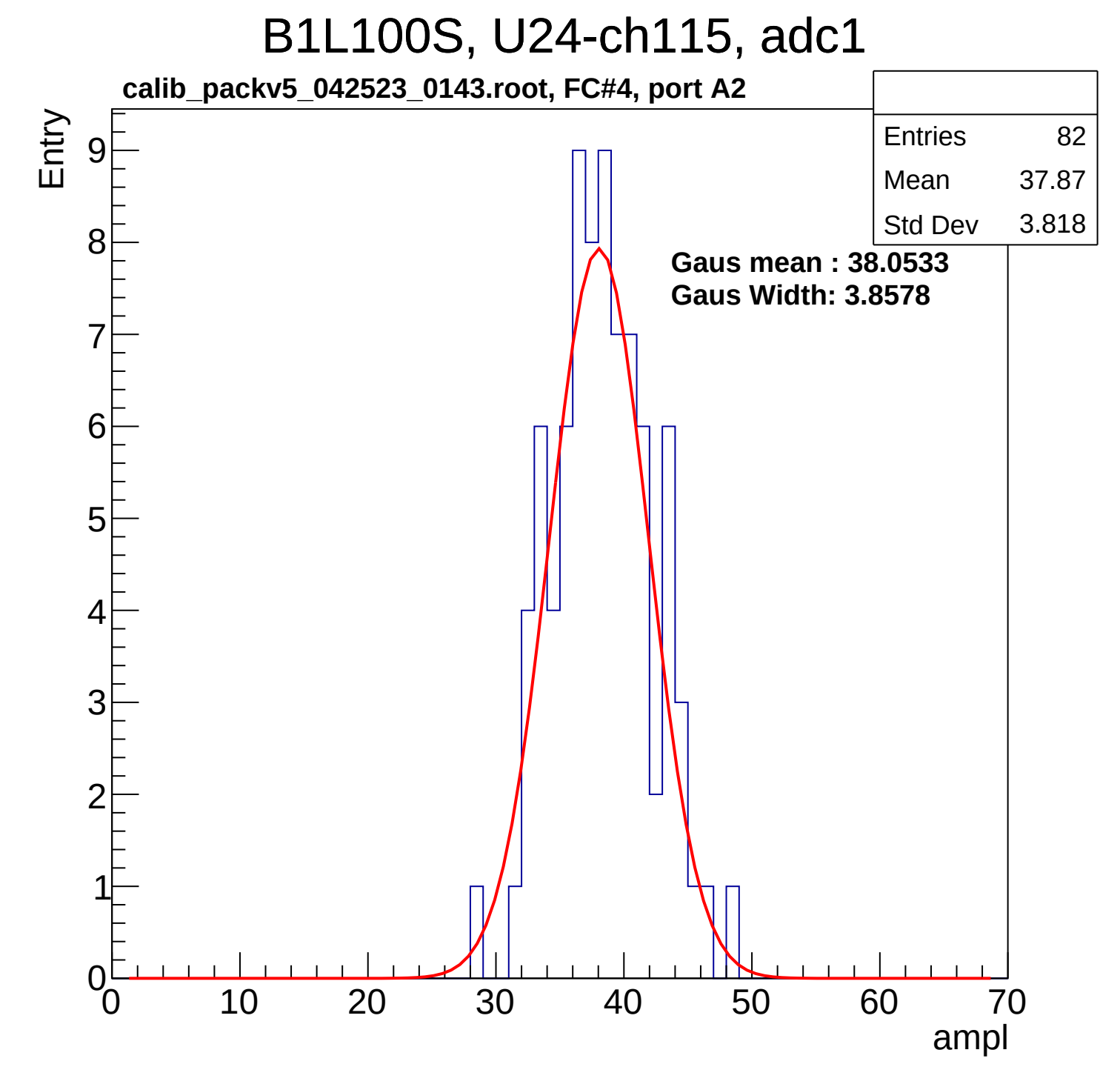
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch115, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.14
Std Dev 3.849
Gaus mean : 30.5869
Gaus Width: 4.0033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 37.87
Std Dev 3.818
Gaus mean : 38.0533
Gaus Width: 3.8578
Entry
ampl
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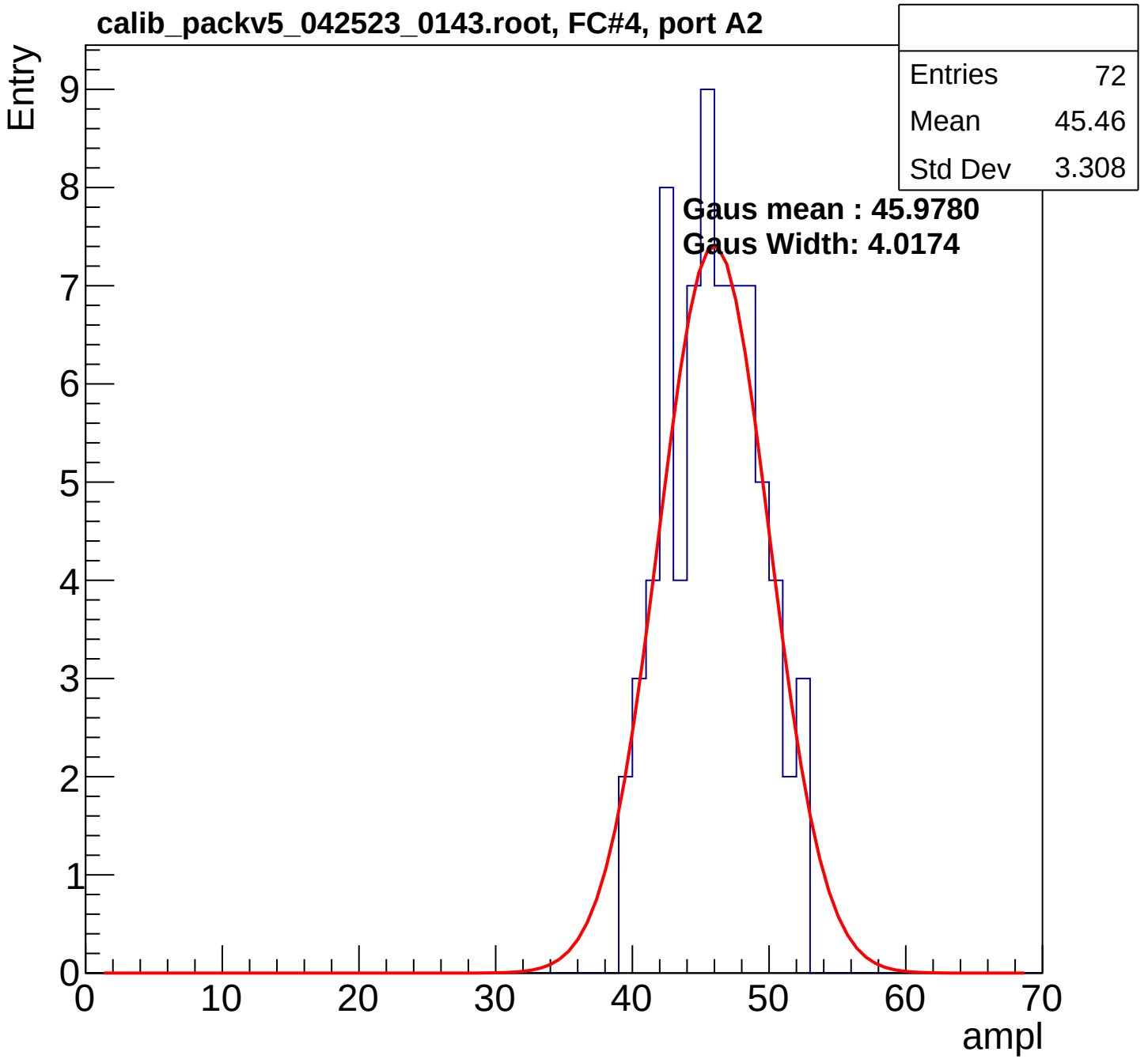
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 45.46
Std Dev 3.308
Gaus mean : 45.9780
Gaus Width: 4.0174
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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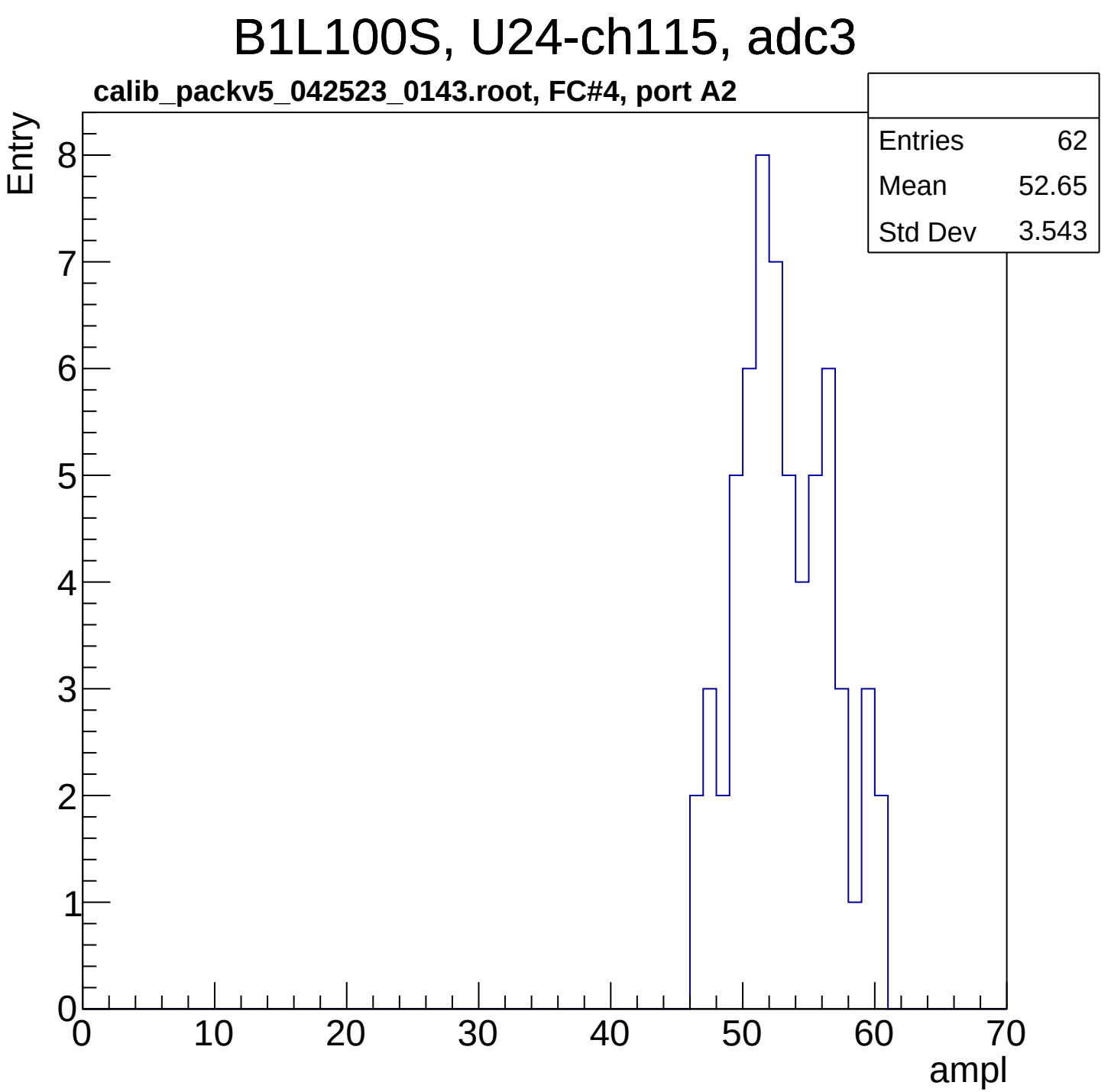
B1L100S, U24-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 52.65
Std Dev 3.543
Entry
ampl
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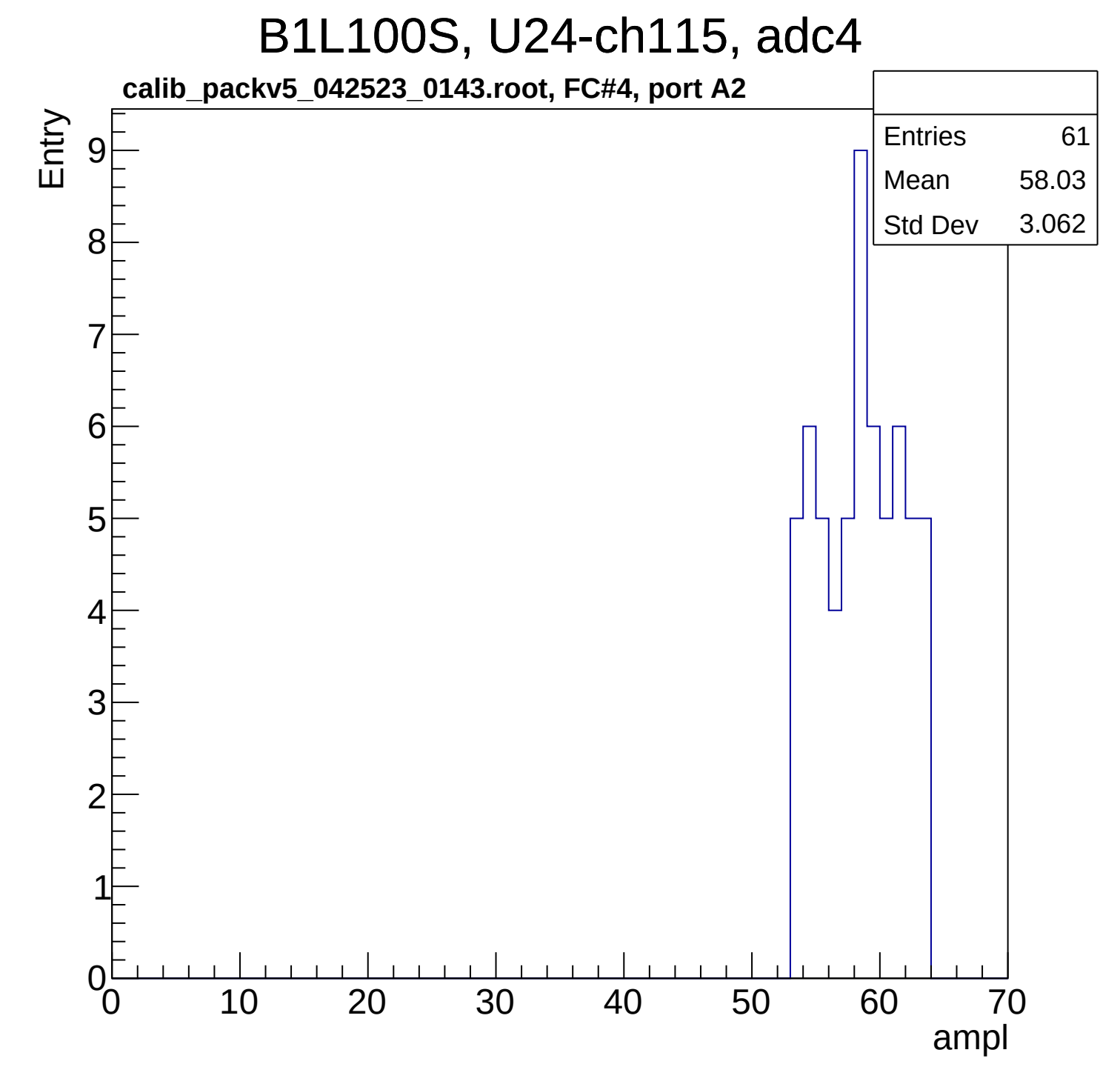
B1L100S, U24-ch115, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 58.03
Std Dev 3.062
Entry
ampl
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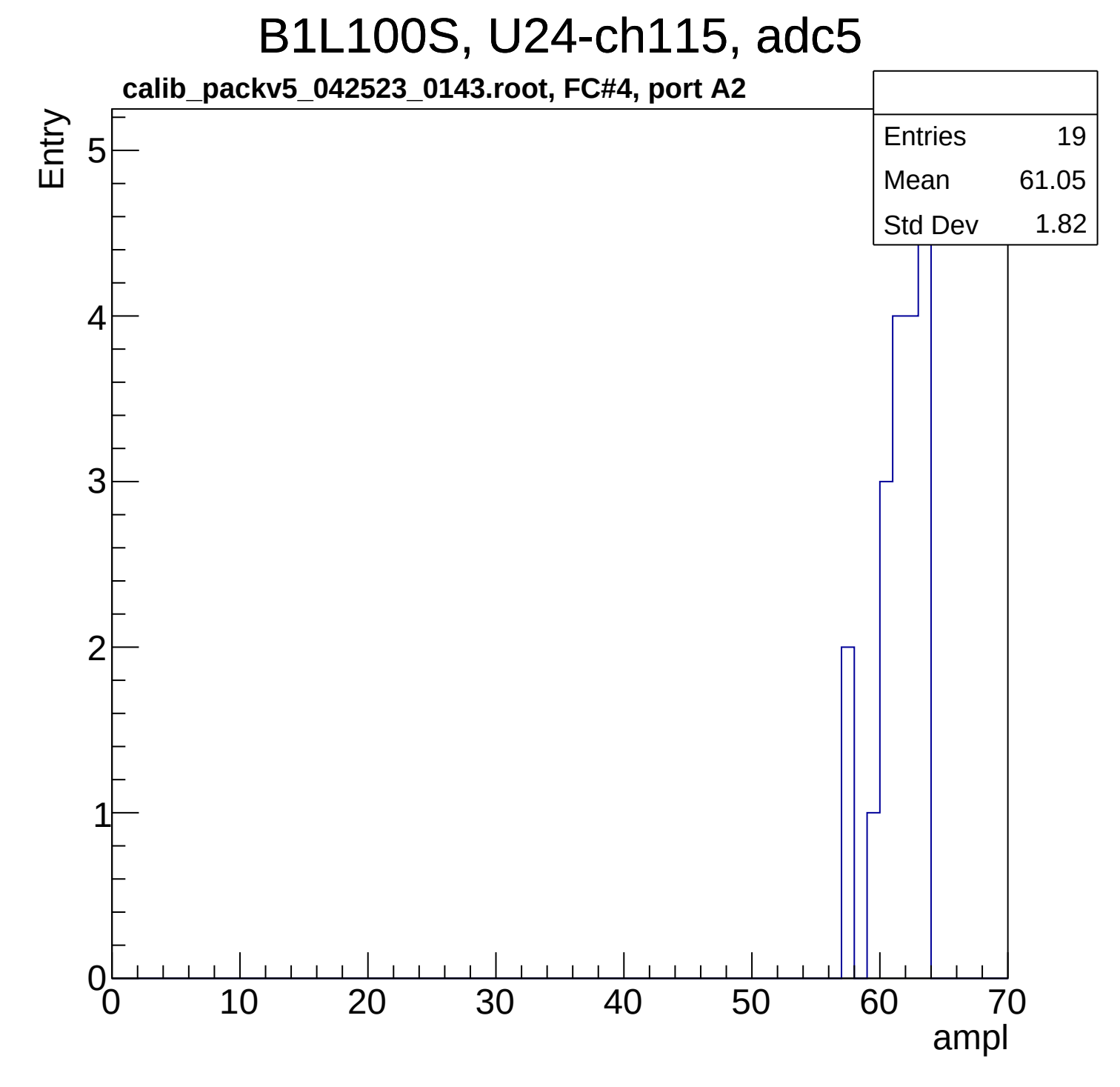
B1L100S, U24-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 19
Mean 61.05
Std Dev 1.82
Entry
ampl
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2
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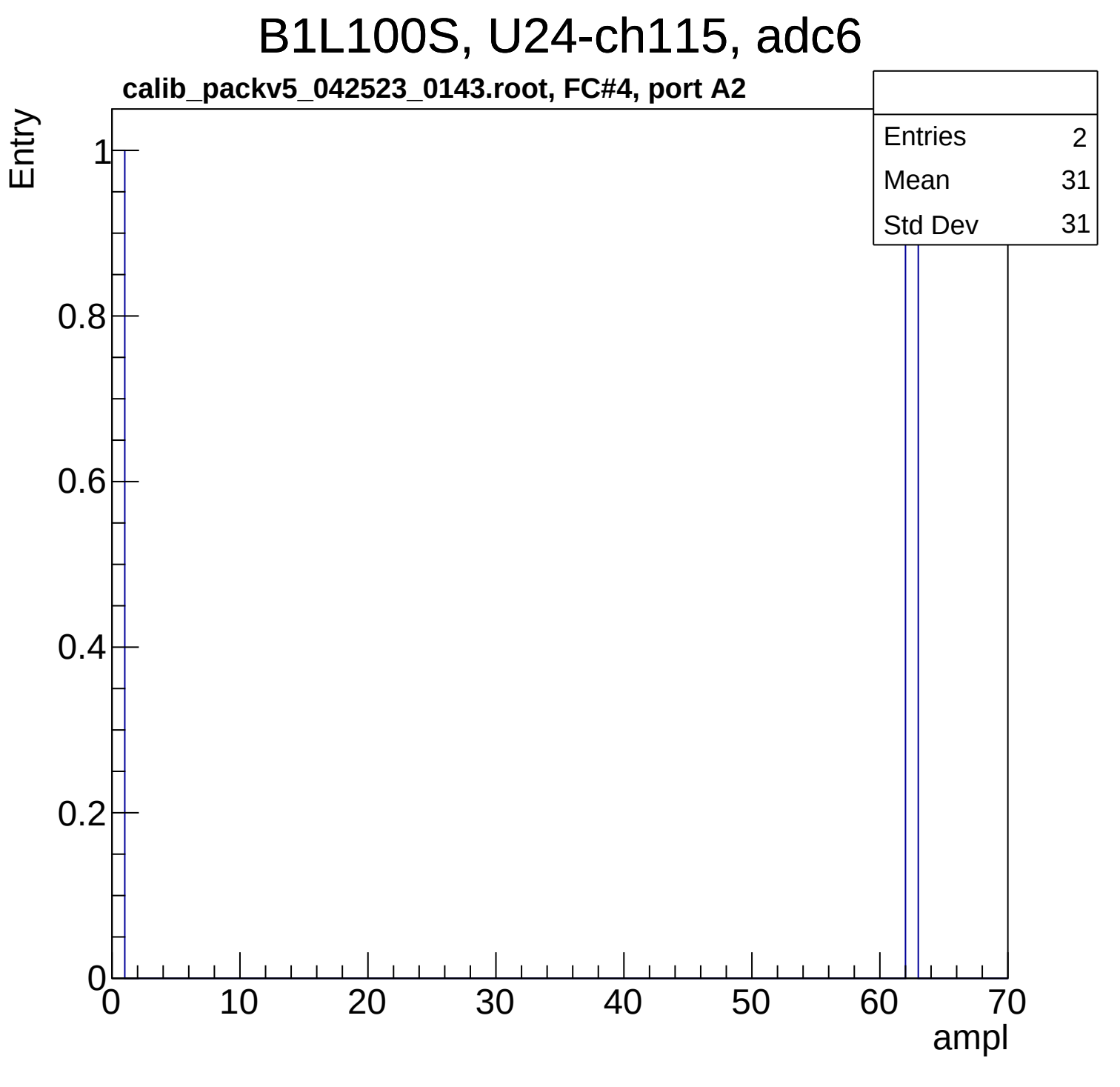
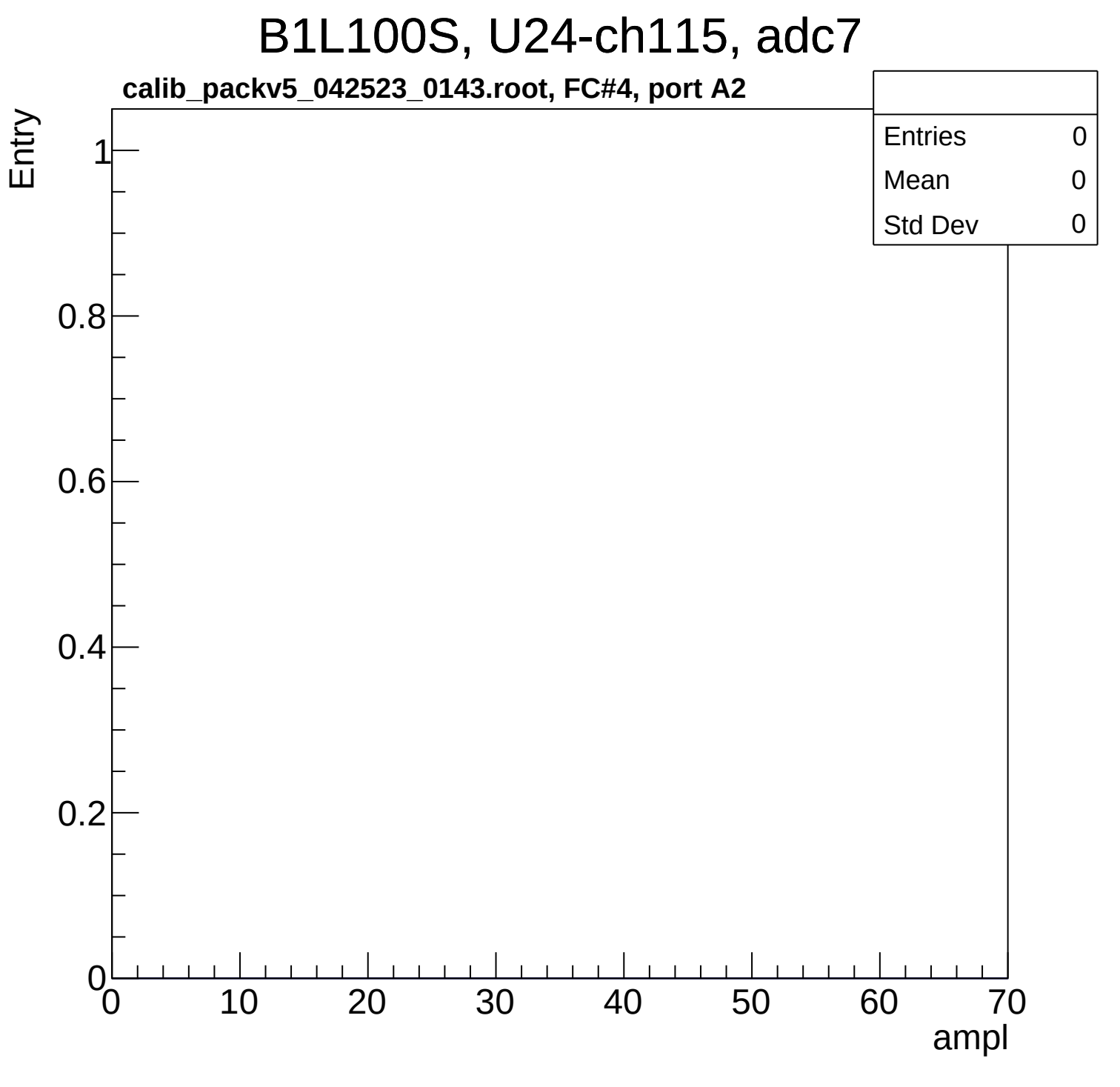
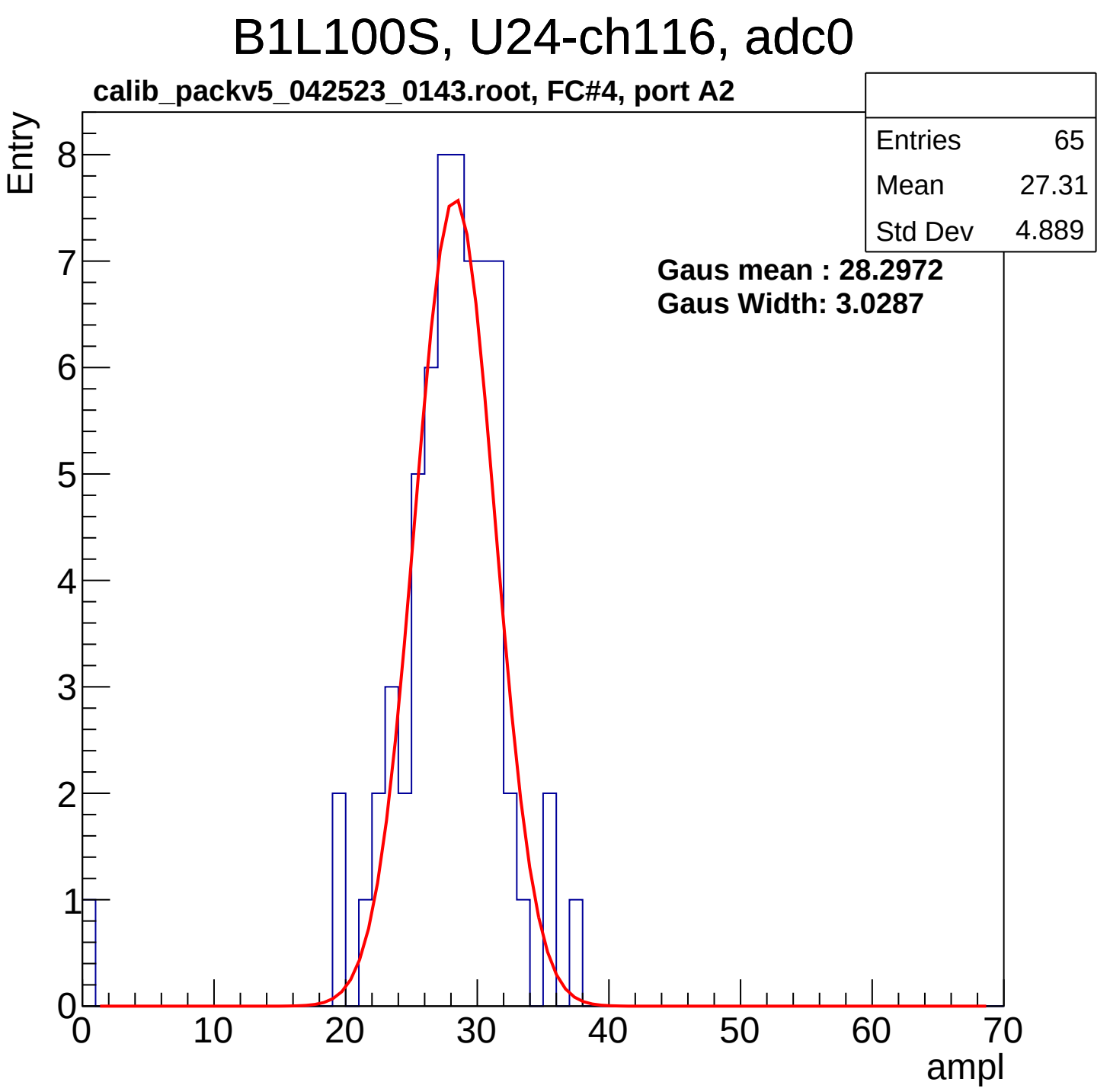
40
50
60
70


B1L100S, U24-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 27.31
Std Dev 4.889
Gaus mean : 28.2972
Gaus Width: 3.0287
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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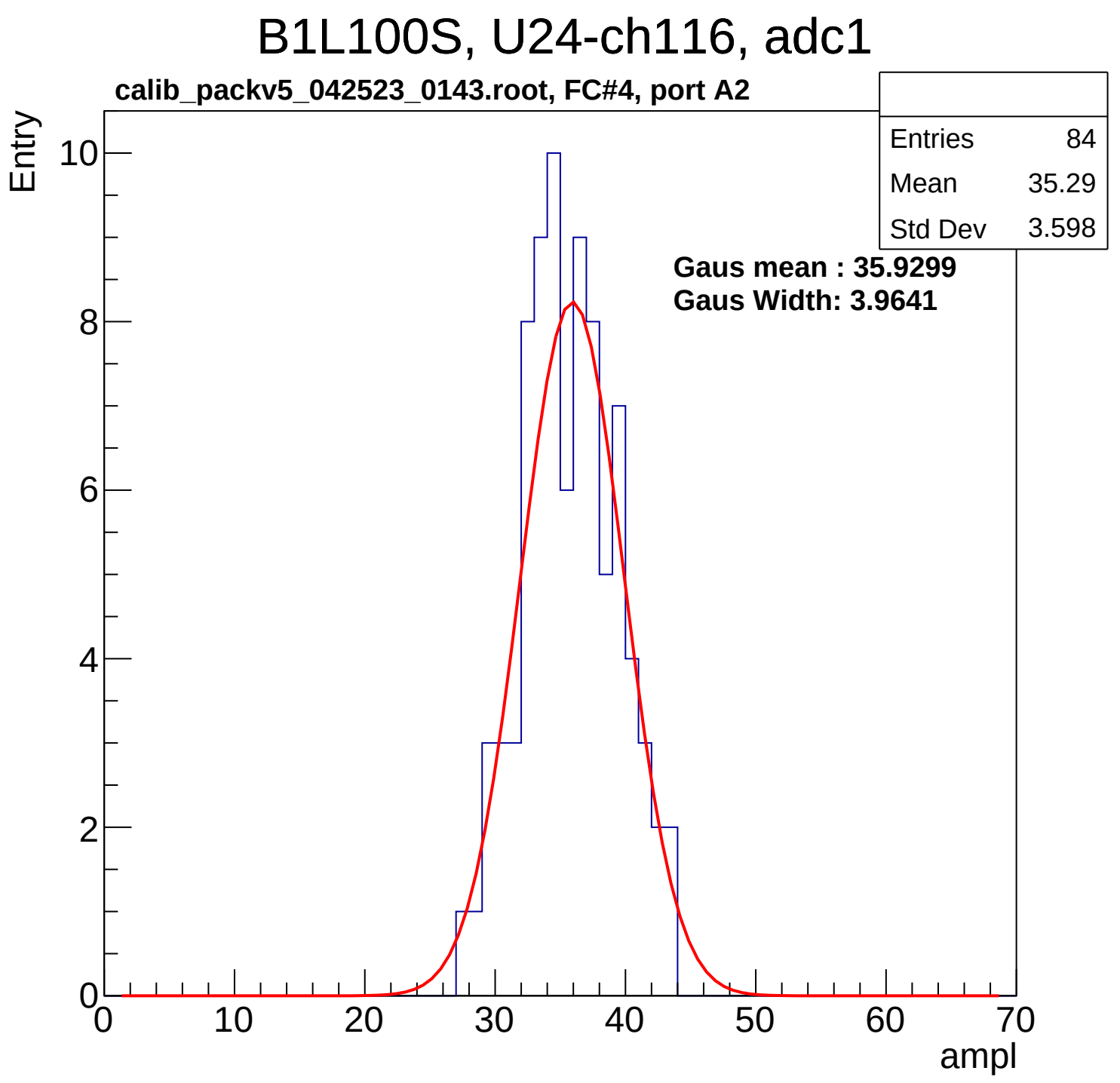
B1L100S, U24-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 35.29
Std Dev 3.598
Gaus mean : 35.9299
Gaus Width: 3.9641
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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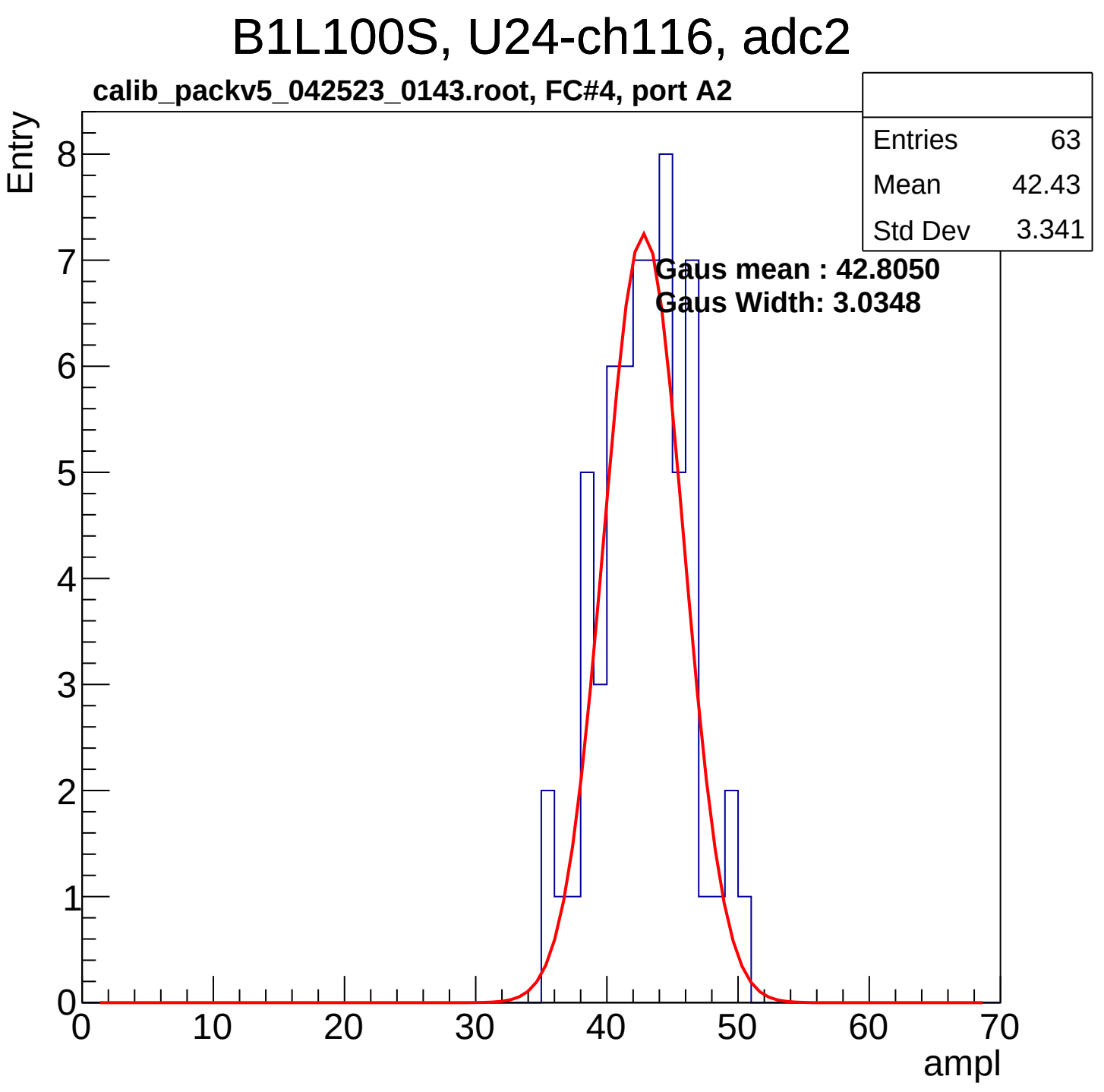
B1L100S, U24-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.43
Std Dev 3.341
Gaus mean : 42.8050
Gaus Width: 3.0348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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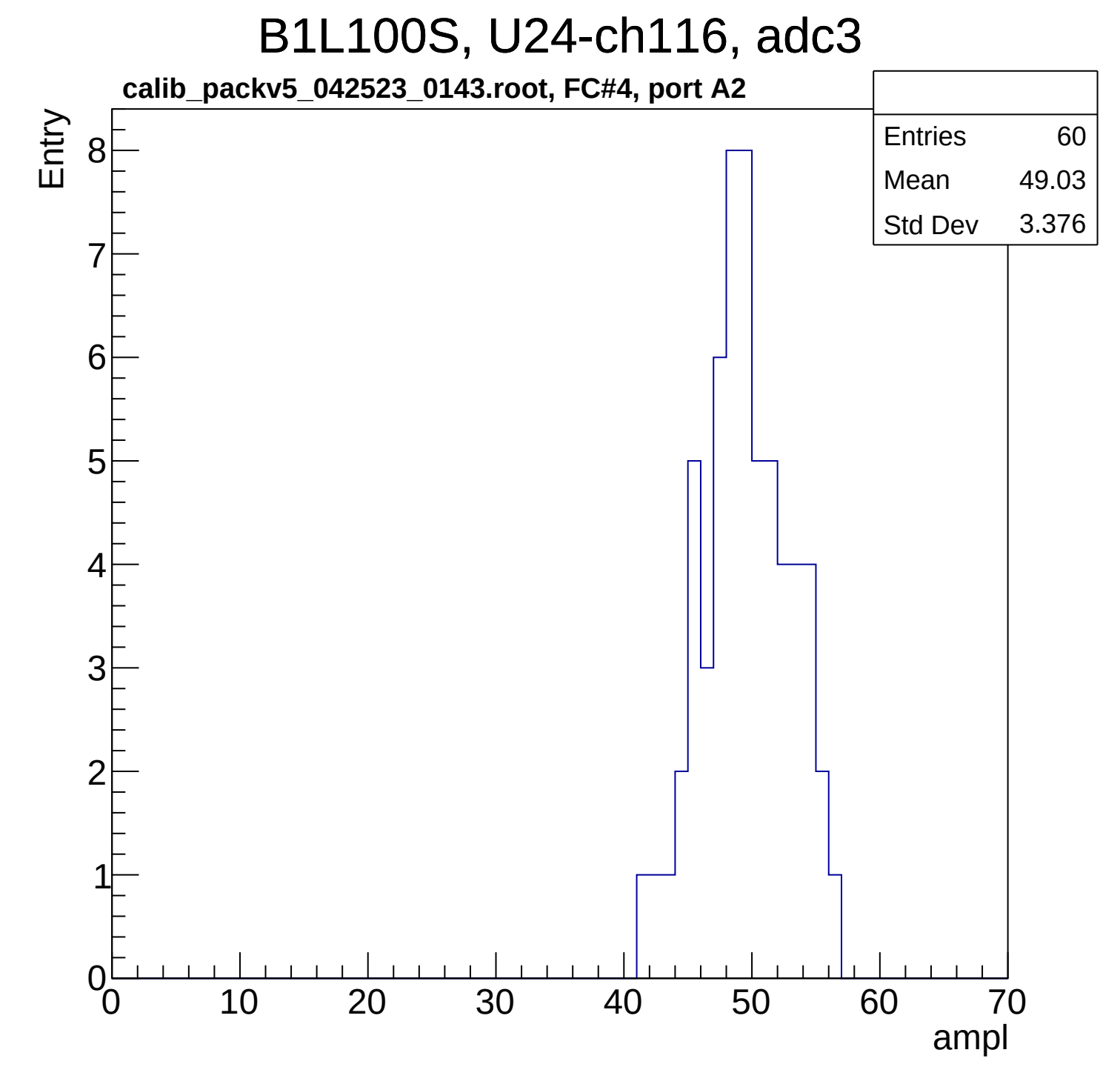
B1L100S, U24-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.03
Std Dev 3.376
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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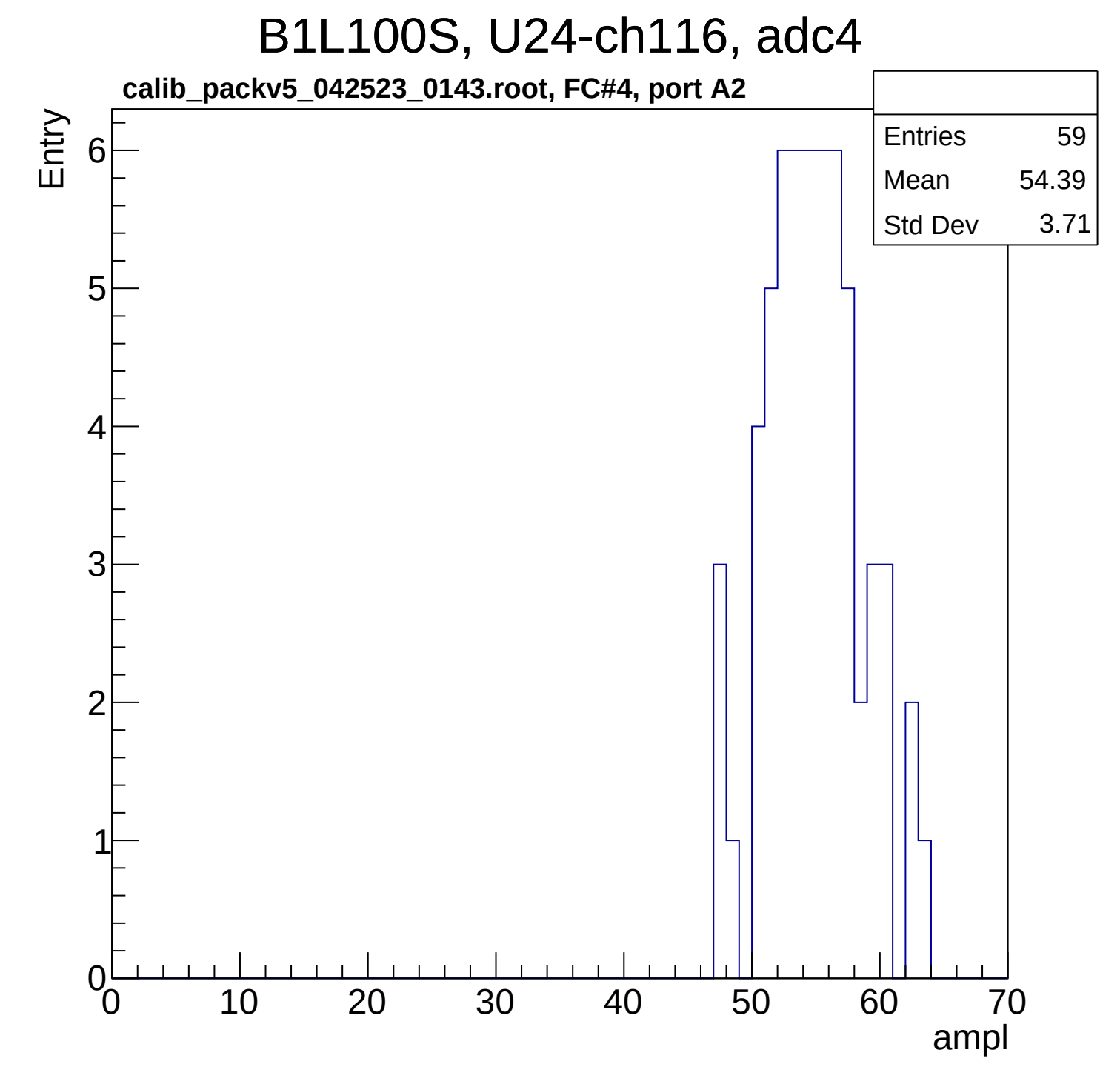
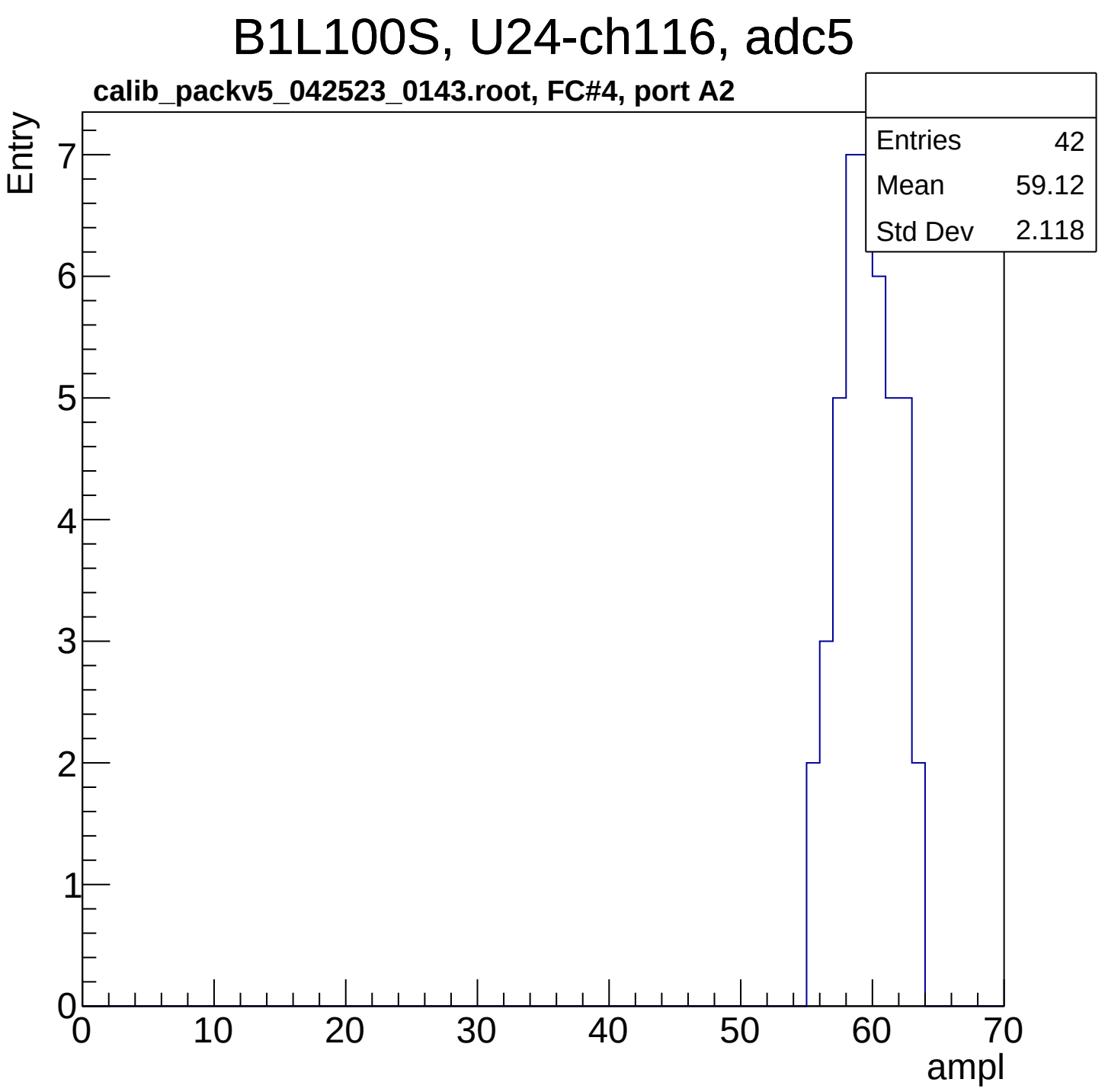
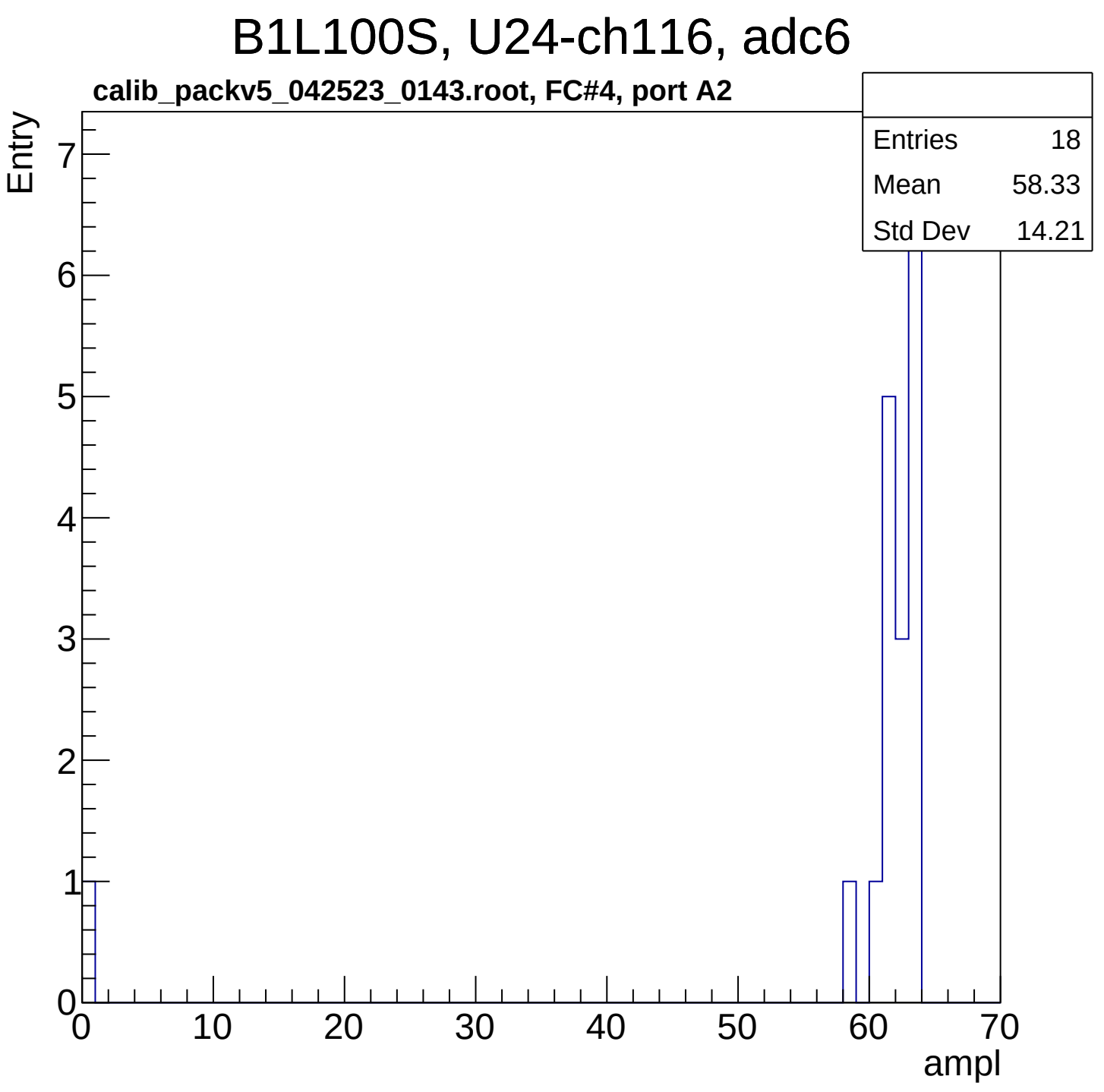
40
50
60
70


B1L100S, U24-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.39
Std Dev 3.71
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U24-ch116, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.12
Std Dev 2.118
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch116, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 18
Mean 58.33
Std Dev 14.21
Entry
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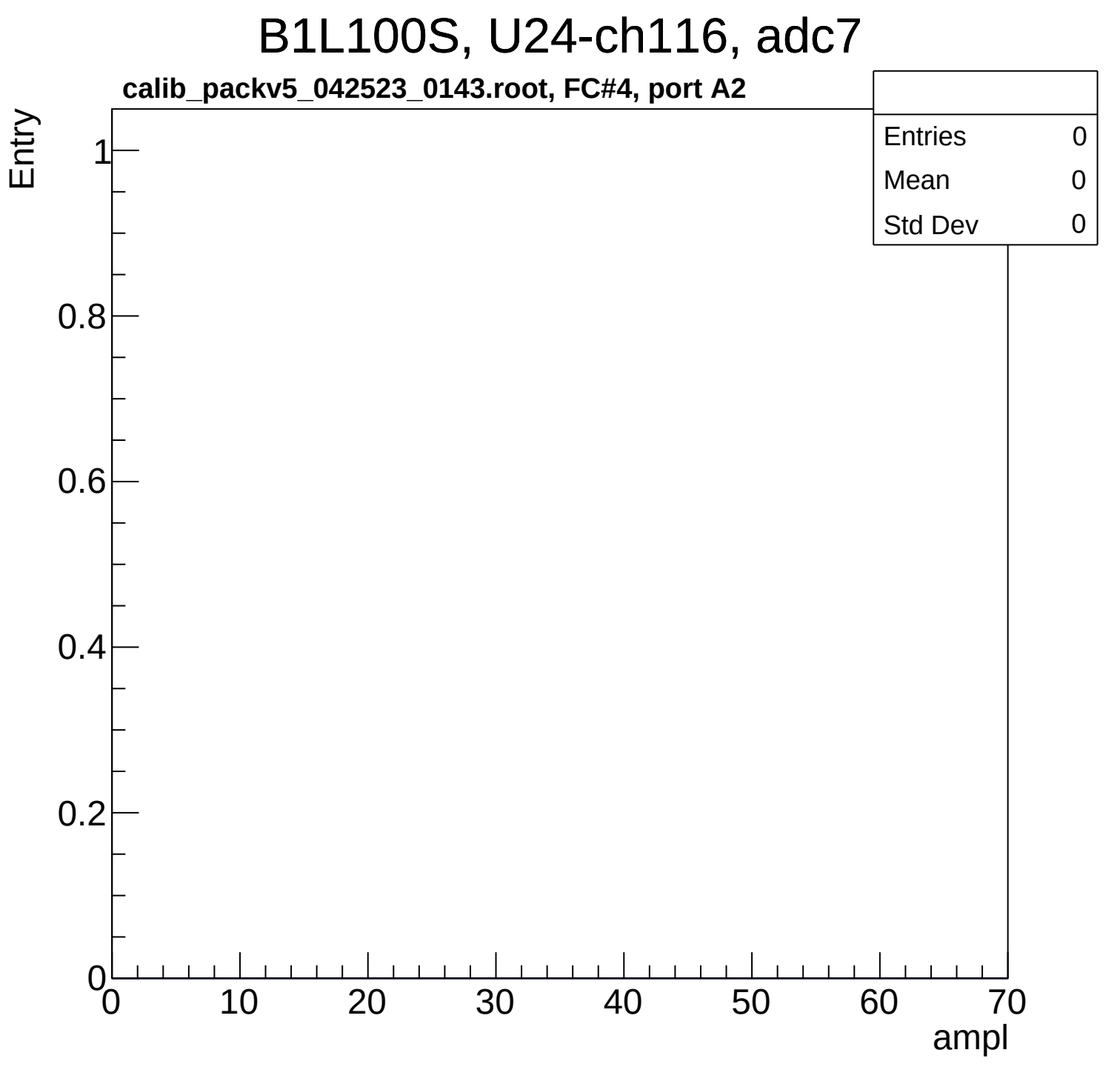
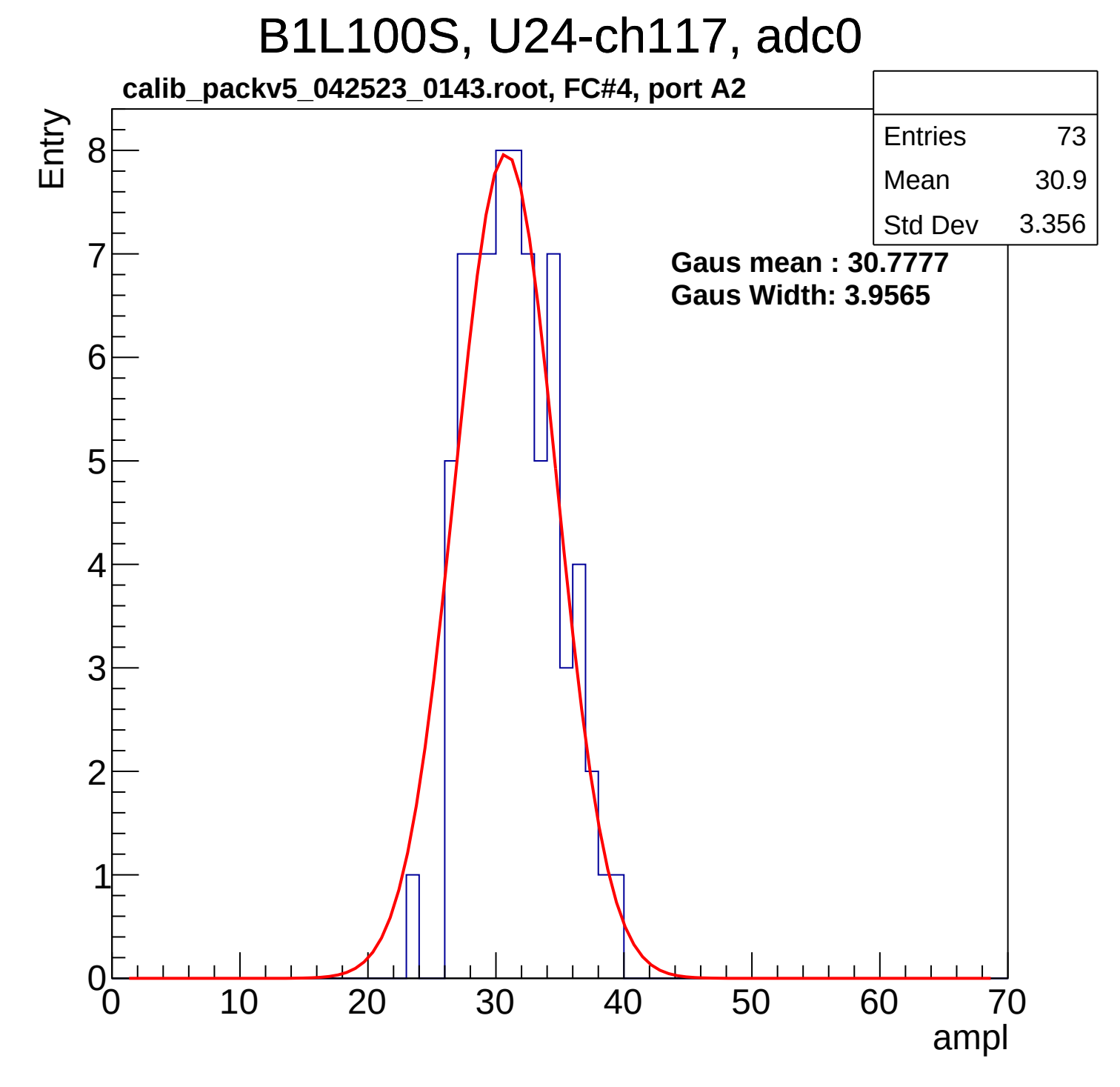
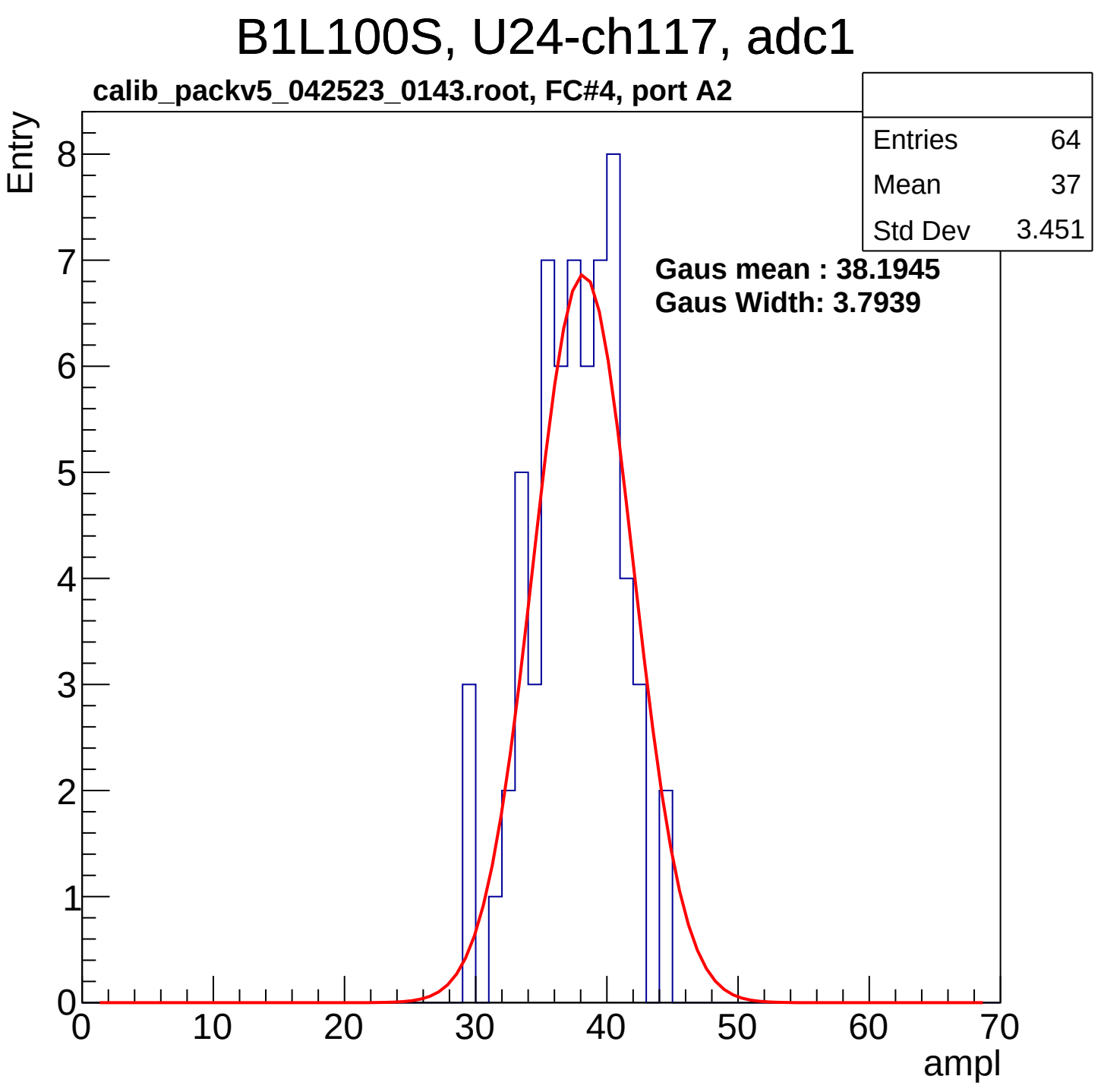
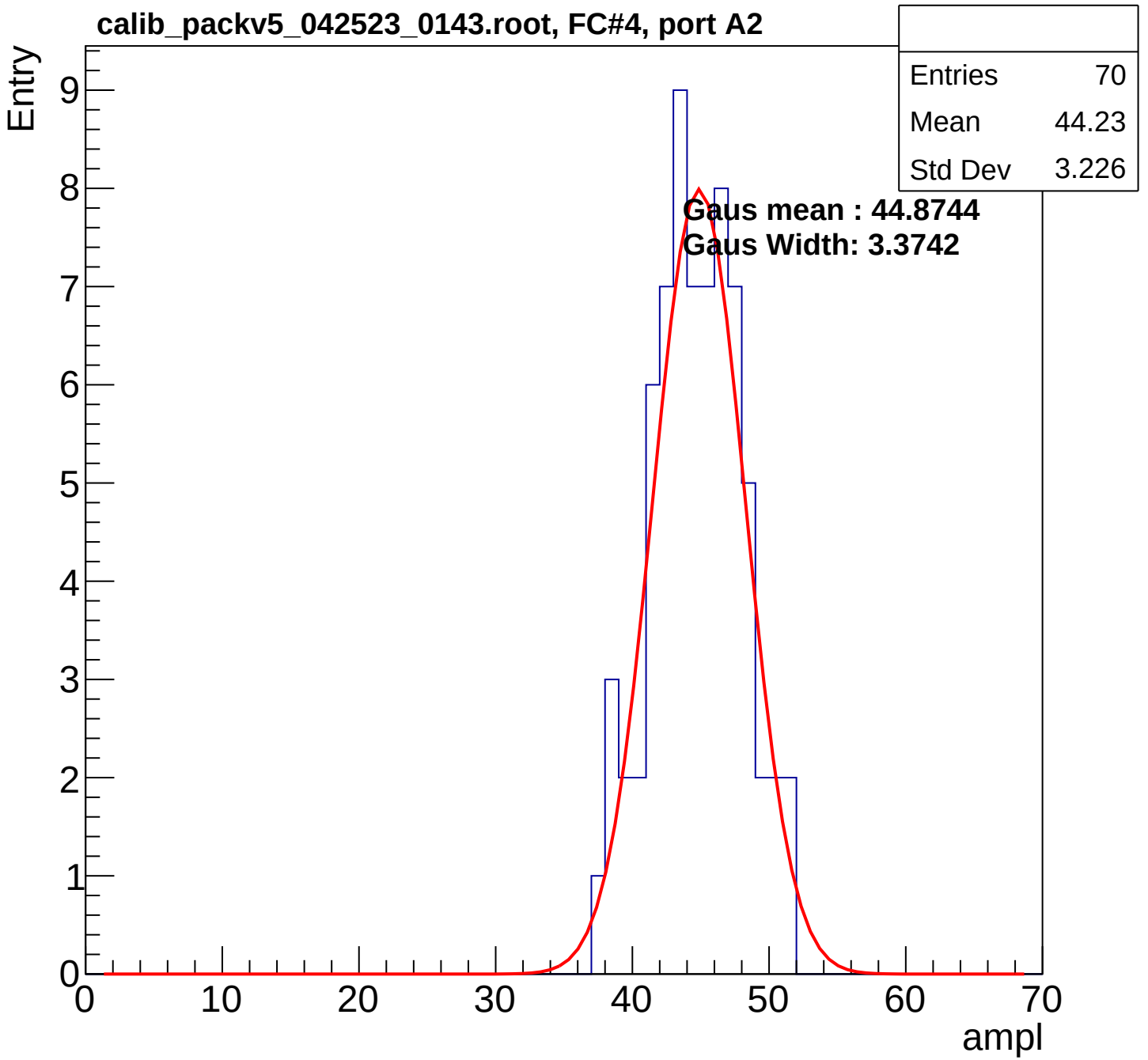
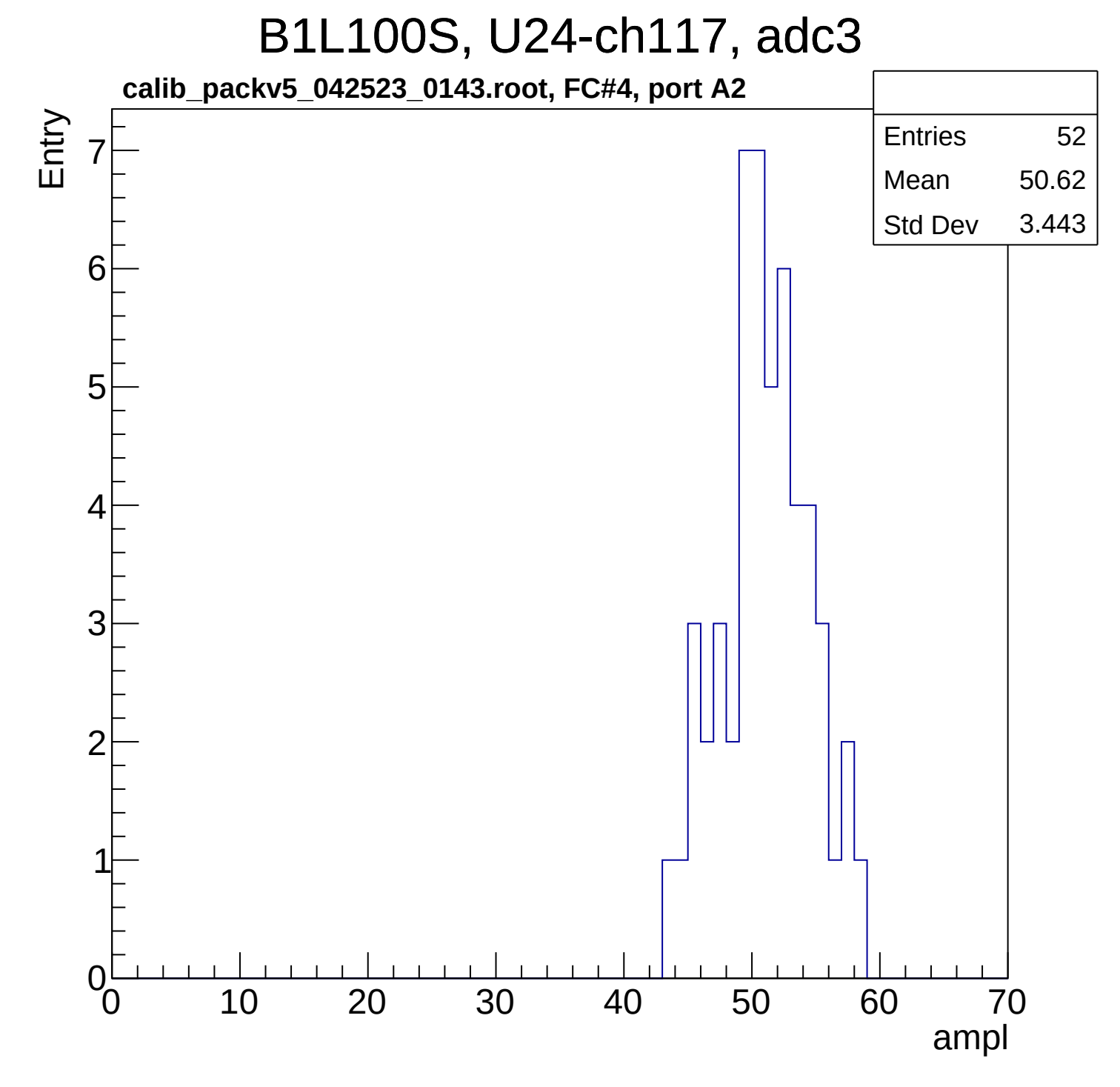
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

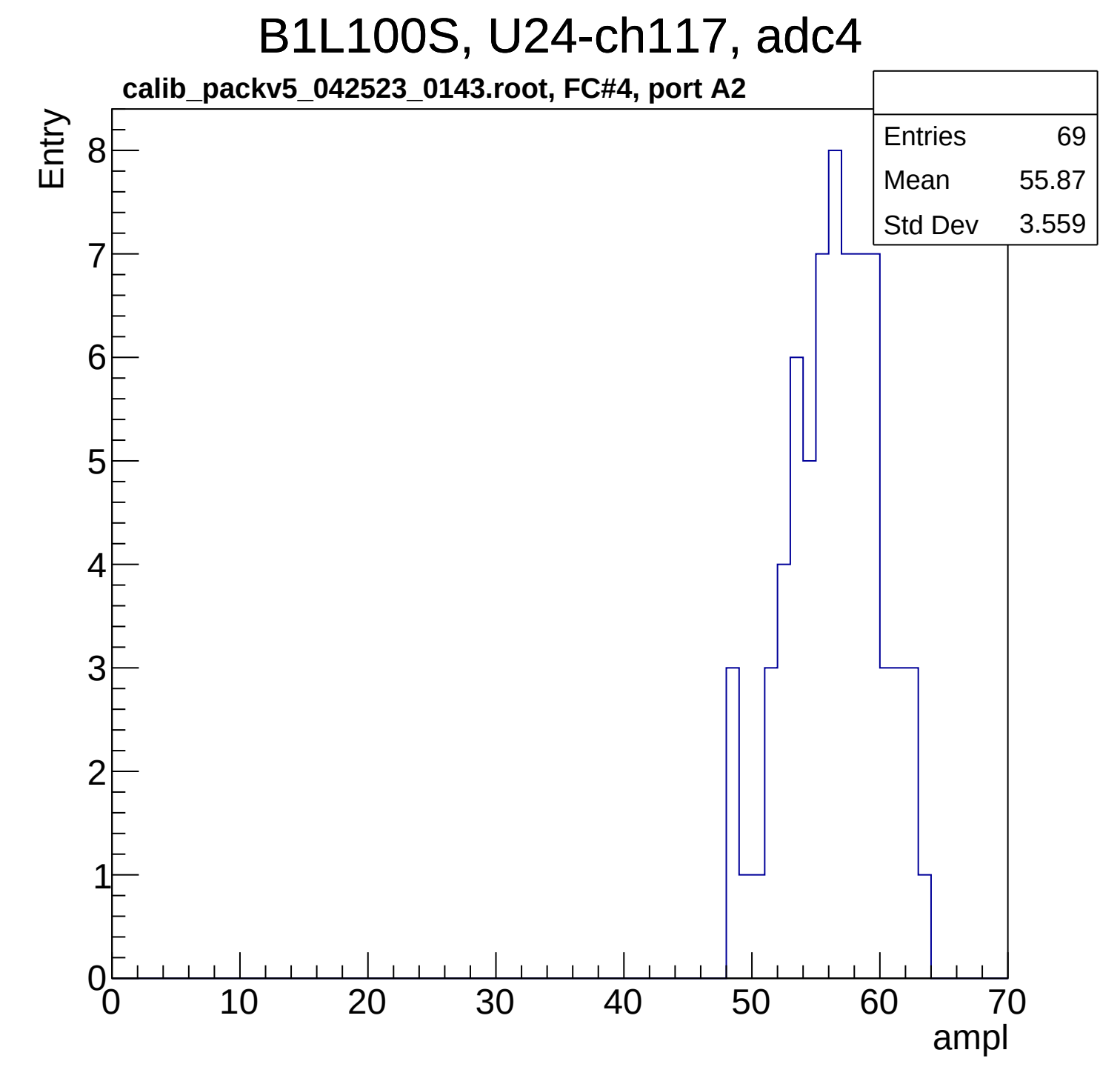

B1L100S, U24-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

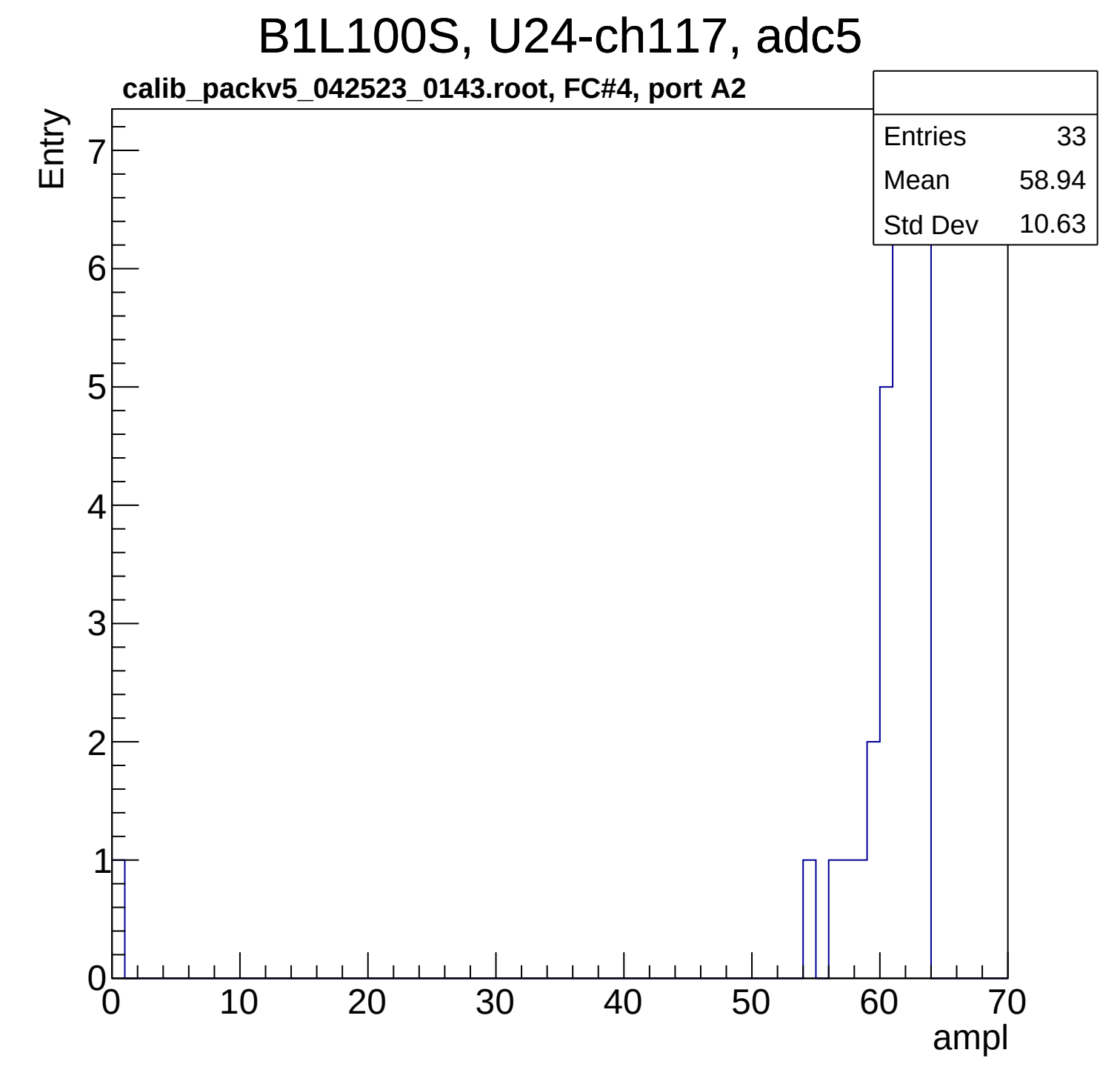

B1L100S, U24-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.9
Std Dev 3.356
Gaus mean : 30.7777
Gaus Width: 3.9565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

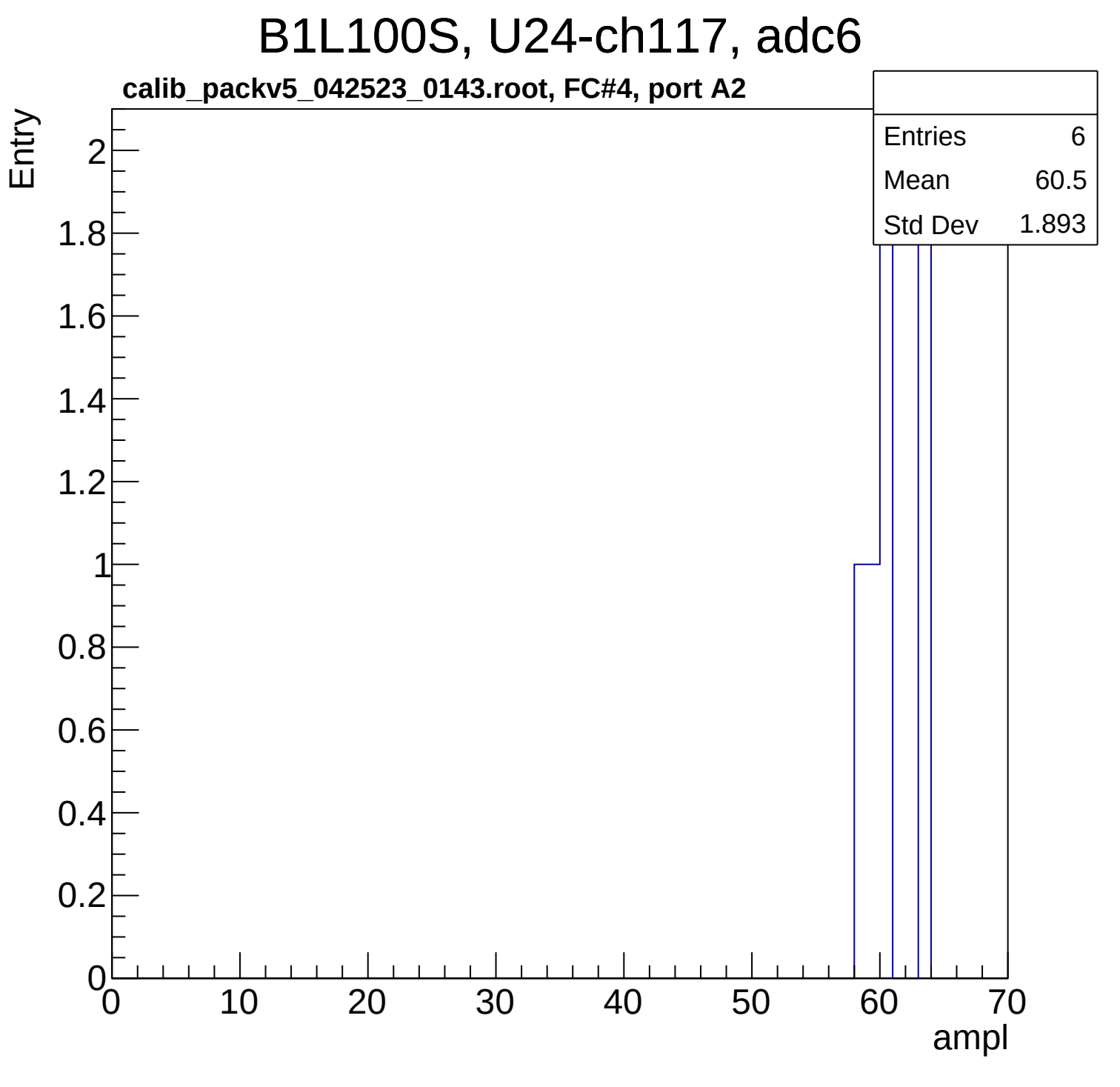

B1L100S, U24-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37
Std Dev 3.451
Gaus mean : 38.1945
Gaus Width: 3.7939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

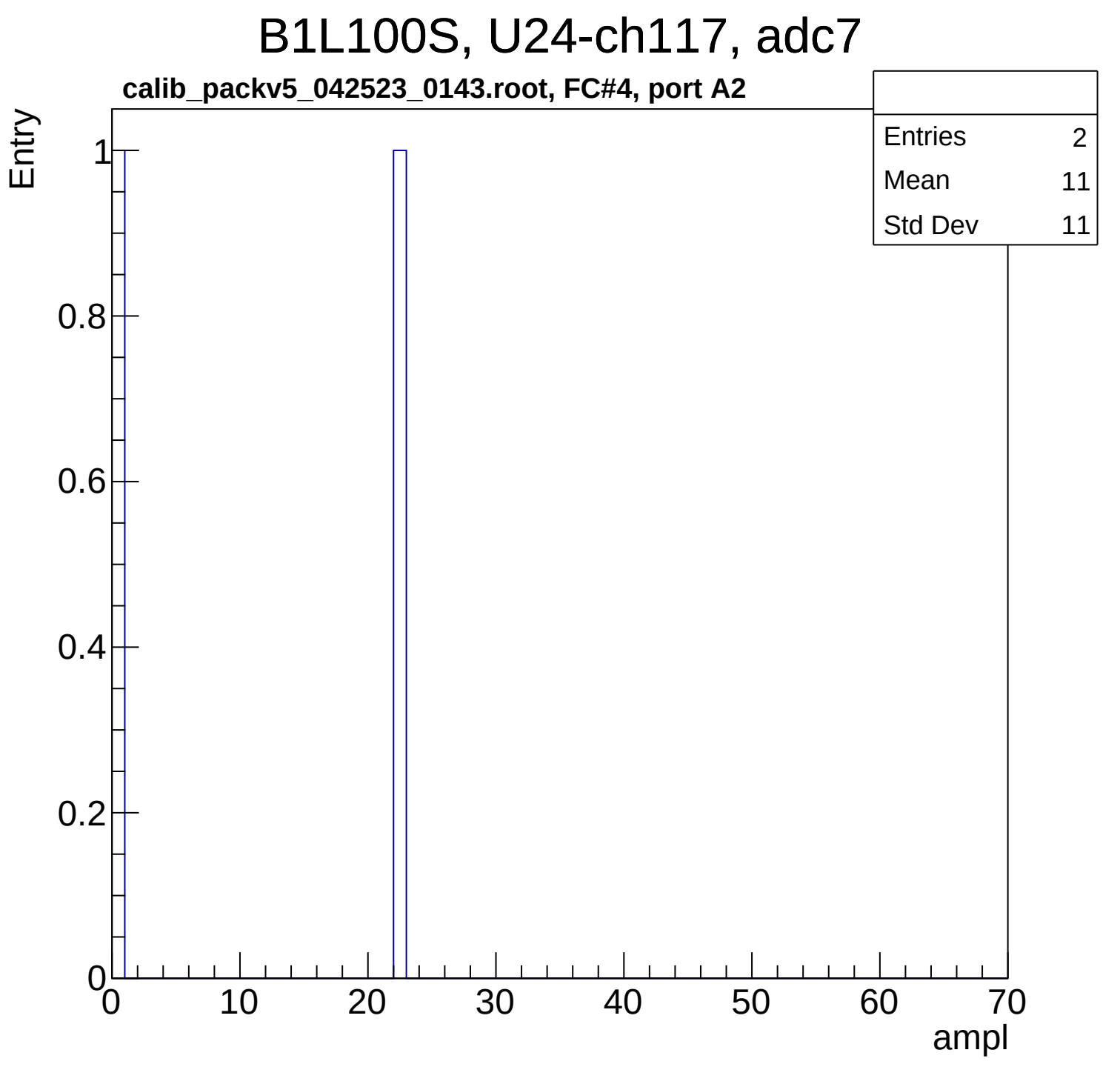

B1L100S, U24-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.23
Std Dev 3.226
Gaus mean : 44.8744
Gaus Width: 3.3742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

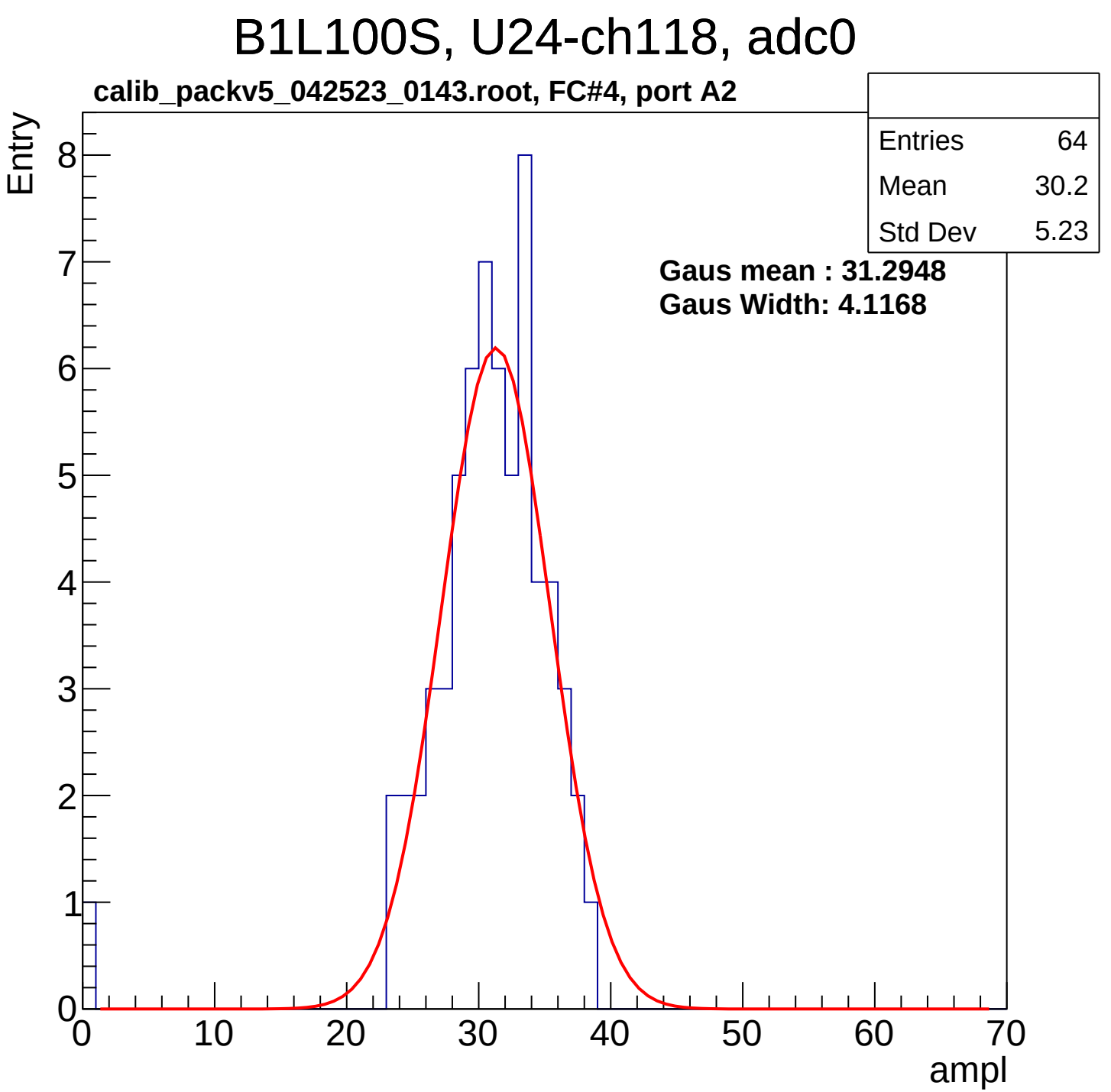

B1L100S, U24-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 50.62
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

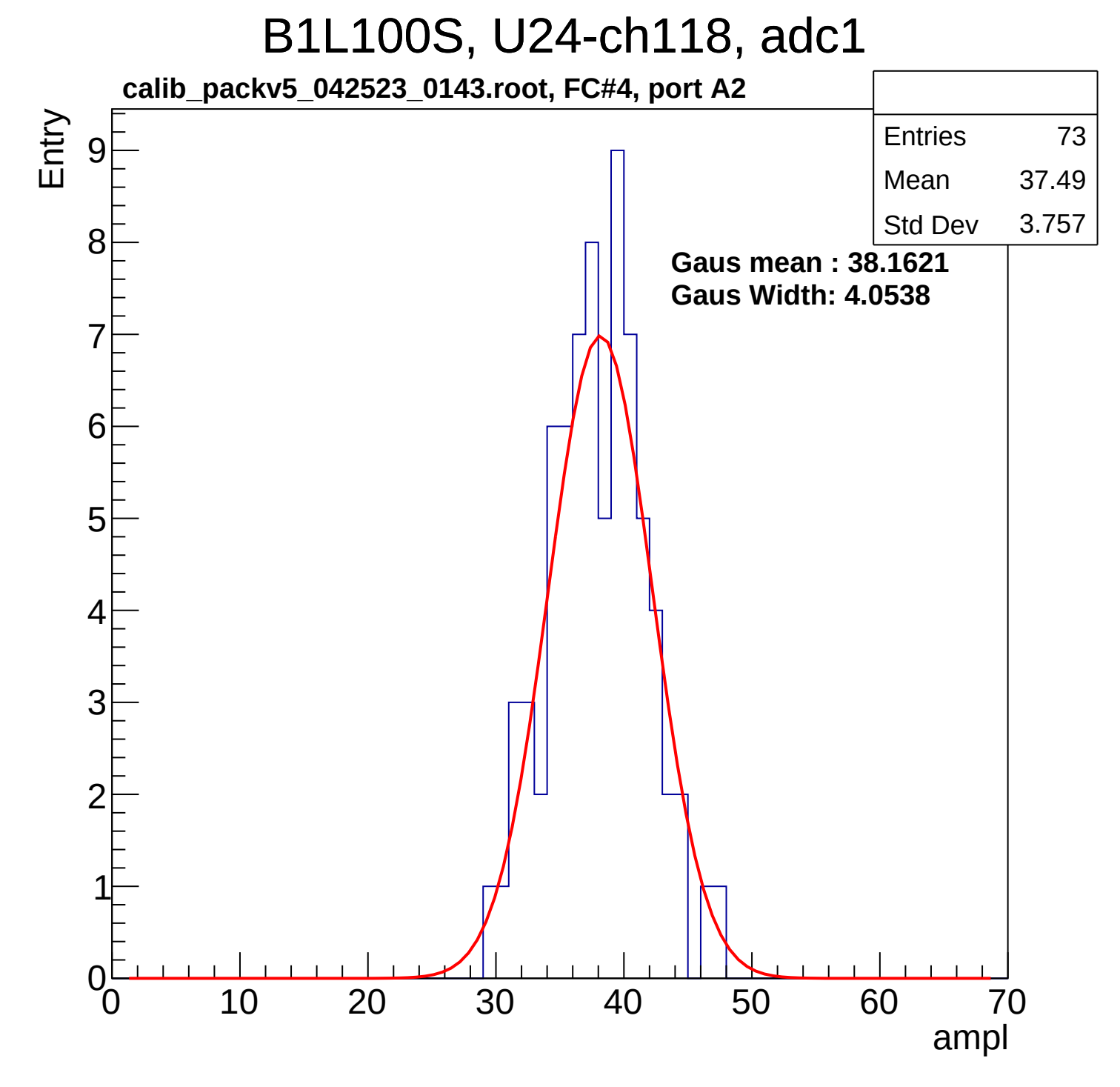

B1L100S, U24-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.87
Std Dev 3.559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

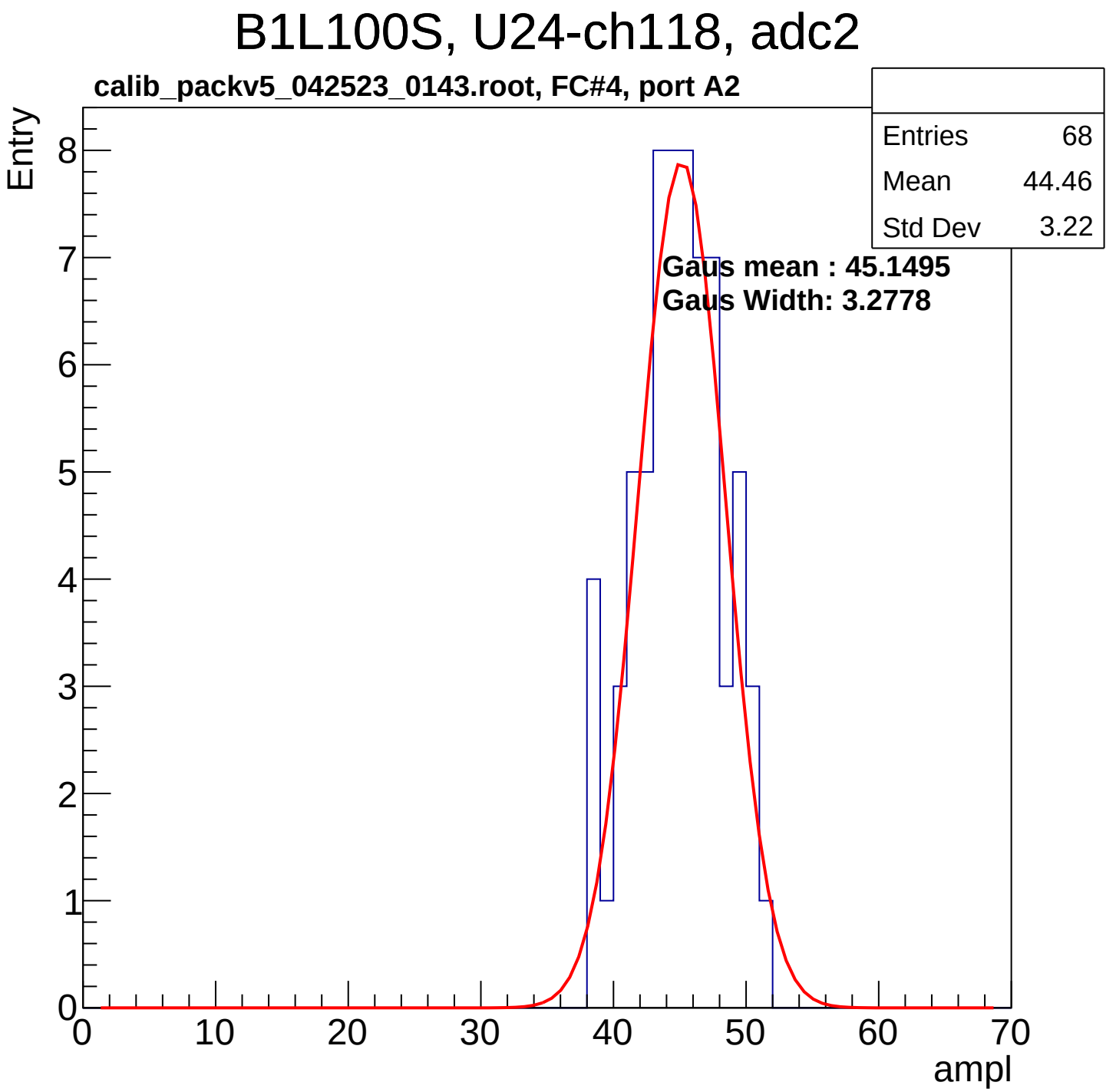

B1L100S, U24-ch117, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.94
Std Dev 10.63
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

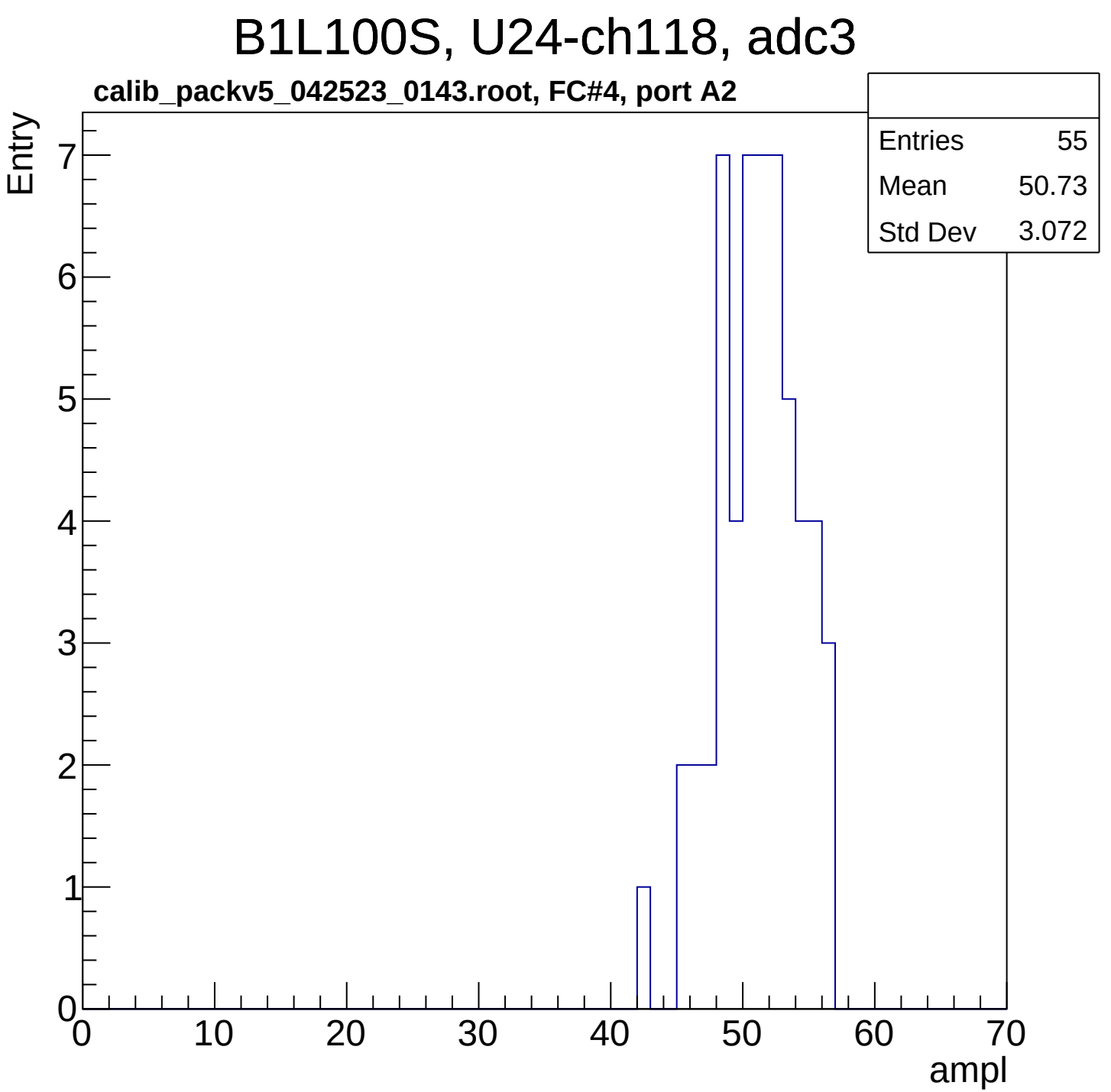

B1L100S, U24-ch117, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 60.5
Std Dev 1.893
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.2
Std Dev 5.23
Gaus mean : 31.2948
Gaus Width: 4.1168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.49
Std Dev 3.757
Gaus mean : 38.1621
Gaus Width: 4.0538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 44.46
Std Dev 3.22
Gaus mean : 45.1495
Gaus Width: 3.2778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.73
Std Dev 3.072
Entry
ampl
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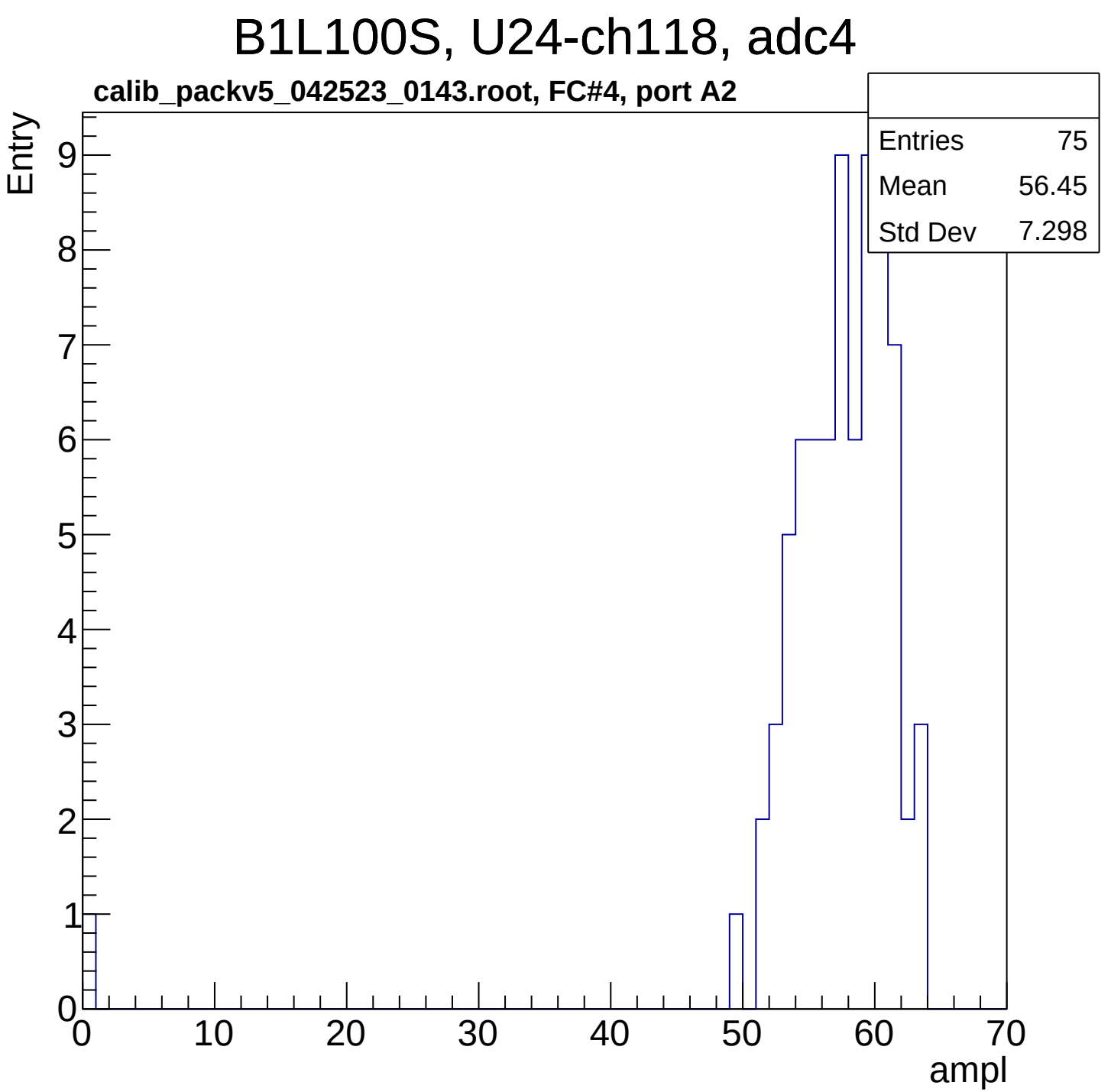
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 56.45
Std Dev 7.298
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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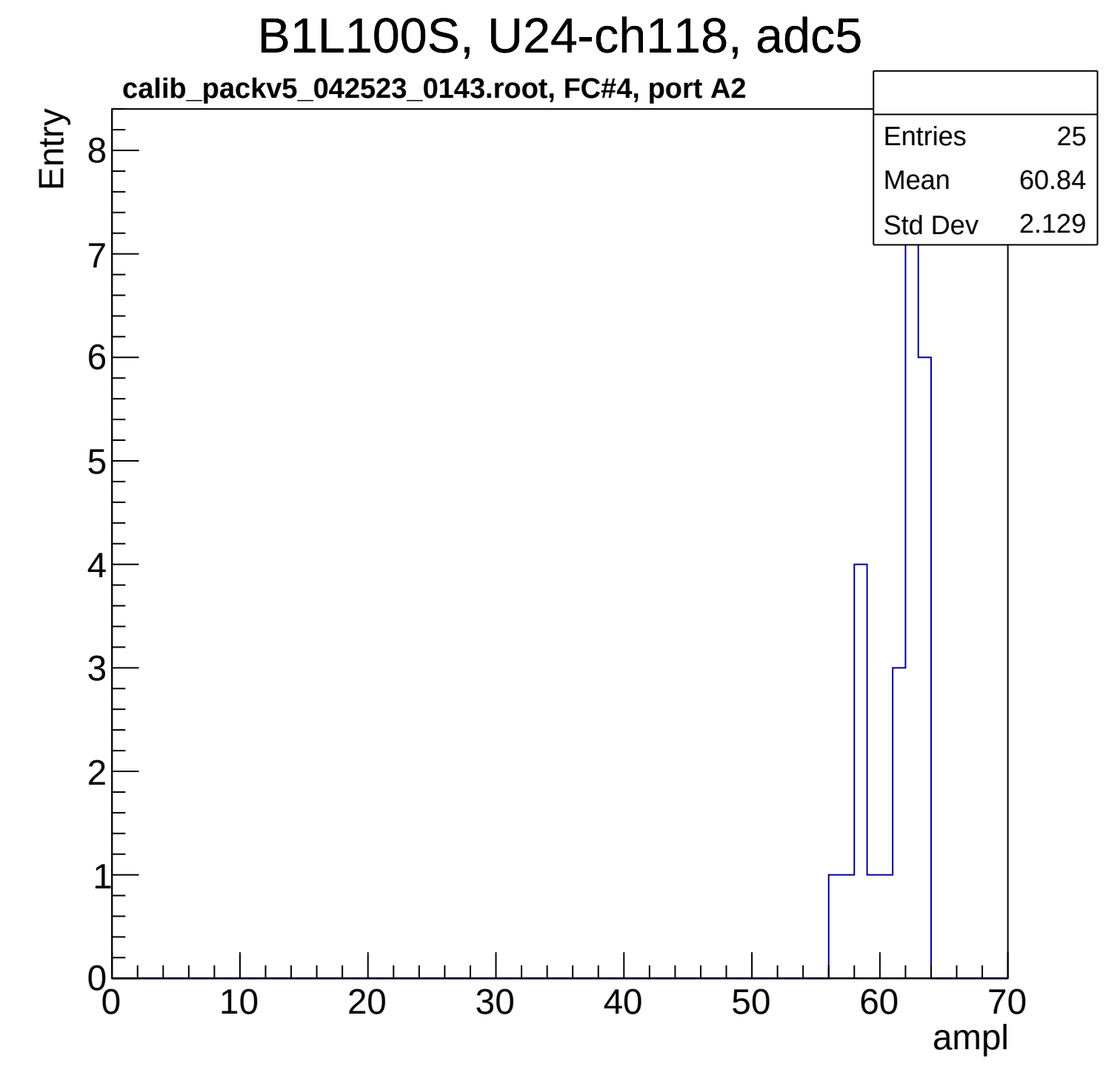
B1L100S, U24-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 60.84
Std Dev 2.129
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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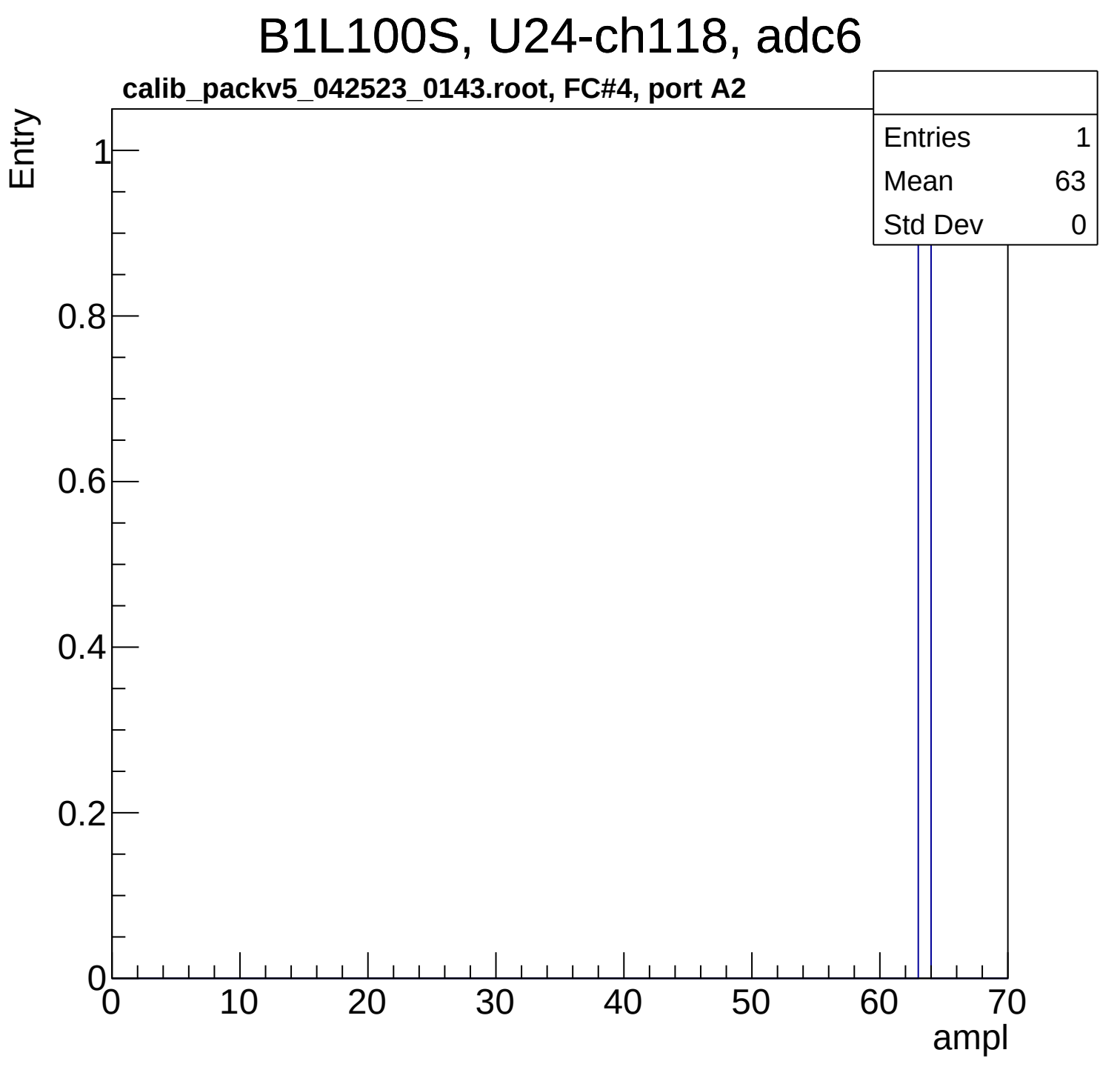
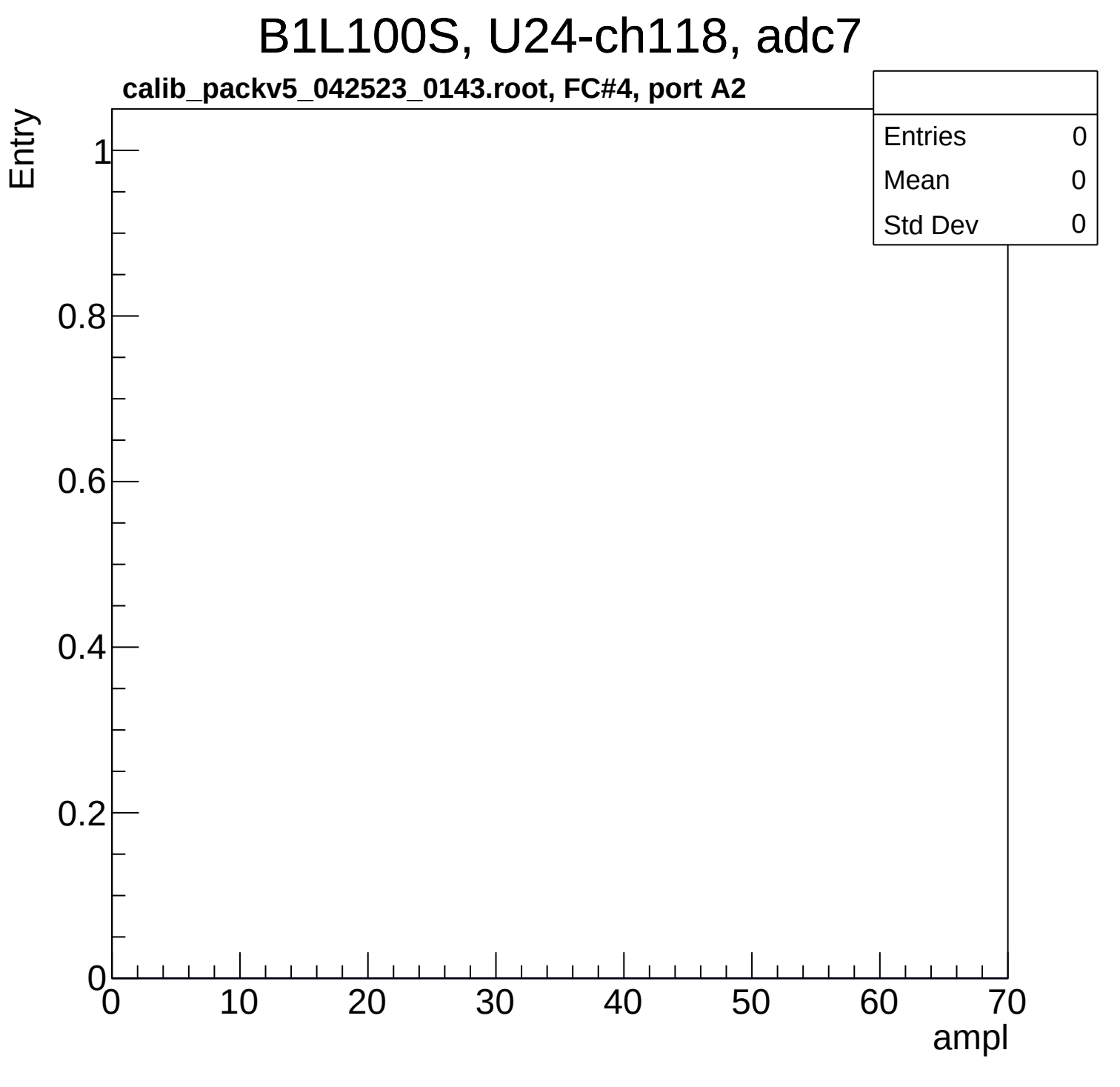

B1L100S, U24-ch118, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch118, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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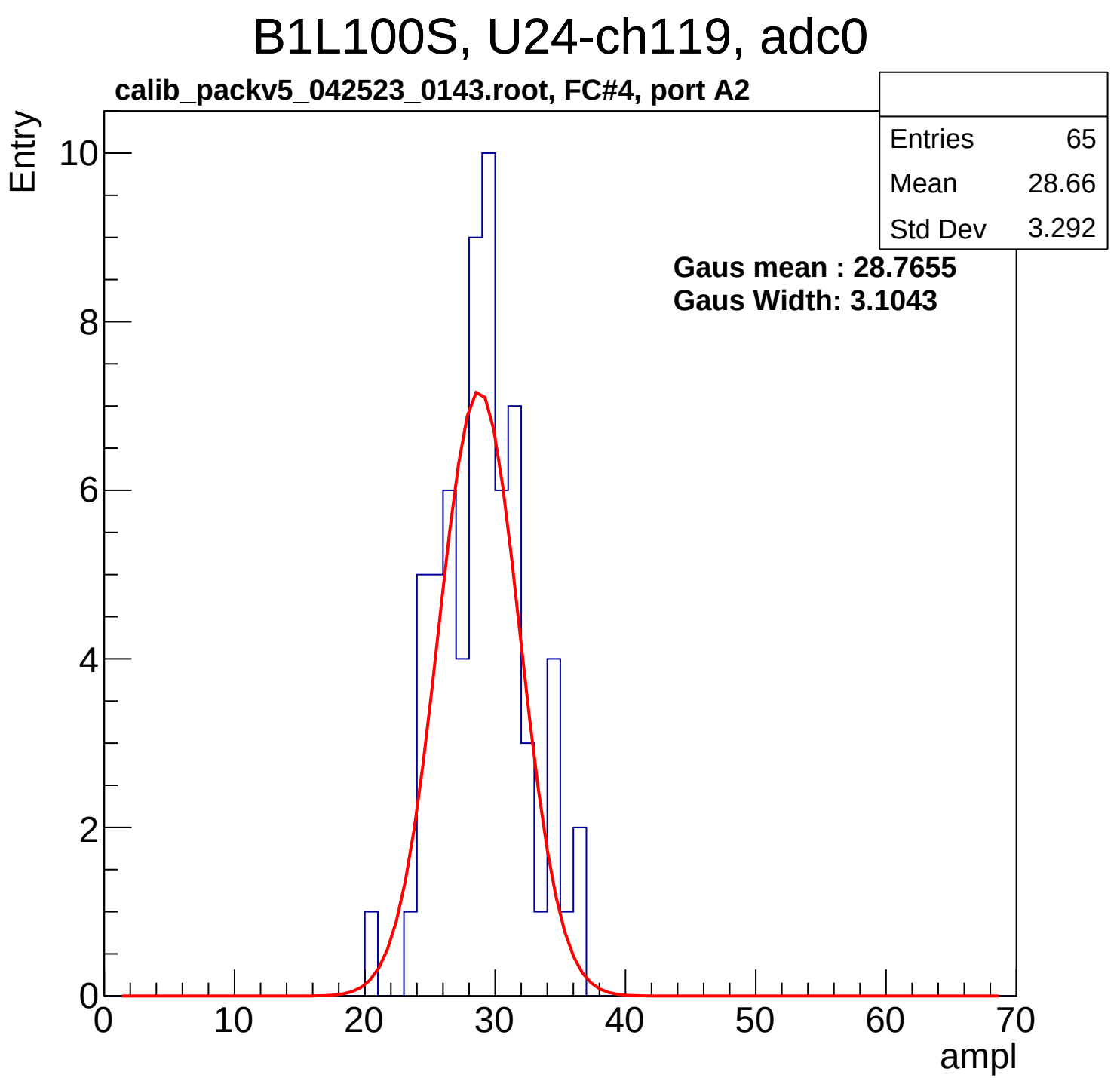
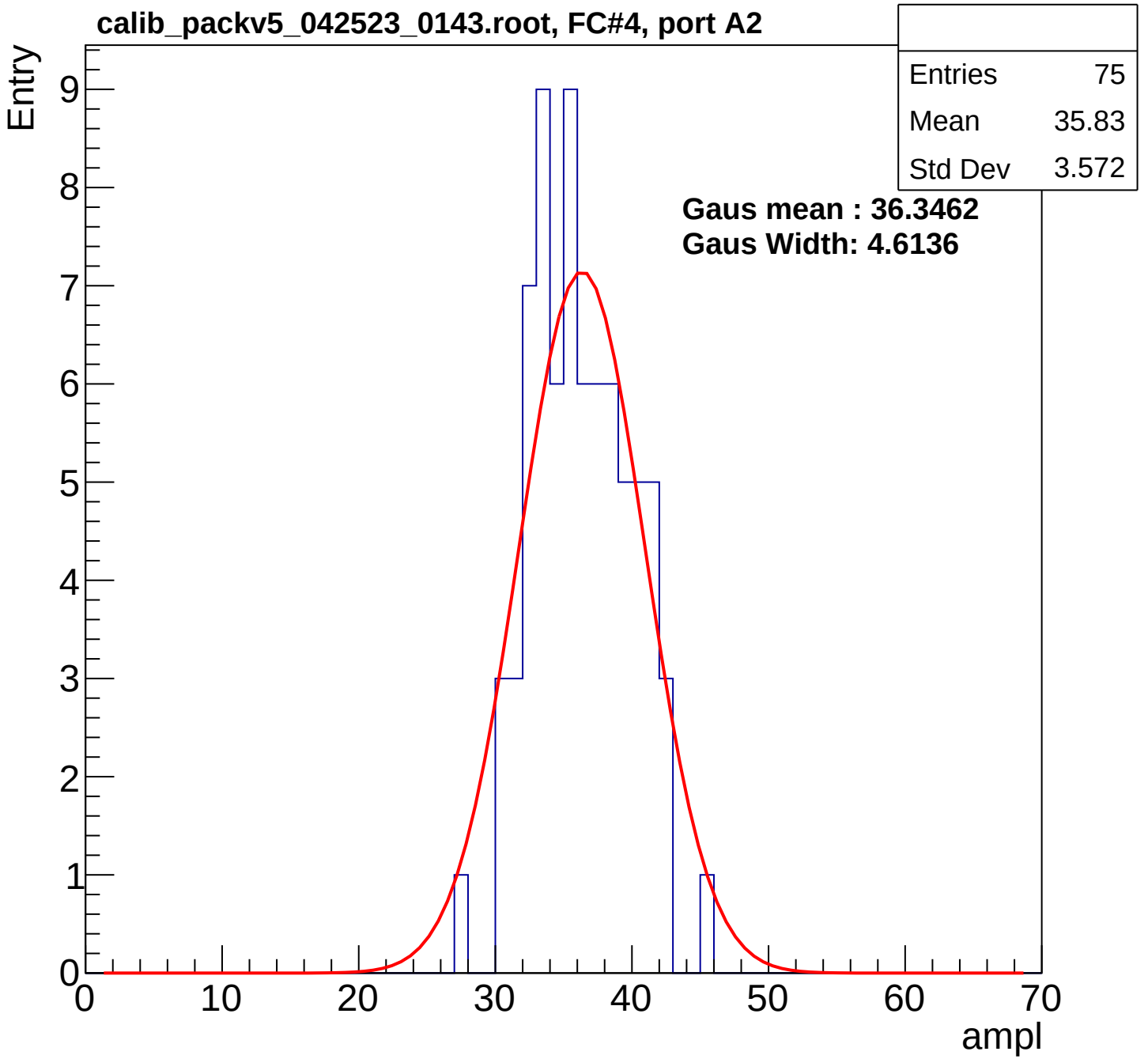
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.66
Std Dev 3.292
Gaus mean : 28.7655
Gaus Width: 3.1043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.83
Std Dev 3.572
Gaus mean : 36.3462
Gaus Width: 4.6136
Entry
ampl
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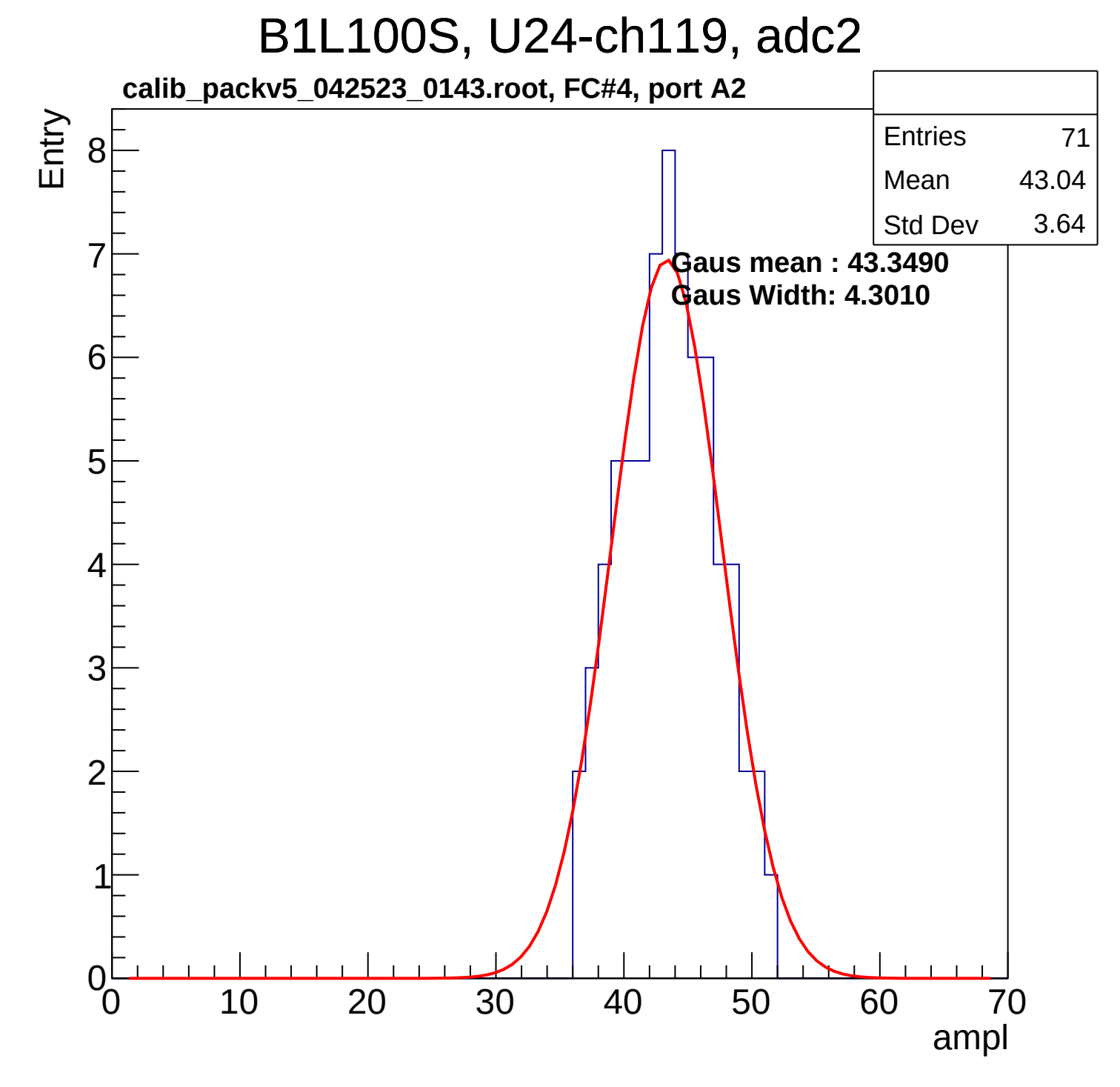
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 43.04
Std Dev 3.64
Gaus mean : 43.3490
Gaus Width: 4.3010
Entry
ampl
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2
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4
5
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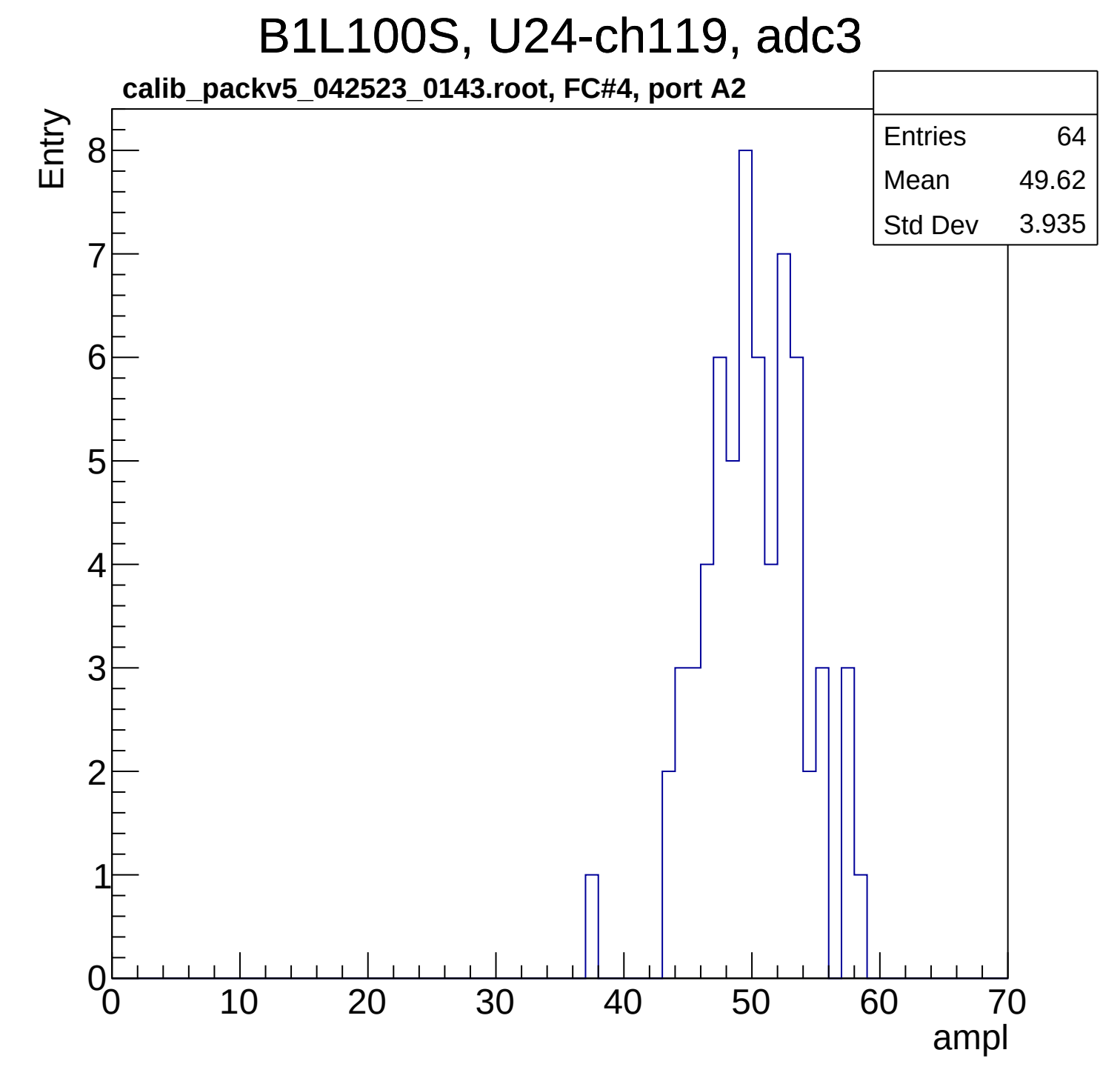
B1L100S, U24-ch119, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.62
Std Dev 3.935
Entry
ampl
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3
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10
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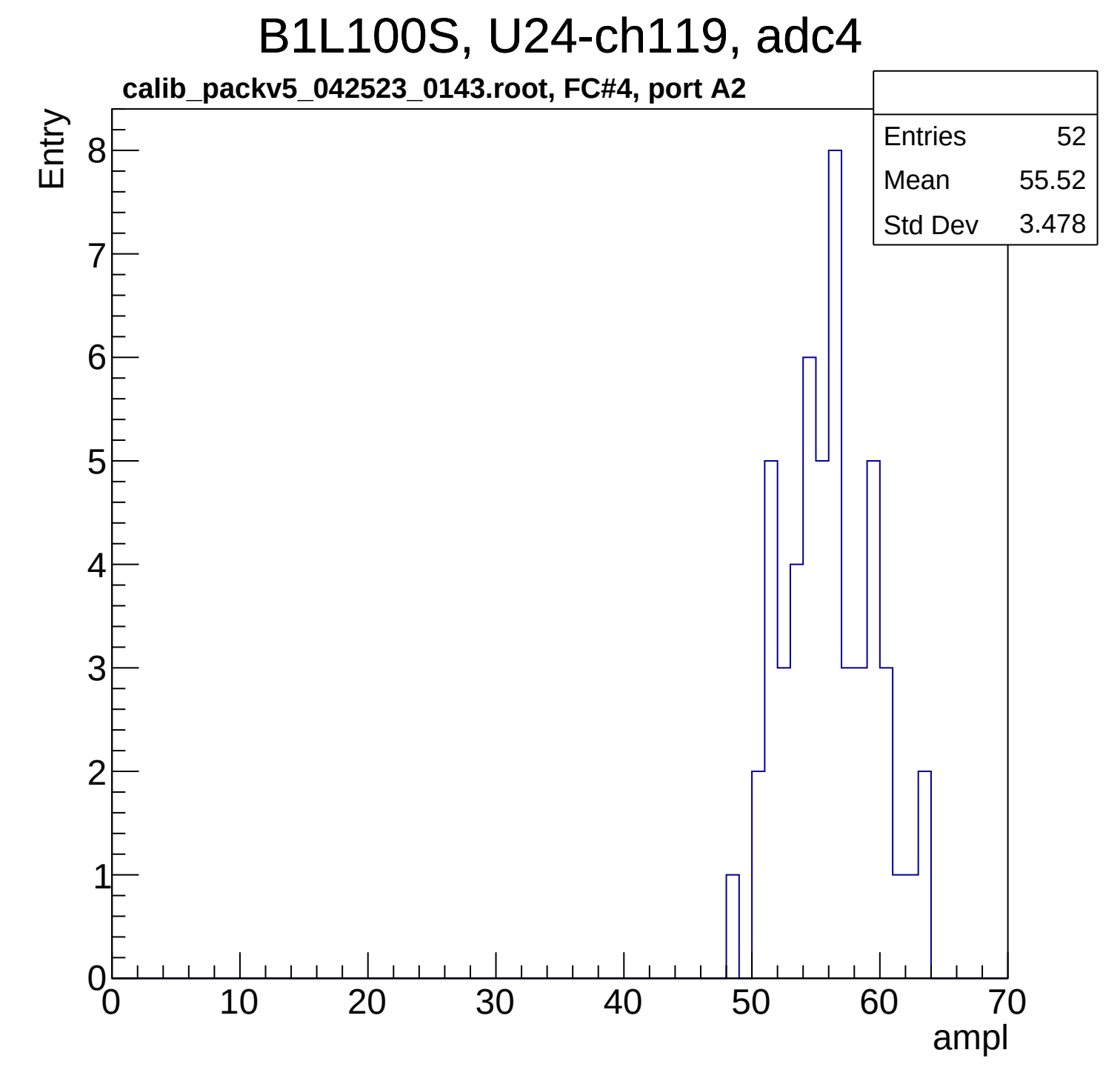
B1L100S, U24-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.52
Std Dev 3.478
Entry
ampl
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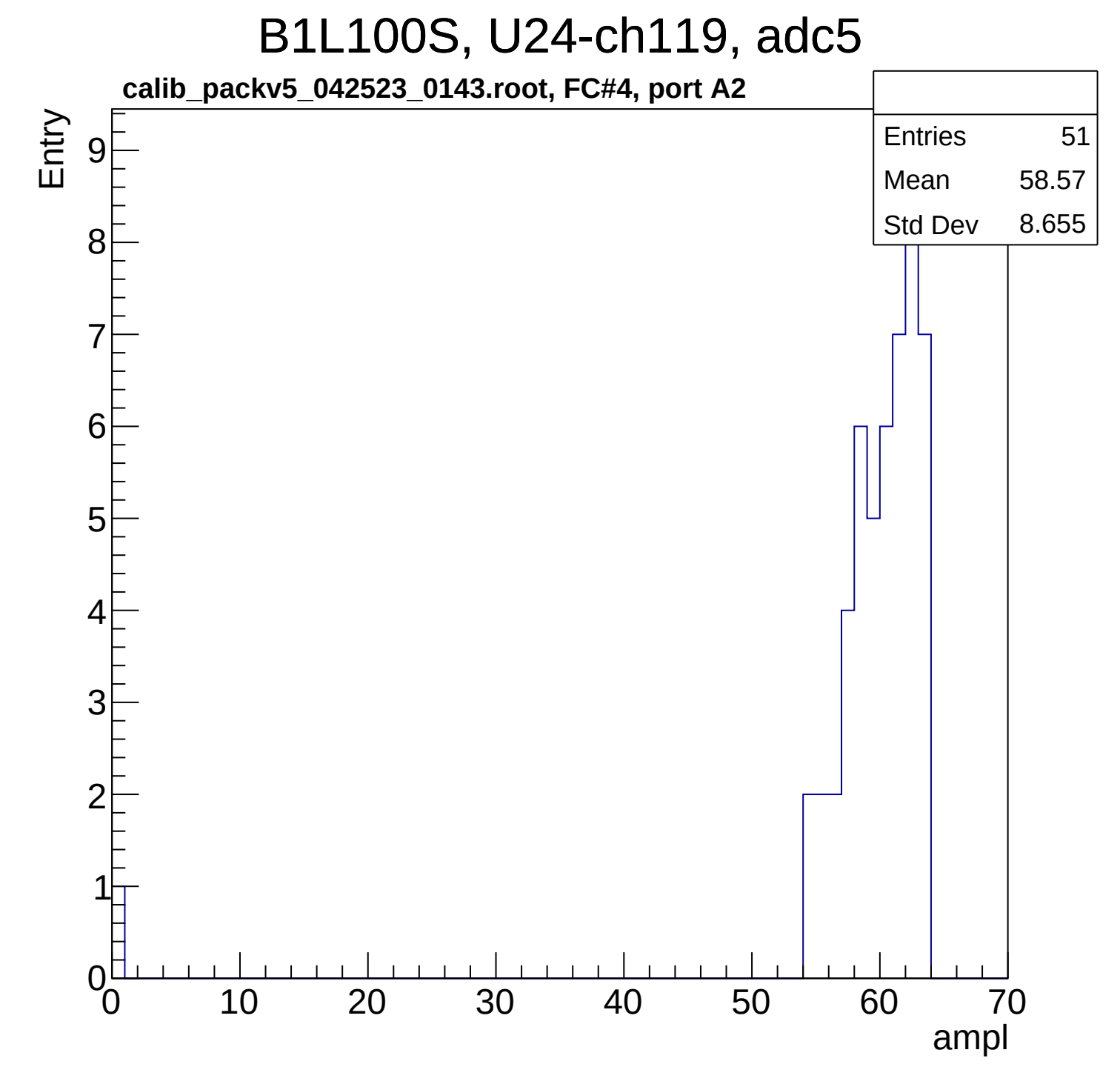
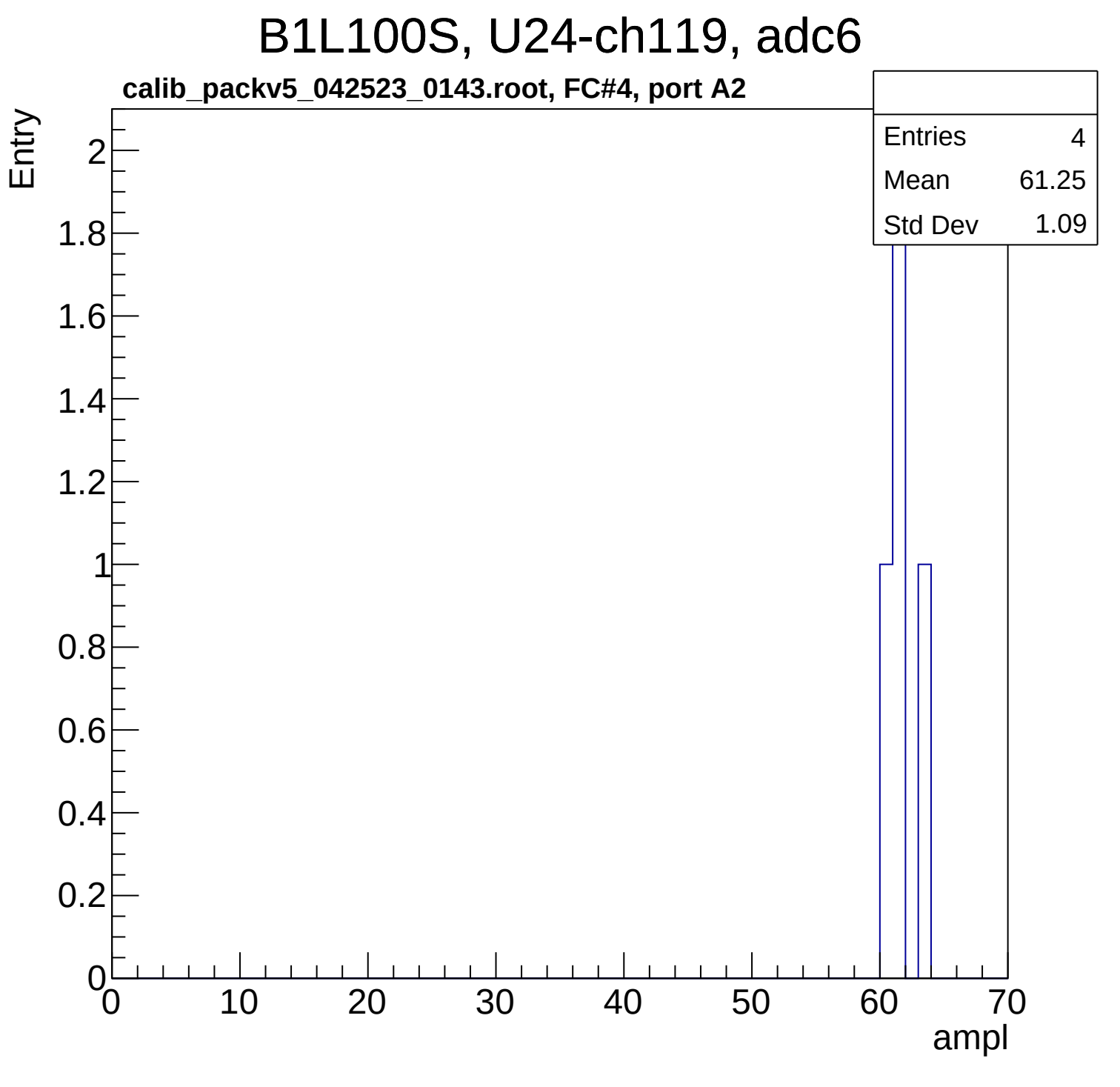

B1L100S, U24-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.57
Std Dev 8.655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.25
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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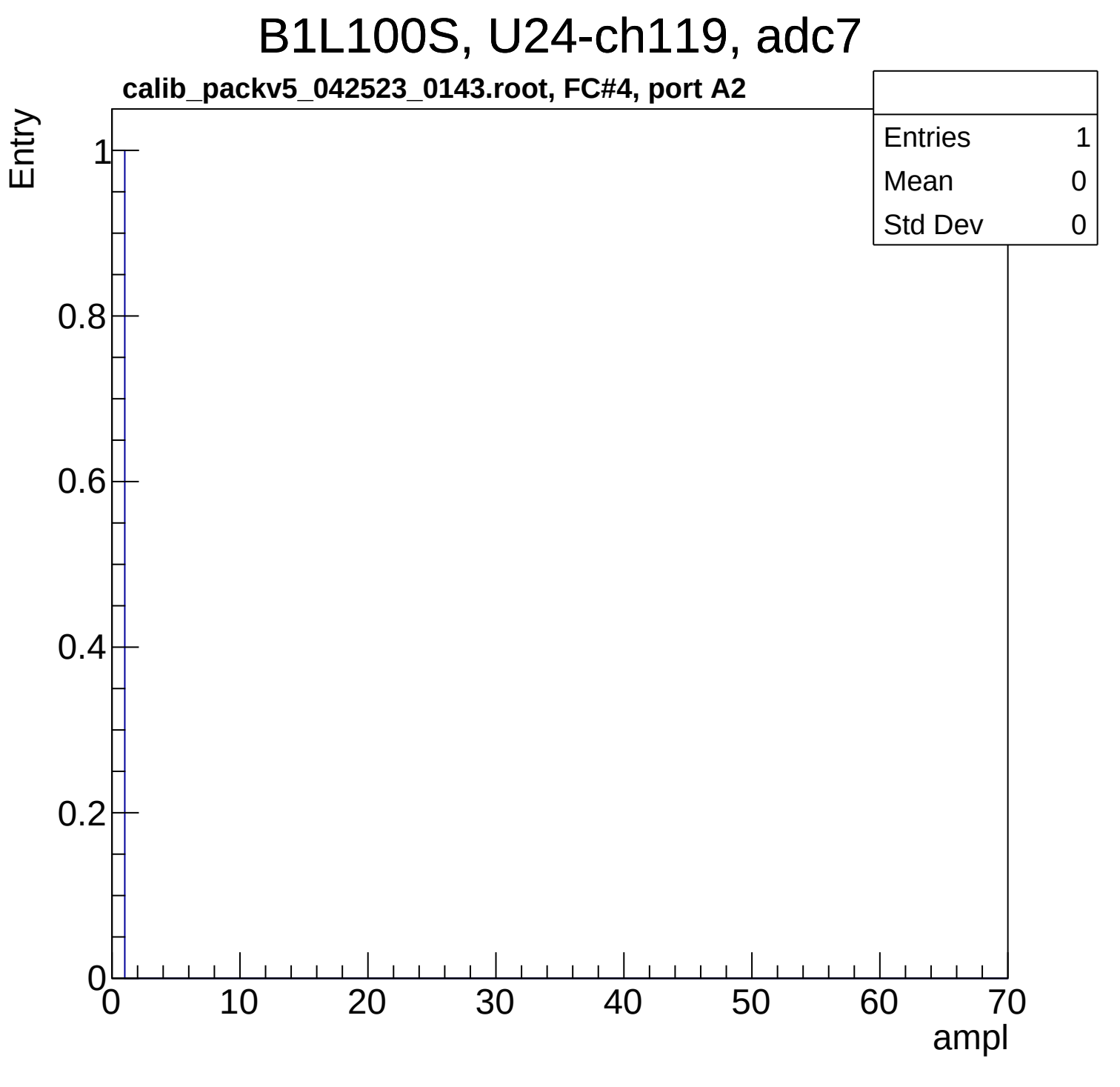
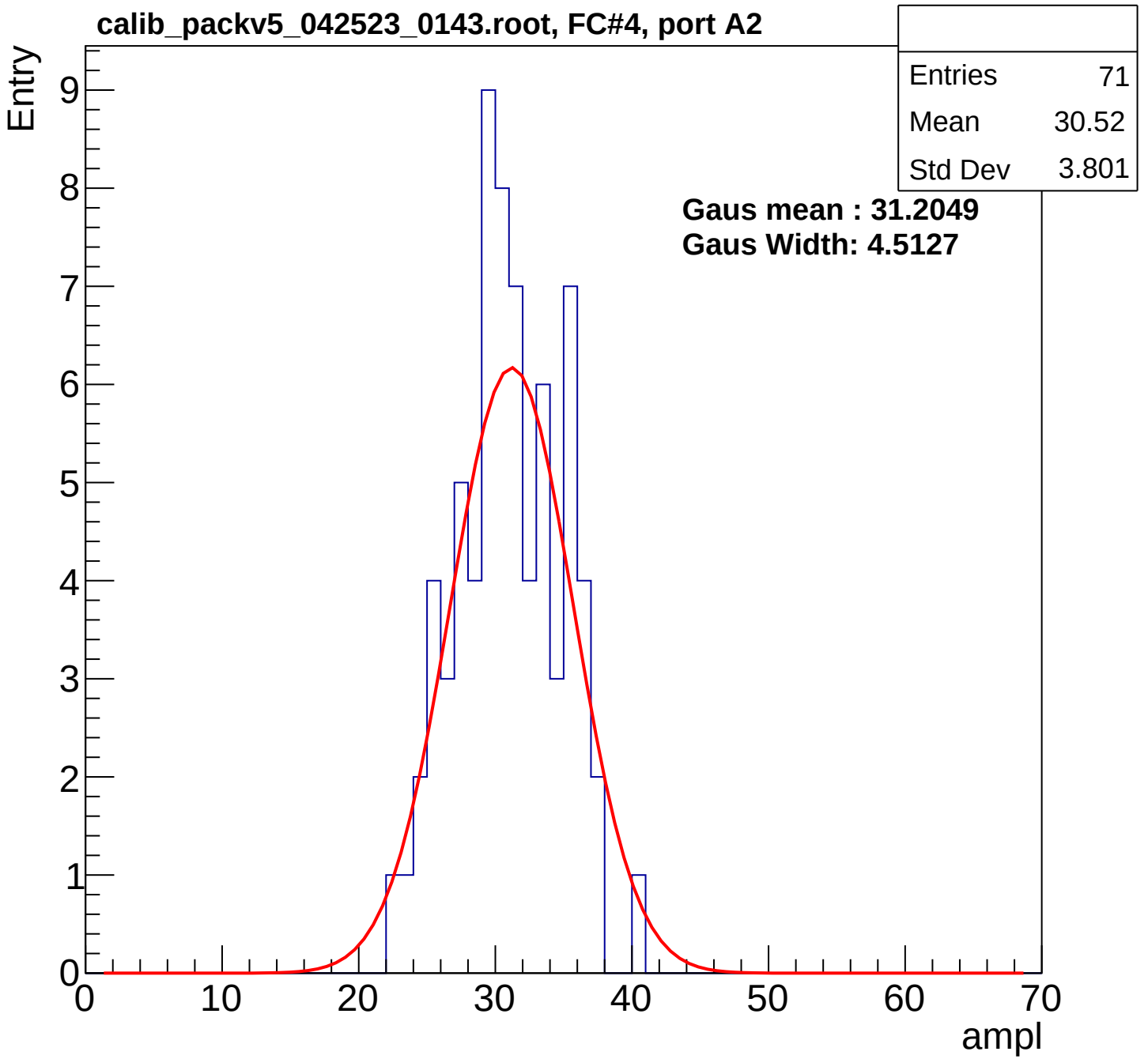
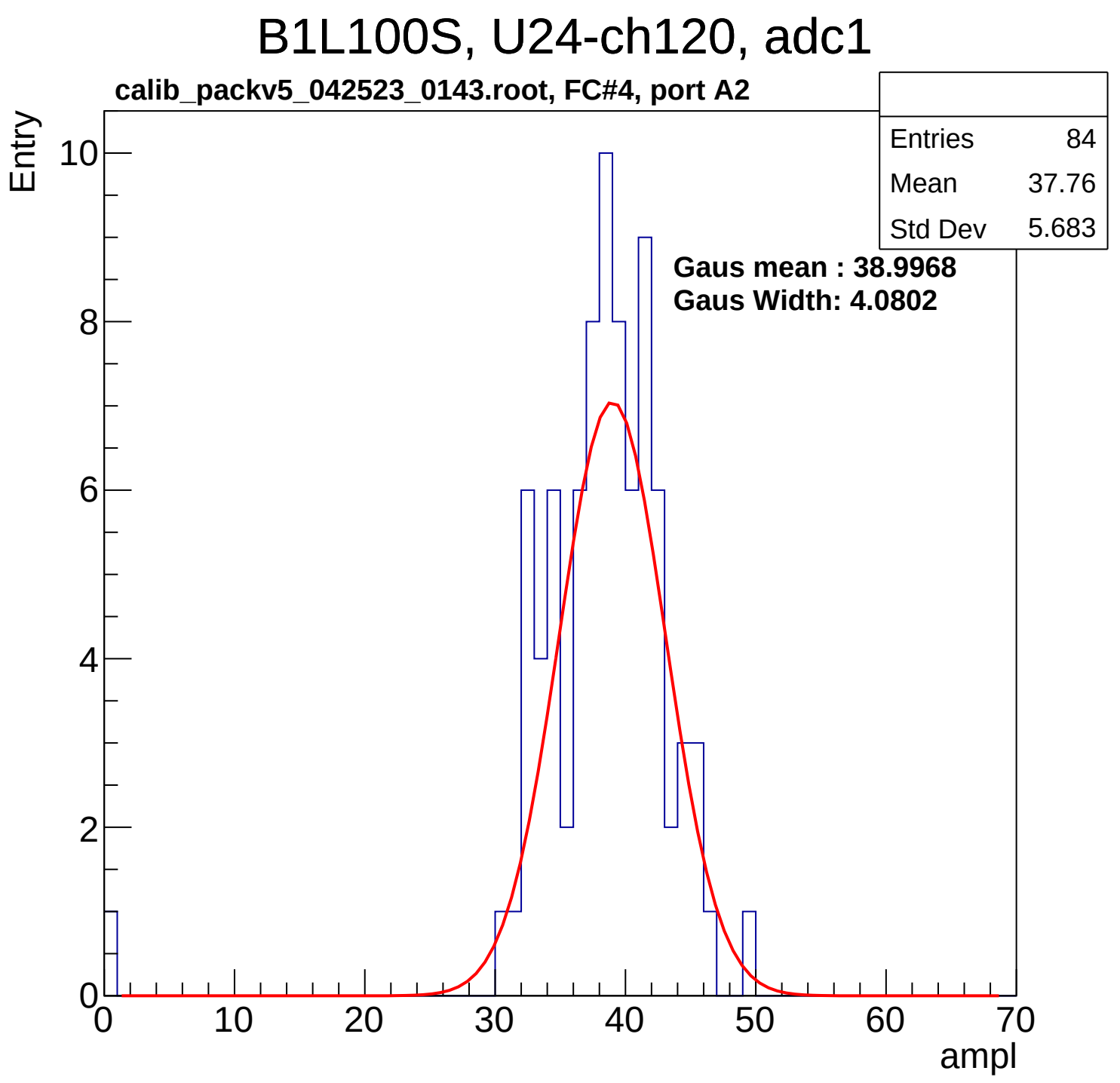
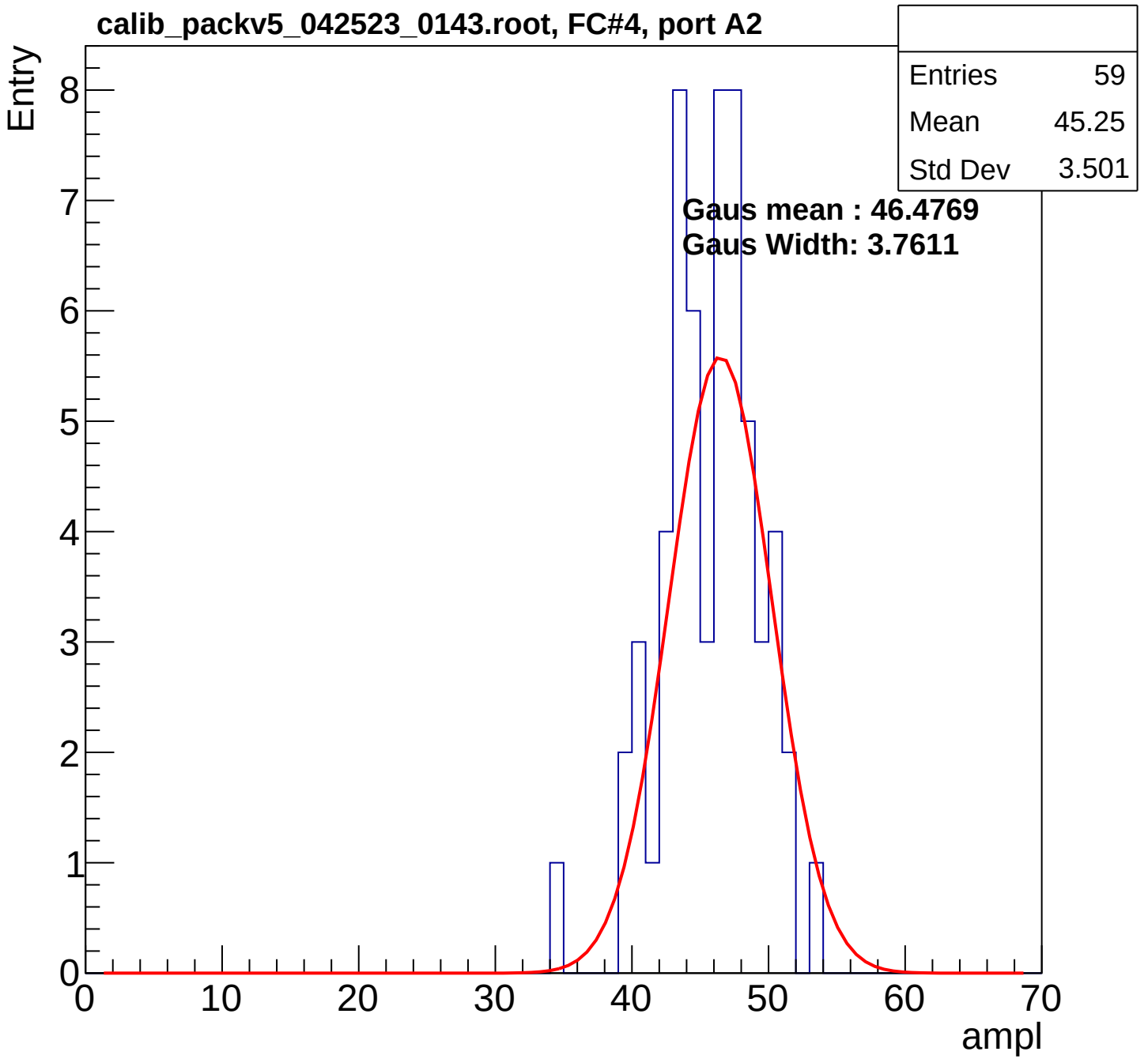

B1L100S, U24-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.52
Std Dev 3.801
Gaus mean : 31.2049
Gaus Width: 4.5127
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.76
Std Dev 5.683
Gaus mean : 38.9968
Gaus Width: 4.0802
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch120, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 45.25
Std Dev 3.501
Gaus mean : 46.4769
Gaus Width: 3.7611
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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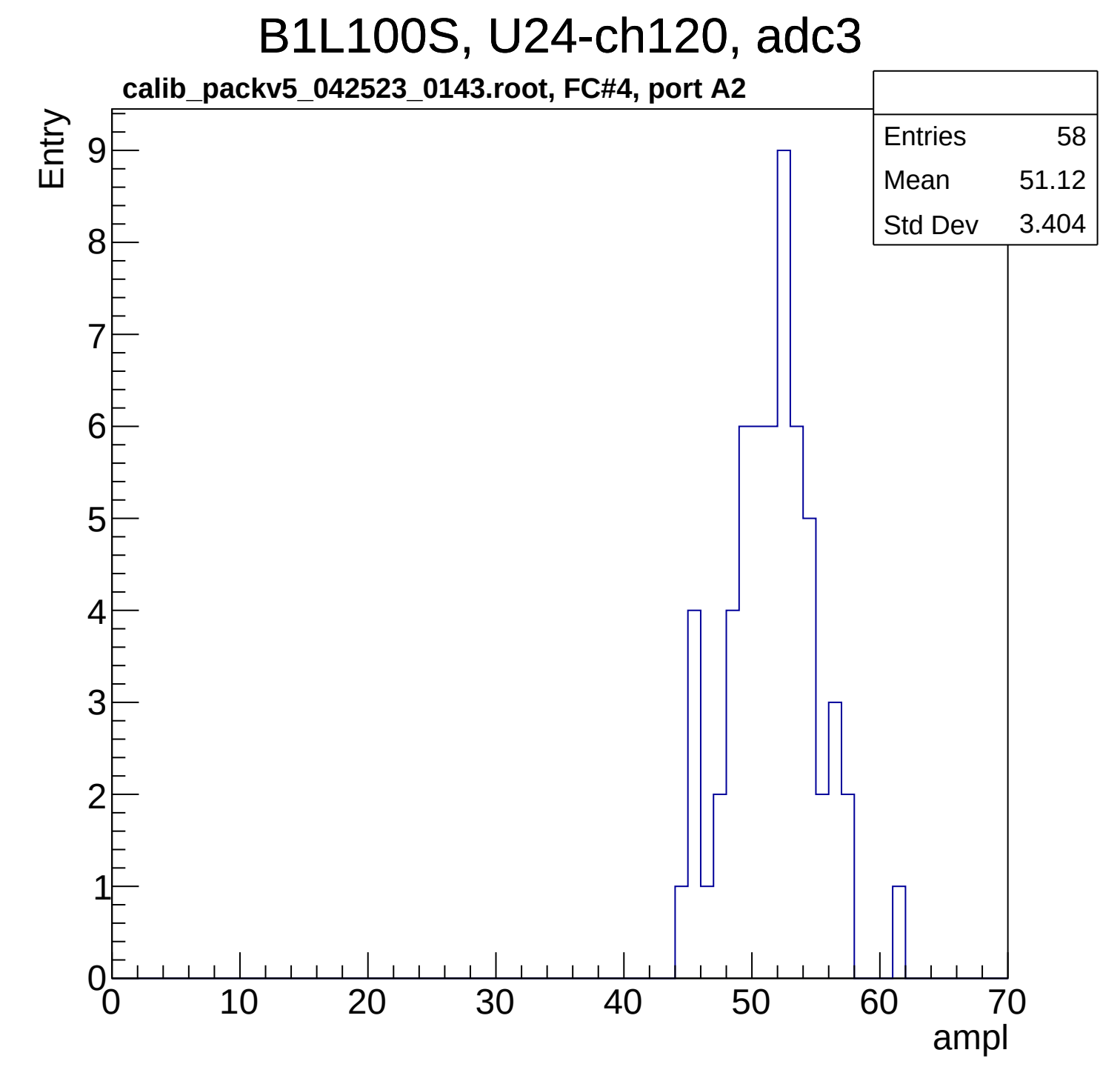
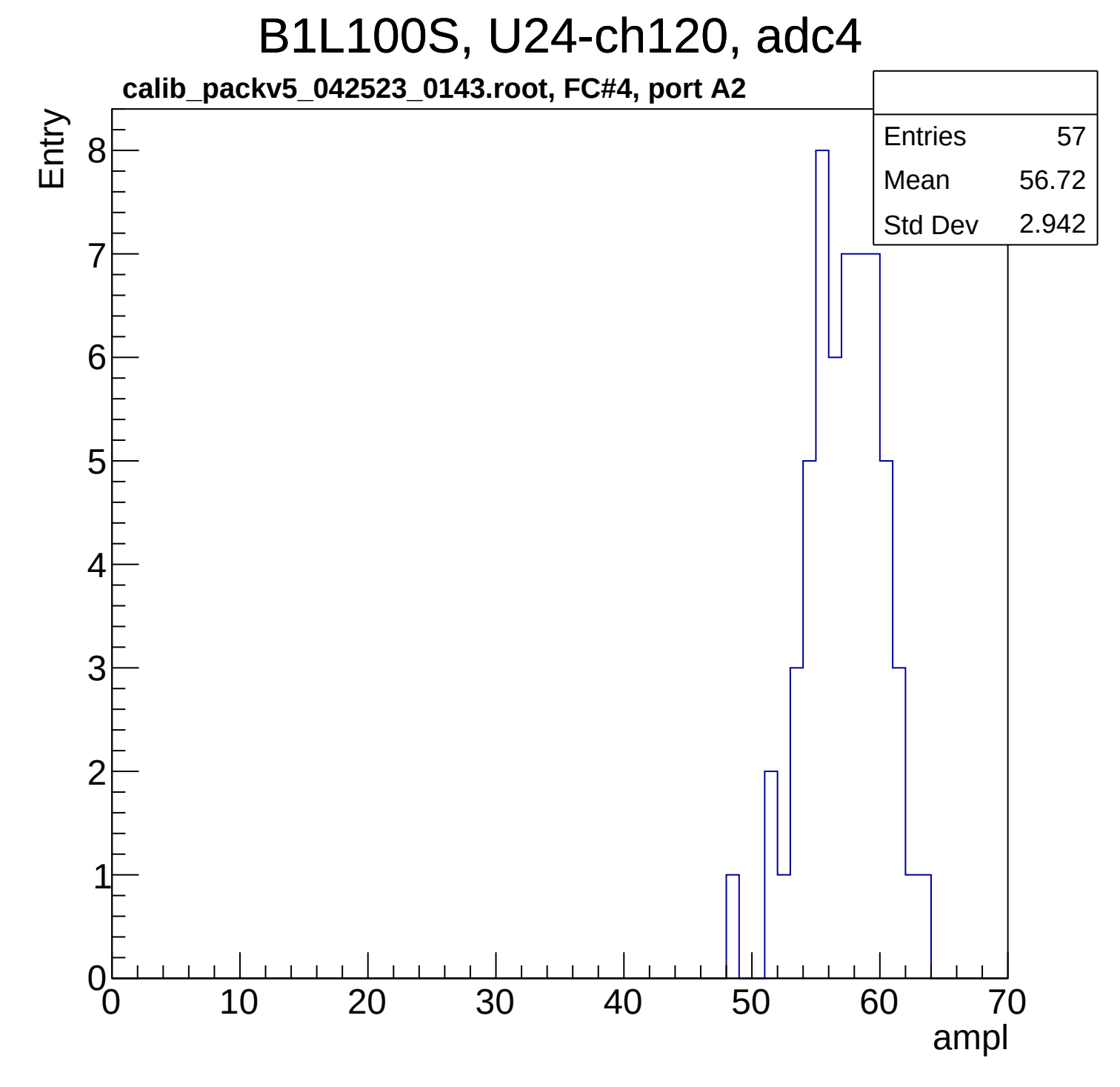
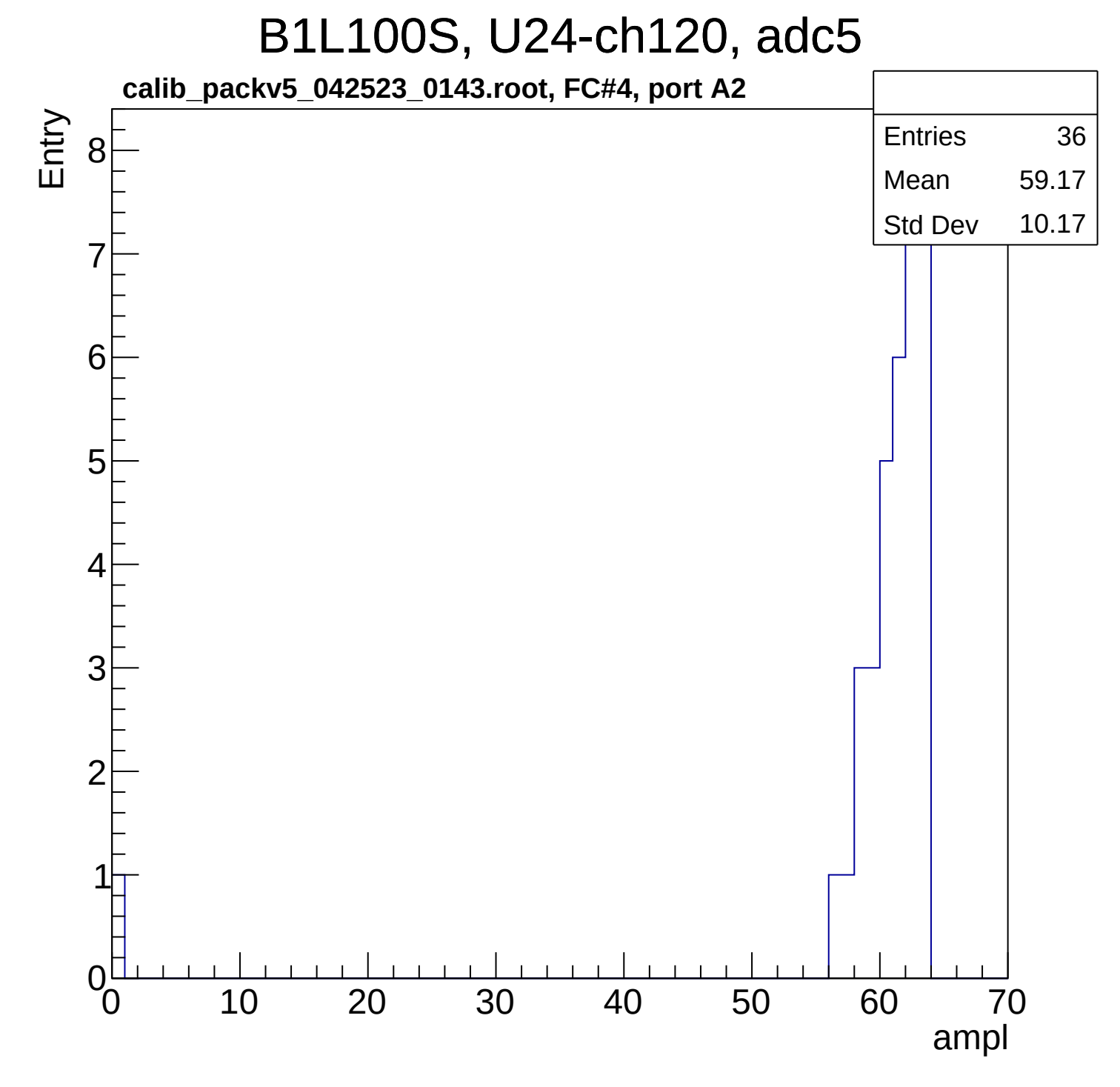
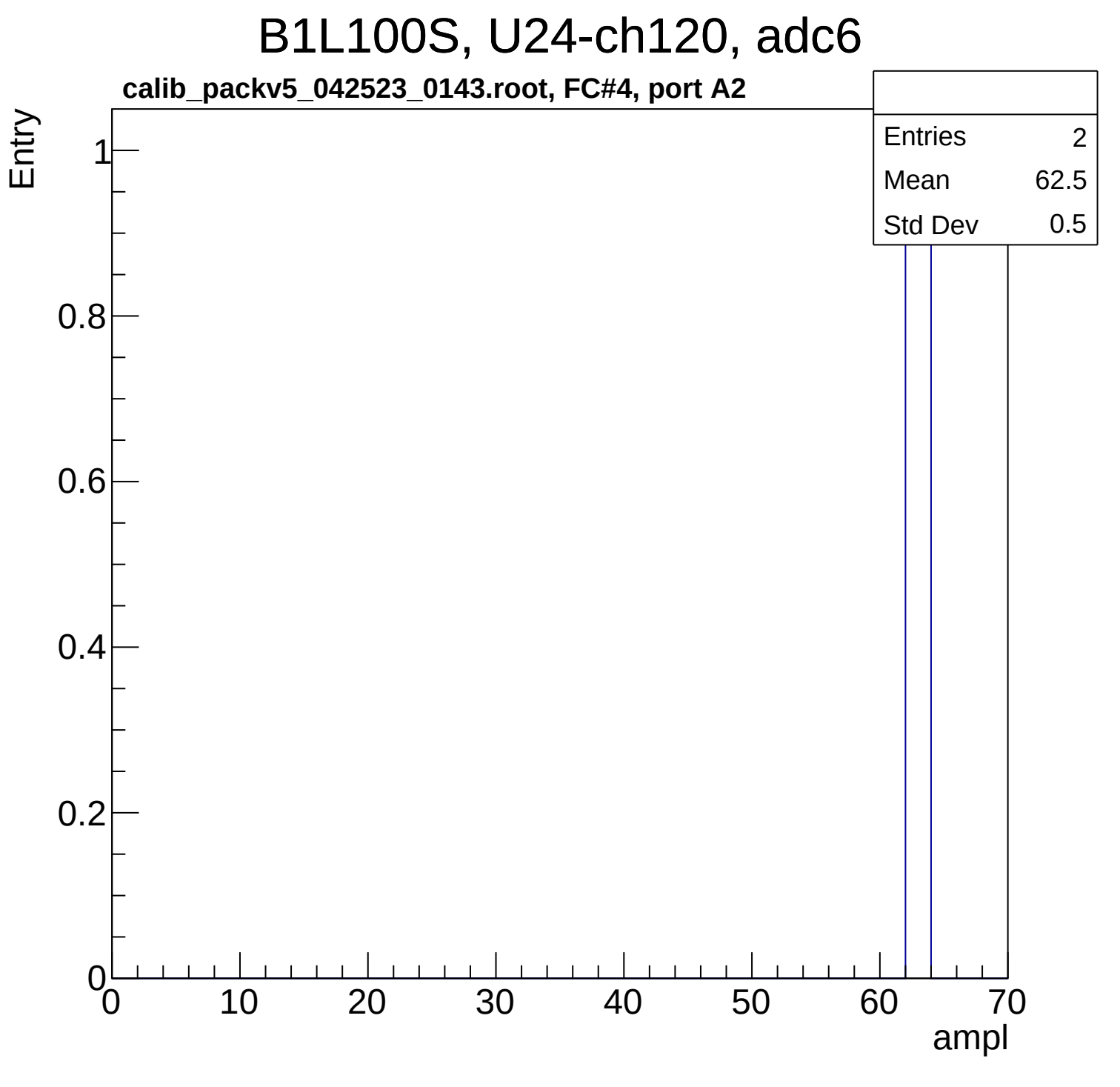
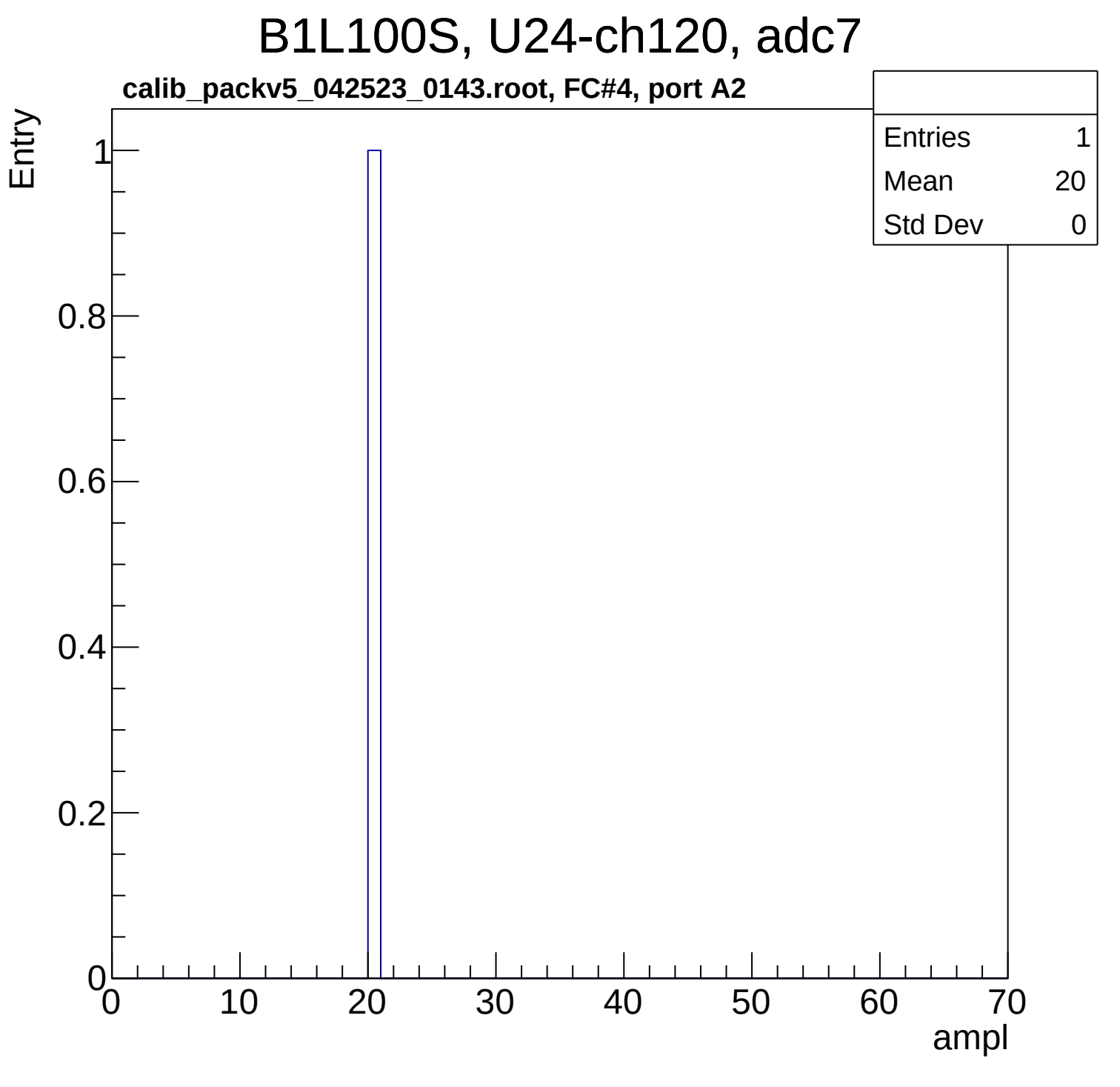
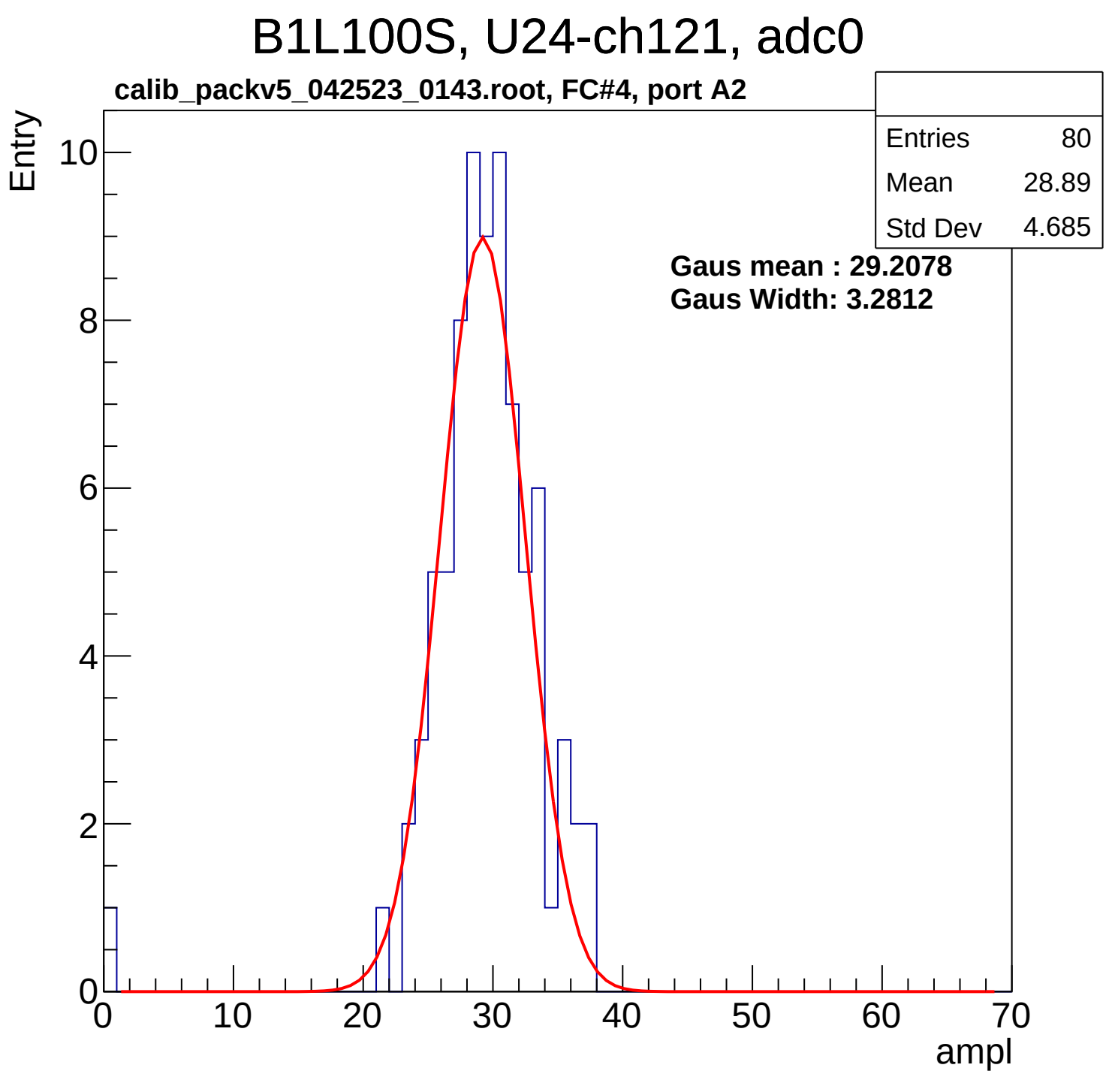

B1L100S, U24-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 51.12
Std Dev 3.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

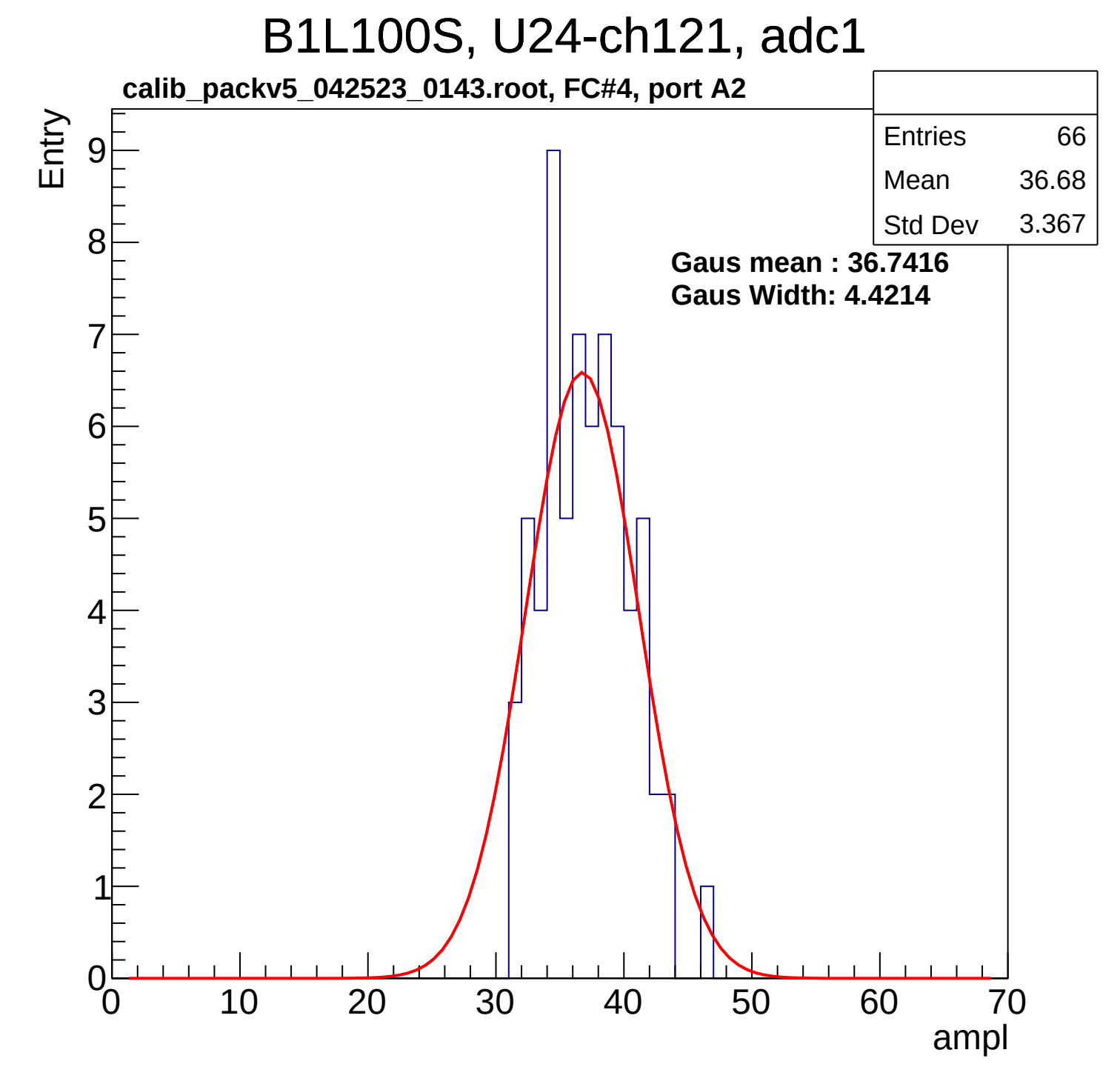

B1L100S, U24-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.72
Std Dev 2.942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.17
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.89
Std Dev 4.685
Gaus mean : 29.2078
Gaus Width: 3.2812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.68
Std Dev 3.367
Gaus mean : 36.7416
Gaus Width: 4.4214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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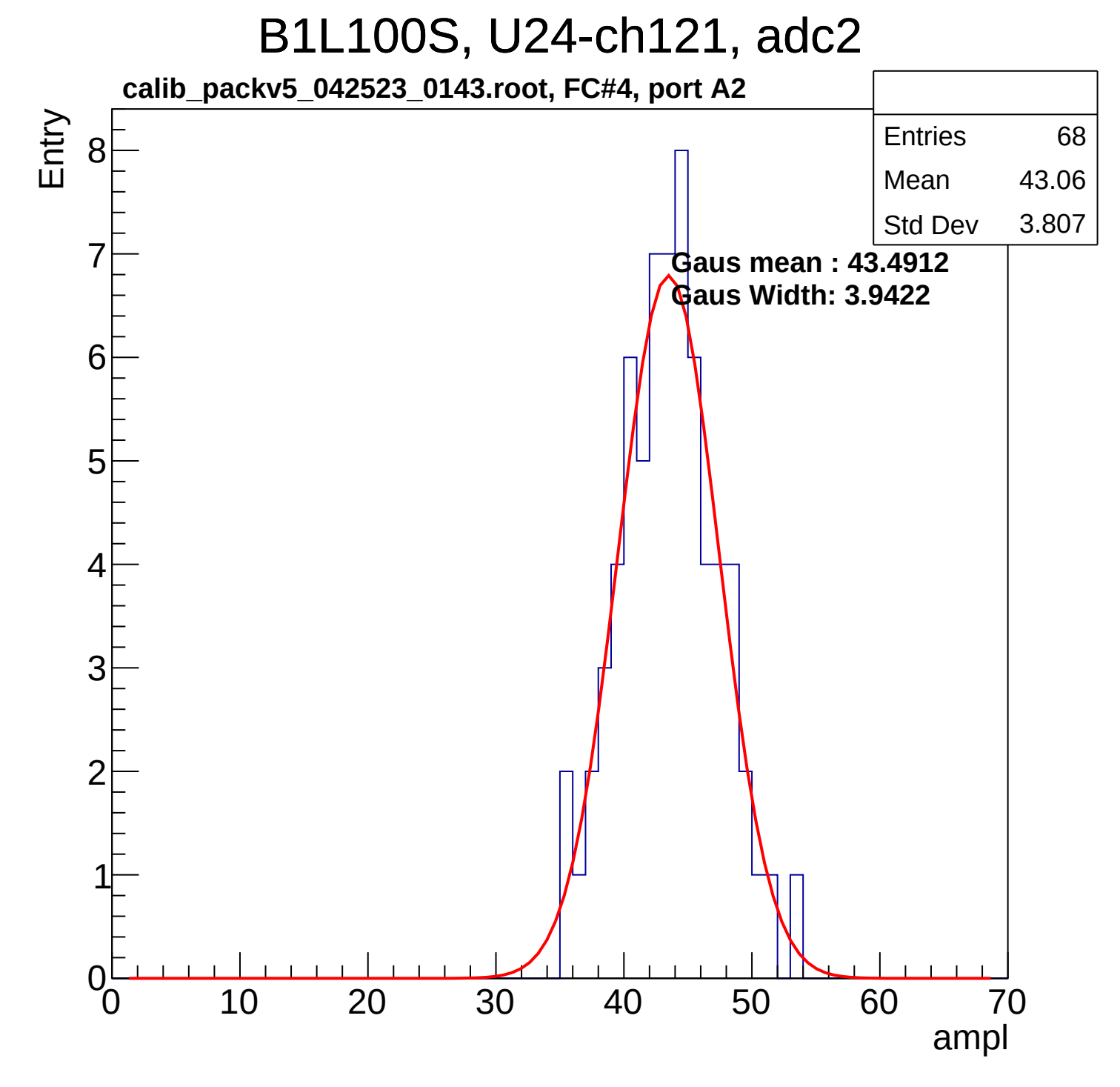
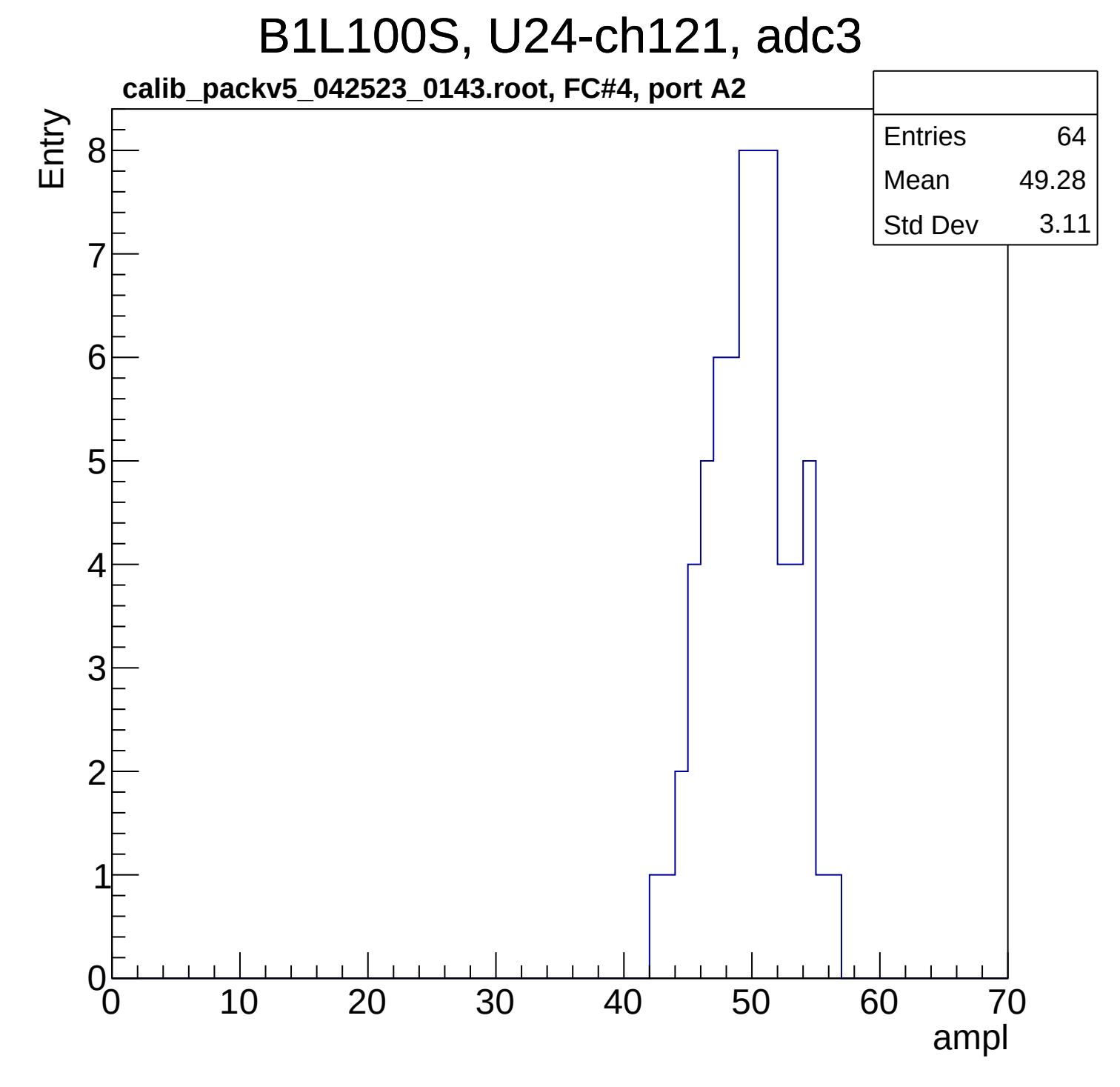

B1L100S, U24-ch121, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.06
Std Dev 3.807
Gaus mean : 43.4912
Gaus Width: 3.9422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.28
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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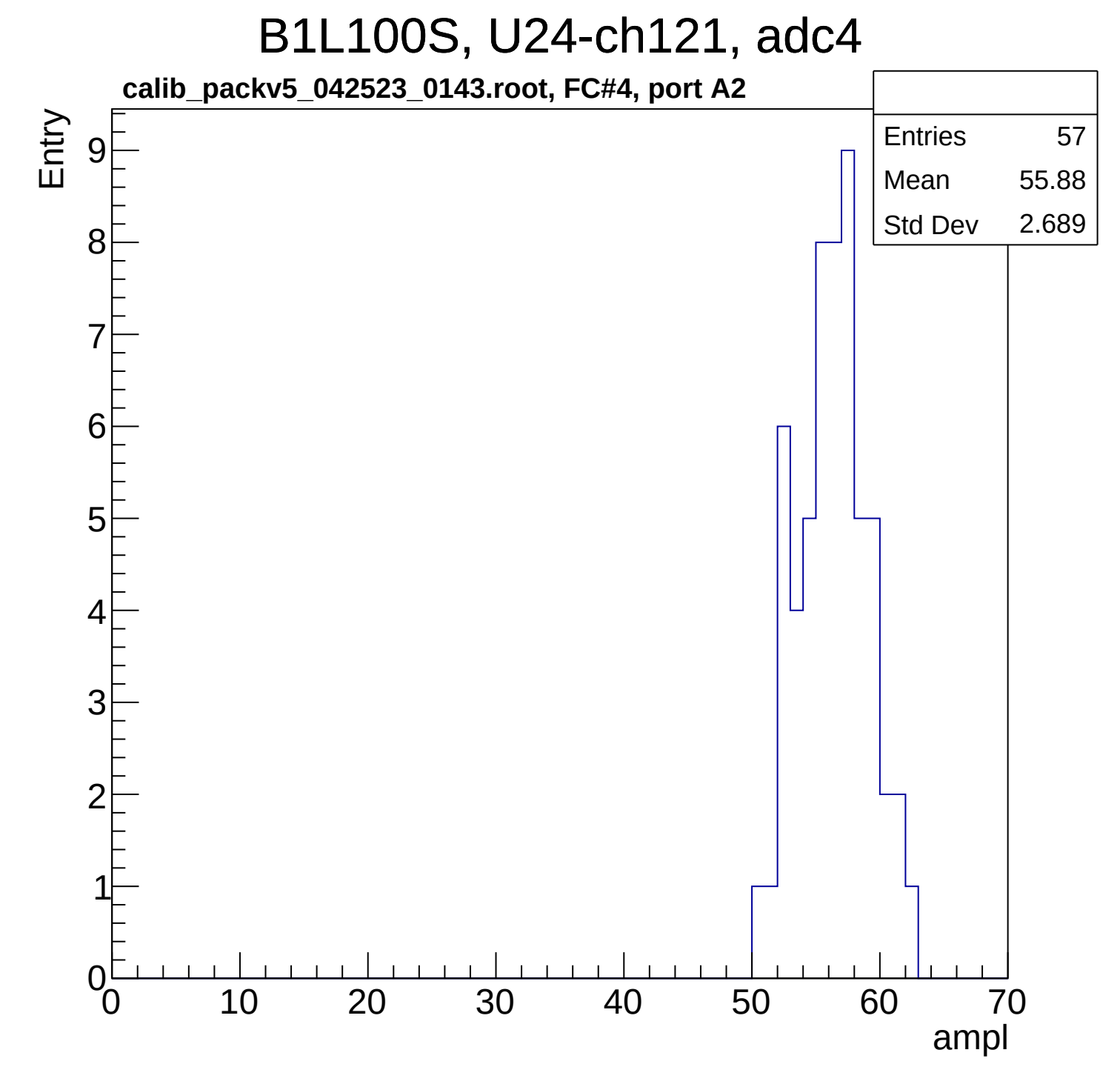
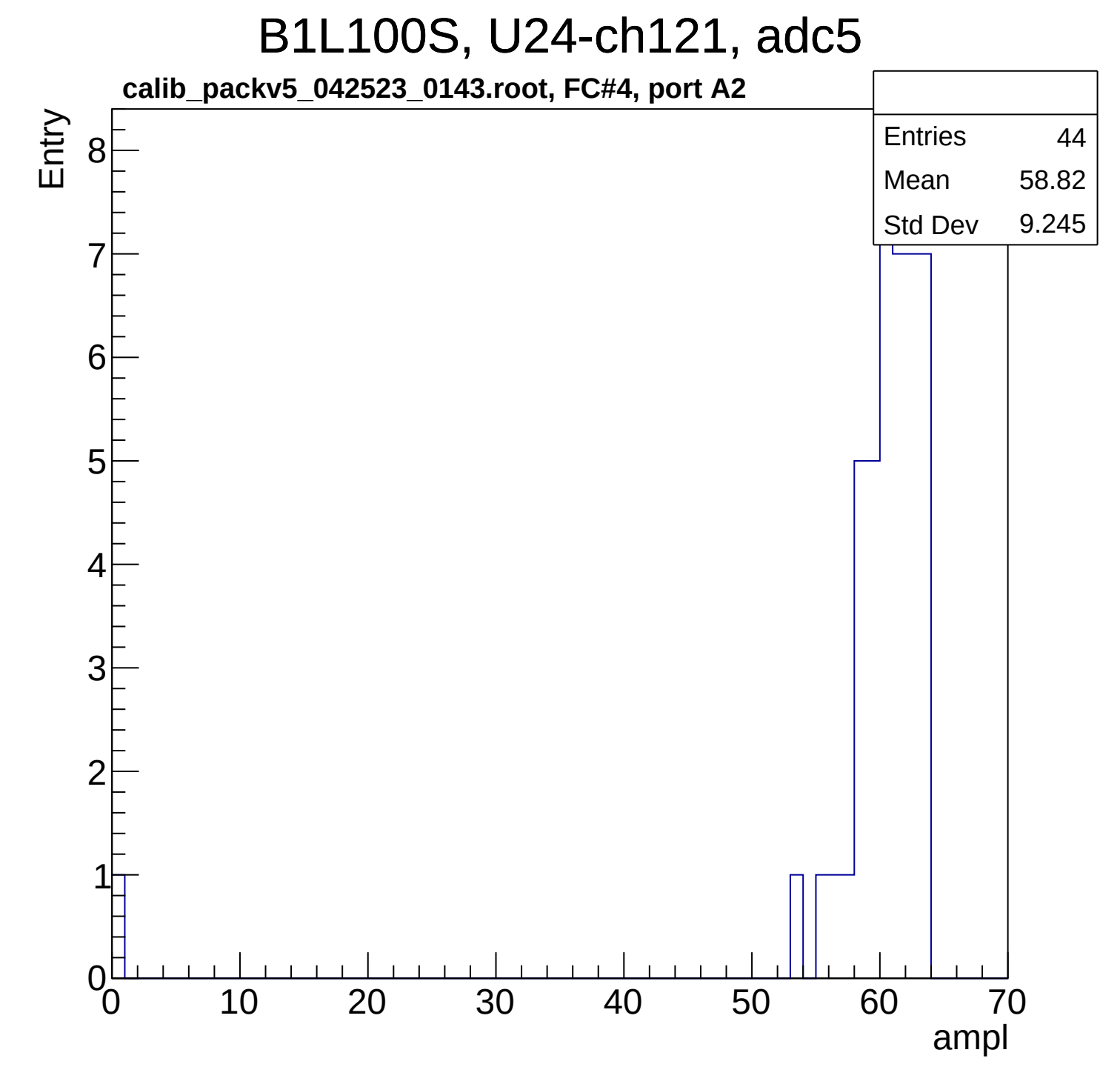

B1L100S, U24-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 55.88
Std Dev 2.689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch121, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.82
Std Dev 9.245
Entry
ampl
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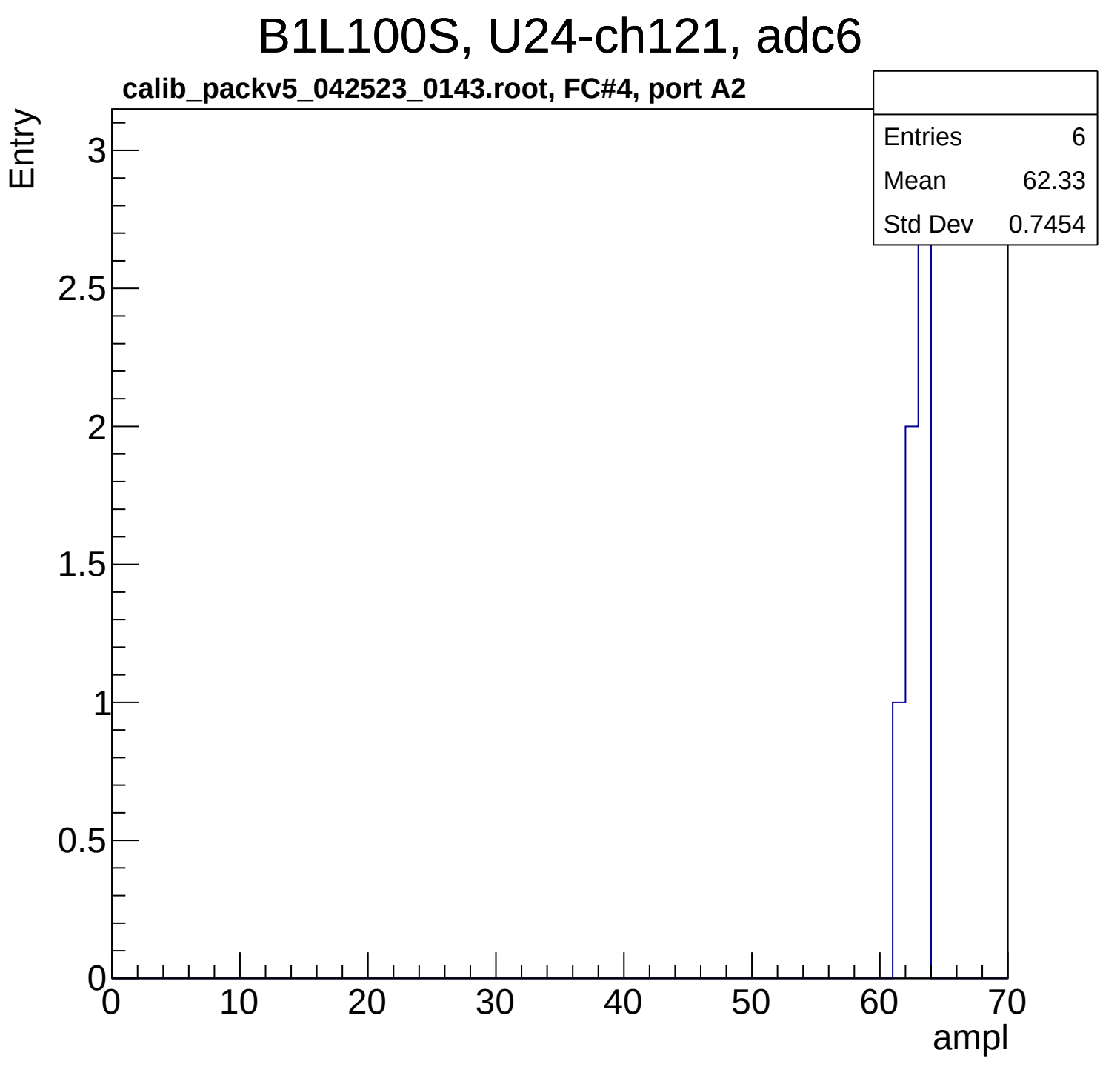
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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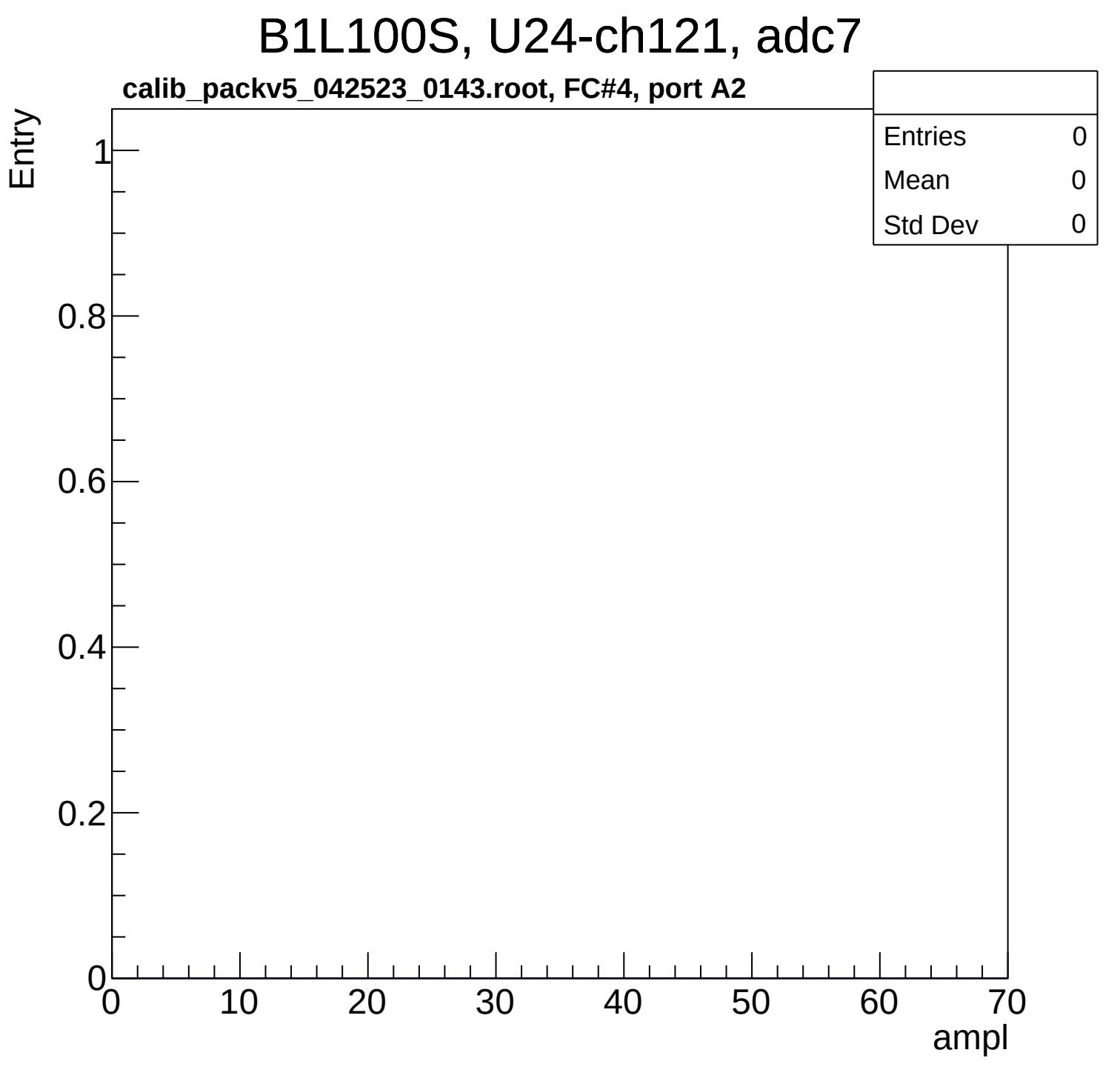
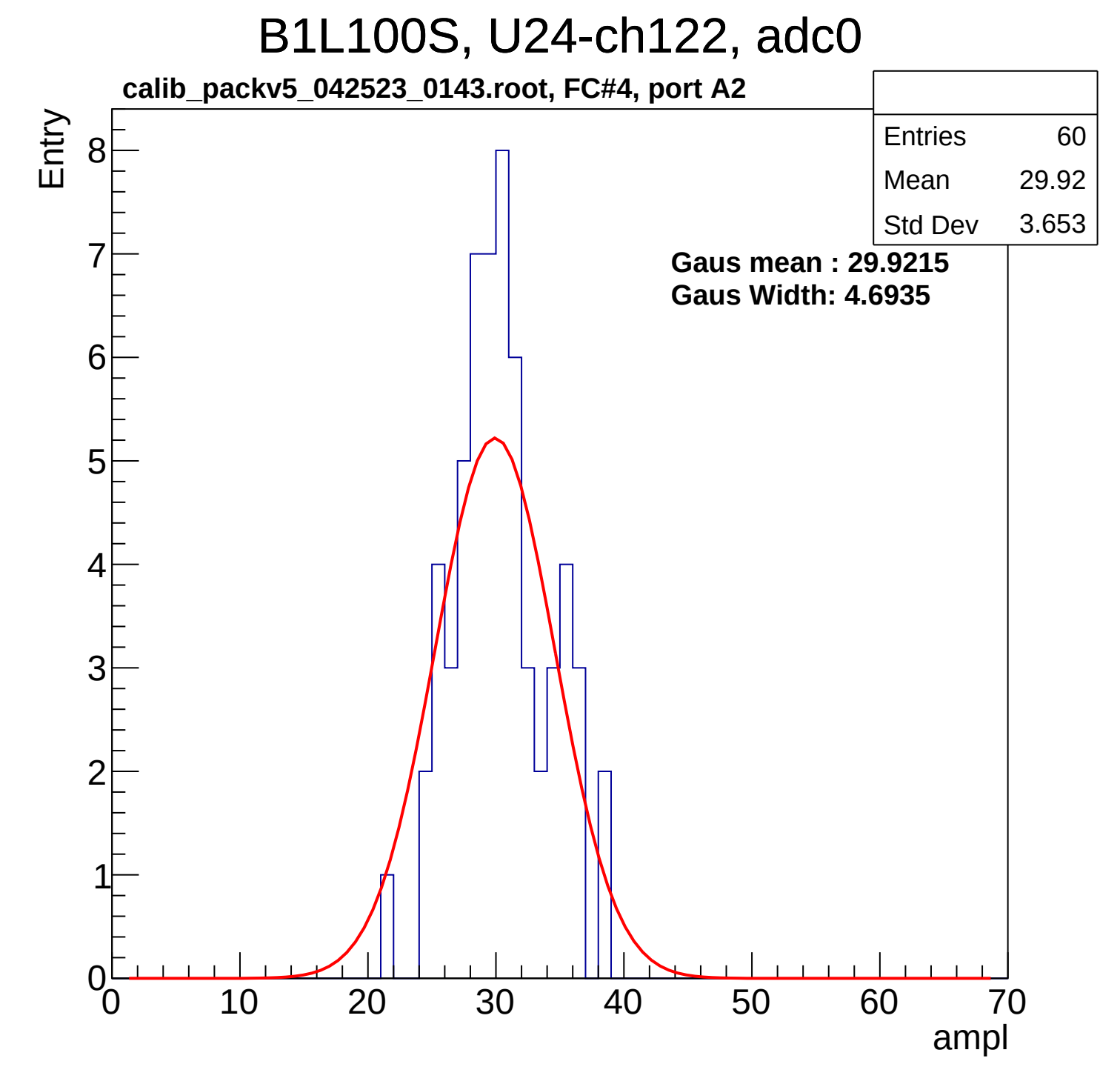
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U24-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.92
Std Dev 3.653
Gaus mean : 29.9215
Gaus Width: 4.6935
Entry
ampl
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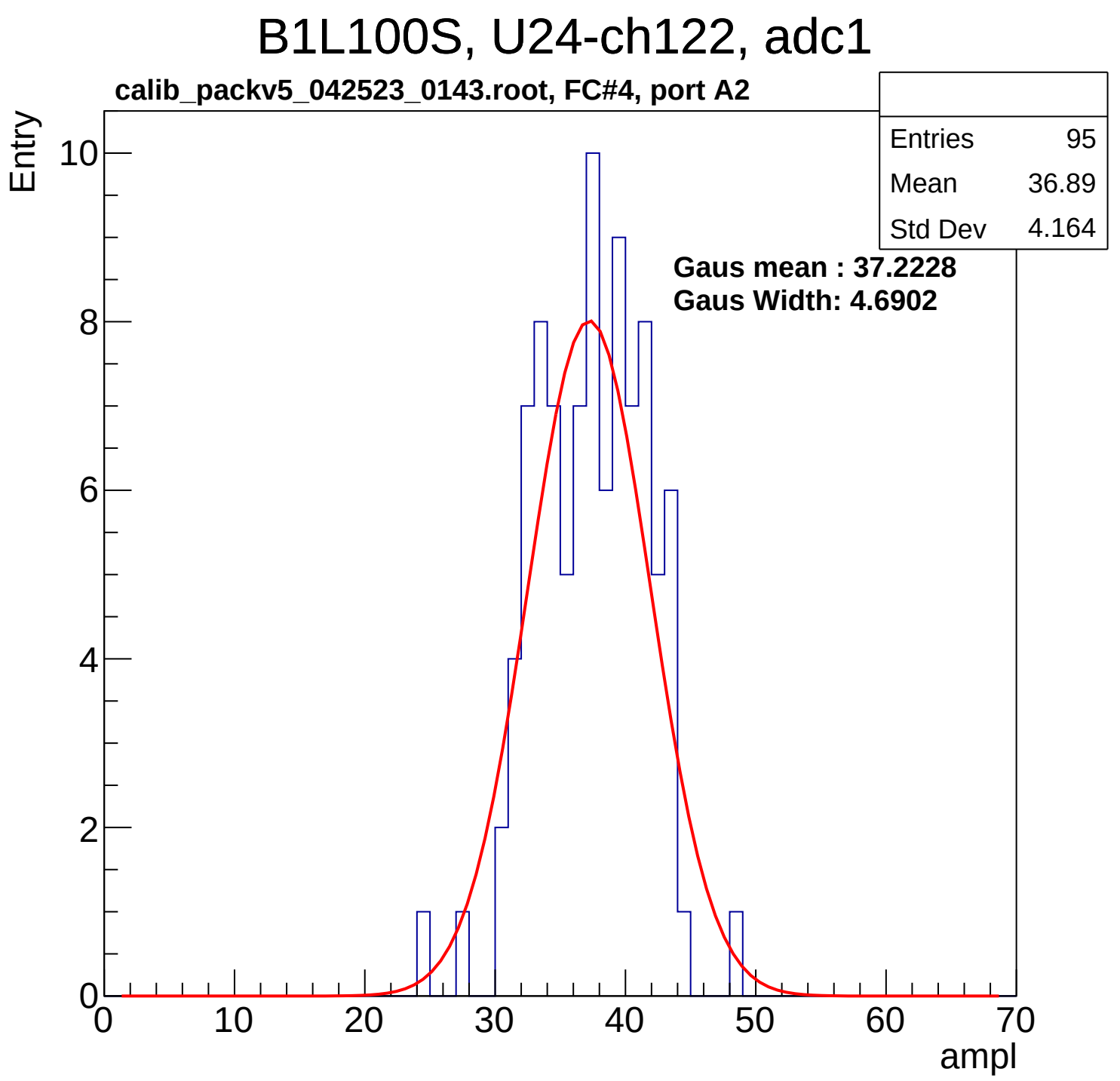
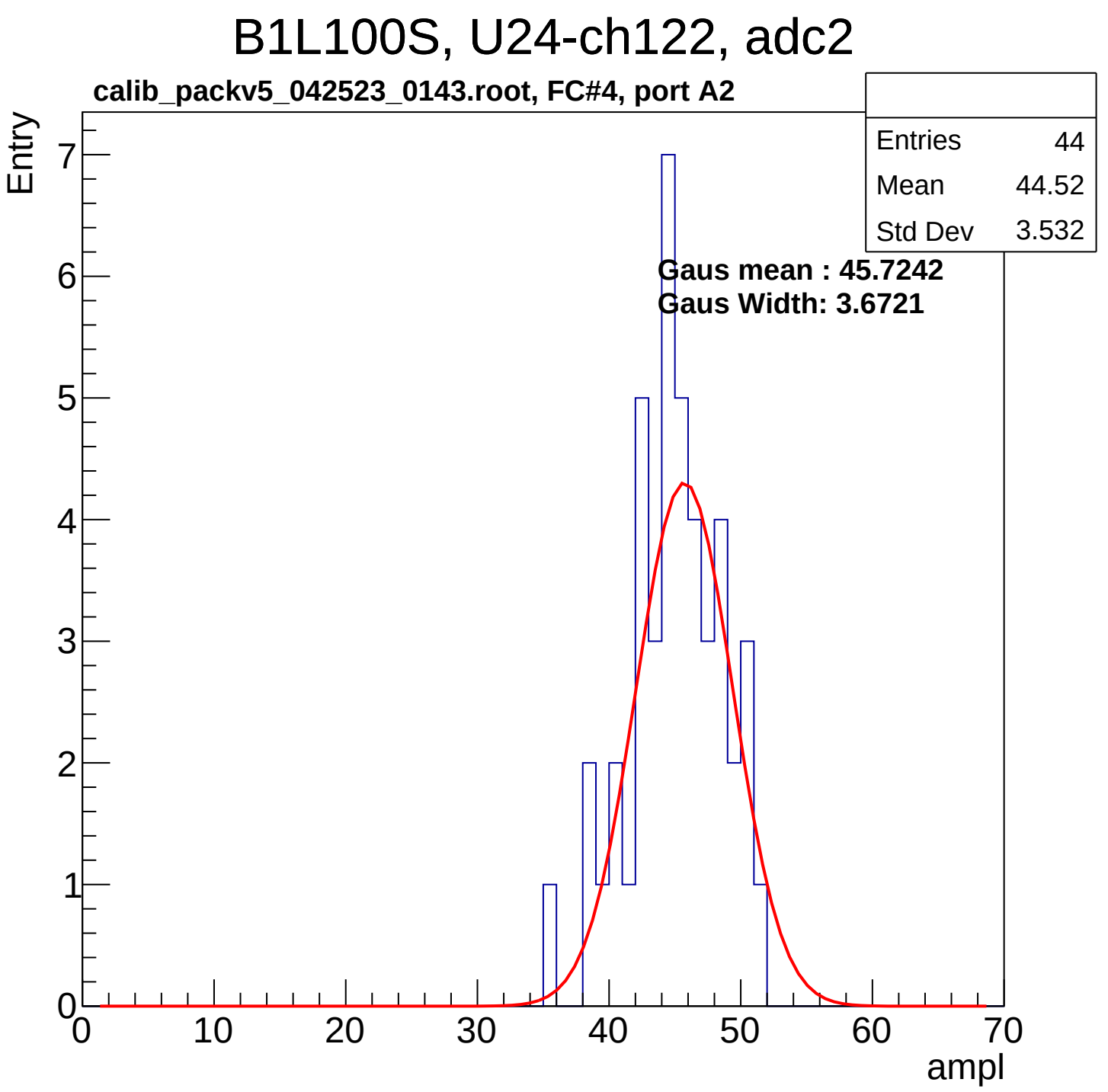
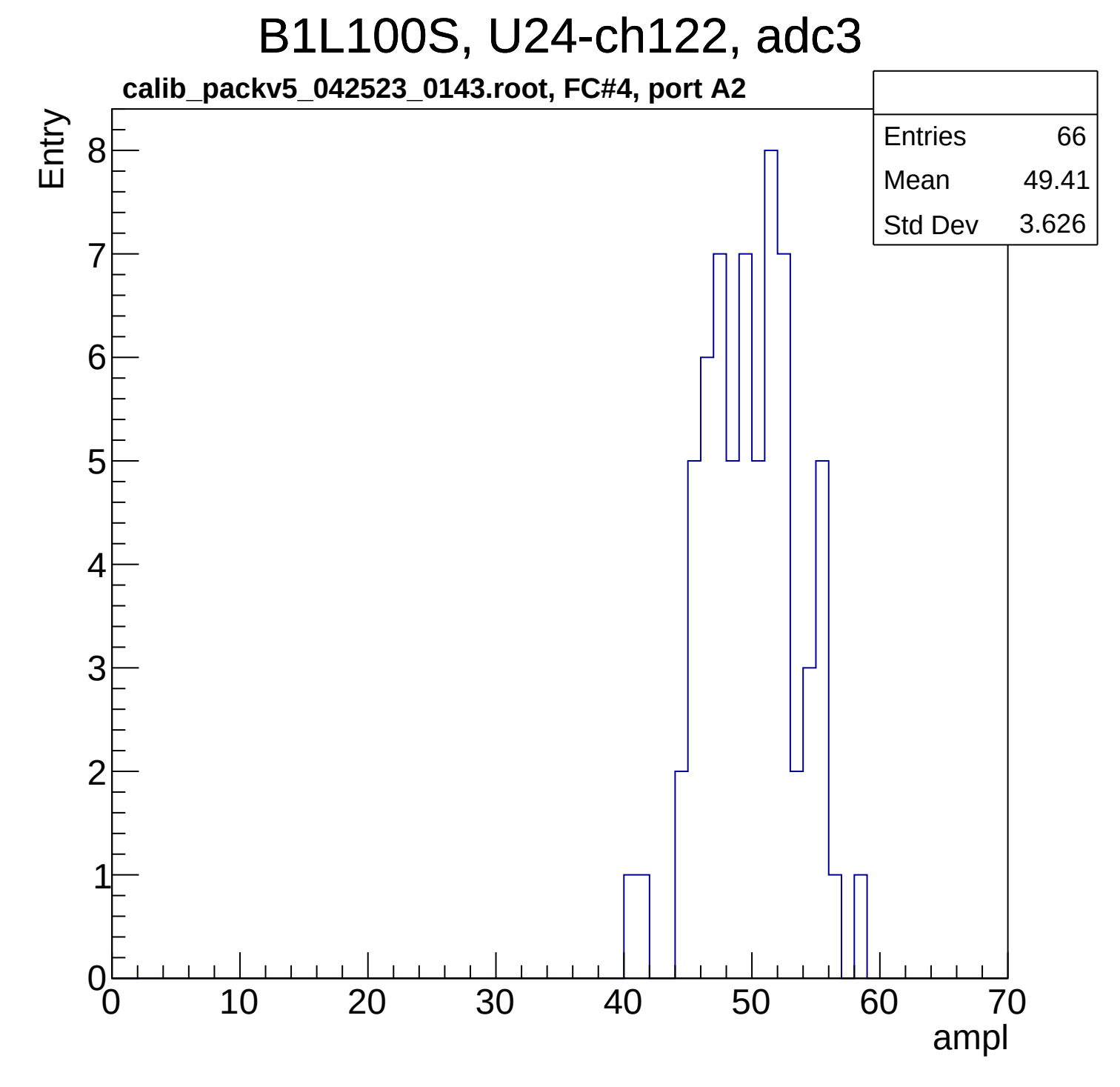
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 36.89
Std Dev 4.164
Gaus mean : 37.2228
Gaus Width: 4.6902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 44.52
Std Dev 3.532
Gaus mean : 45.7242
Gaus Width: 3.6721
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.41
Std Dev 3.626
Entry
ampl
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2
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4
5
6
7
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0
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40
50
60
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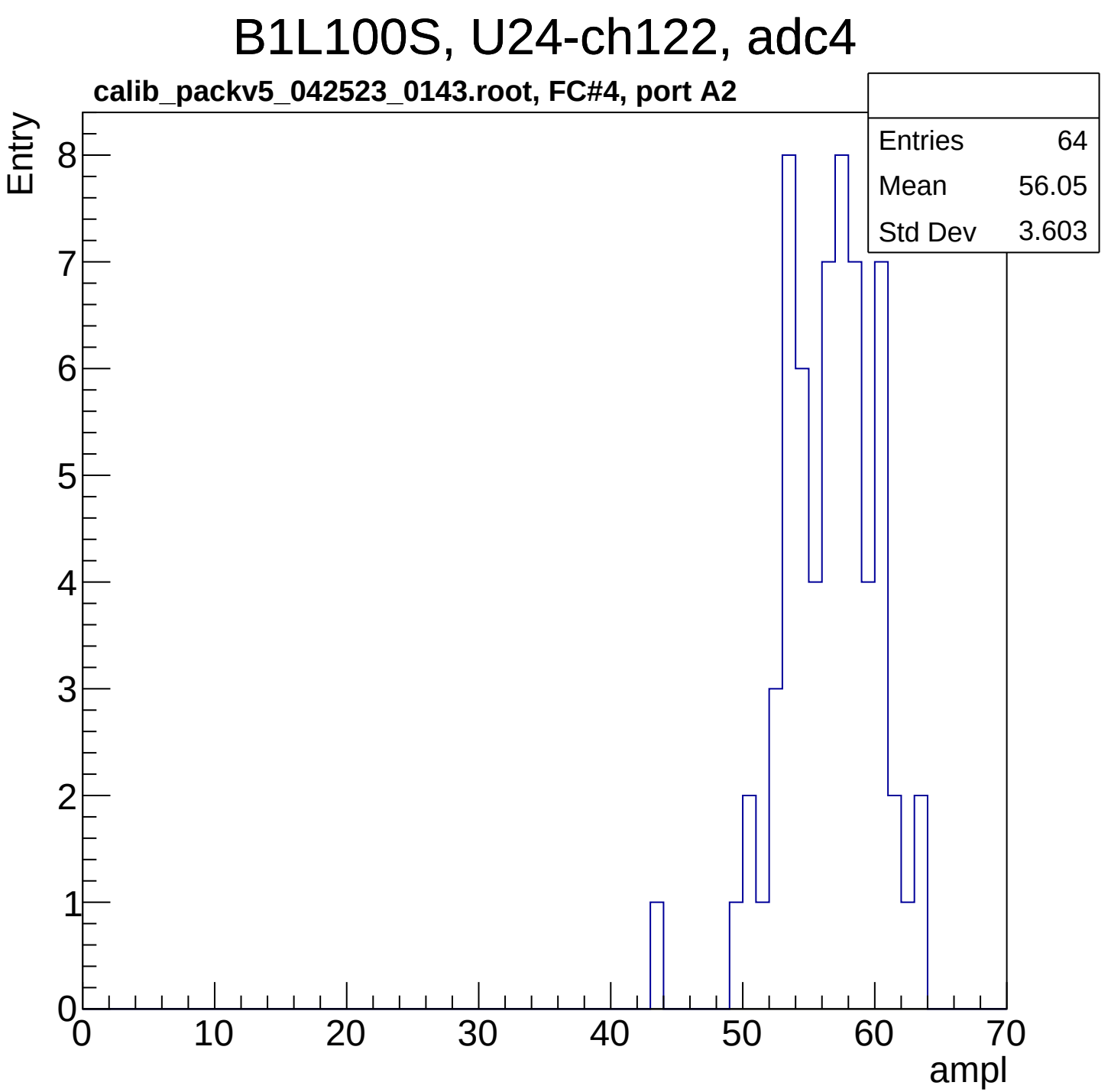
B1L100S, U24-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 56.05
Std Dev 3.603
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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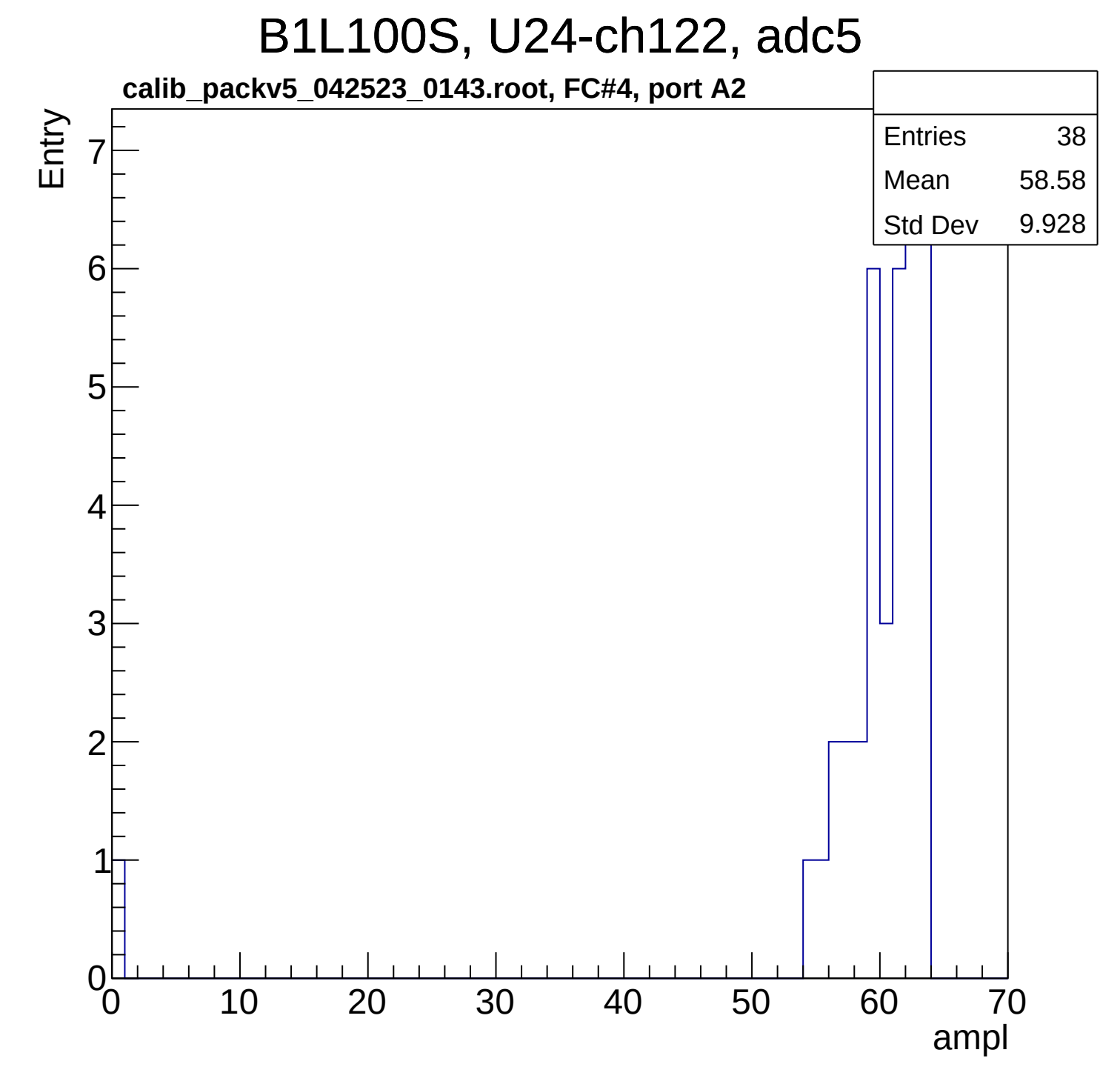
B1L100S, U24-ch122, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.58
Std Dev 9.928
Entry
ampl
0
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2
3
4
5
6
7
0
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20
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40
50
60
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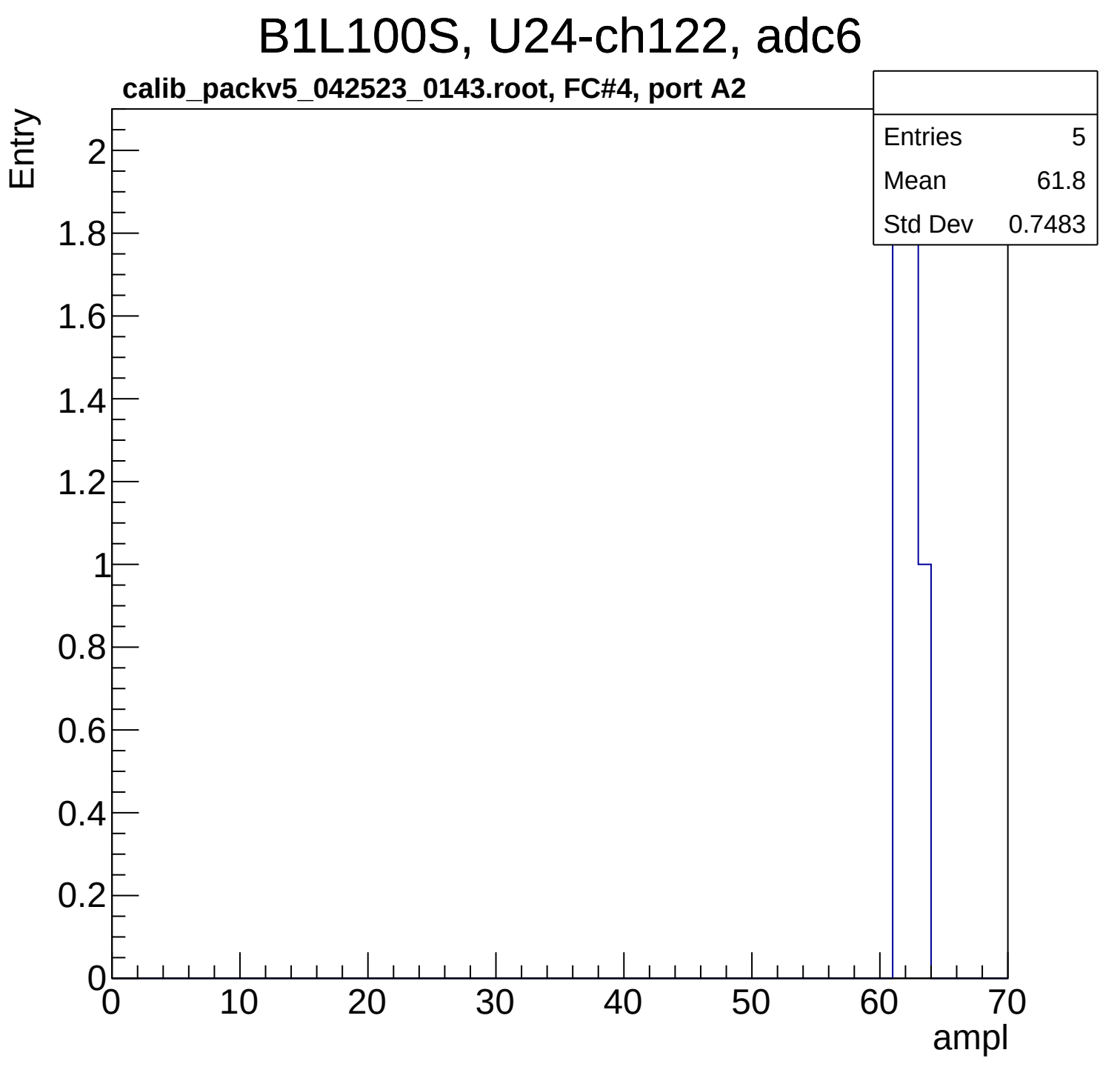
B1L100S, U24-ch122, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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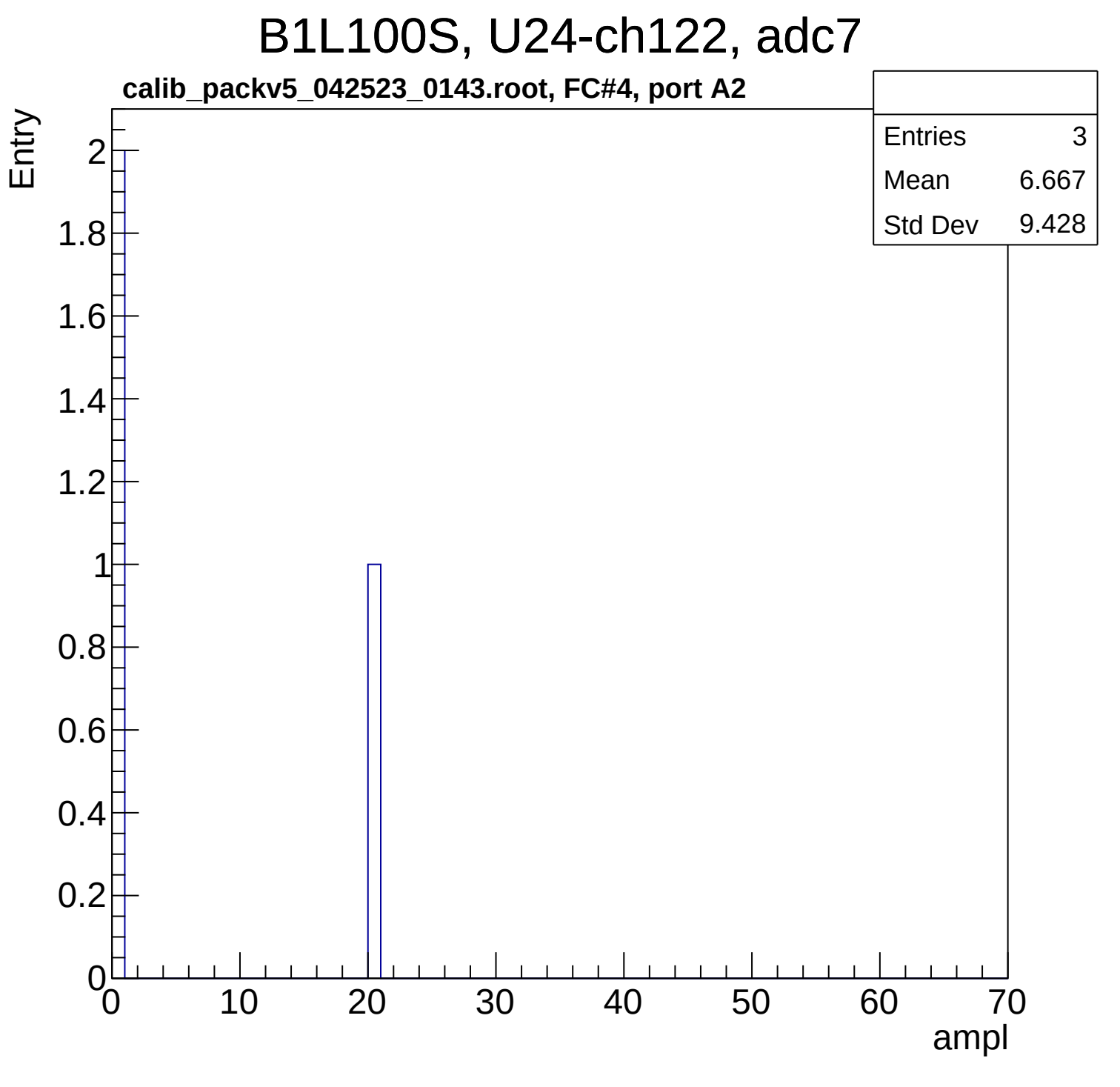
B1L100S, U24-ch122, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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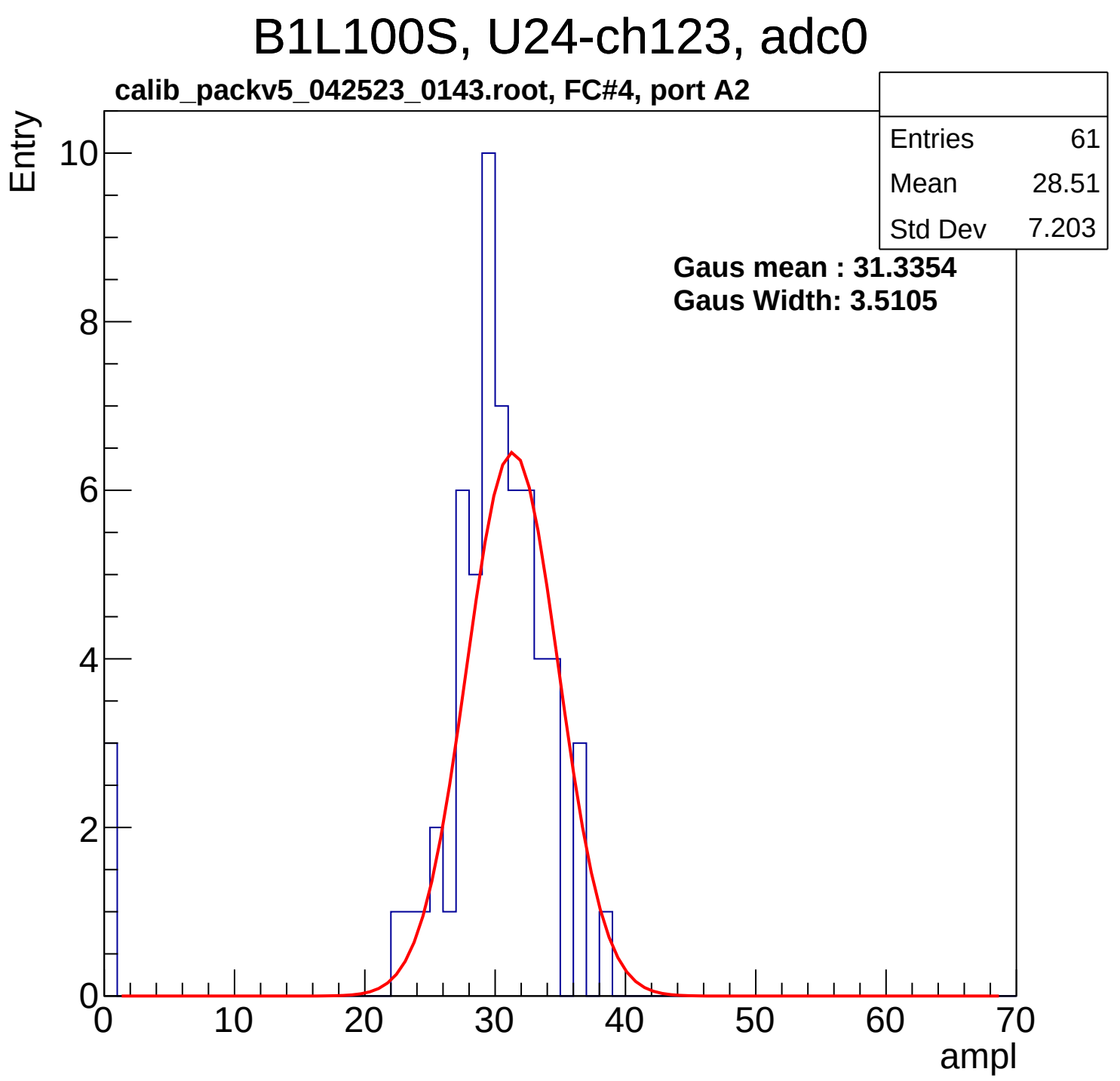
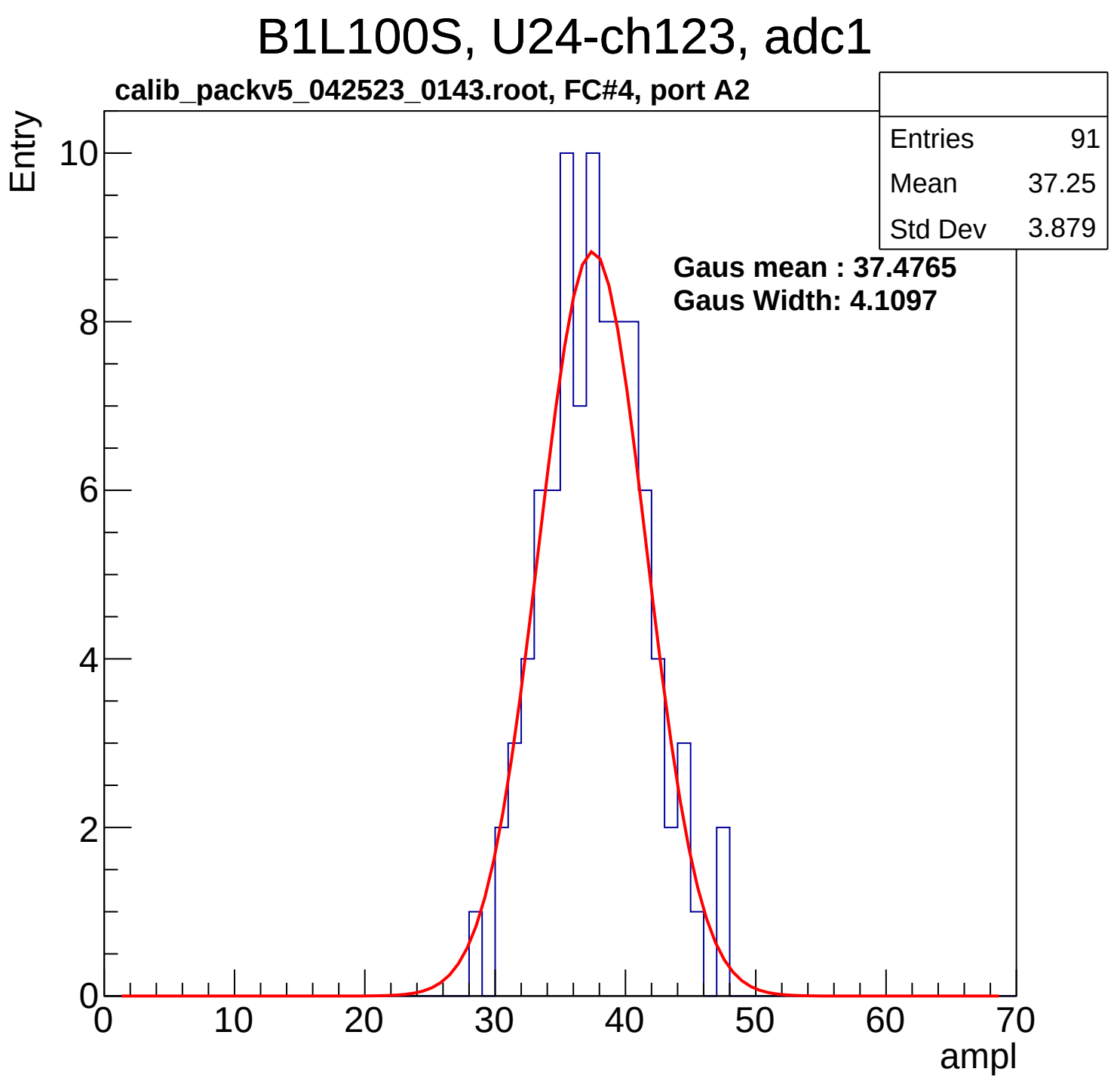
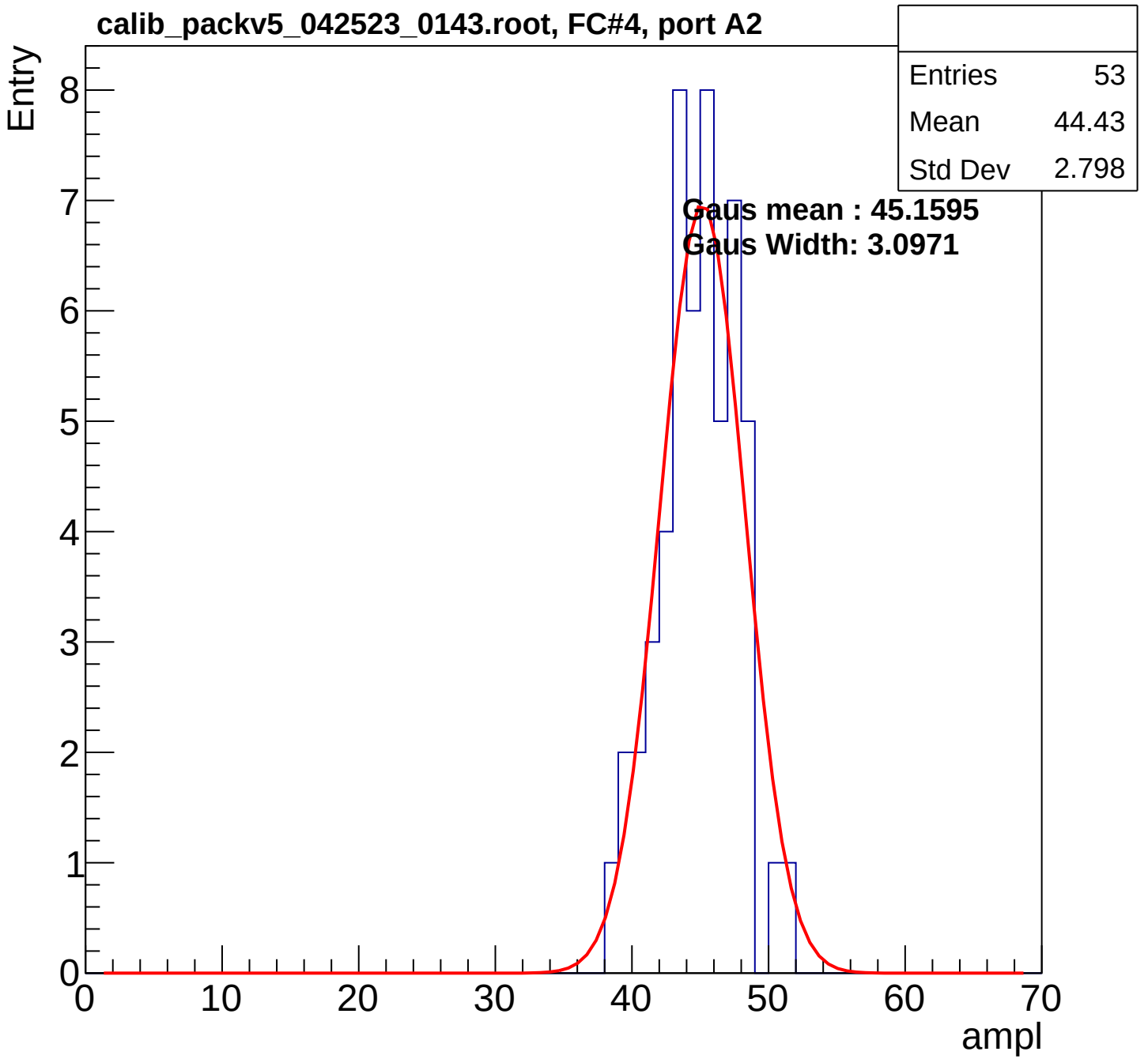

B1L100S, U24-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.51
Std Dev 7.203
Gaus mean : 31.3354
Gaus Width: 3.5105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 37.25
Std Dev 3.879
Gaus mean : 37.4765
Gaus Width: 4.1097
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 44.43
Std Dev 2.798
Gaus mean : 45.1595
Gaus Width: 3.0971
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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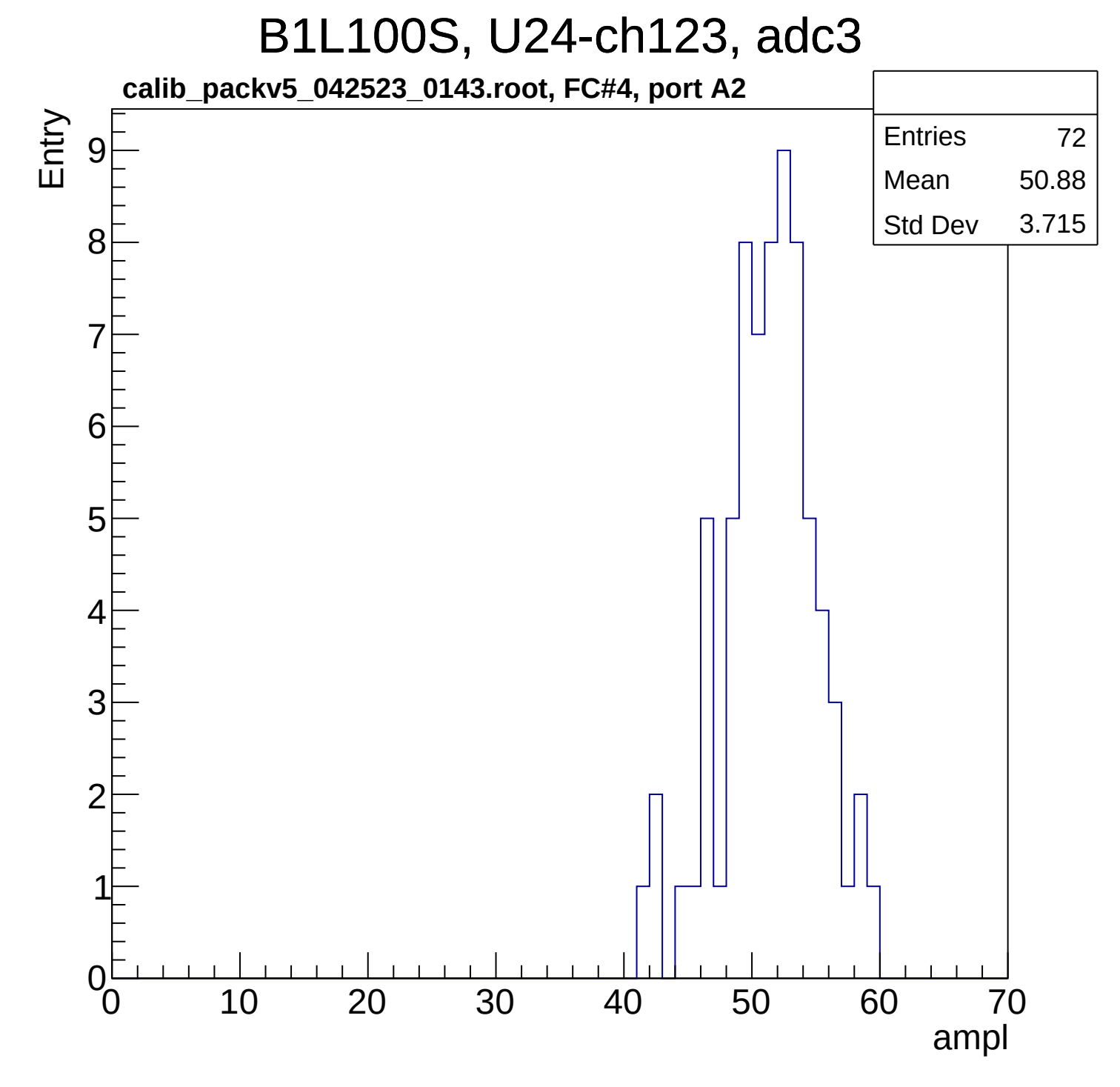
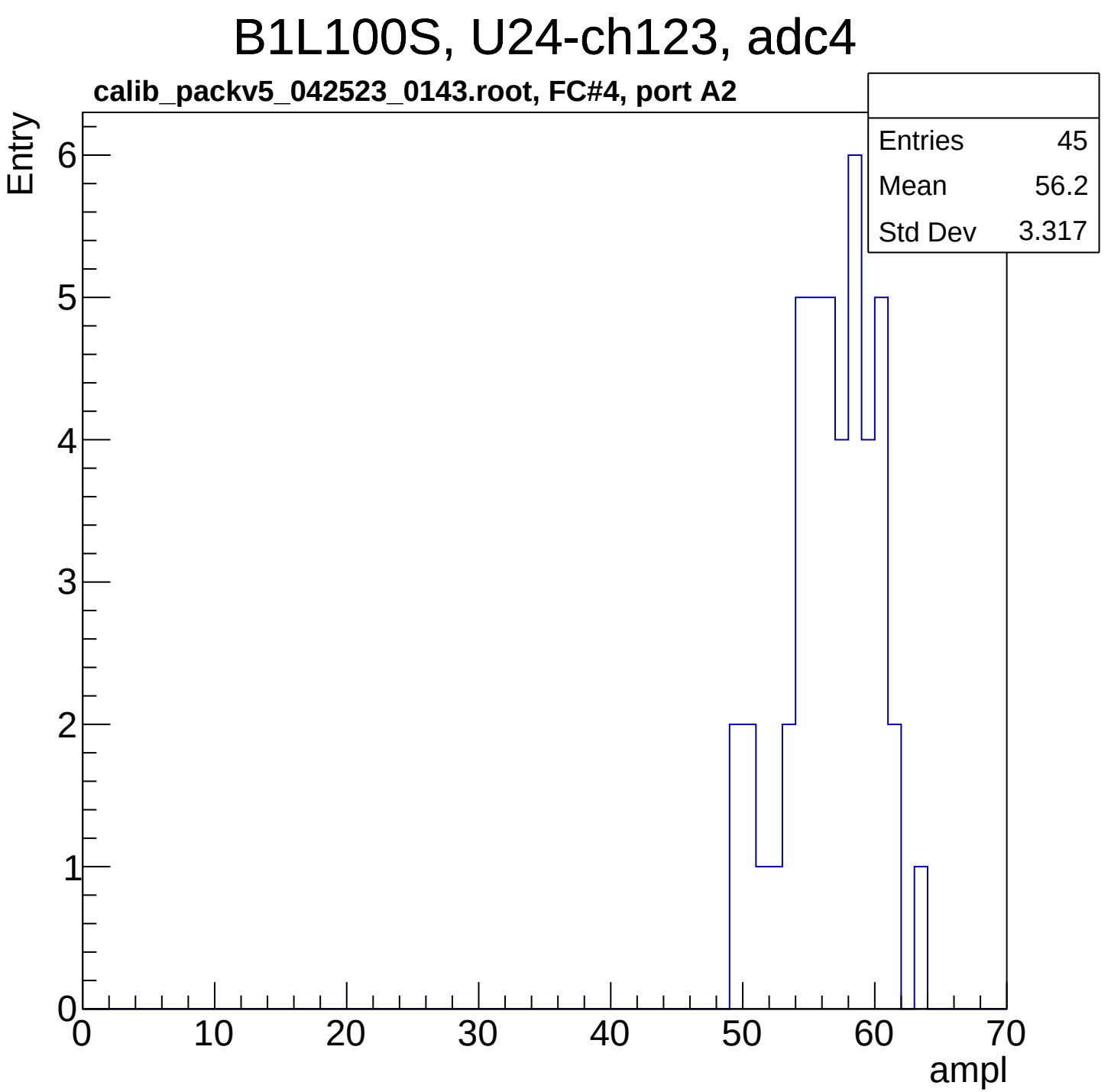

B1L100S, U24-ch123, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.88
Std Dev 3.715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 56.2
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
0
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60
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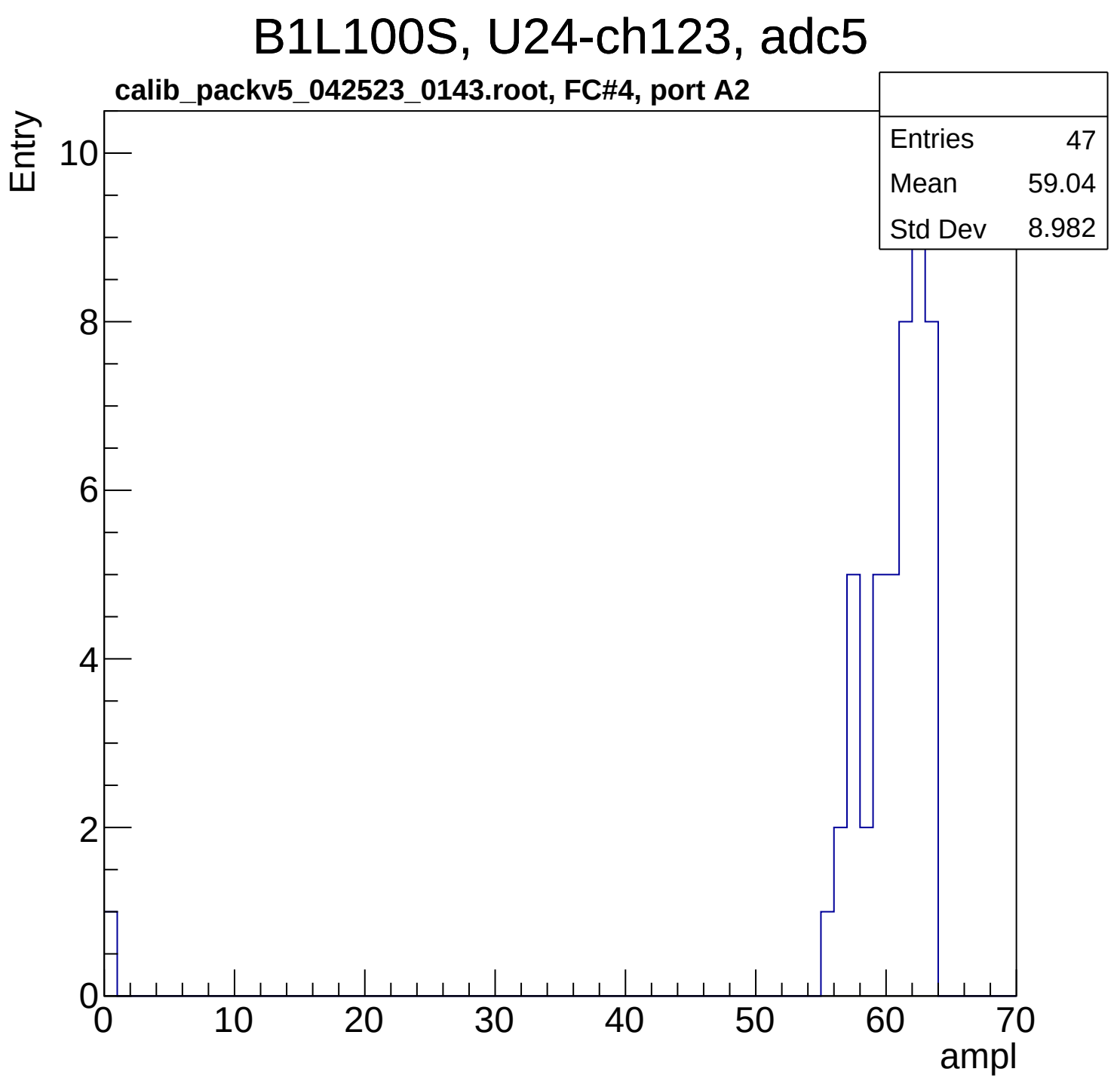
B1L100S, U24-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.04
Std Dev 8.982
Entry
ampl
0
2
4
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8
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40
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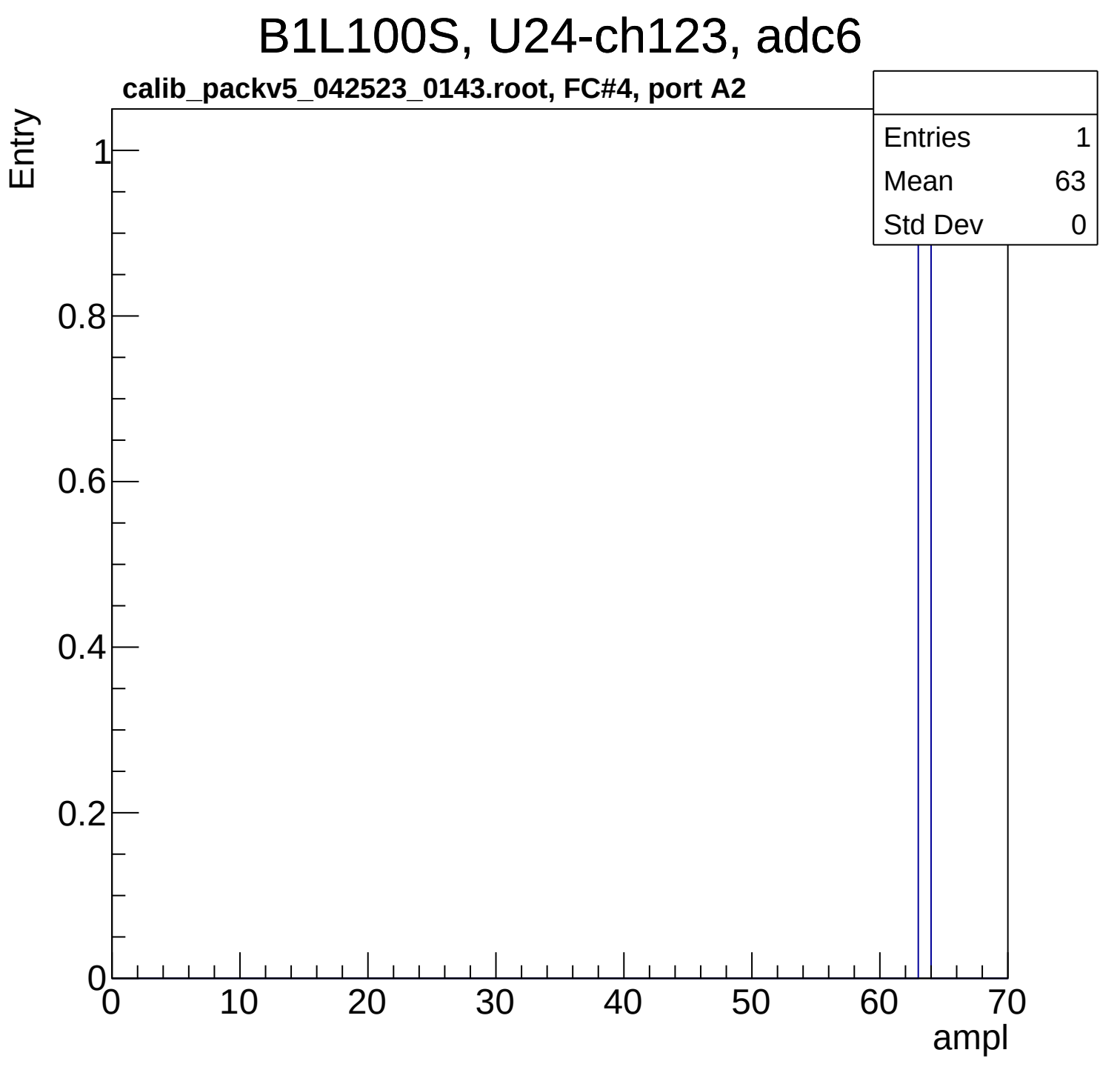
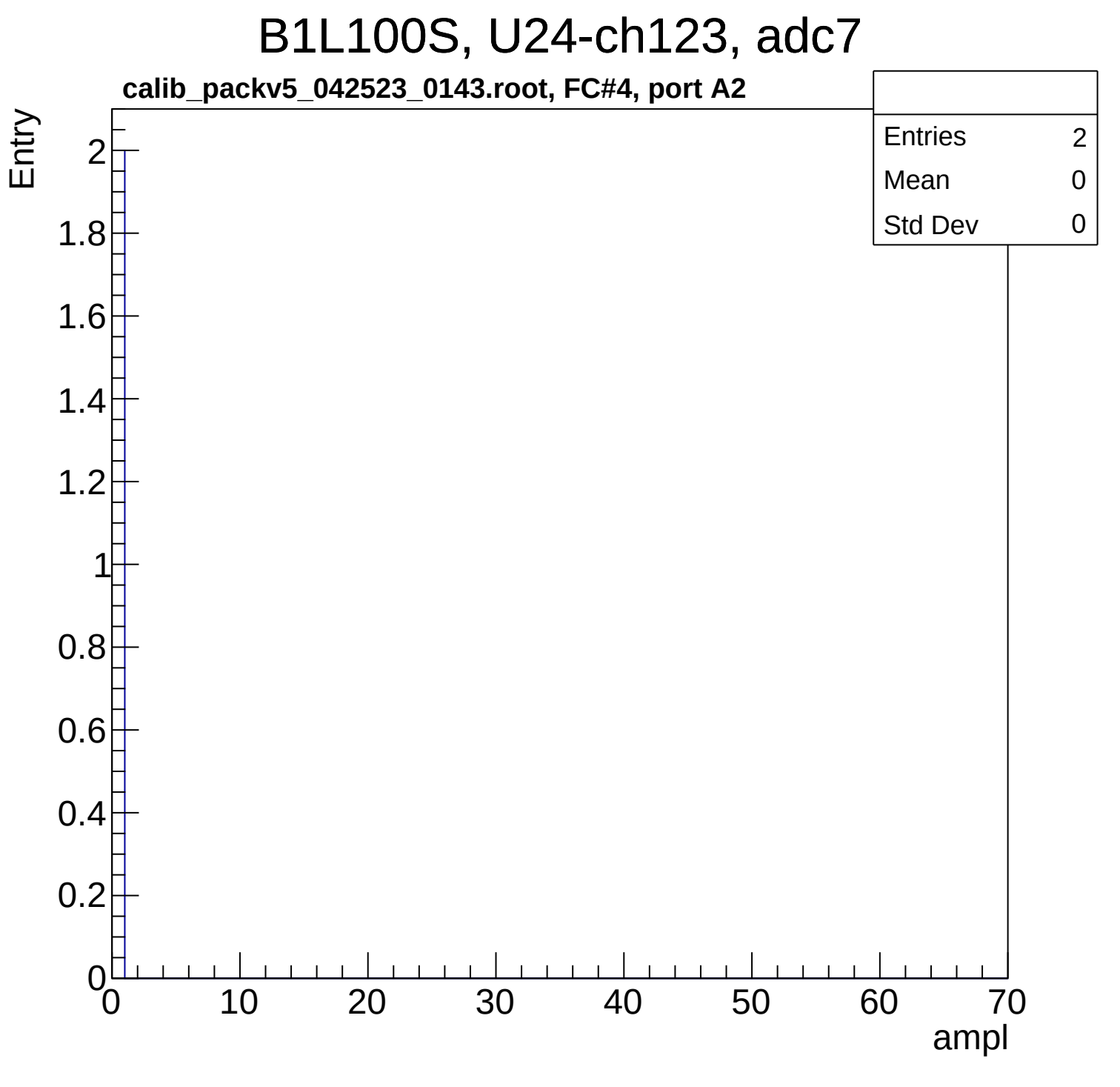
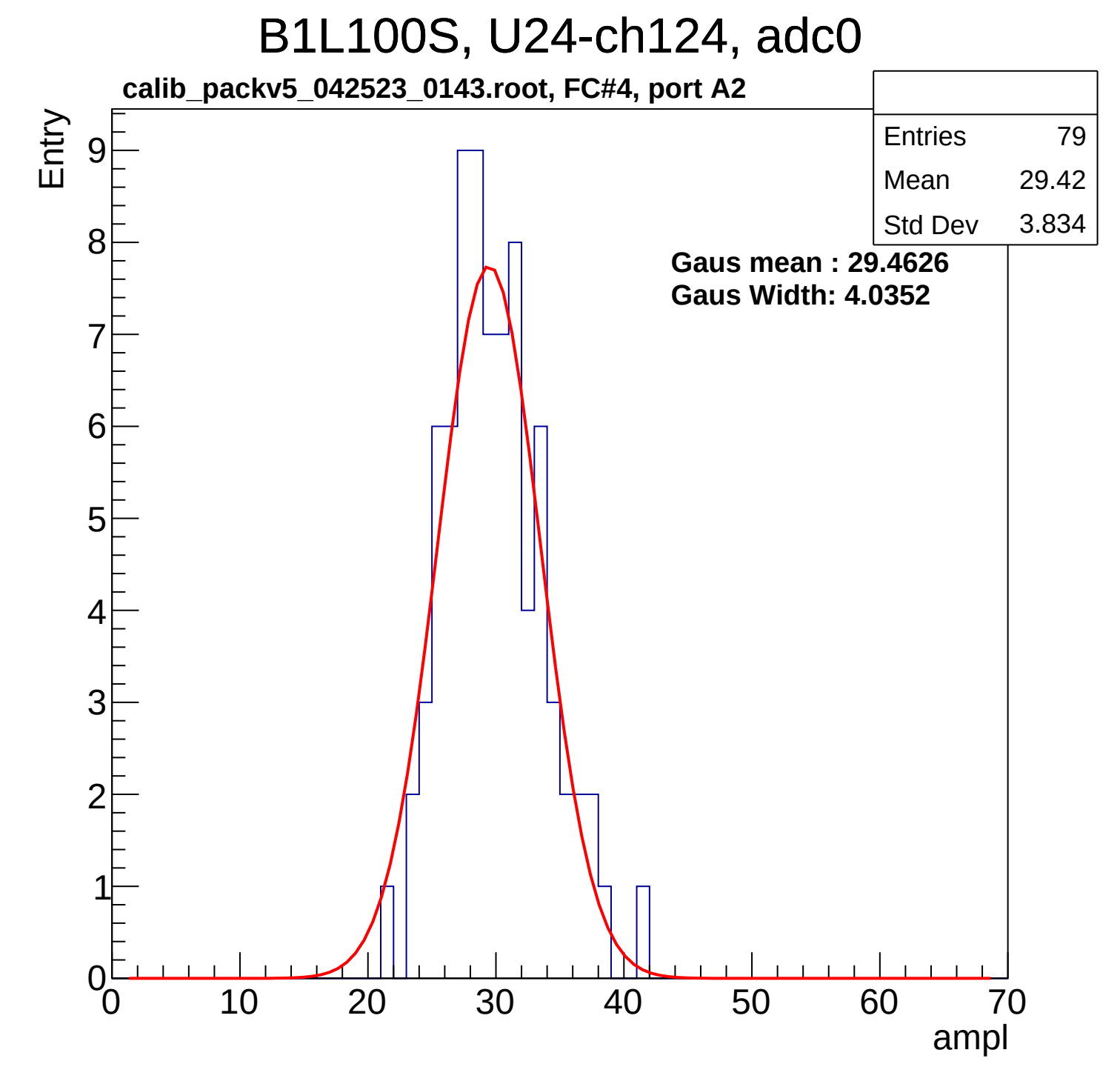

B1L100S, U24-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.42
Std Dev 3.834
Gaus mean : 29.4626
Gaus Width: 4.0352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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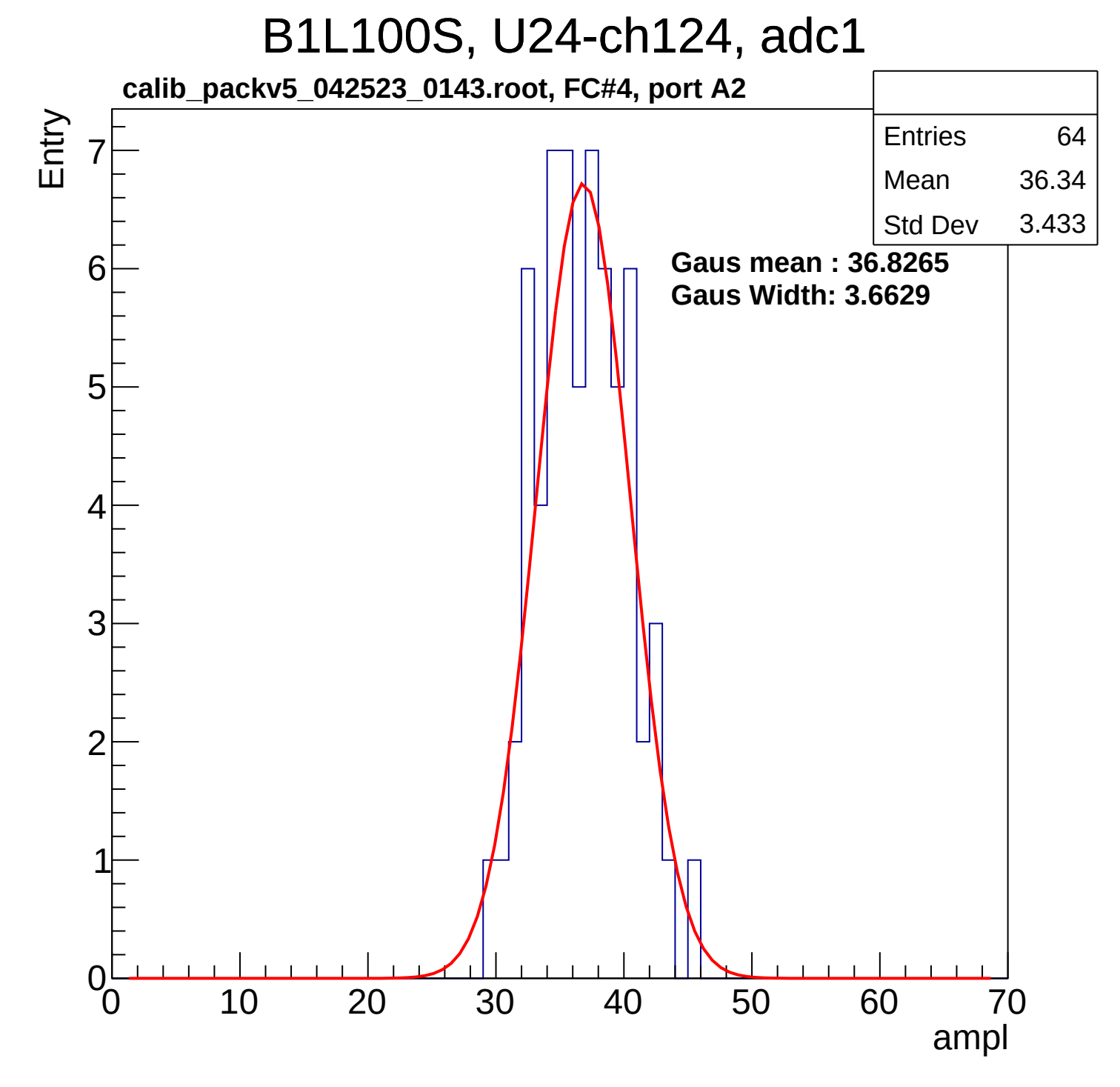
B1L100S, U24-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.34
Std Dev 3.433
Gaus mean : 36.8265
Gaus Width: 3.6629
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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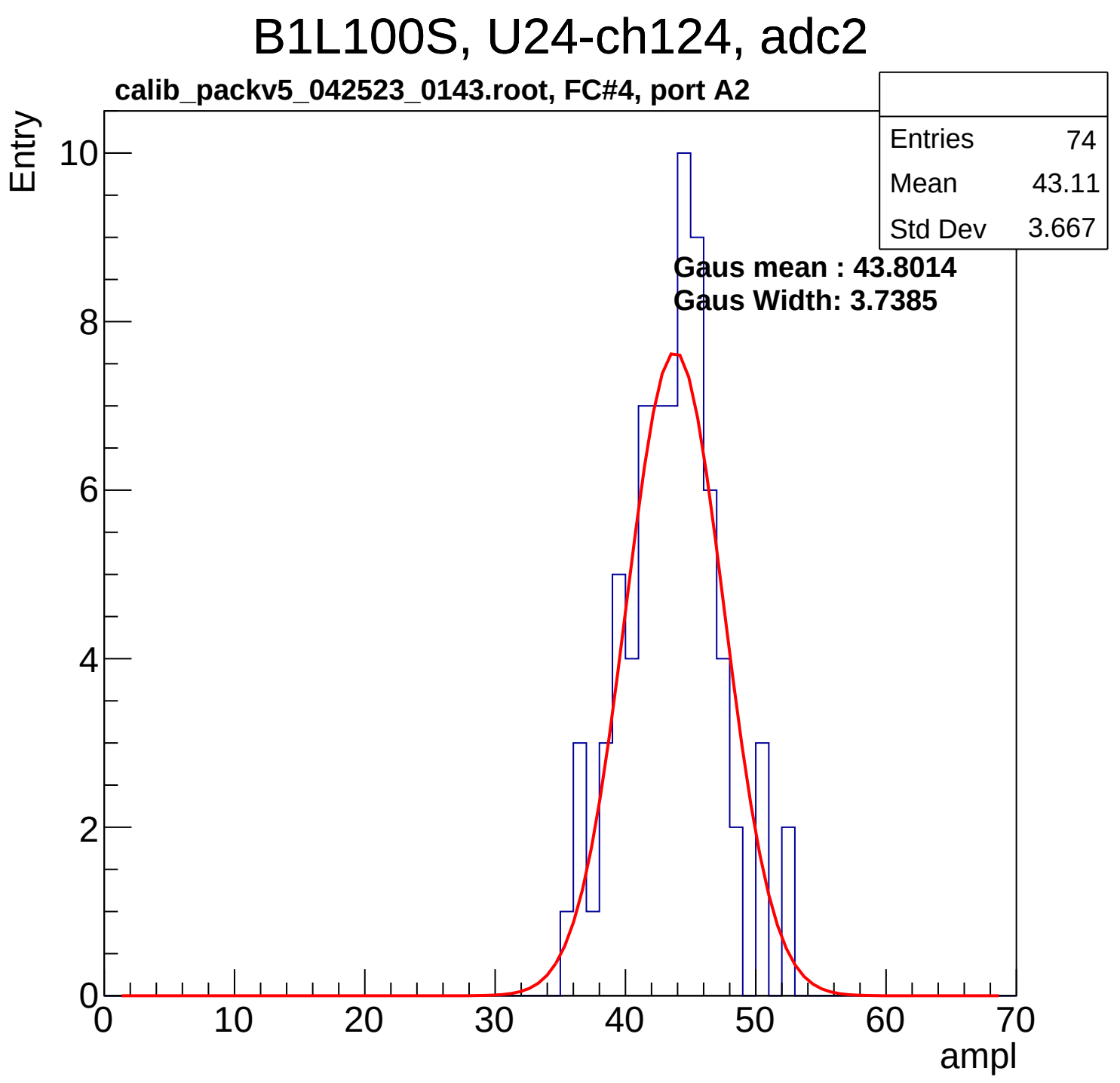
B1L100S, U24-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 43.11
Std Dev 3.667
Gaus mean : 43.8014
Gaus Width: 3.7385
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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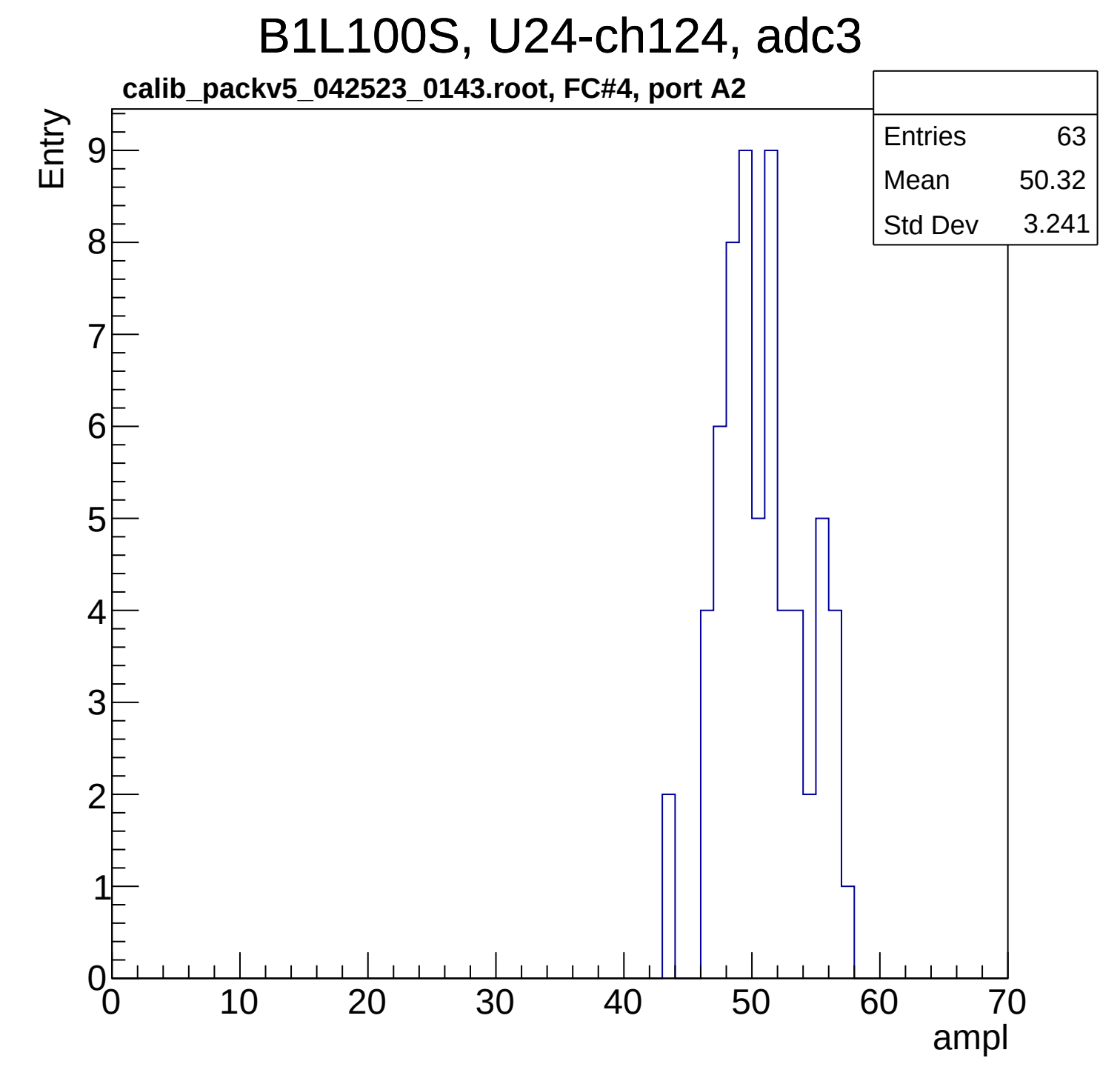
B1L100S, U24-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.32
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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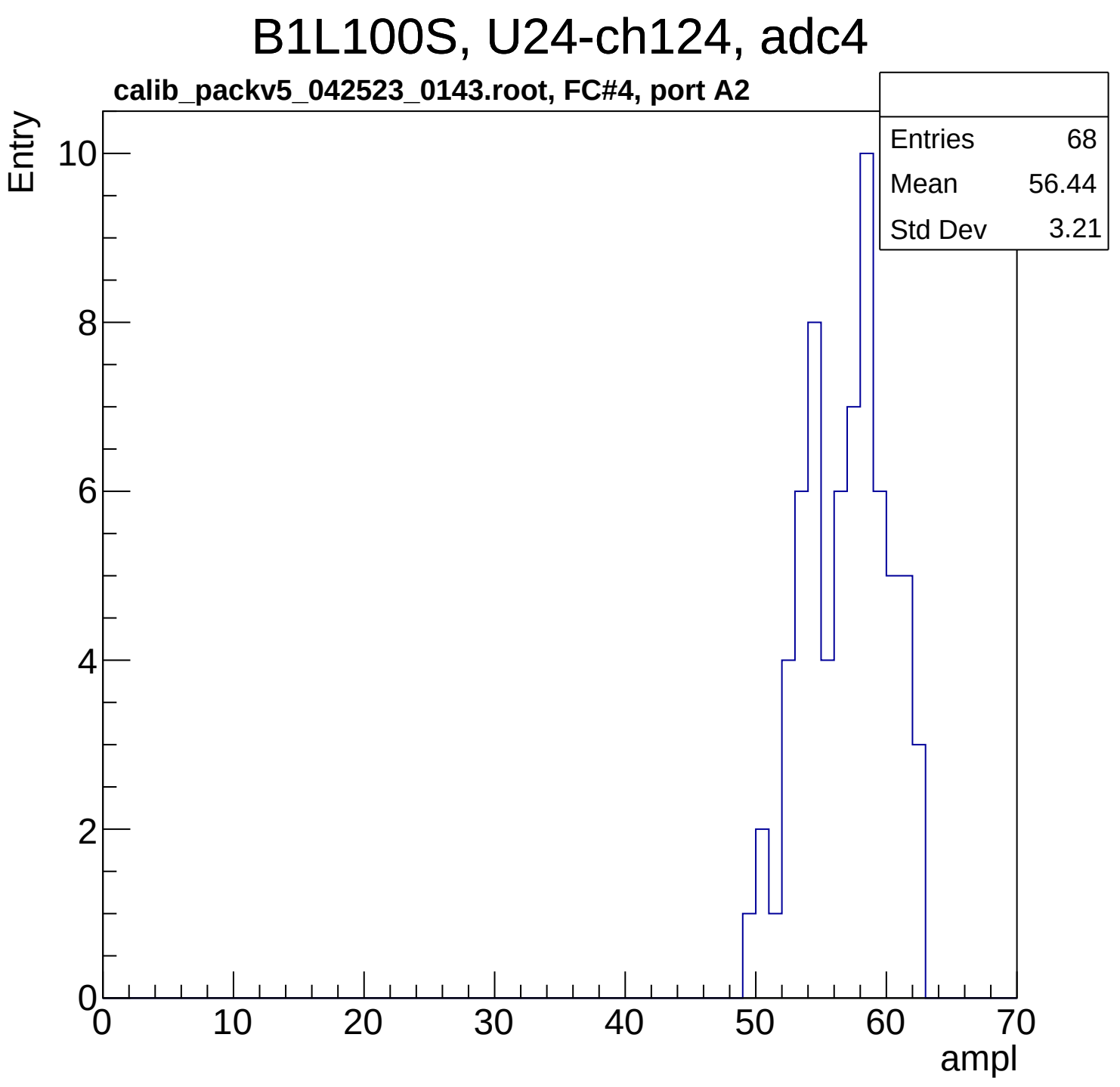
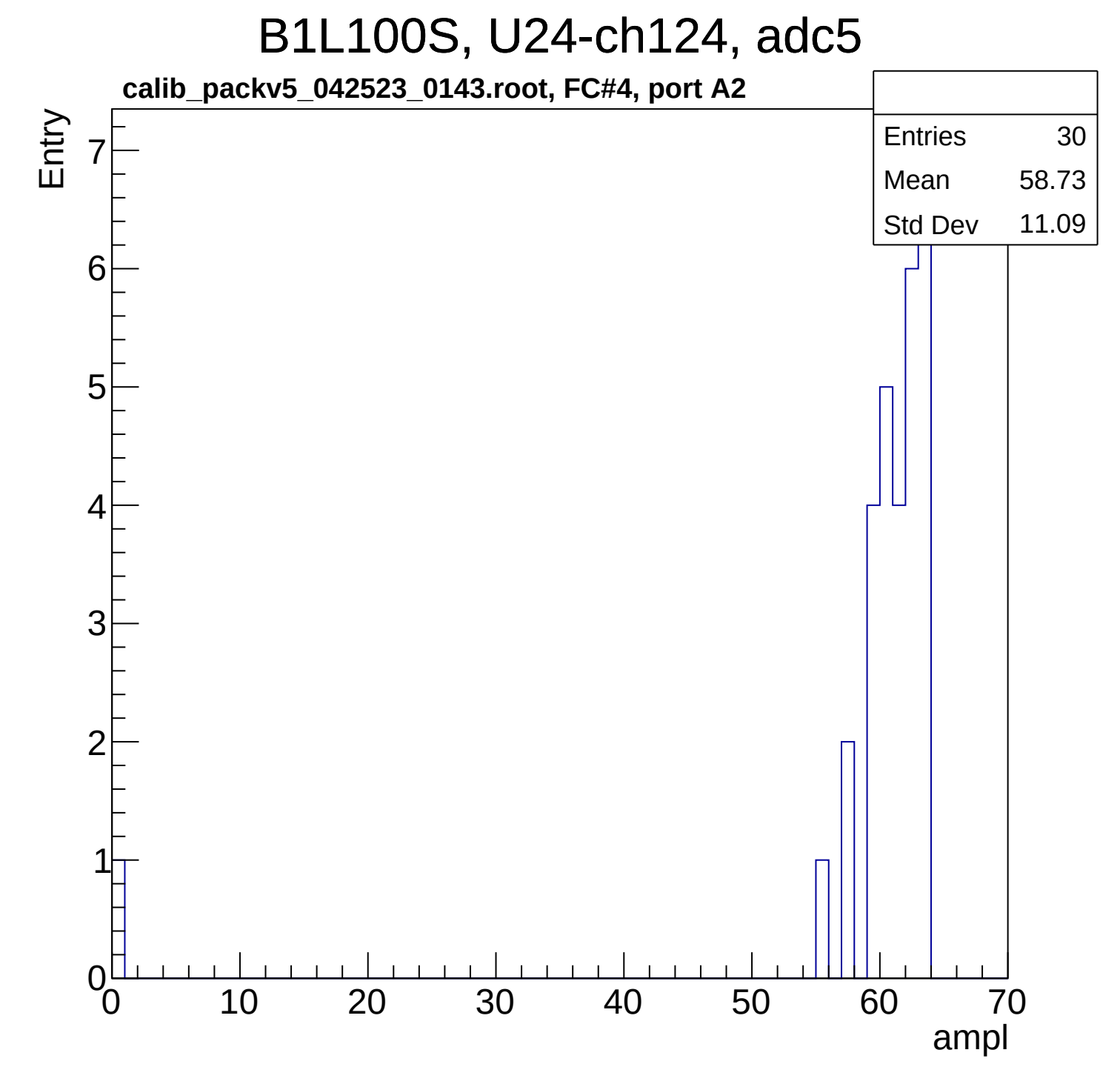

B1L100S, U24-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.44
Std Dev 3.21
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch124, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.73
Std Dev 11.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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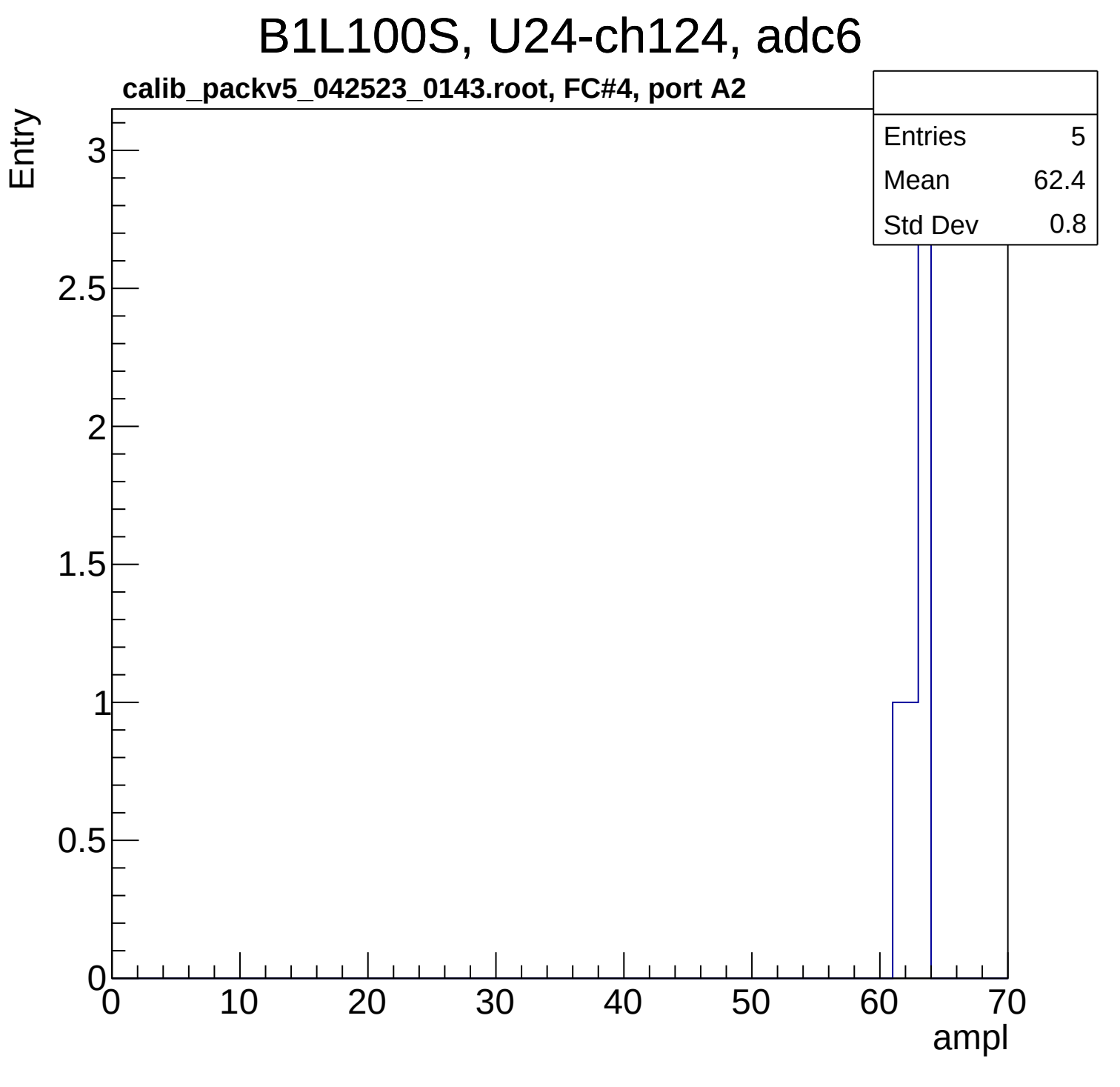
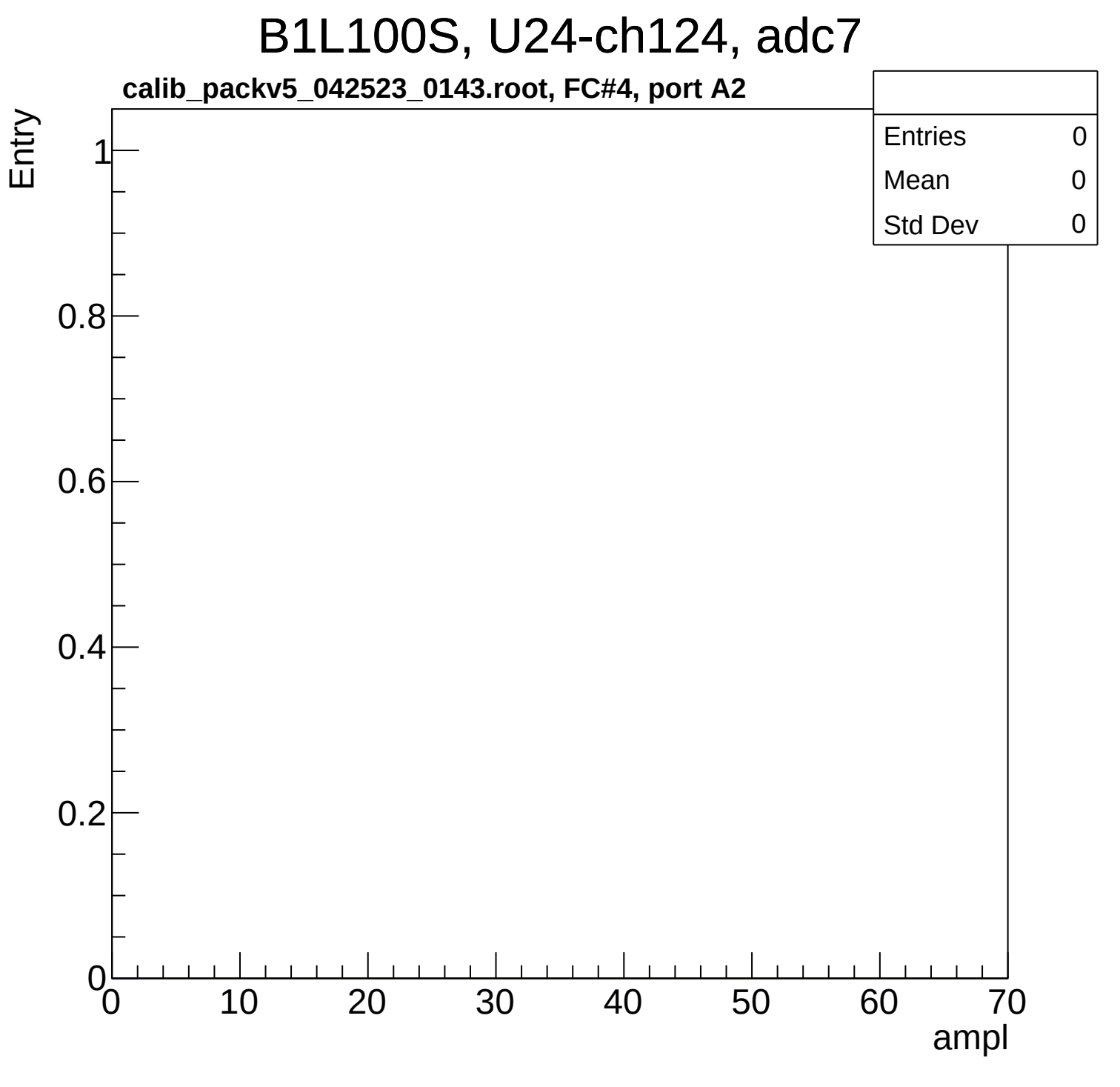
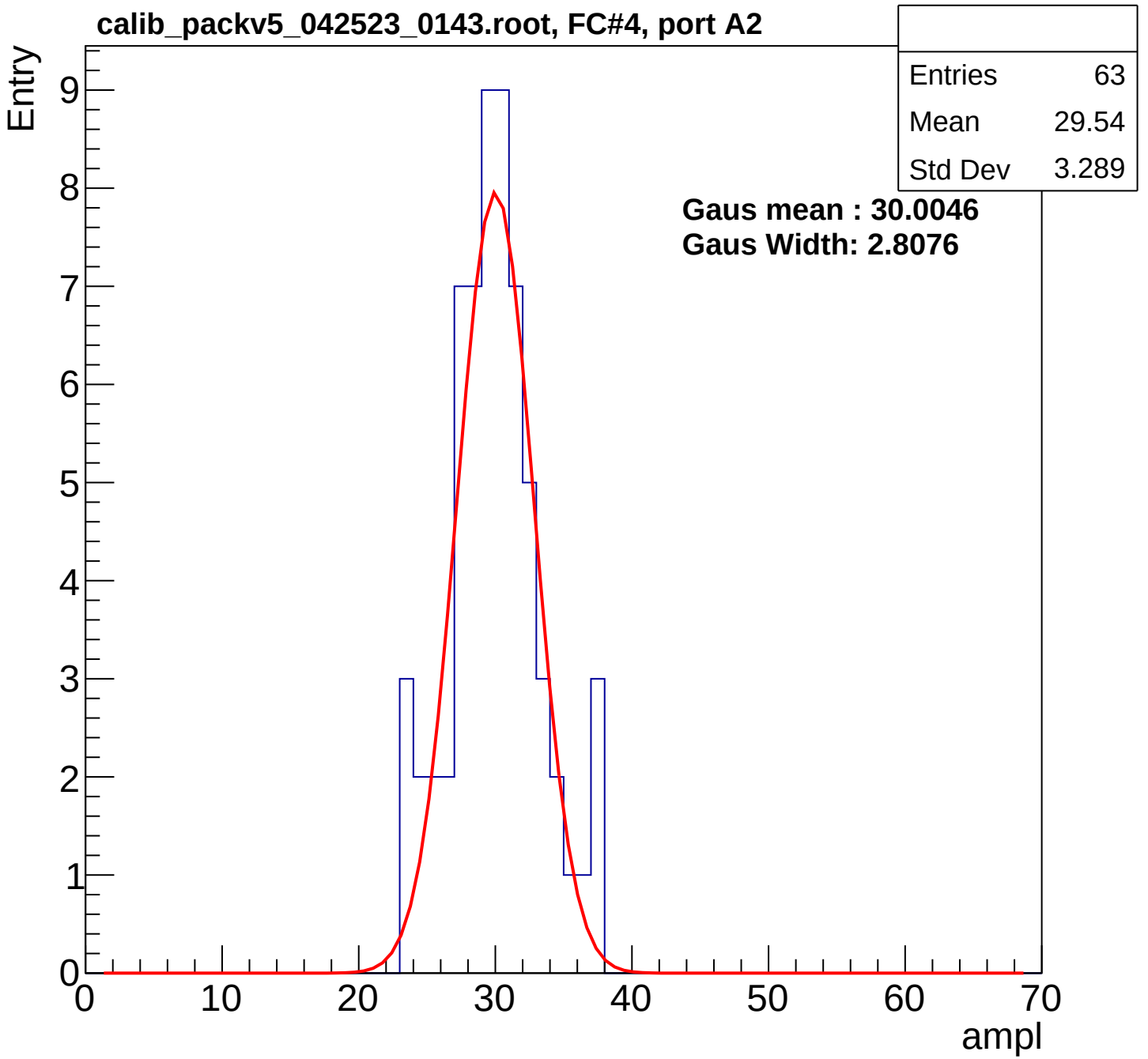
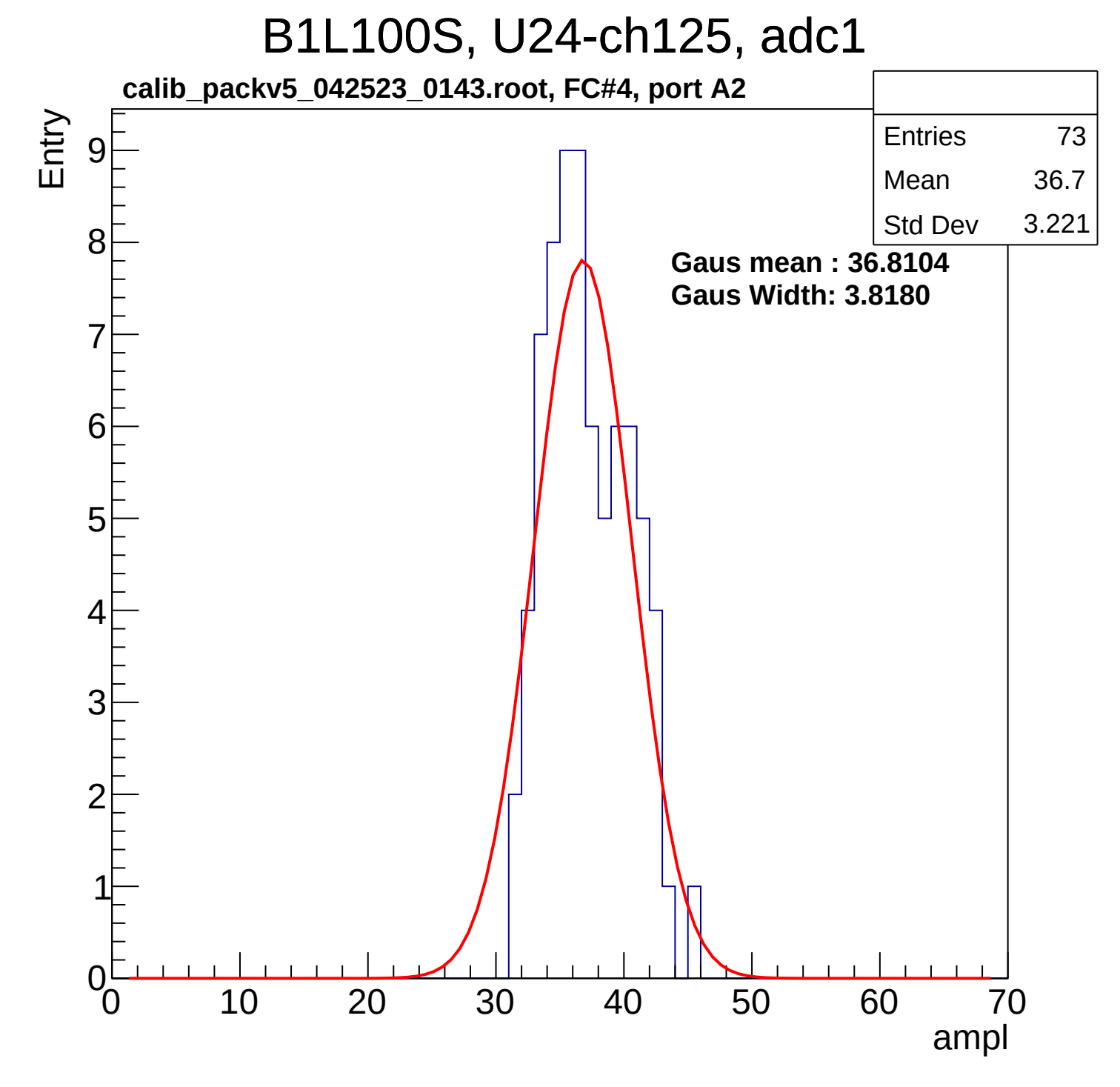
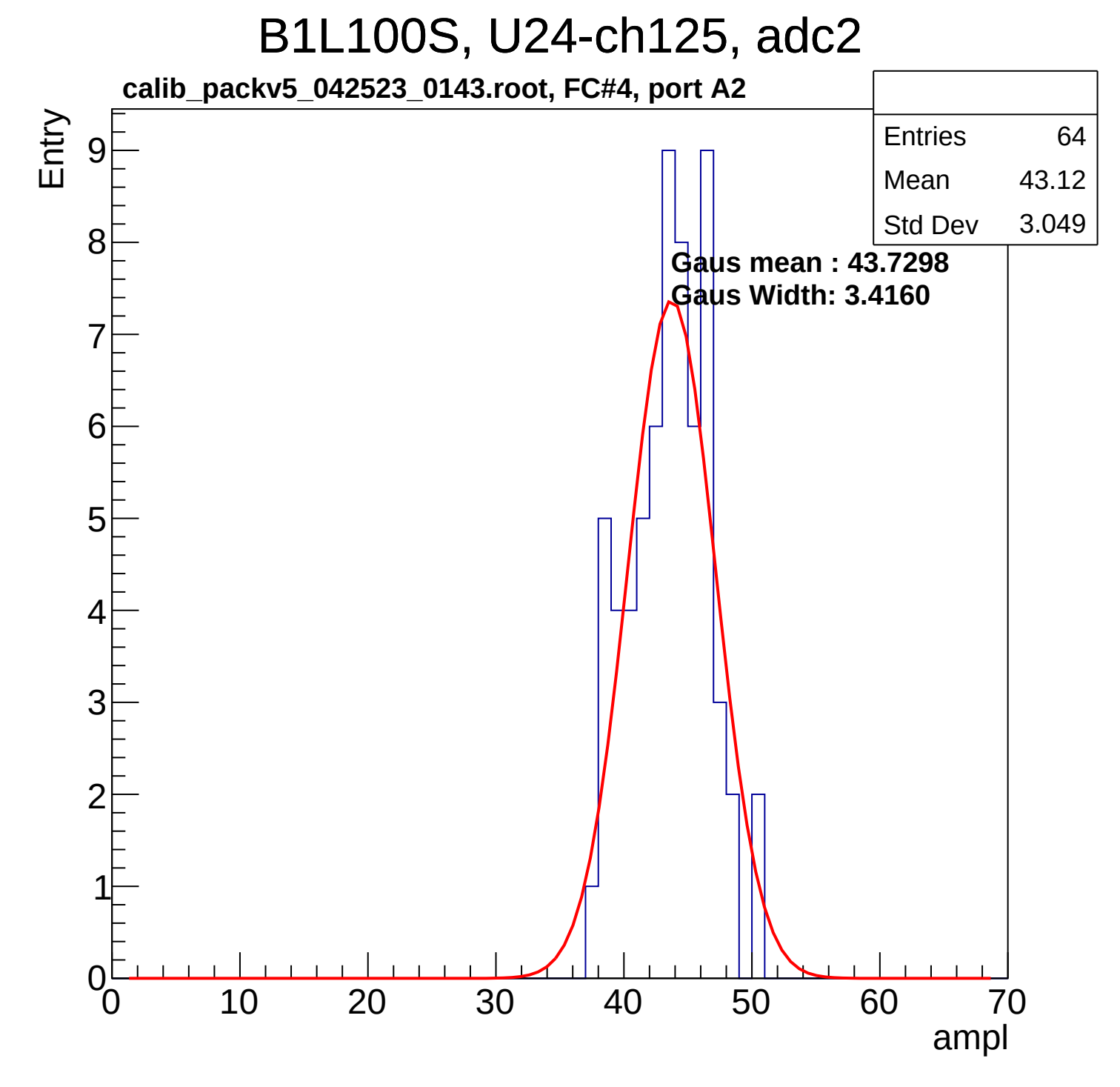
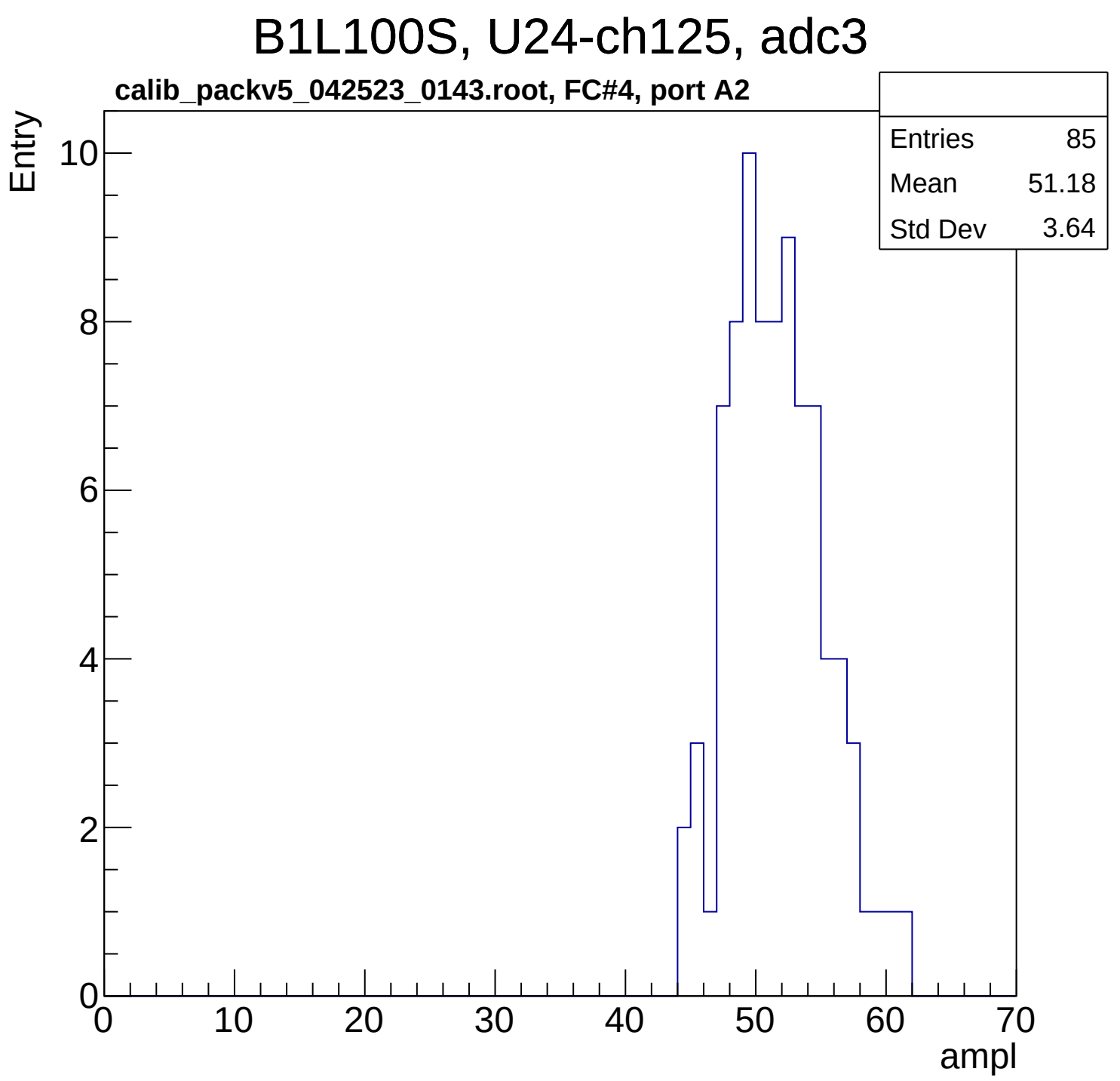

B1L100S, U24-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

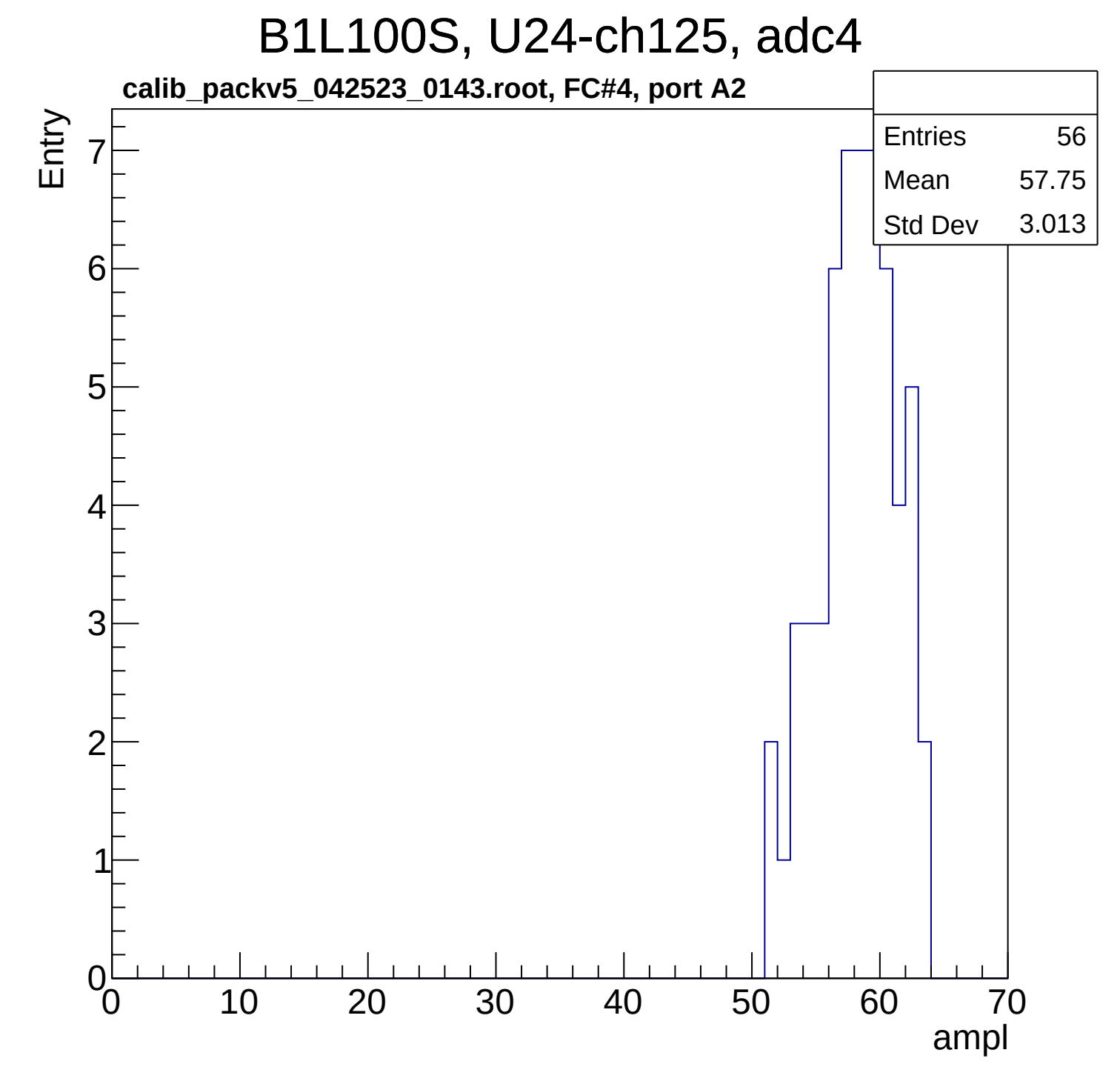

B1L100S, U24-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

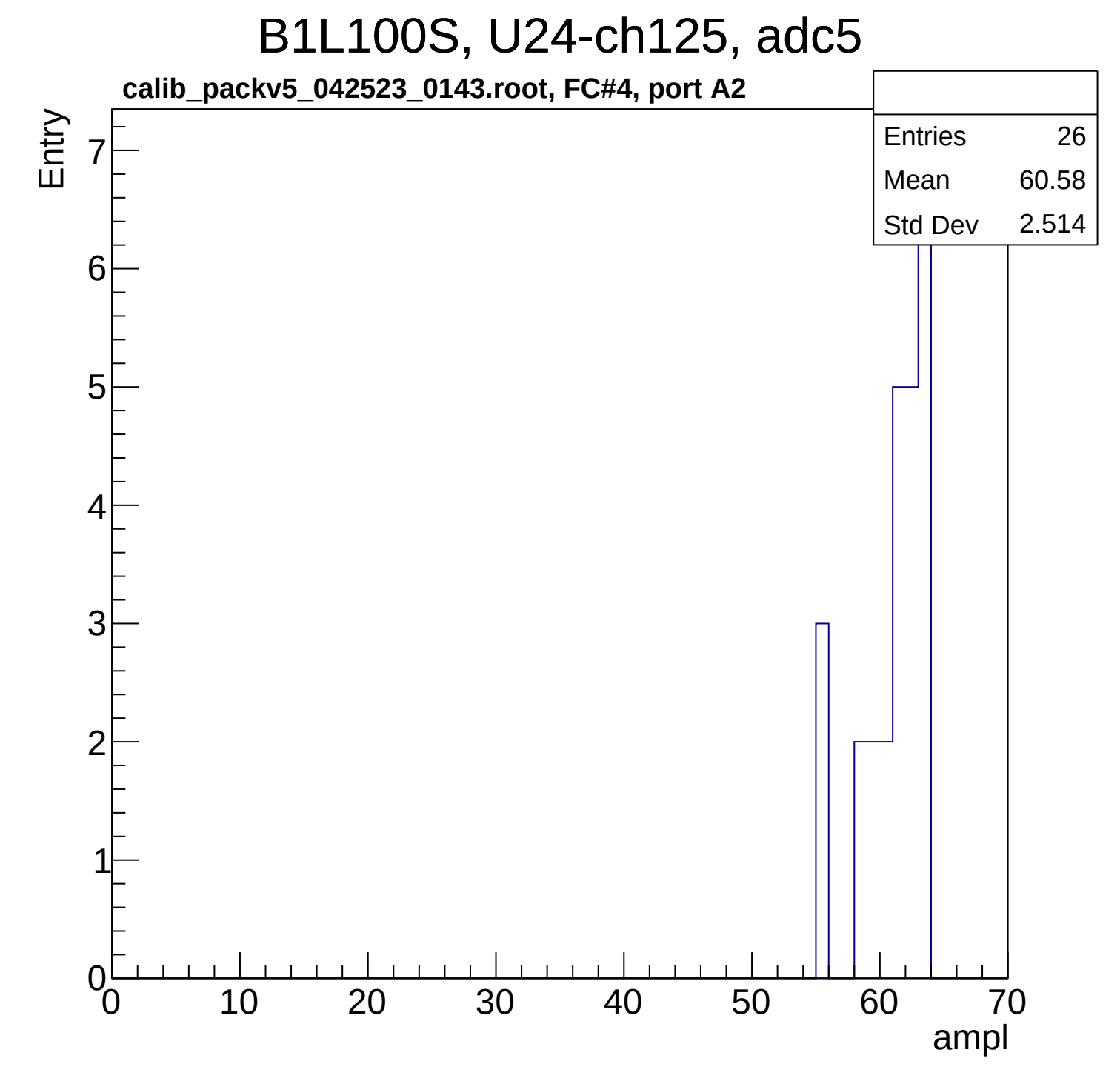

B1L100S, U24-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.54
Std Dev 3.289
Gaus mean : 30.0046
Gaus Width: 2.8076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

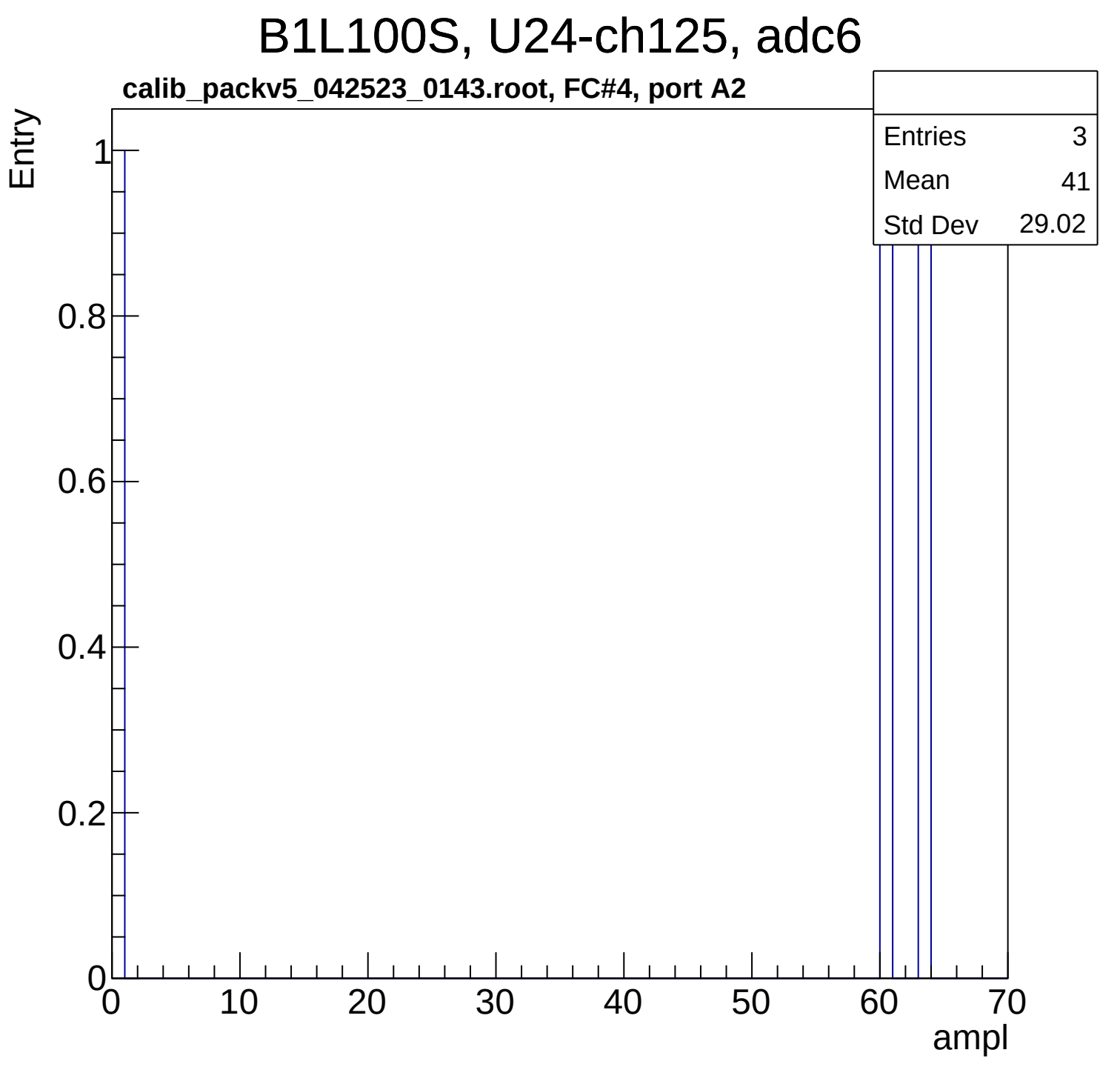

B1L100S, U24-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.7
Std Dev 3.221
Gaus mean : 36.8104
Gaus Width: 3.8180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.12
Std Dev 3.049
Gaus mean : 43.7298
Gaus Width: 3.4160
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U24-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 51.18
Std Dev 3.64
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U24-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.75
Std Dev 3.013
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 60.58
Std Dev 2.514
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41
Std Dev 29.02
Entry
1
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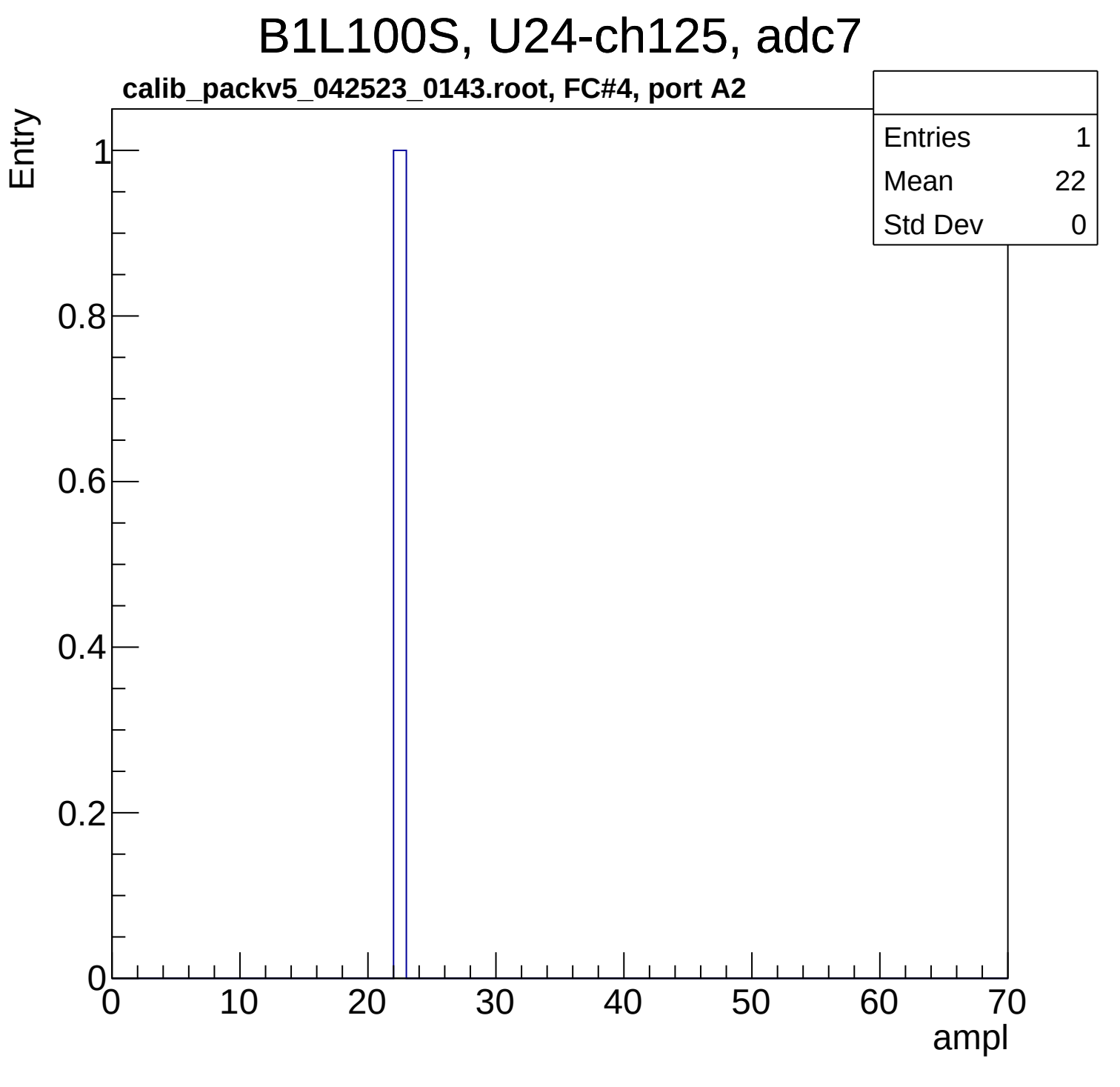
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch125, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
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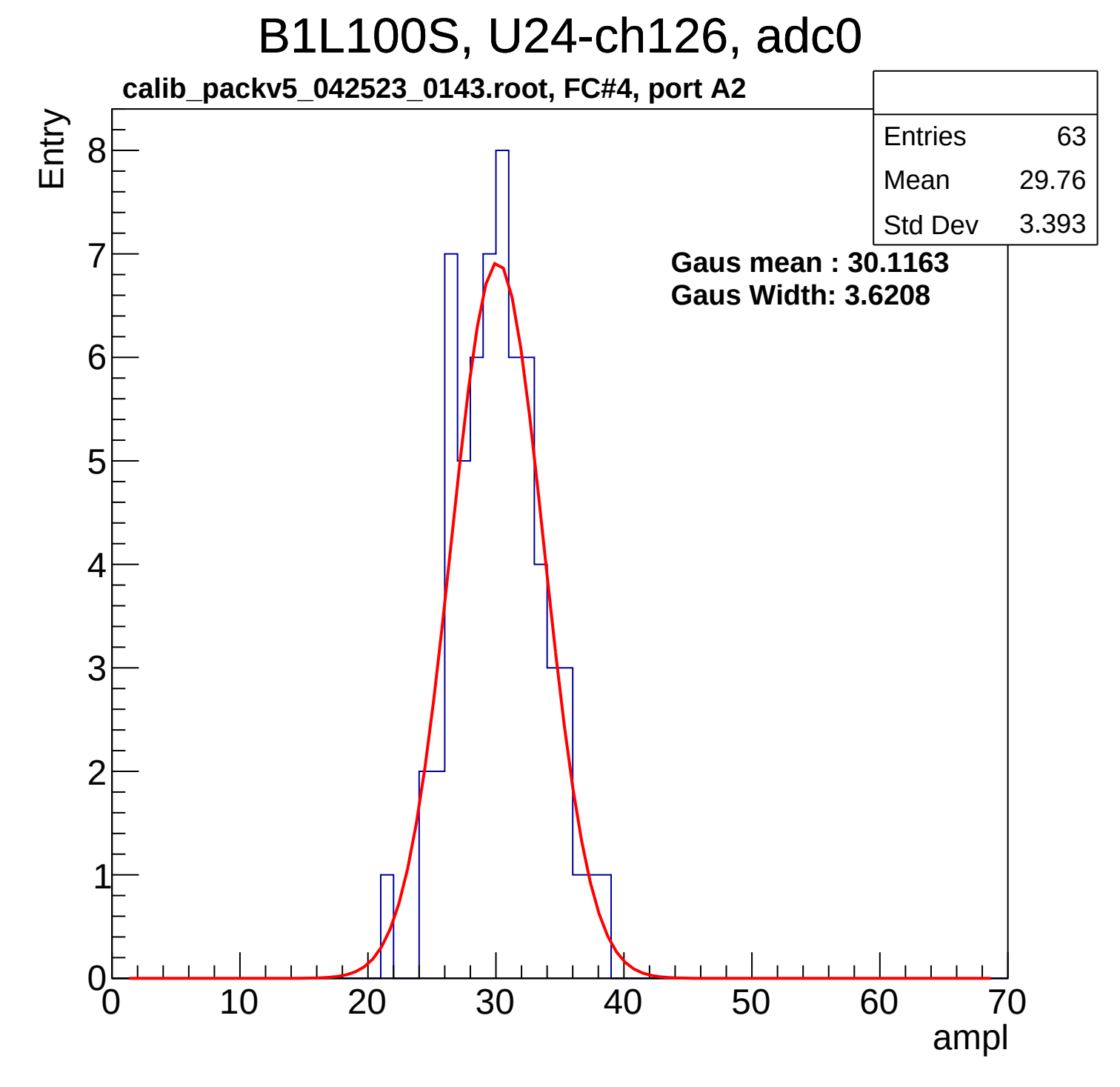
40
50
60
70
ampl


B1L100S, U24-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.76
Std Dev 3.393
Gaus mean : 30.1163
Gaus Width: 3.6208
Entry
ampl
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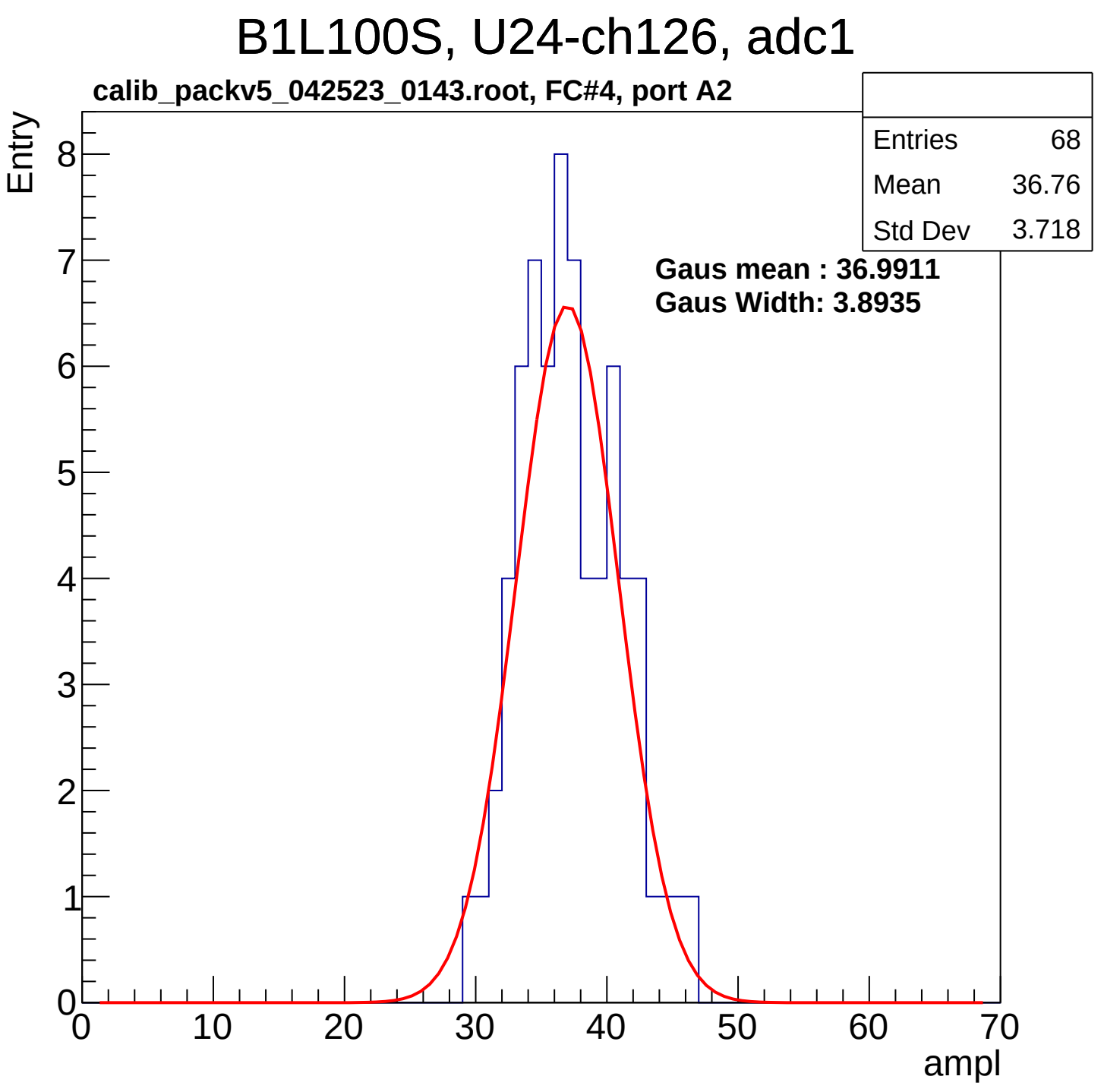
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch126, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.76
Std Dev 3.718
Gaus mean : 36.9911
Gaus Width: 3.8935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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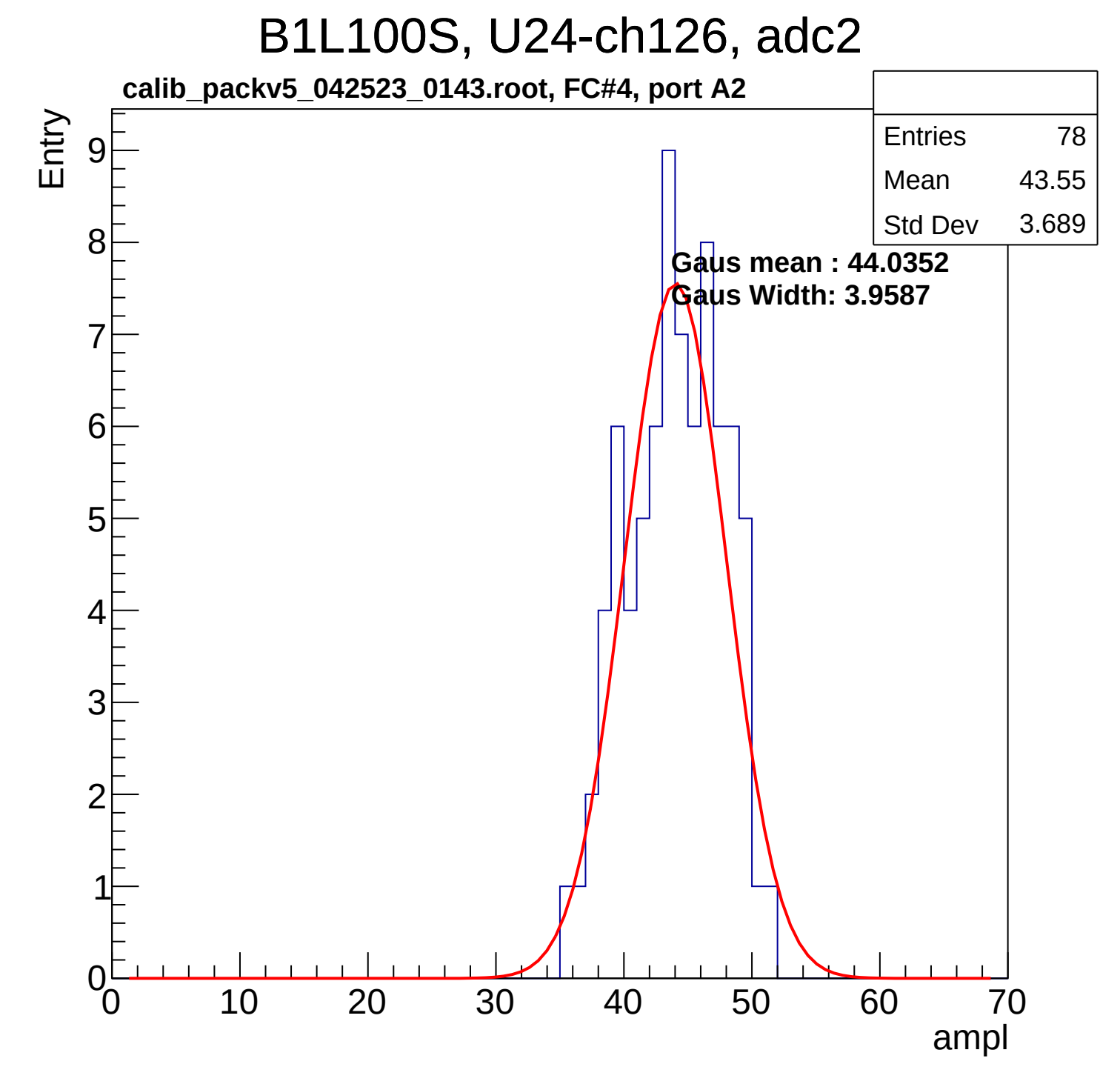
40
50
60
70


B1L100S, U24-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.55
Std Dev 3.689
Gaus mean : 44.0352
Gaus Width: 3.9587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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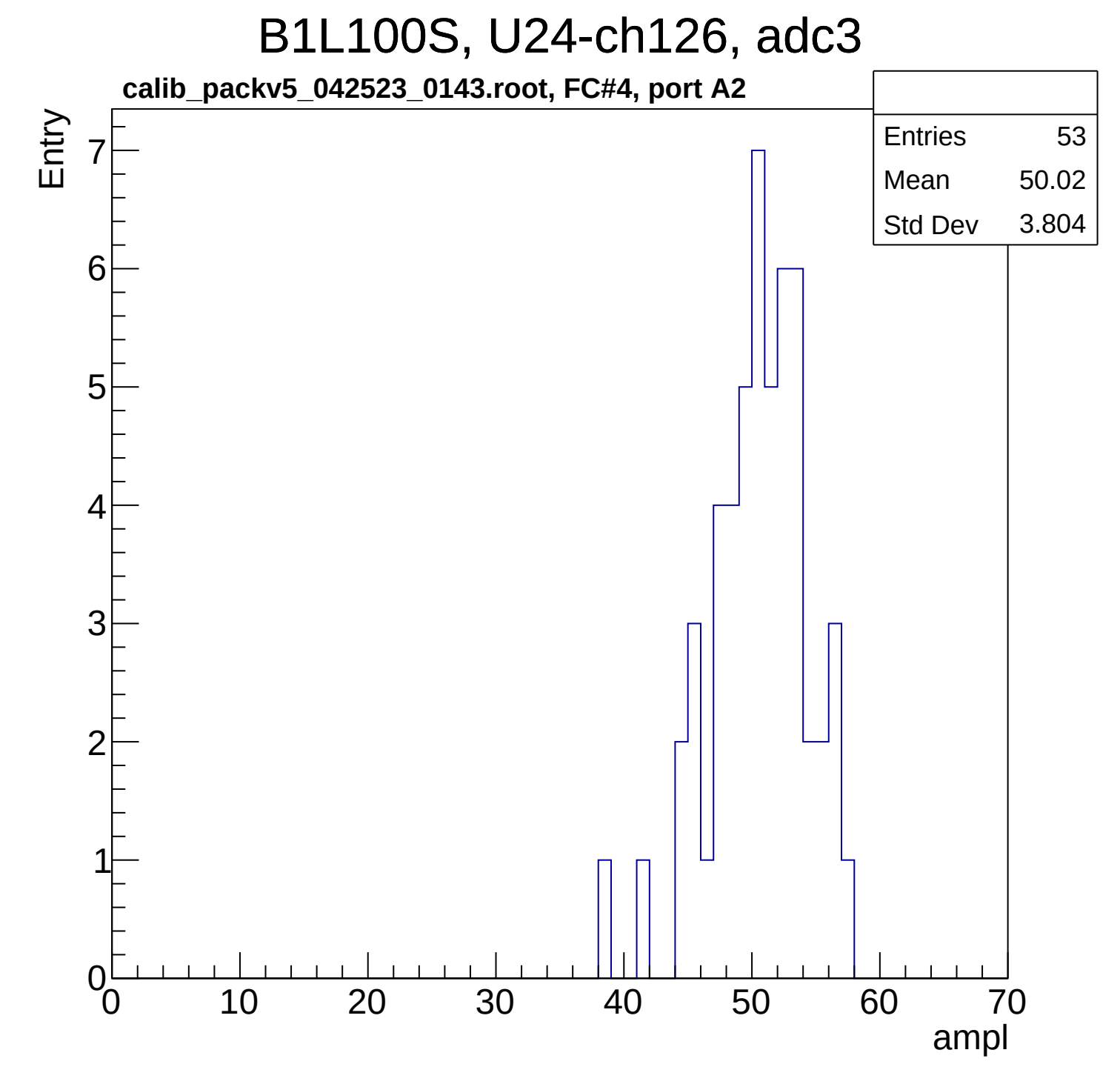
B1L100S, U24-ch126, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 50.02
Std Dev 3.804
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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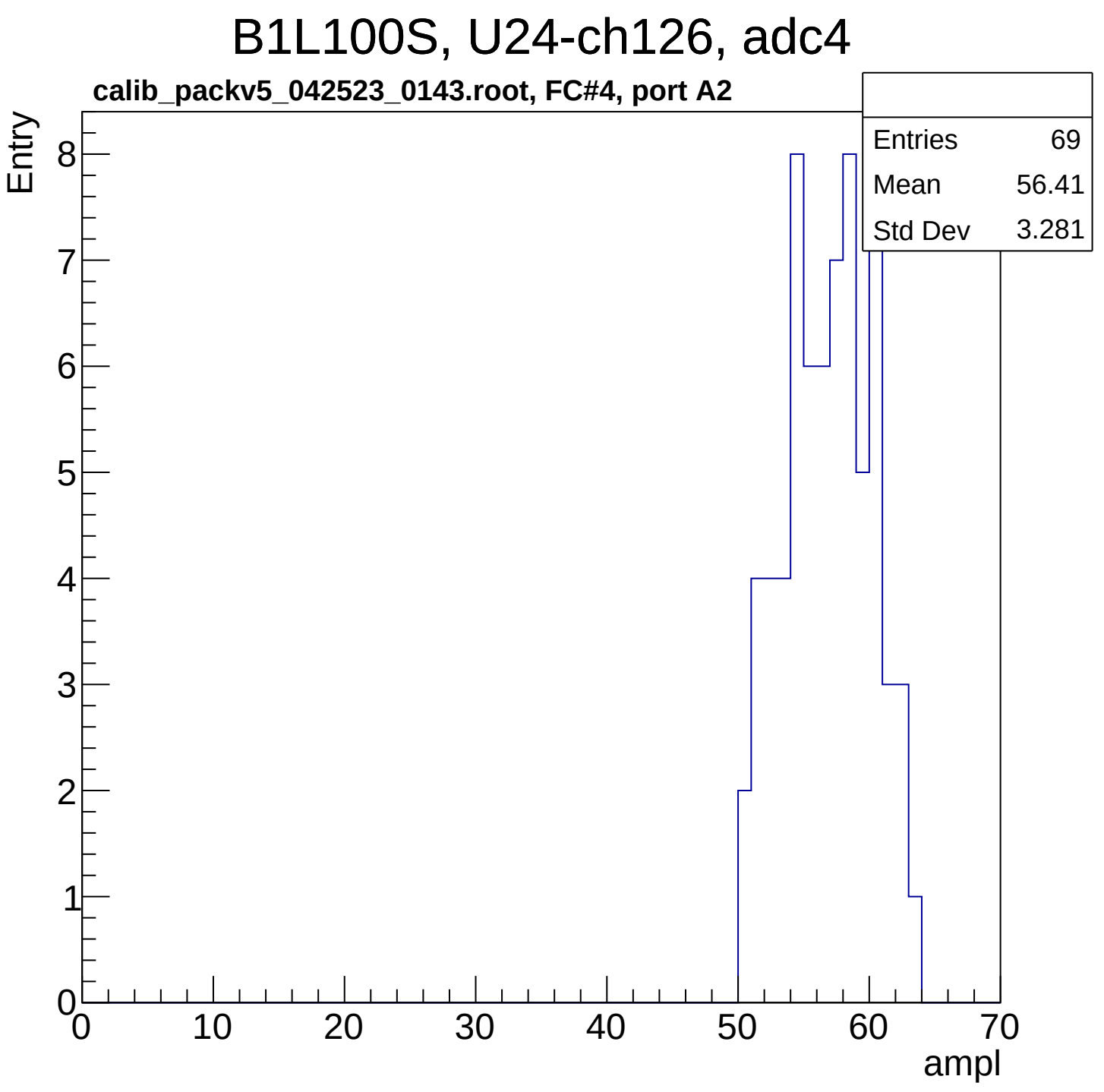
40
50
60
70


B1L100S, U24-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.41
Std Dev 3.281
Entry
ampl
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0
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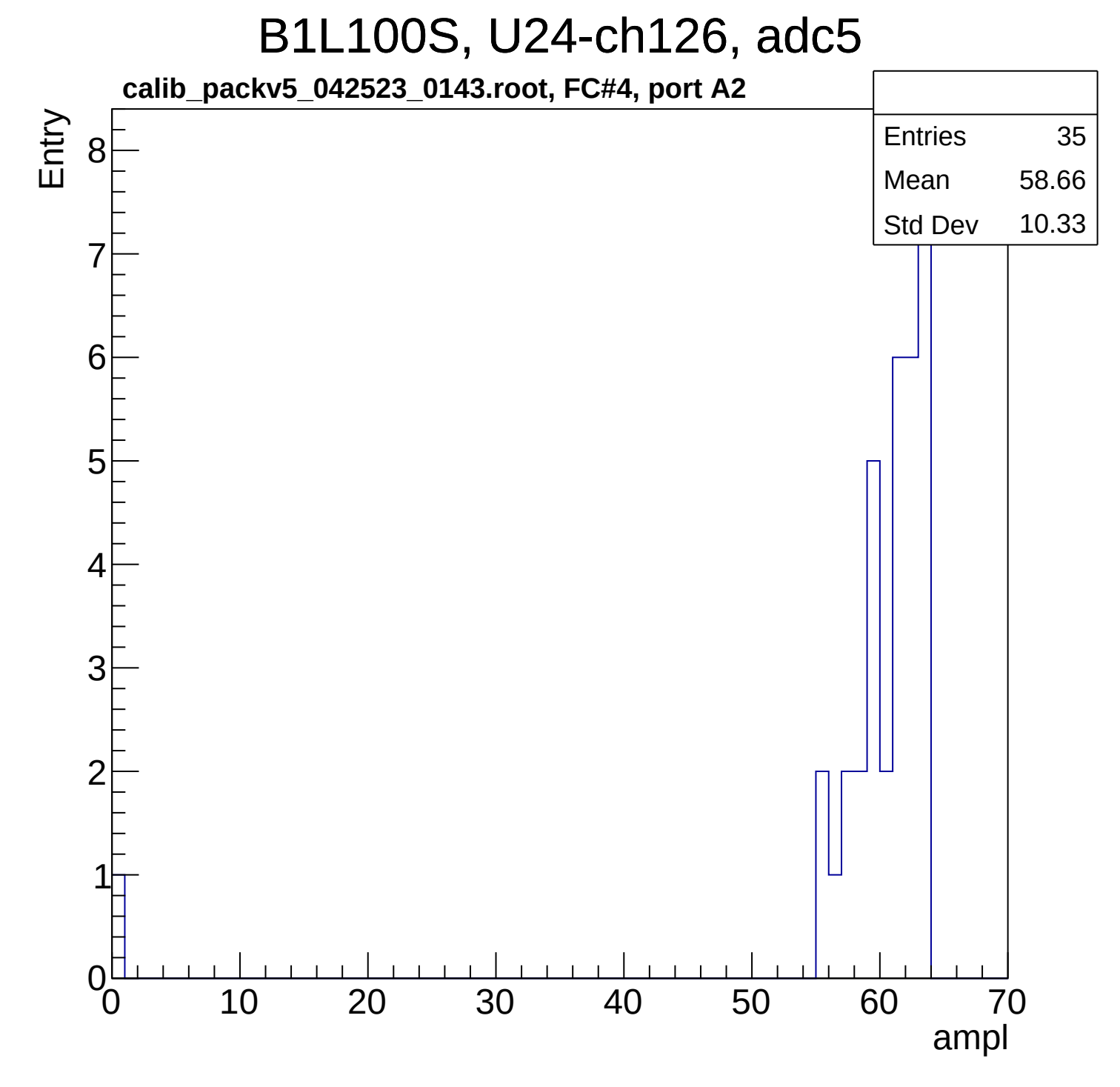
B1L100S, U24-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.66
Std Dev 10.33
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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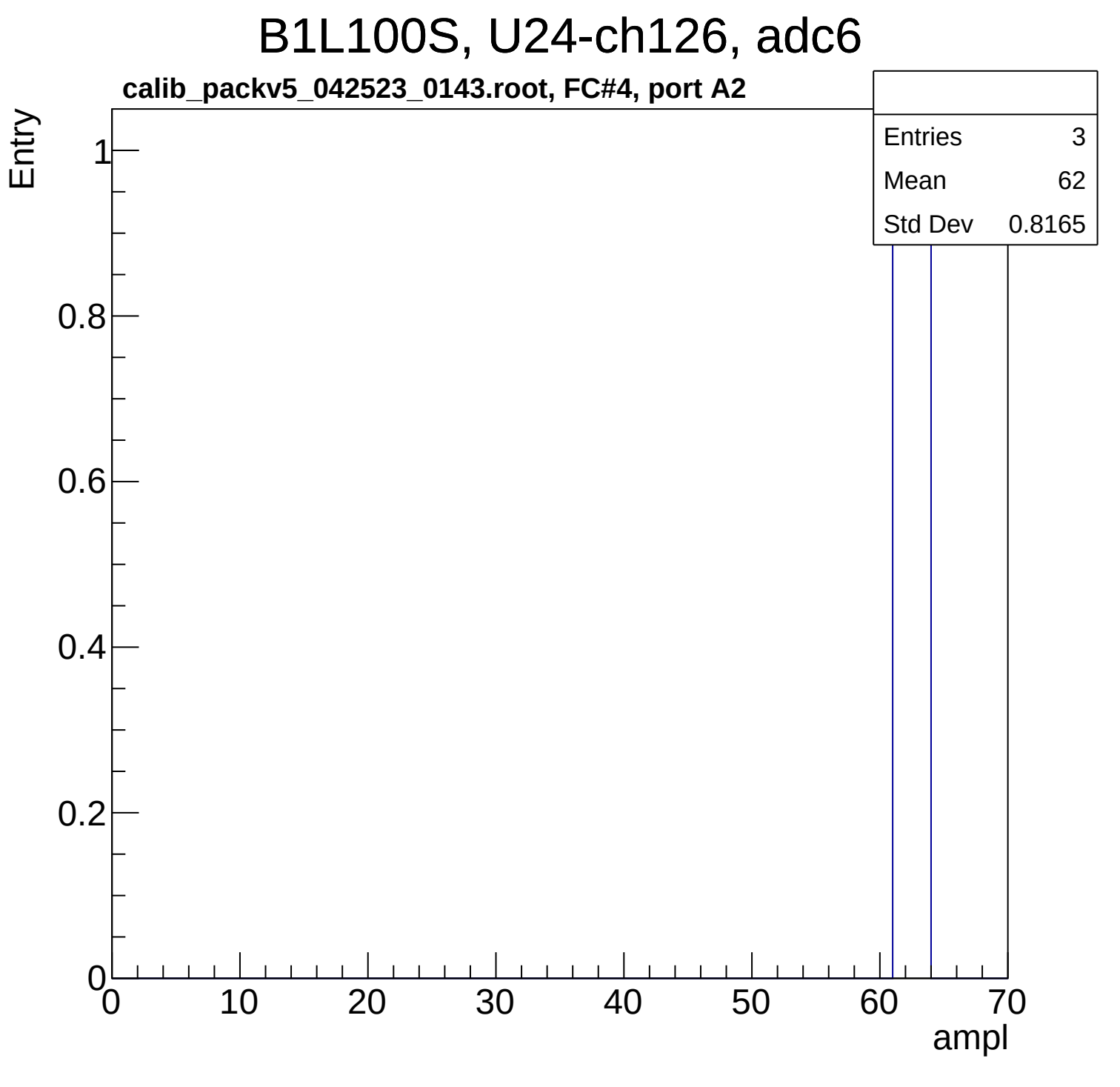
B1L100S, U24-ch126, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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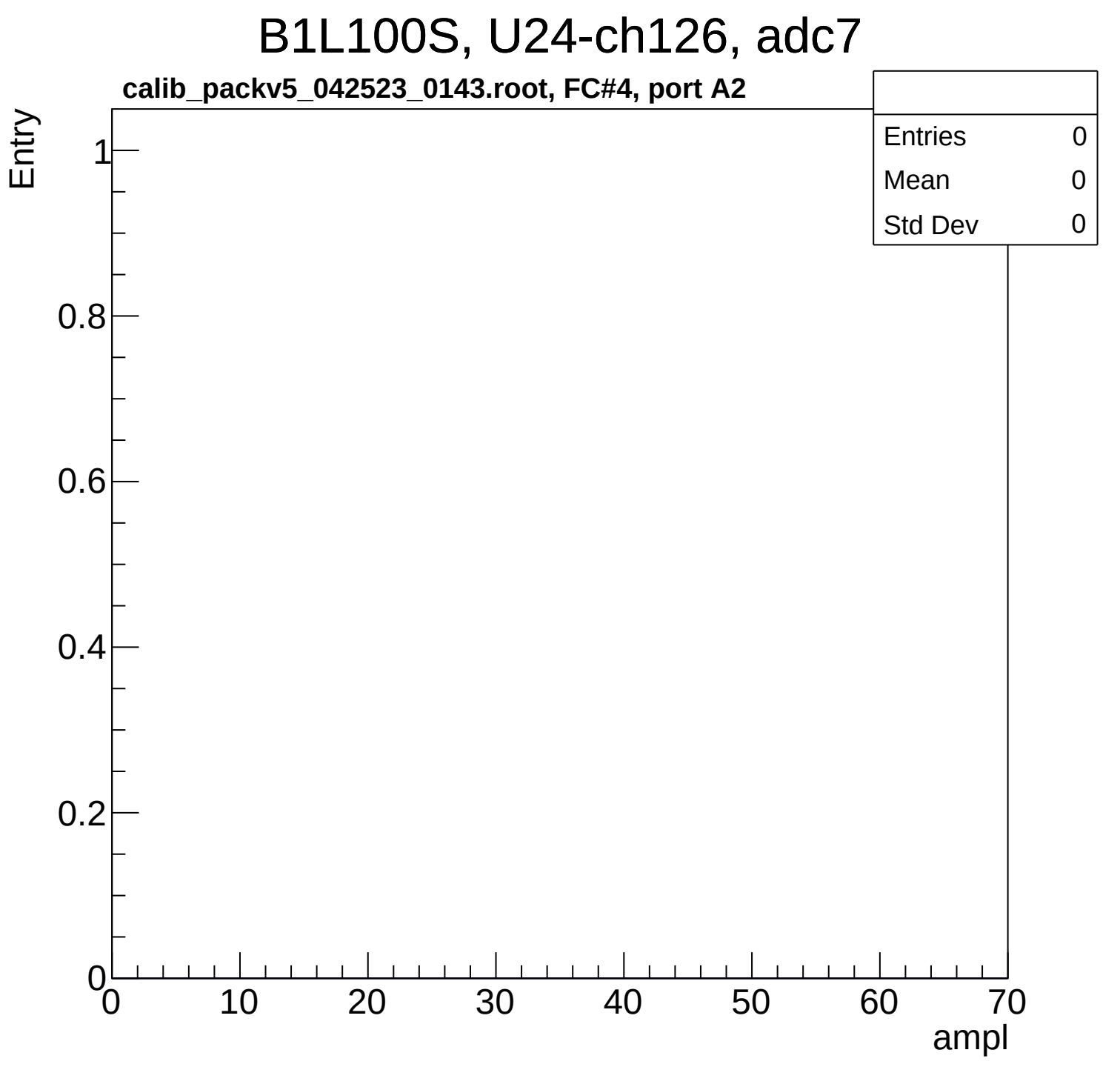
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch126, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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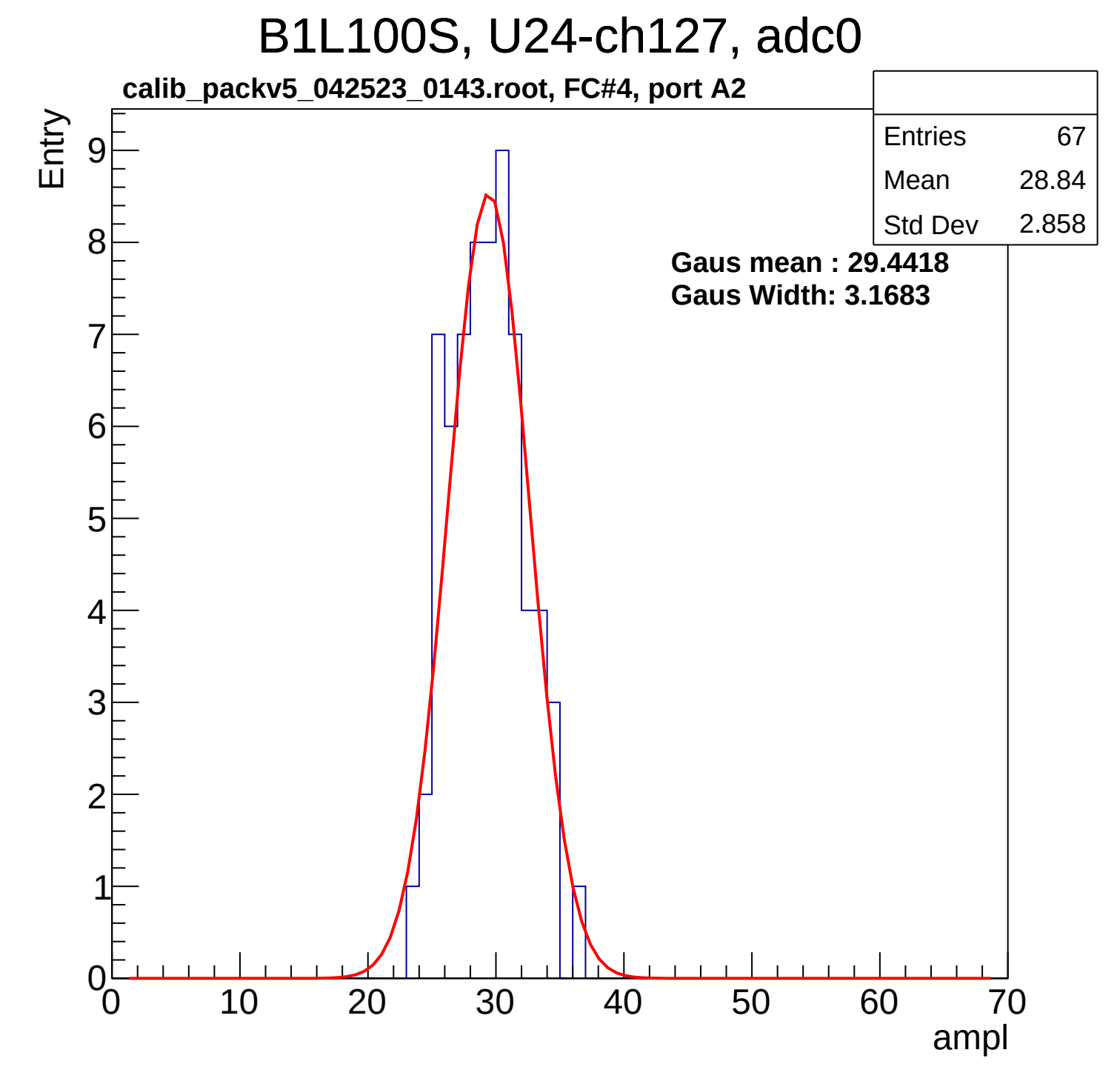
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.84
Std Dev 2.858
Gaus mean : 29.4418
Gaus Width: 3.1683
Entry
ampl
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3
4
5
6
7
8
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0
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40
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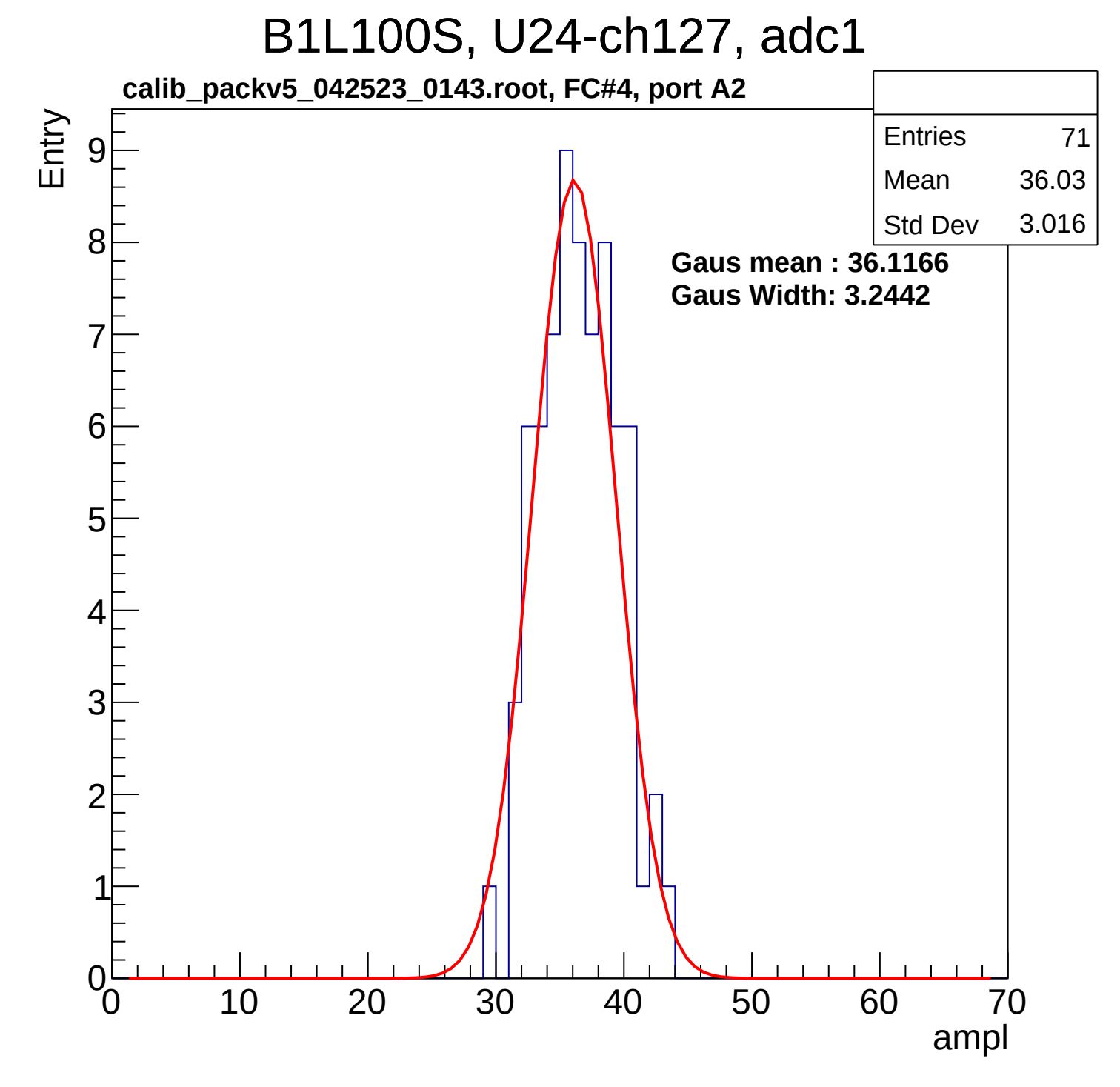
B1L100S, U24-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.03
Std Dev 3.016
Gaus mean : 36.1166
Gaus Width: 3.2442
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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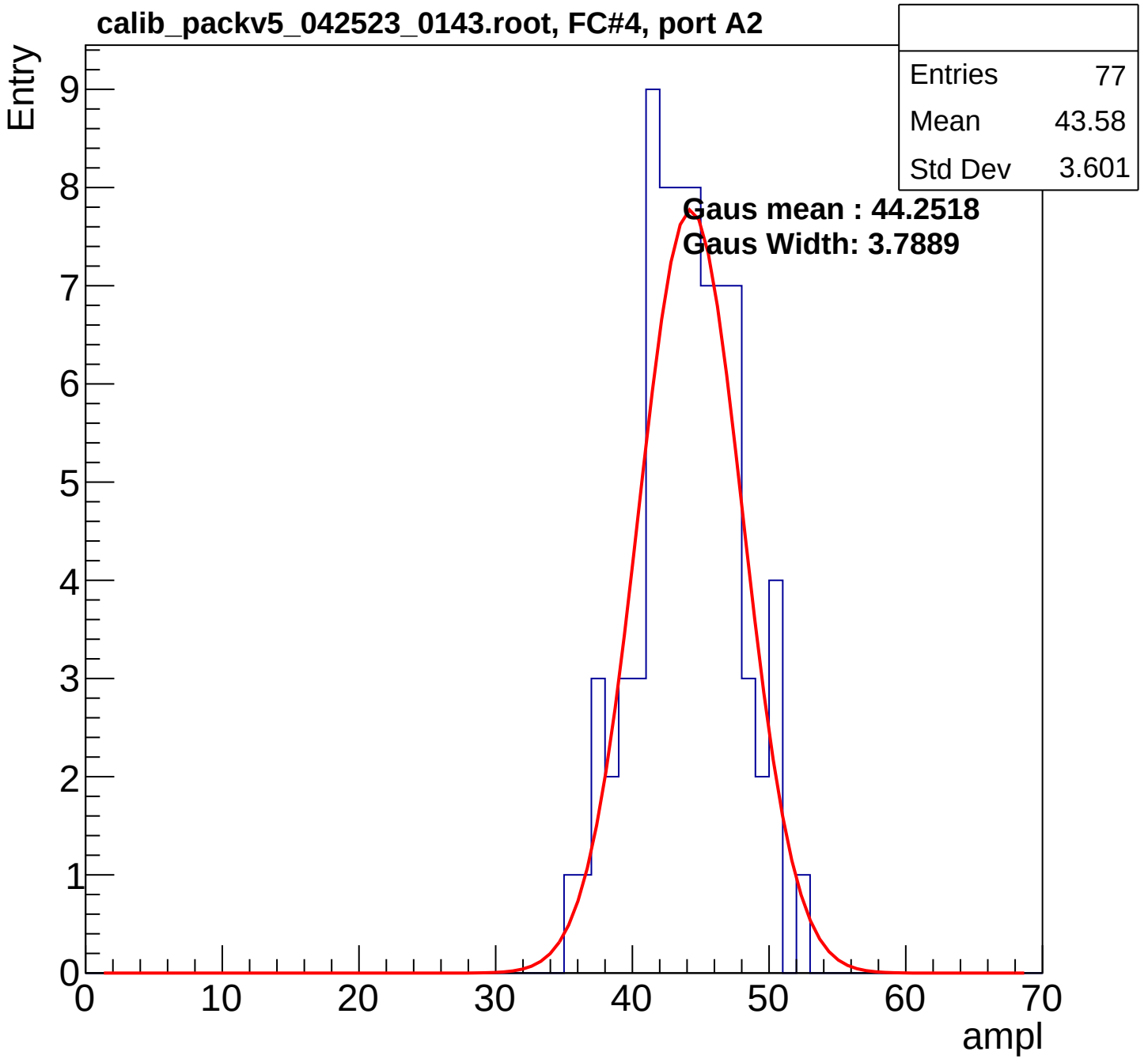
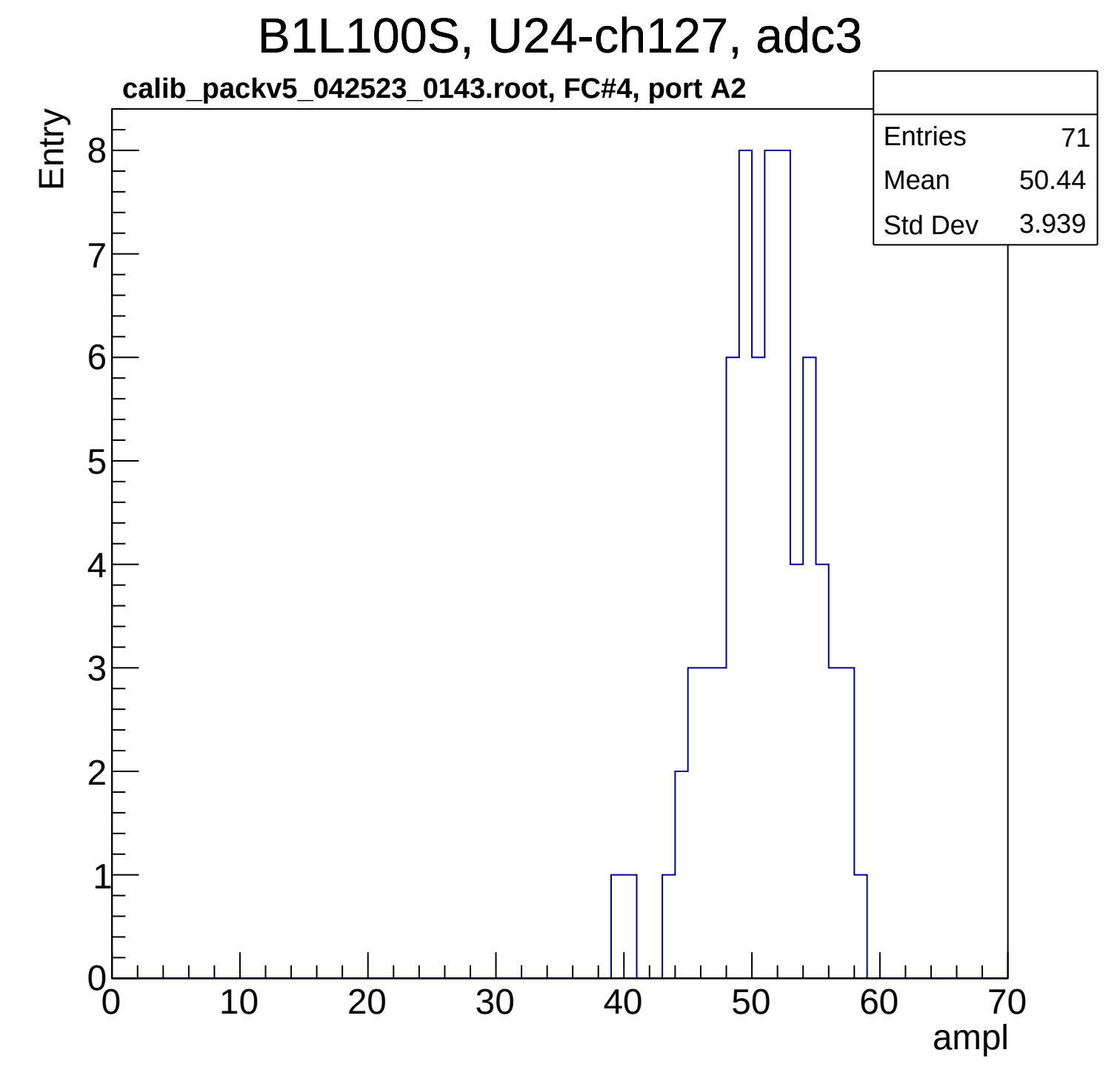
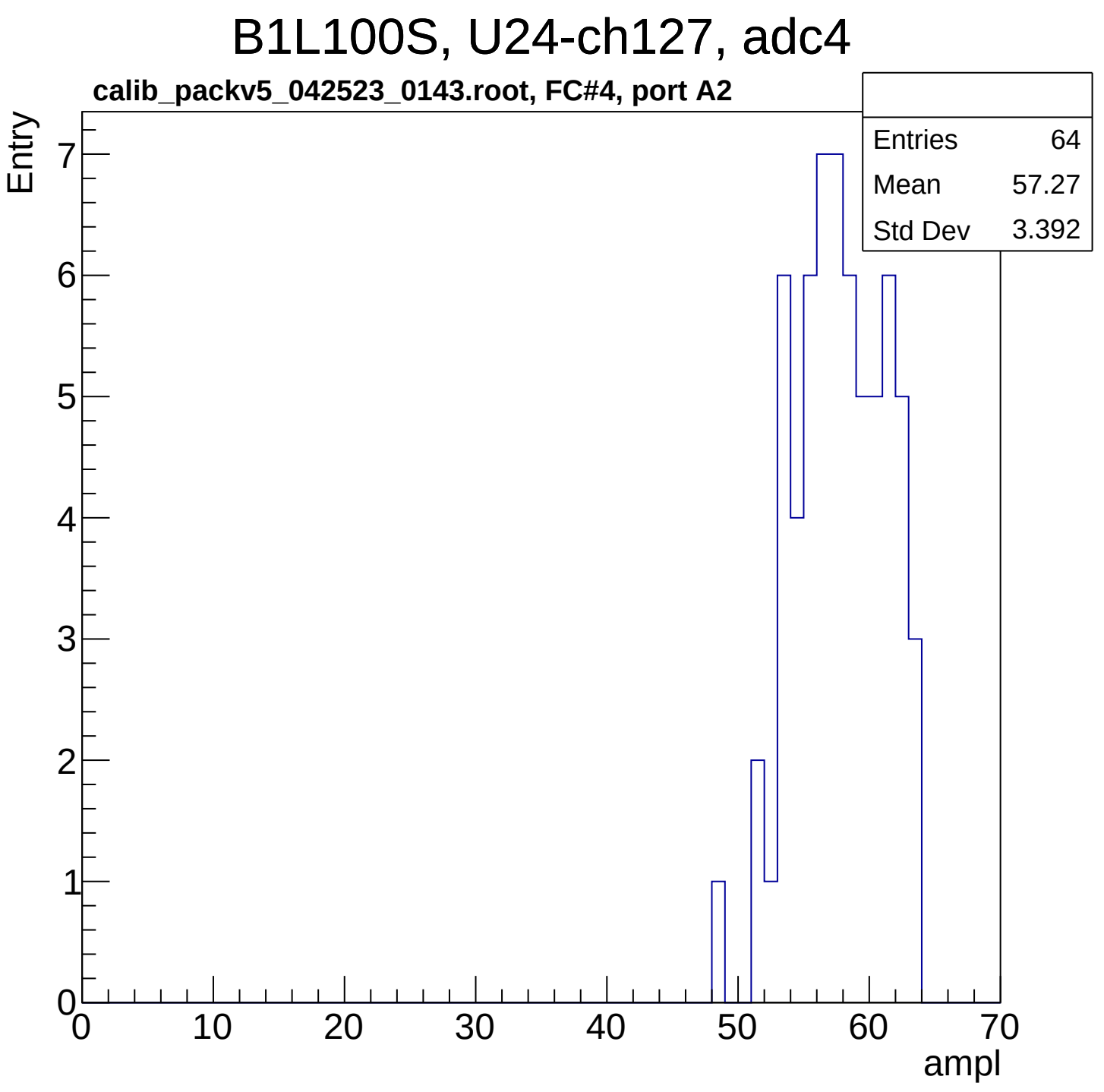
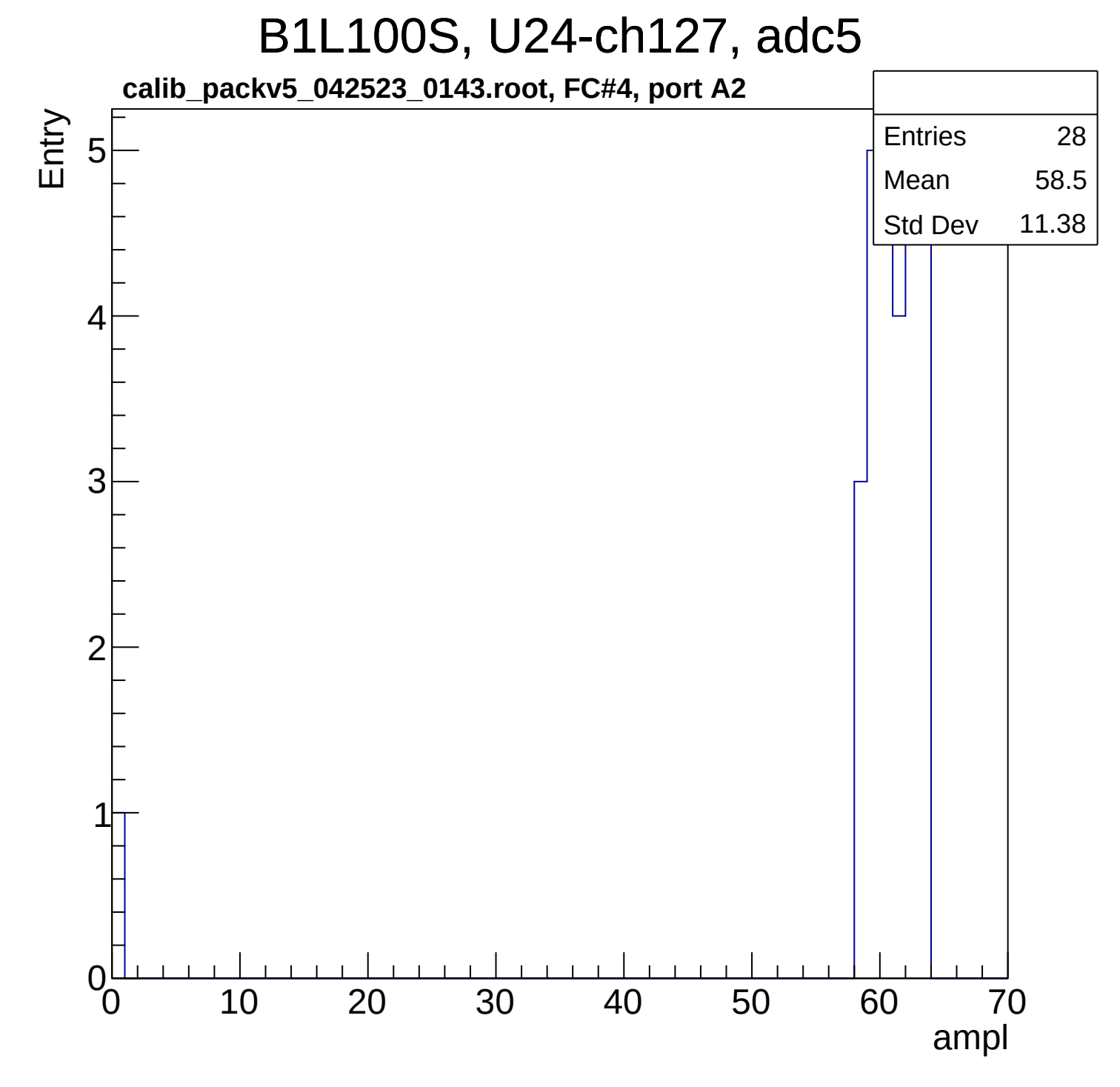
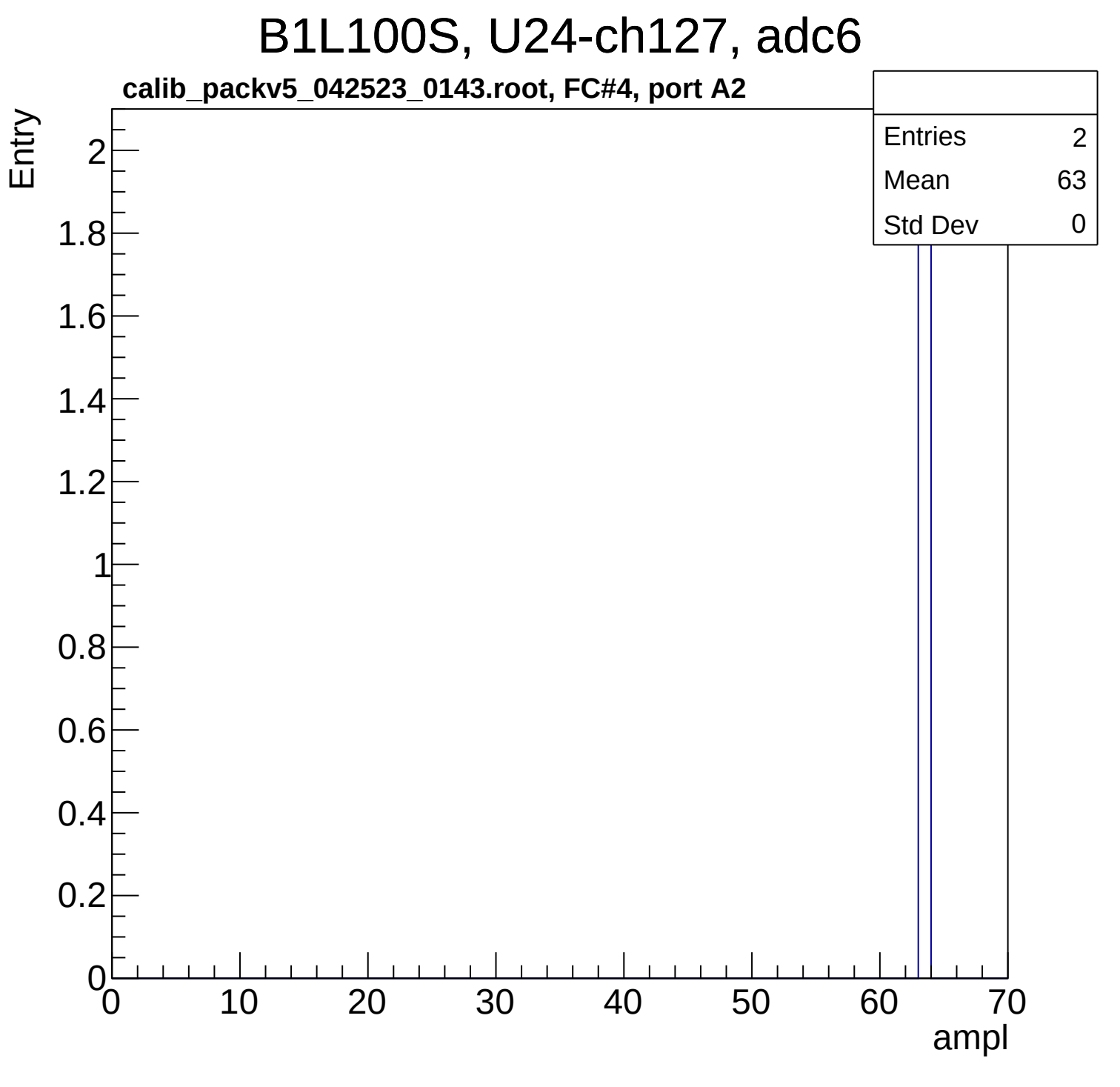
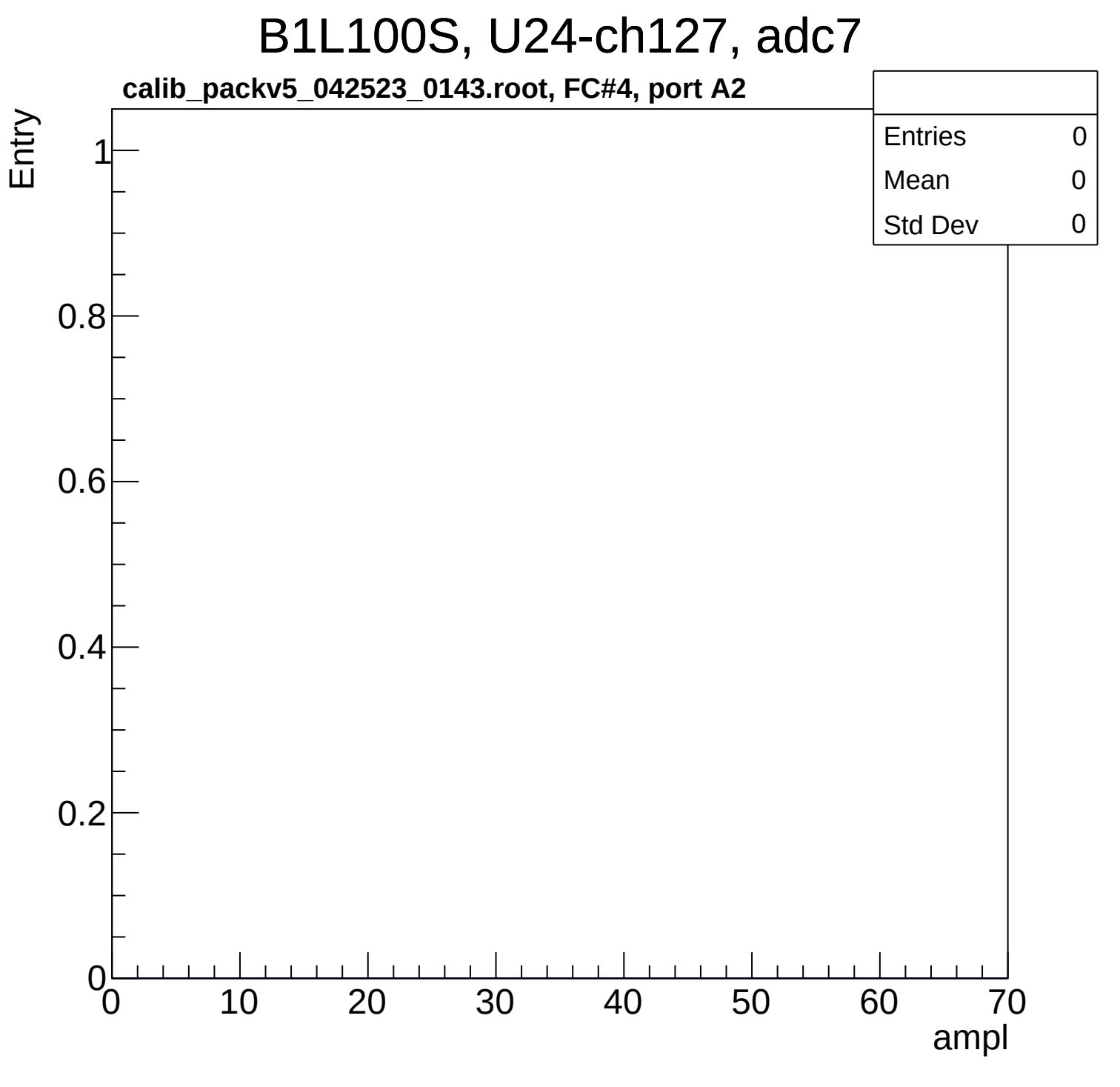

B1L100S, U24-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 43.58
Std Dev 3.601
Gaus mean : 44.2518
Gaus Width: 3.7889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

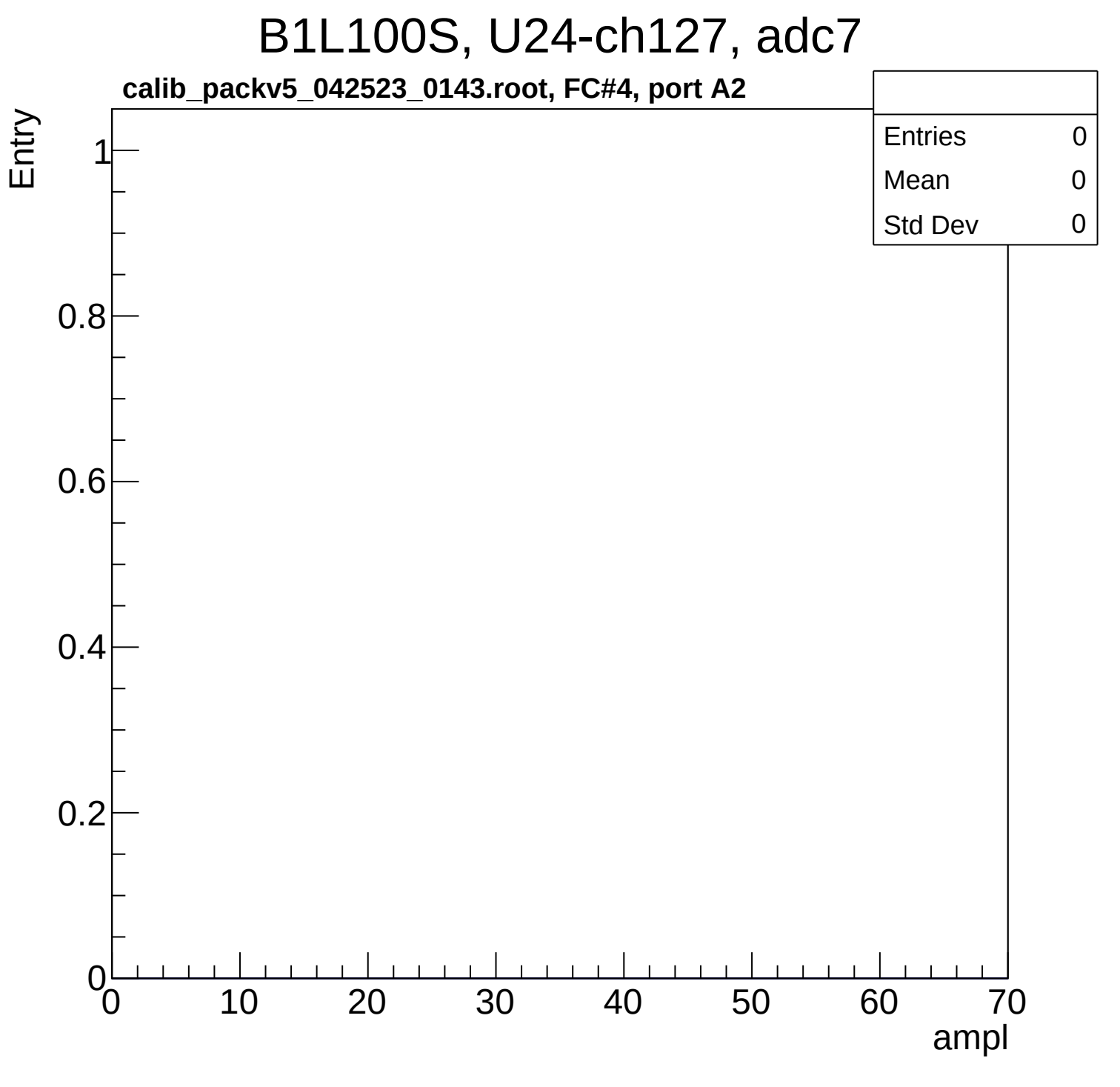

B1L100S, U24-ch127, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.44
Std Dev 3.939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U24-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.27
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U24-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.5
Std Dev 11.38
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U24-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl