

B1L104S, U8-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	263
Mean	34.77
Std Dev	26.24

Turn on : 48.0527

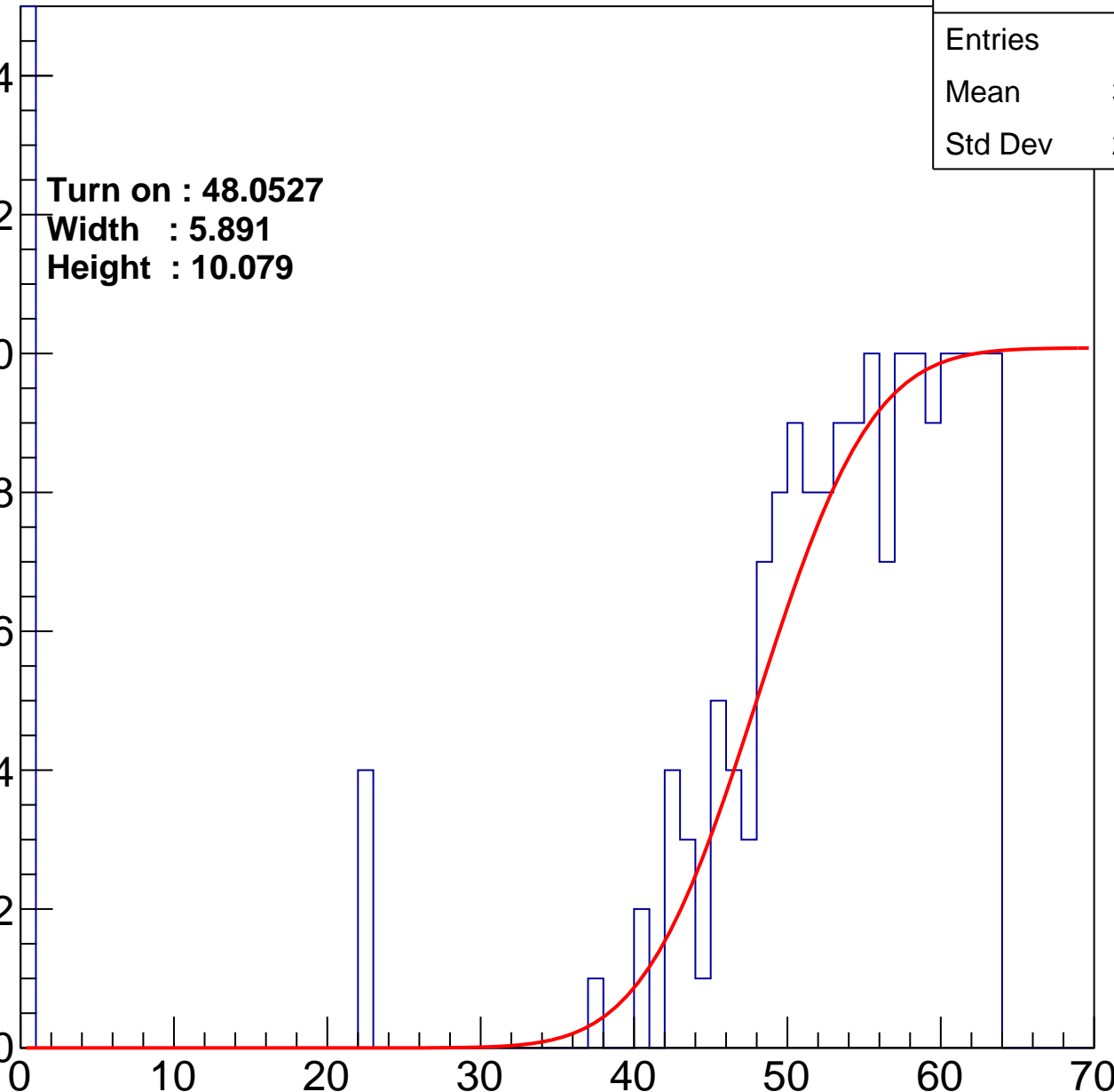
Width : 5.891

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch1

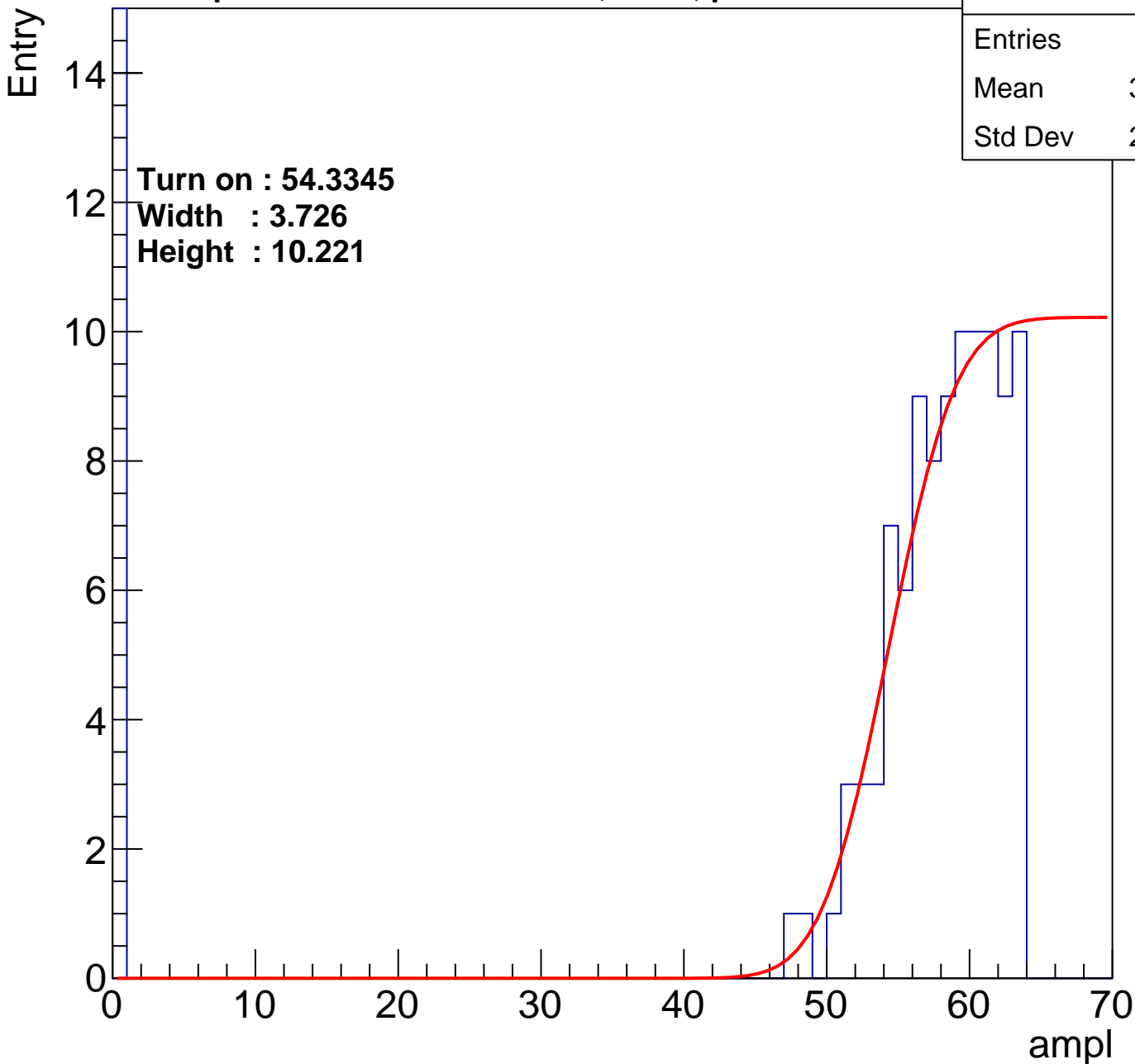
calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	34.68
Std Dev	28.53

Turn on : 54.3345

Width : 3.726

Height : 10.221



B1L104S, U8-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.52
Std Dev	28.6

Turn on : 52.3447

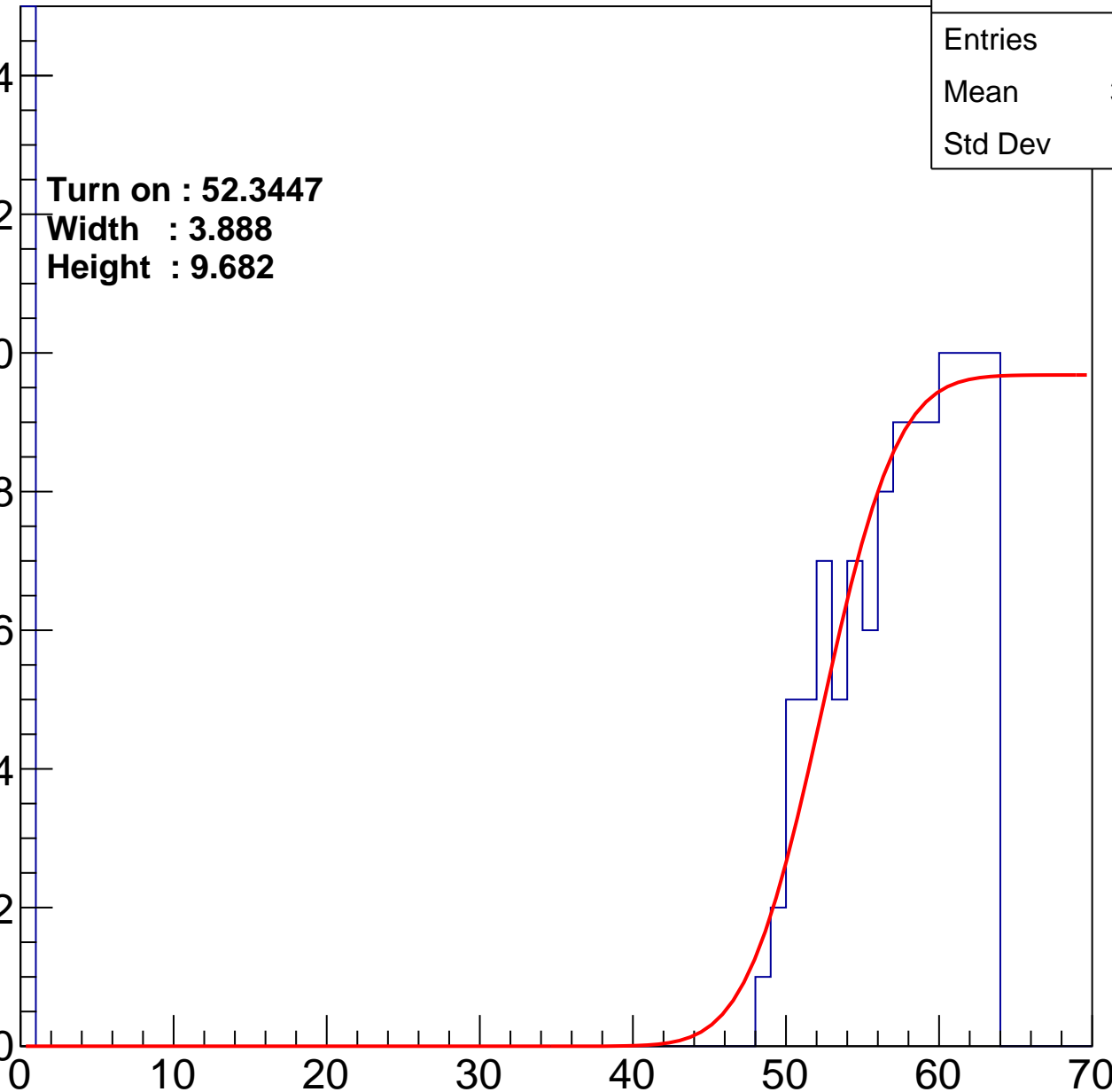
Width : 3.888

Height : 9.682

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch3

calib_packv5_033123_0516.root, FC#4, port A1

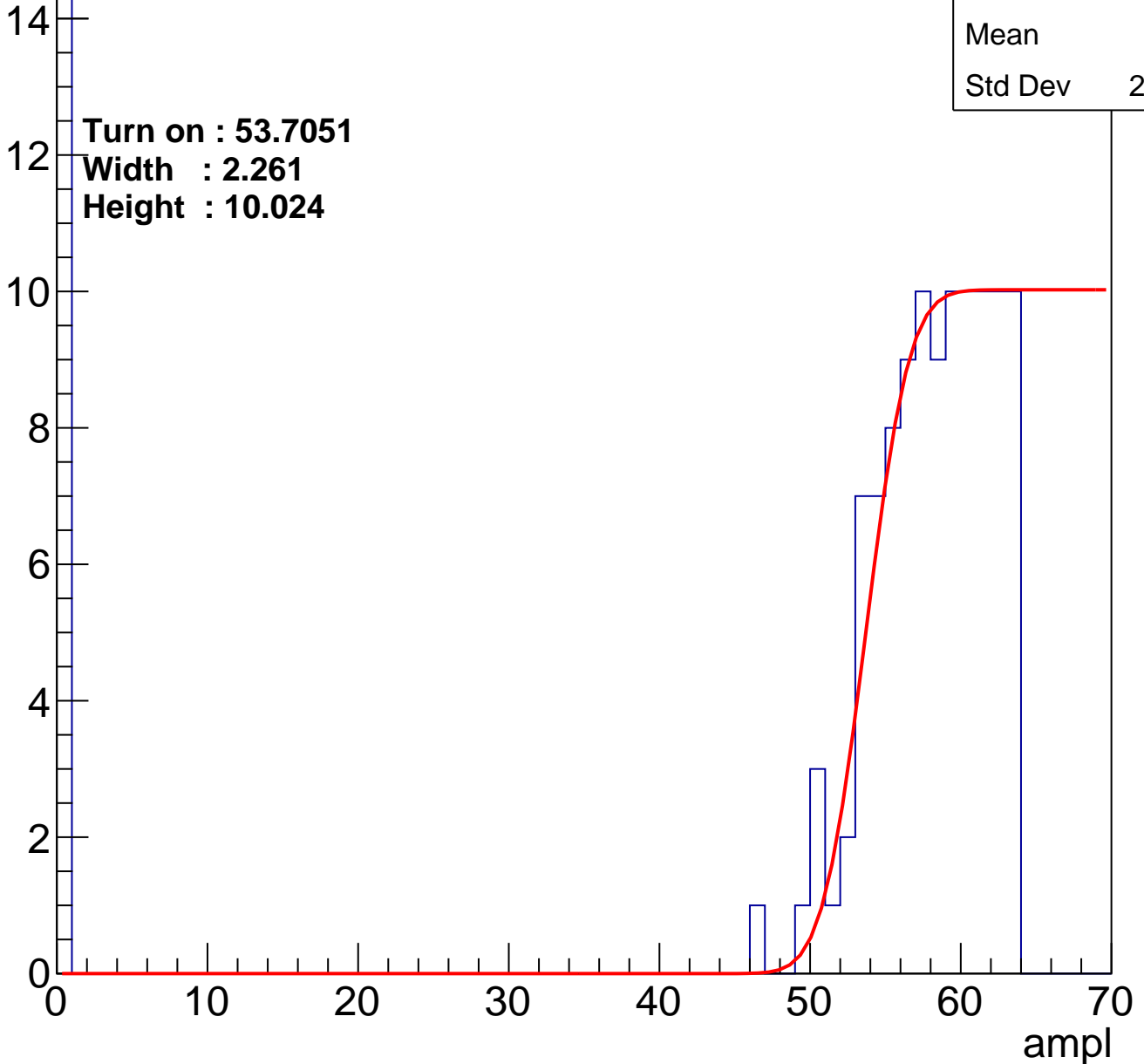
Entry

Entries	218
Mean	28.6
Std Dev	28.98

Turn on : 53.7051

Width : 2.261

Height : 10.024



B1L104S, U8-ch4

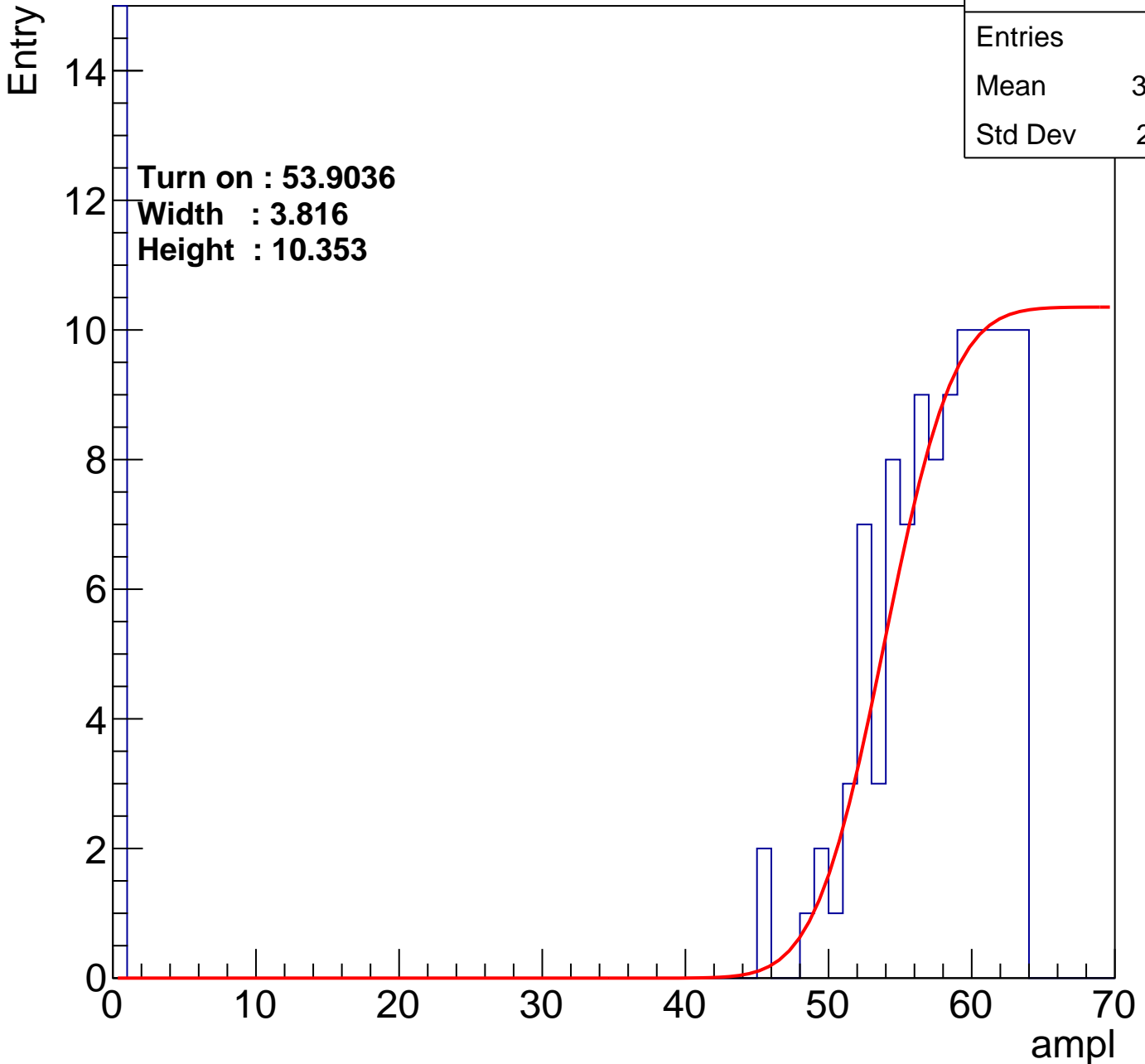
calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	37.79
Std Dev	27.41

Turn on : 53.9036

Width : 3.816

Height : 10.353



B1L104S, U8-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	31.38
Std Dev	29.03

Turn on : 54.3052

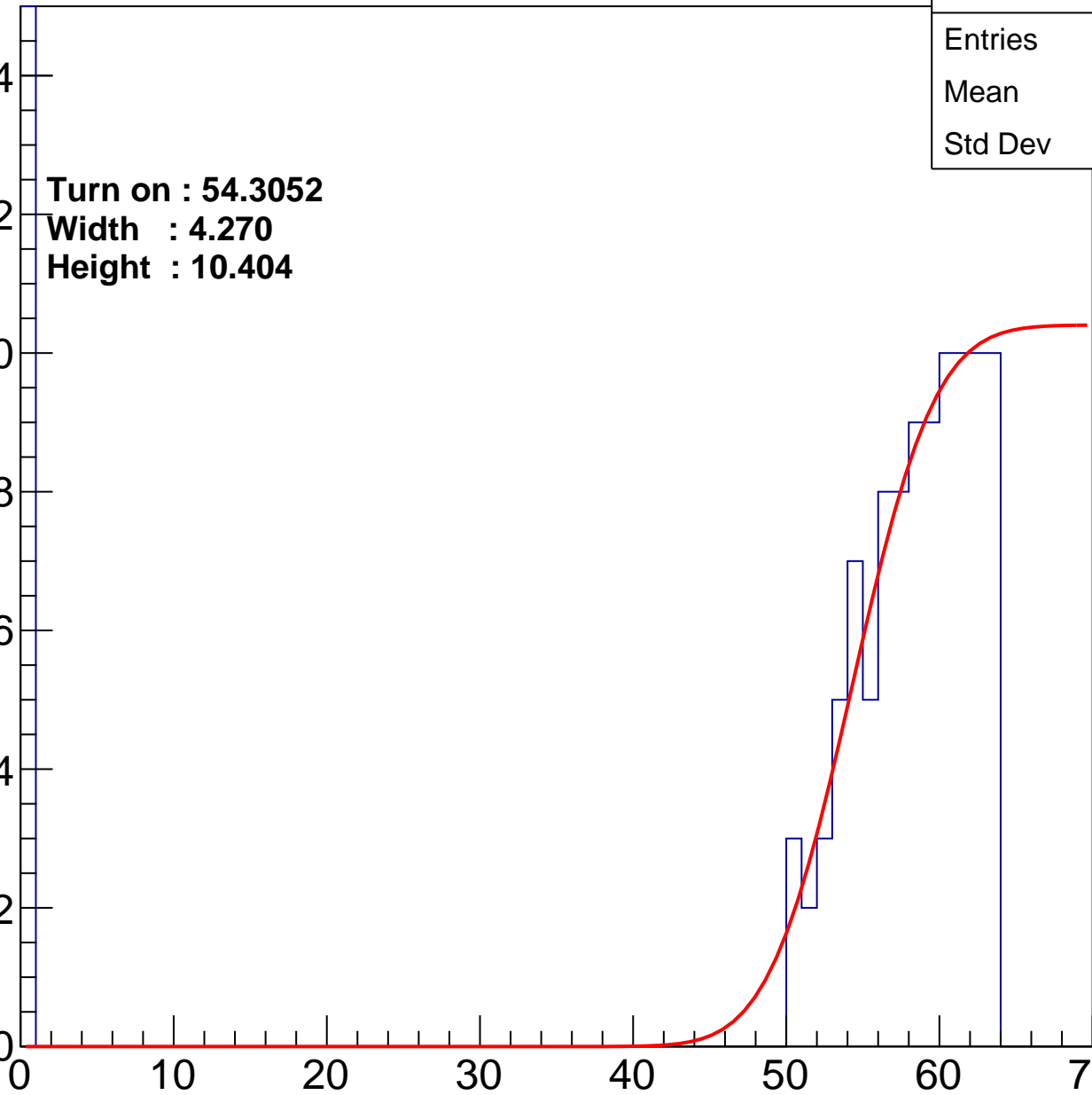
Width : 4.270

Height : 10.404

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	229
Mean	33.93
Std Dev	27.57

Turn on : 51.1678

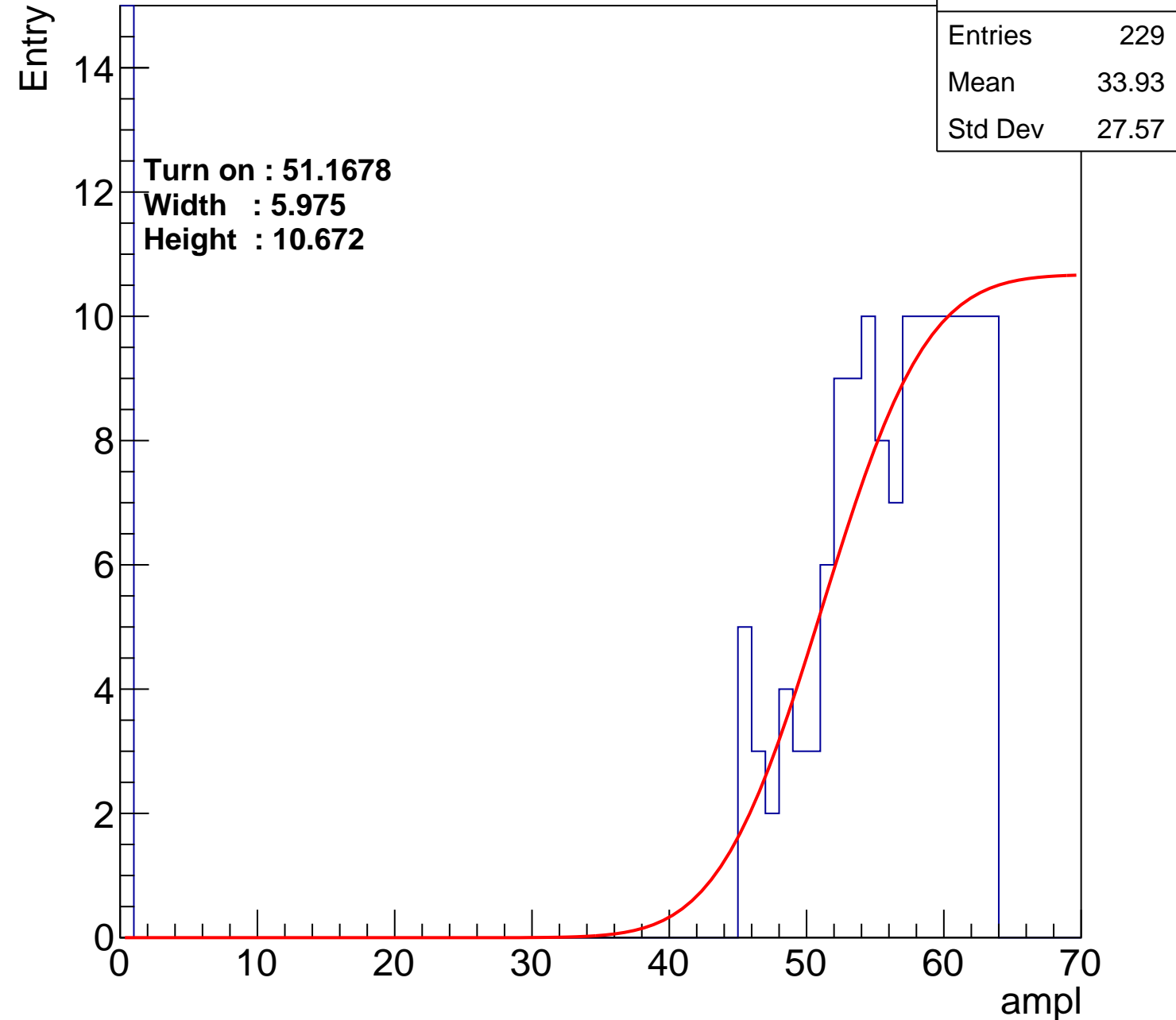
Width : 5.975

Height : 10.672

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	33.02
Std Dev	29.01

Turn on : 54.4994

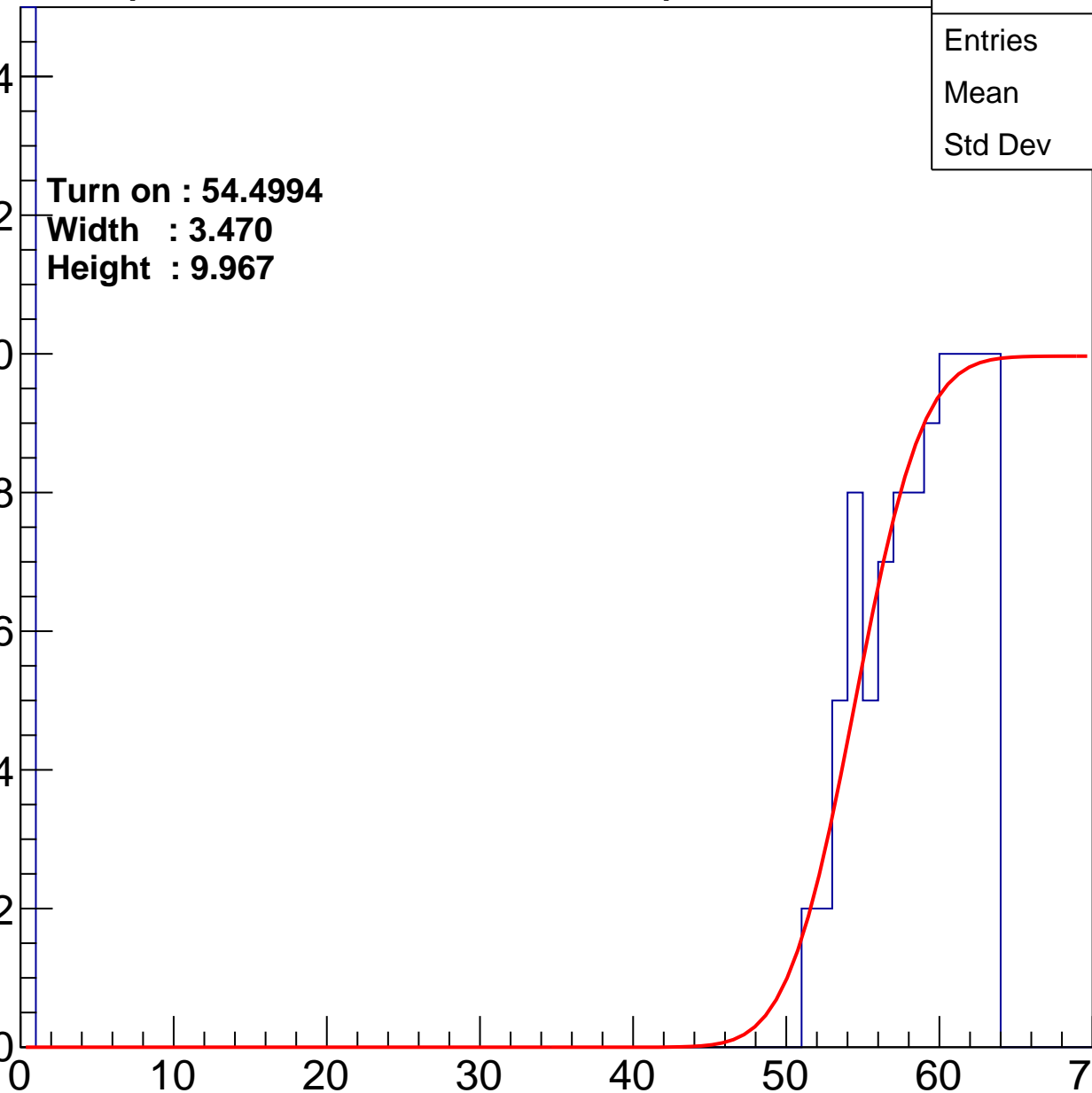
Width : 3.470

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch8

calib_packv5_033123_0516.root, FC#4, port A1

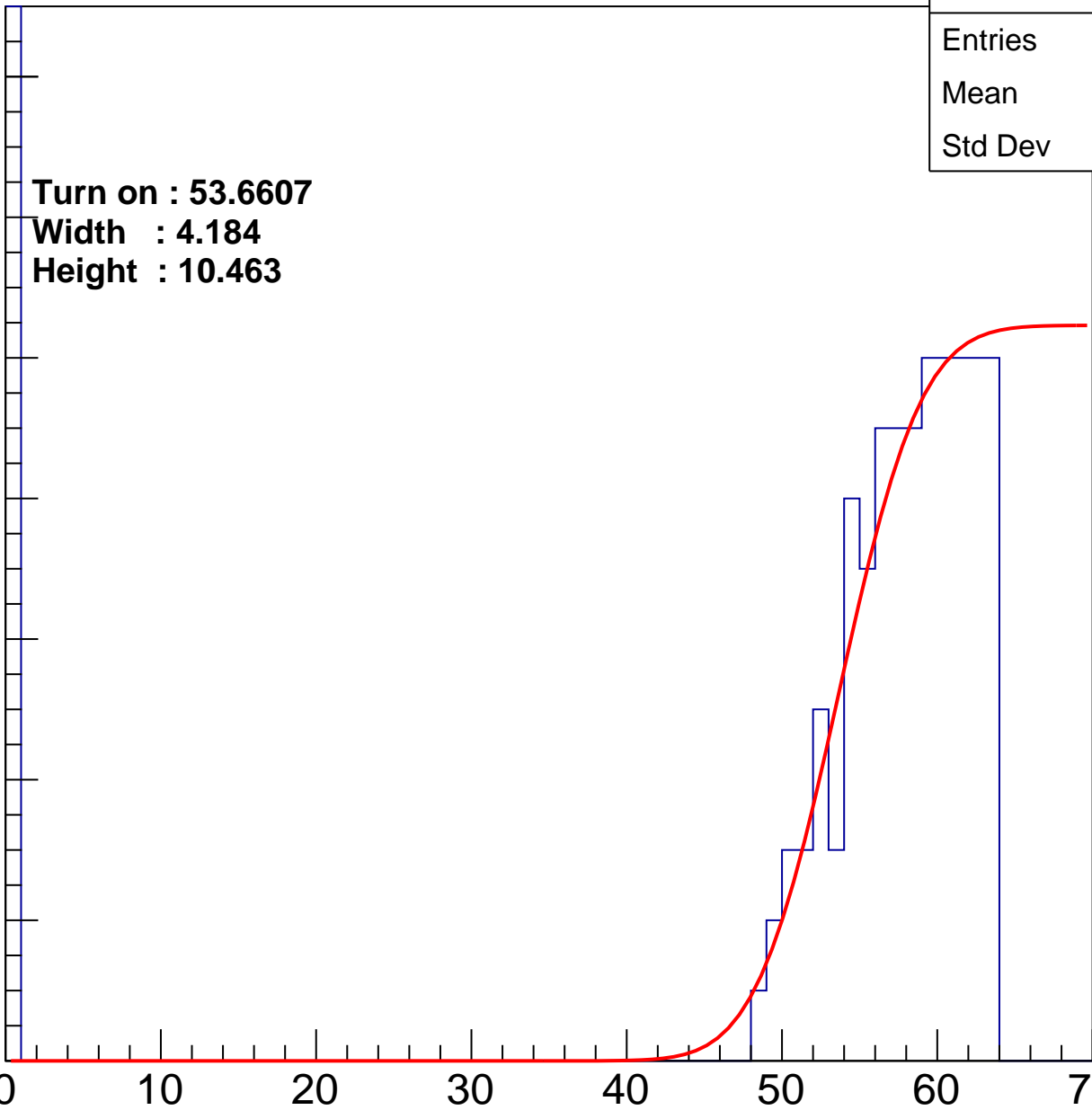
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6607
Width : 4.184
Height : 10.463

Entries	168
Mean	37.35
Std Dev	27.65

ampl



B1L104S, U8-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	30.68
Std Dev	29.18

Turn on : 54.3253

Width : 3.674

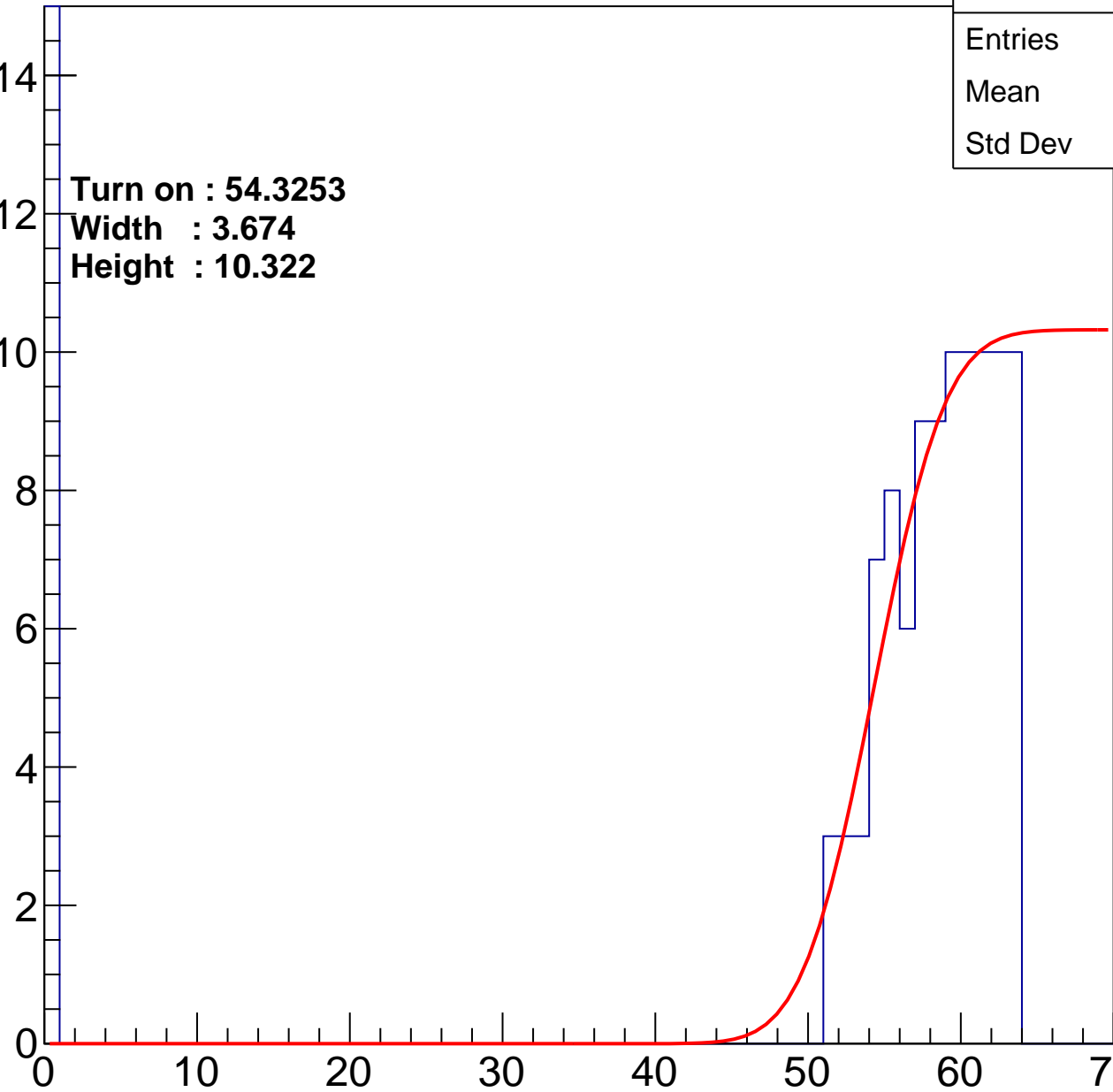
Height : 10.322

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U8-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	30.01
Std Dev	28.77

Turn on : 54.0413

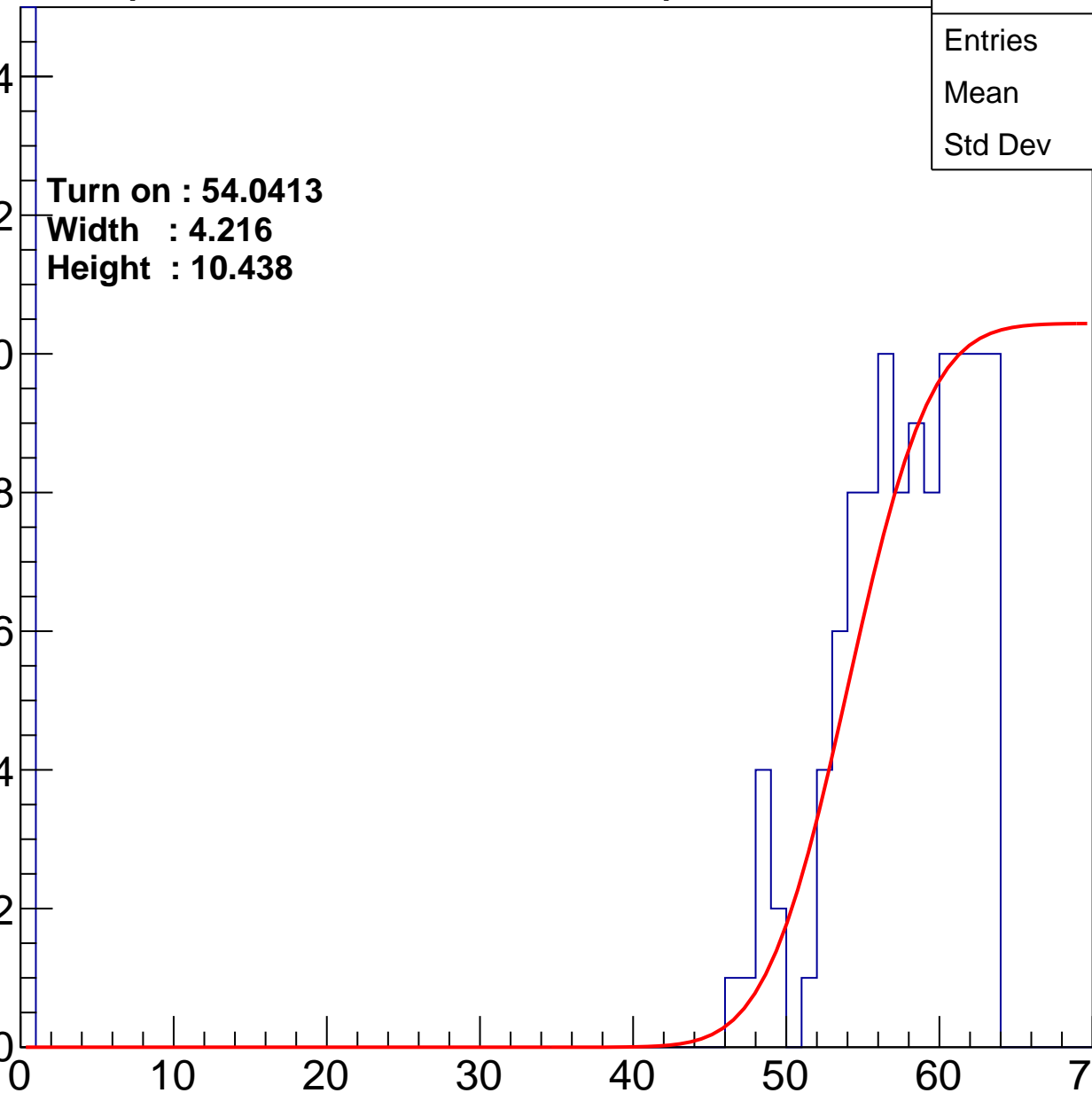
Width : 4.216

Height : 10.438

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	36.81
Std Dev	28.25

Turn on : 54.3519

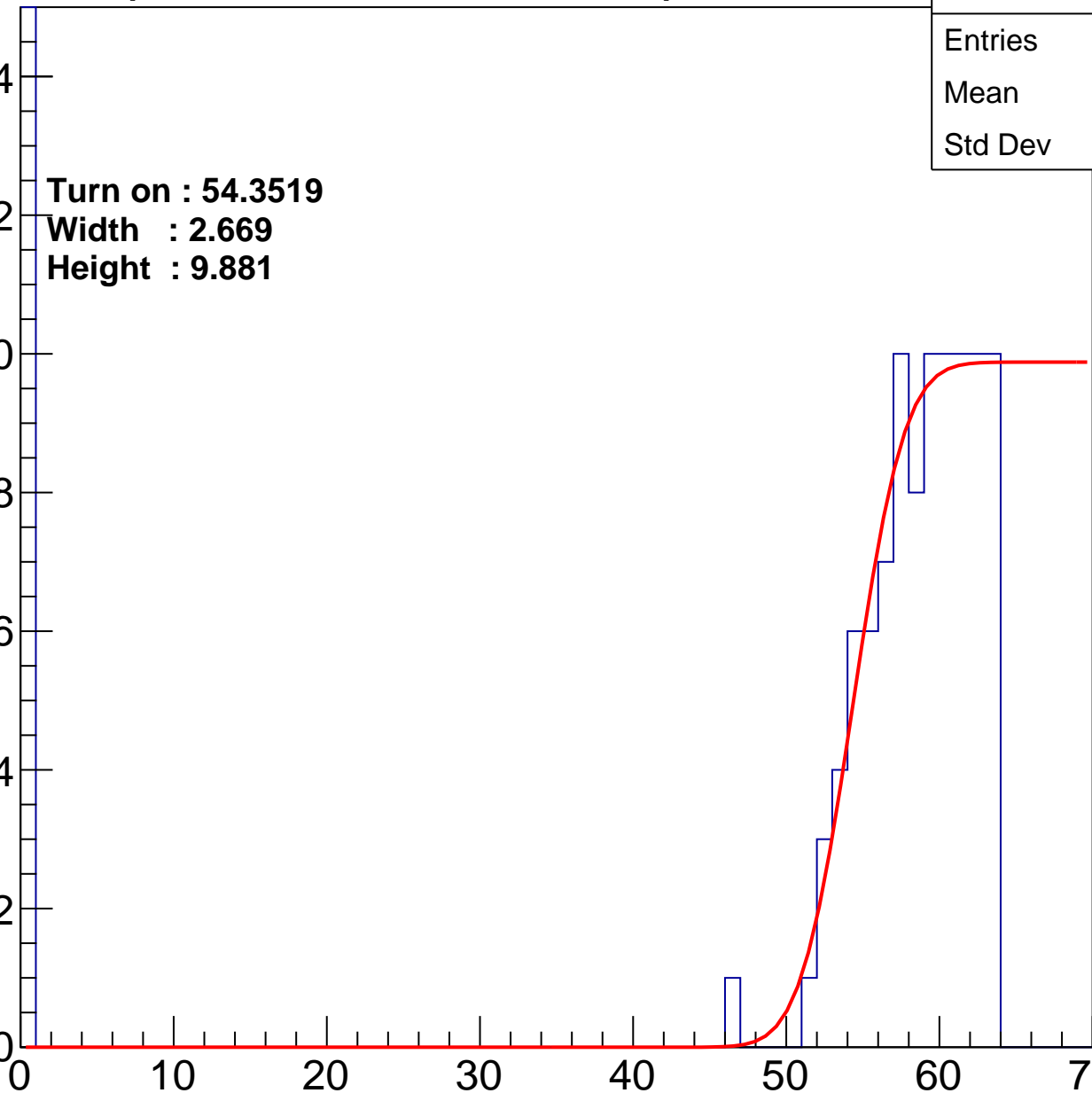
Width : 2.669

Height : 9.881

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	28.6
Std Dev	28.99

Turn on : 53.0177

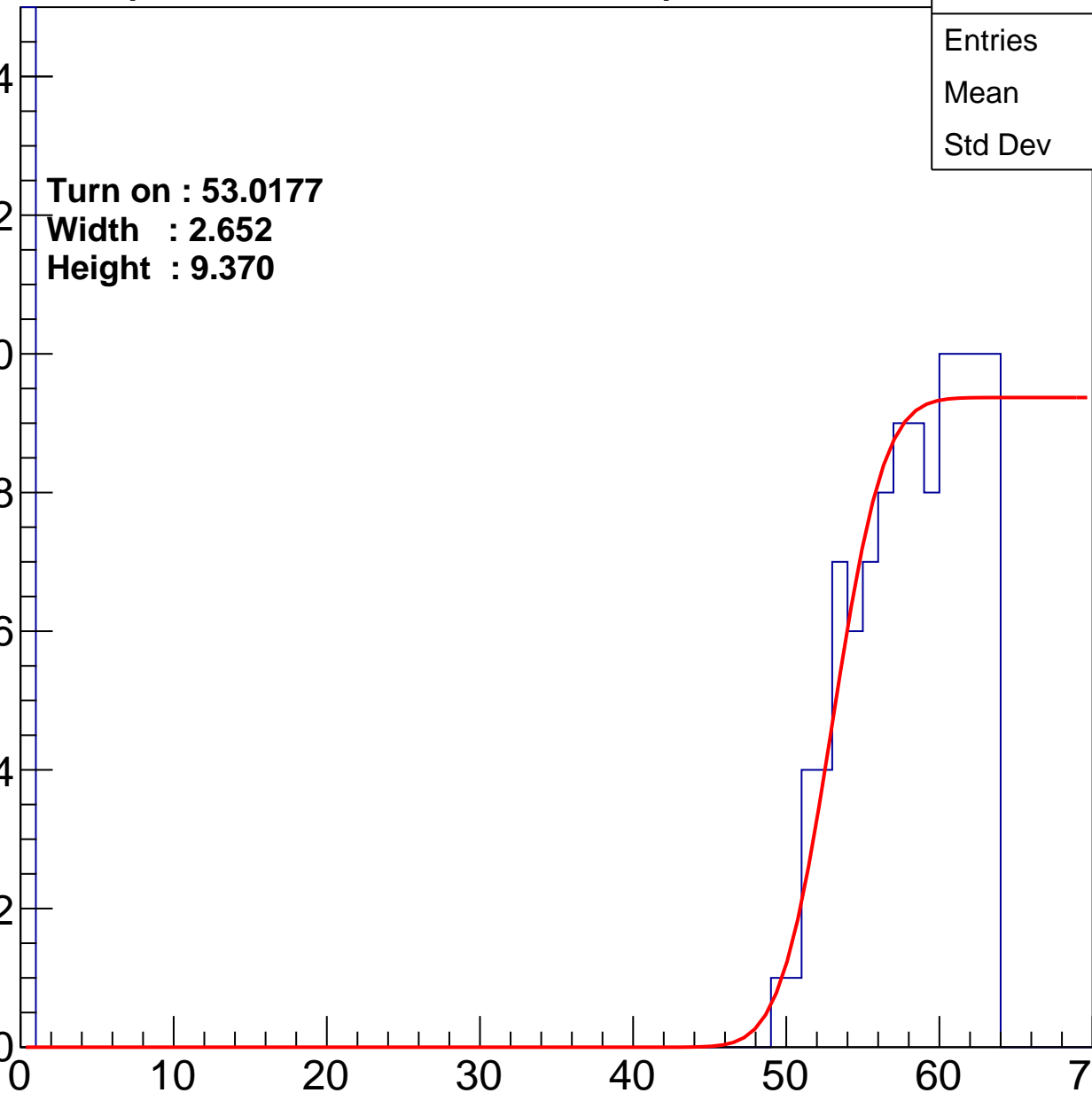
Width : 2.652

Height : 9.370

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	39.7
Std Dev	26.78

Turn on : 54.1153

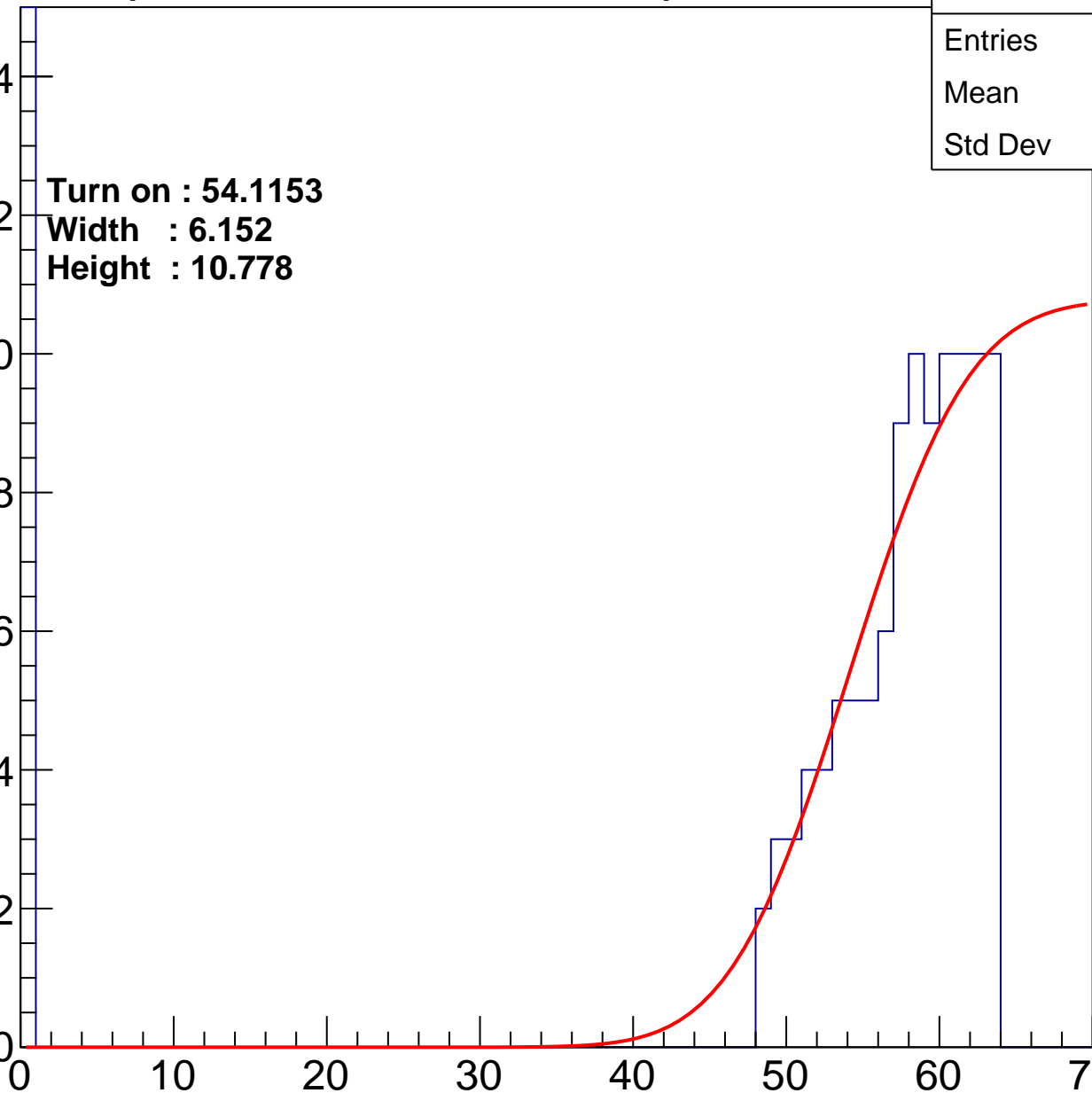
Width : 6.152

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	31.07
Std Dev	28.74

Turn on : 53.0276

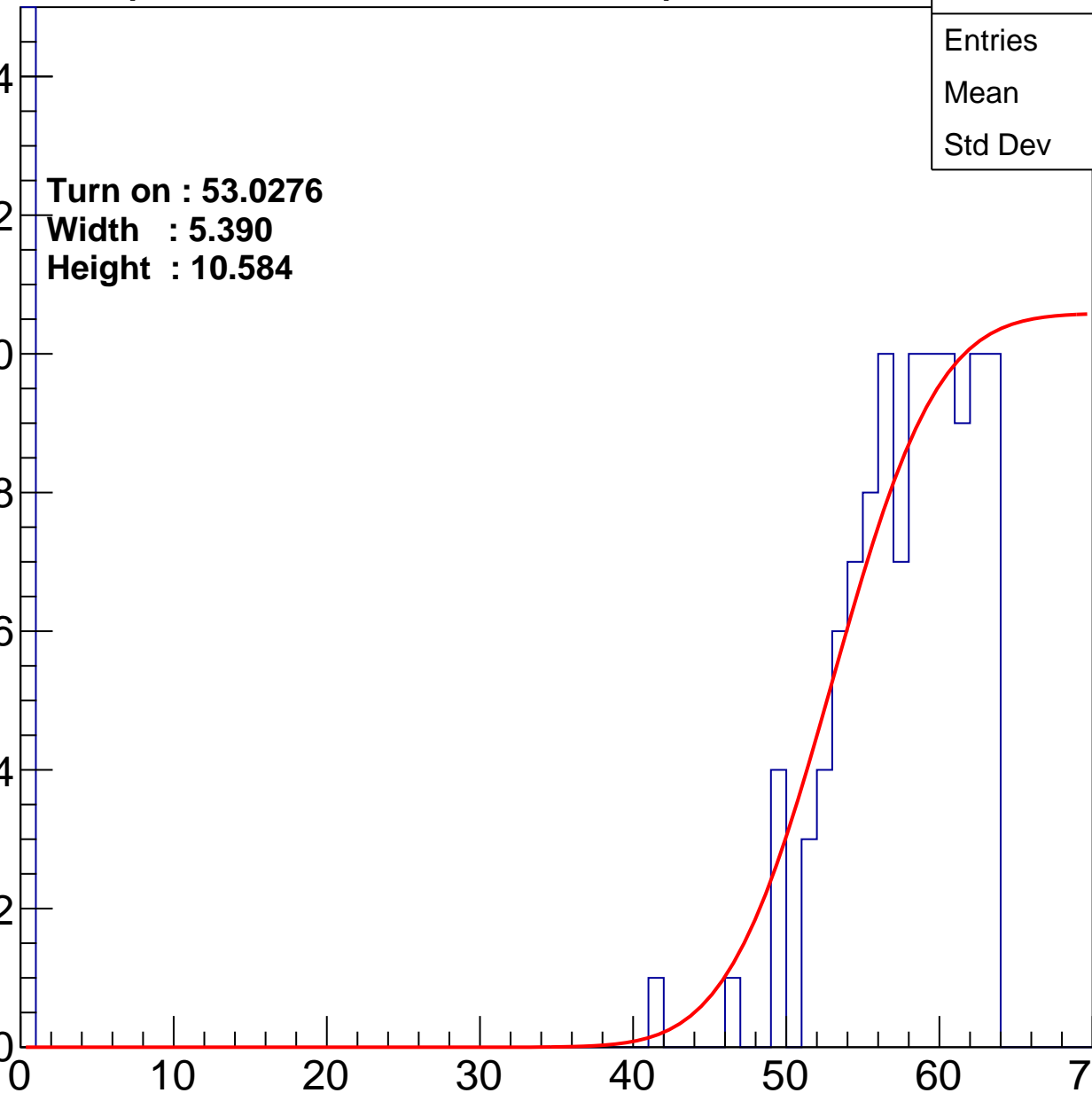
Width : 5.390

Height : 10.584

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch15

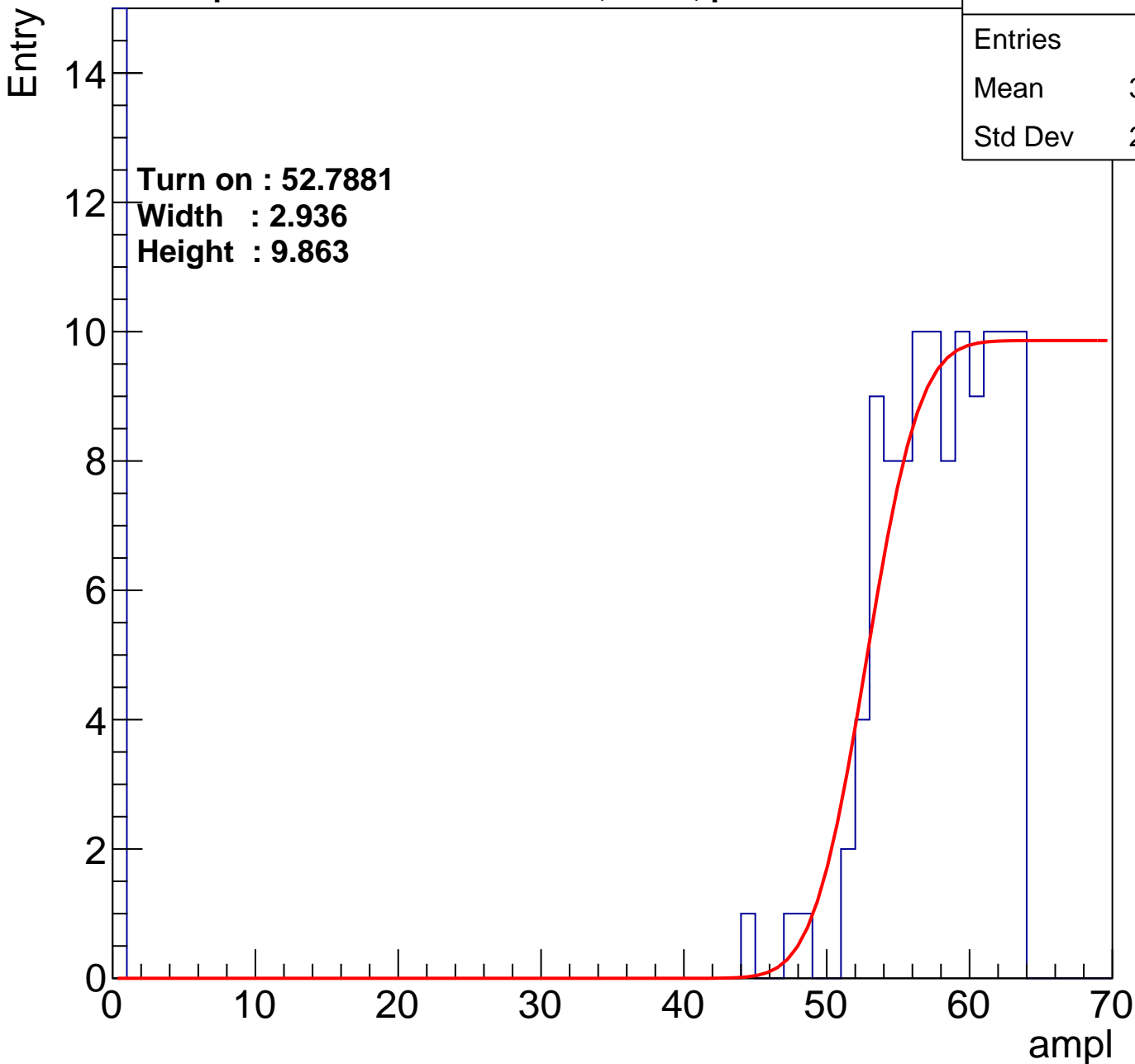
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.07
Std Dev	28.57

Turn on : 52.7881

Width : 2.936

Height : 9.863



B1L104S, U8-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.54
Std Dev	27.58

Turn on : 55.2966

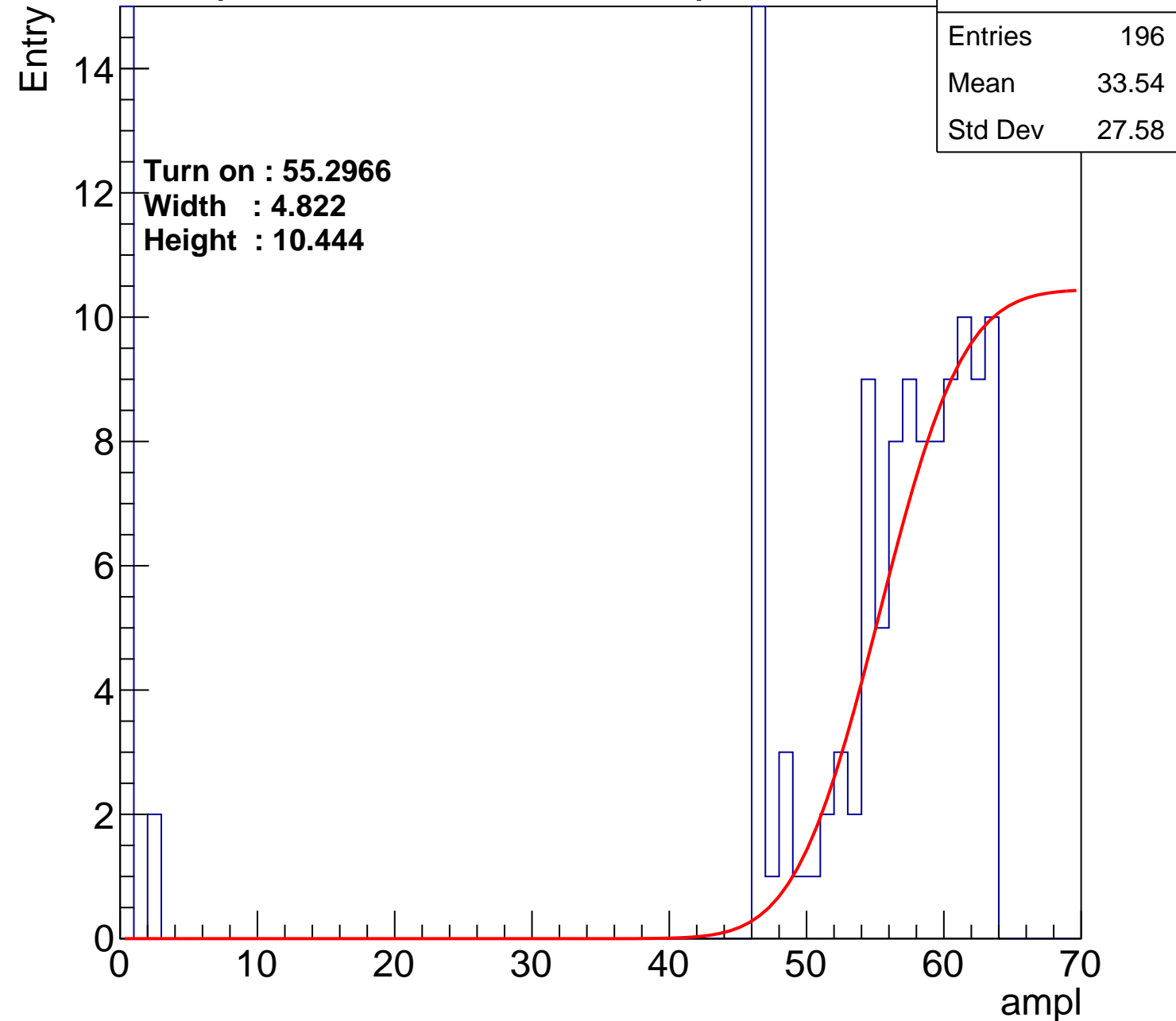
Width : 4.822

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	28.29
Std Dev	29.6

Turn on : 56.3998

Width : 3.255

Height : 10.000

Entry

80

70

60

50

40

30

20

10

0

0

10

20

30

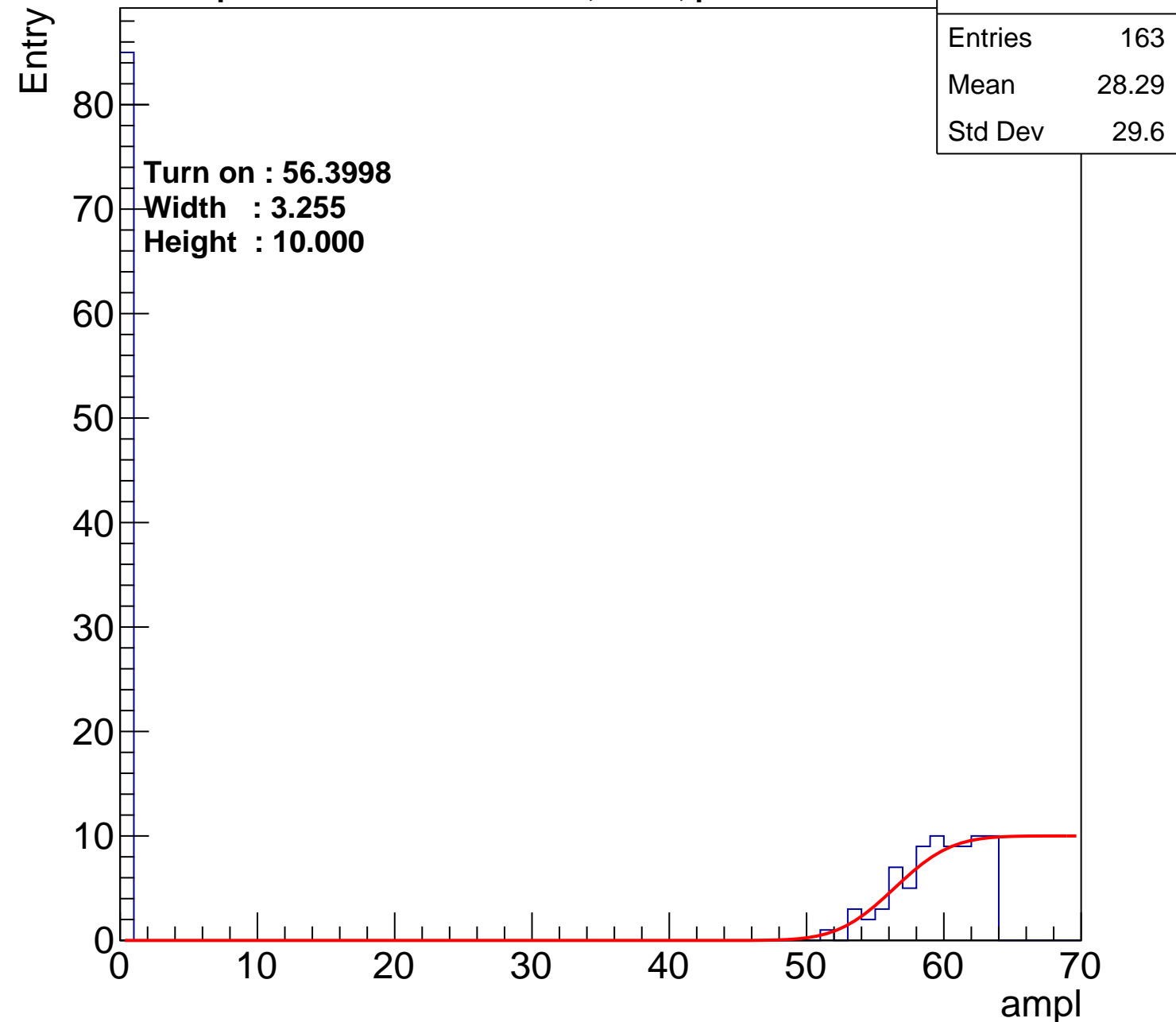
40

50

60

70

ampl



B1L104S, U8-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	34.8
Std Dev	28.15

Turn on : 53.1908

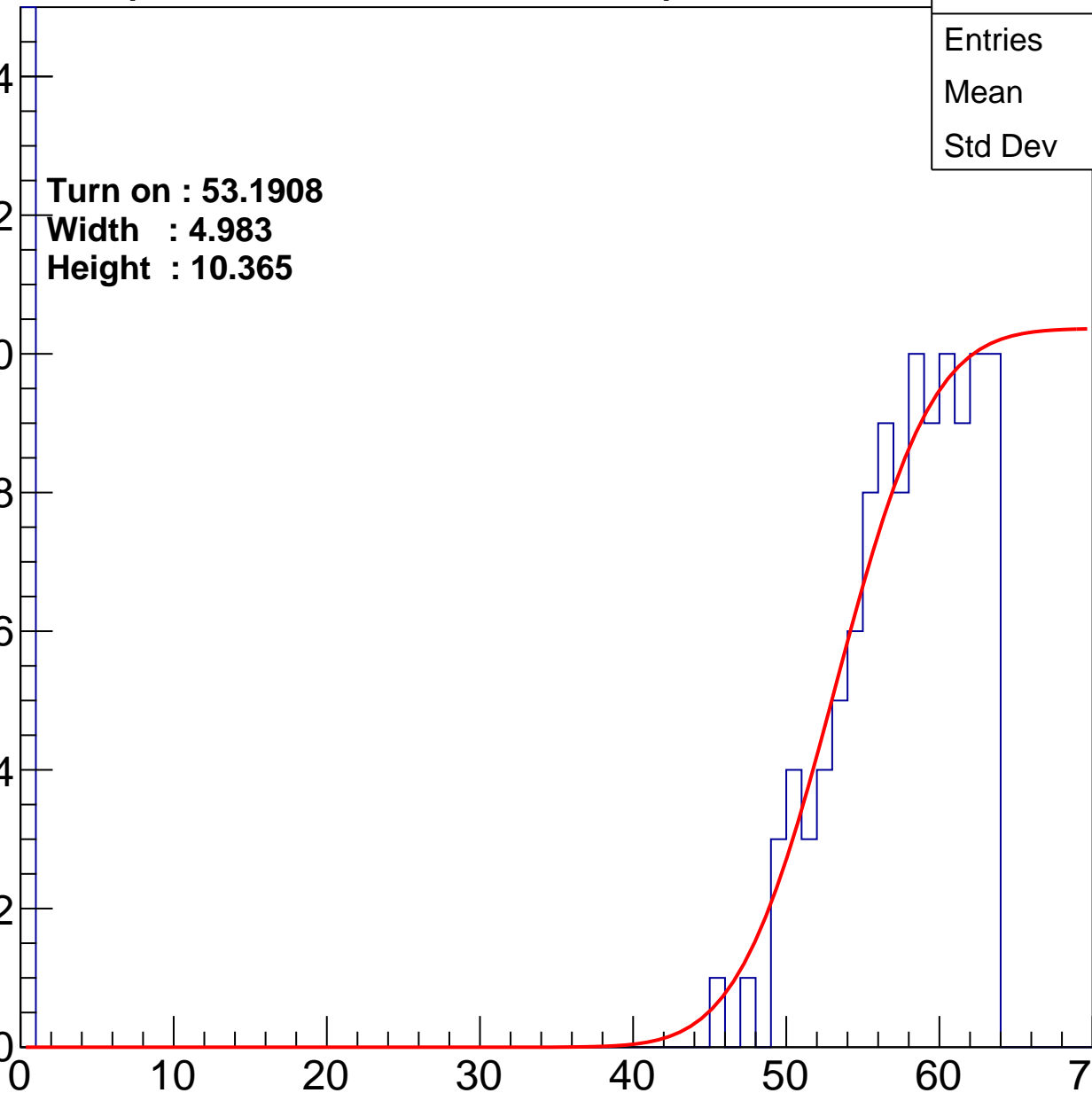
Width : 4.983

Height : 10.365

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.17
Std Dev	28.57

Turn on : 52.6431

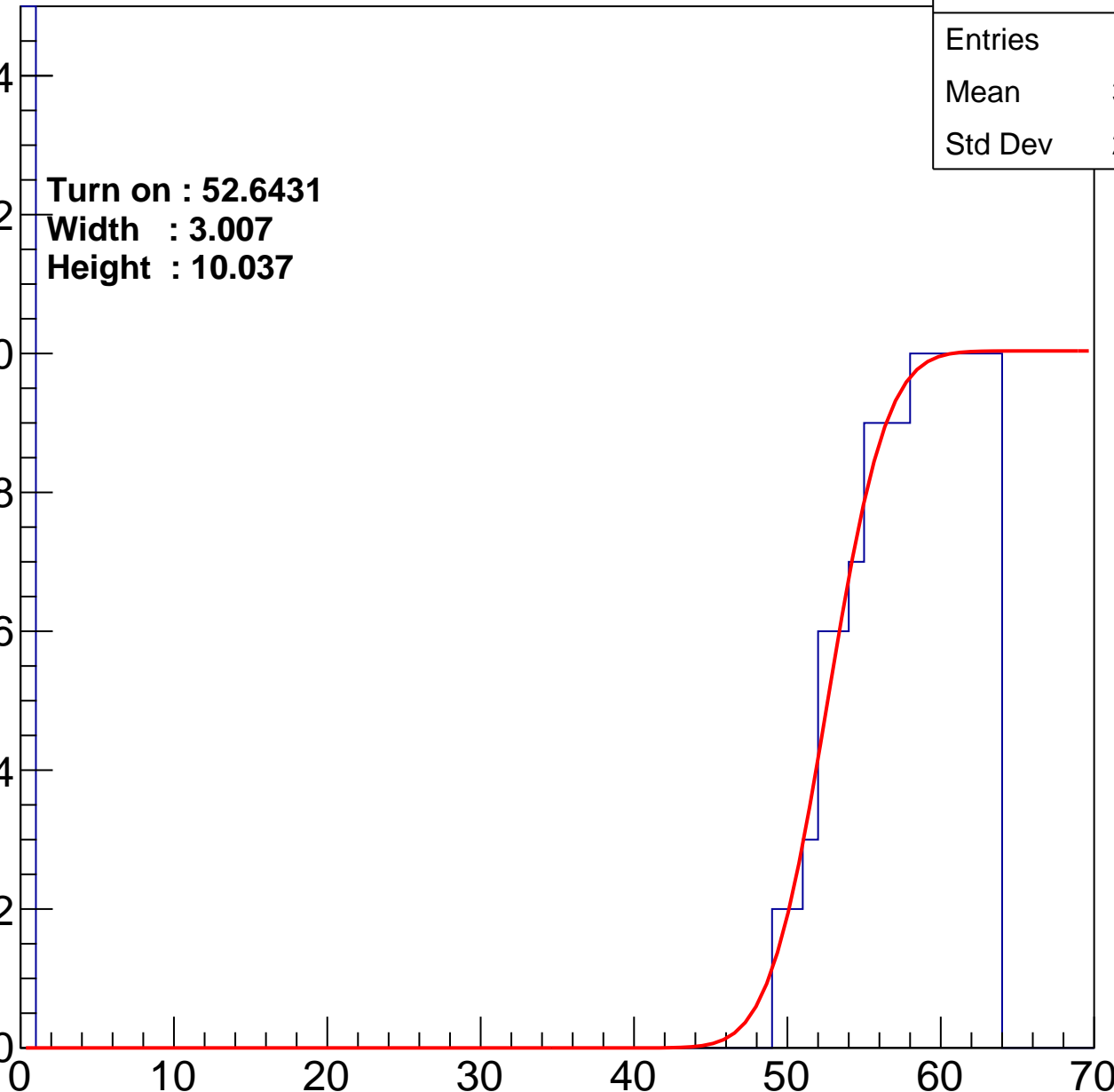
Width : 3.007

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch20

calib_packv5_033123_0516.root, FC#4, port A1

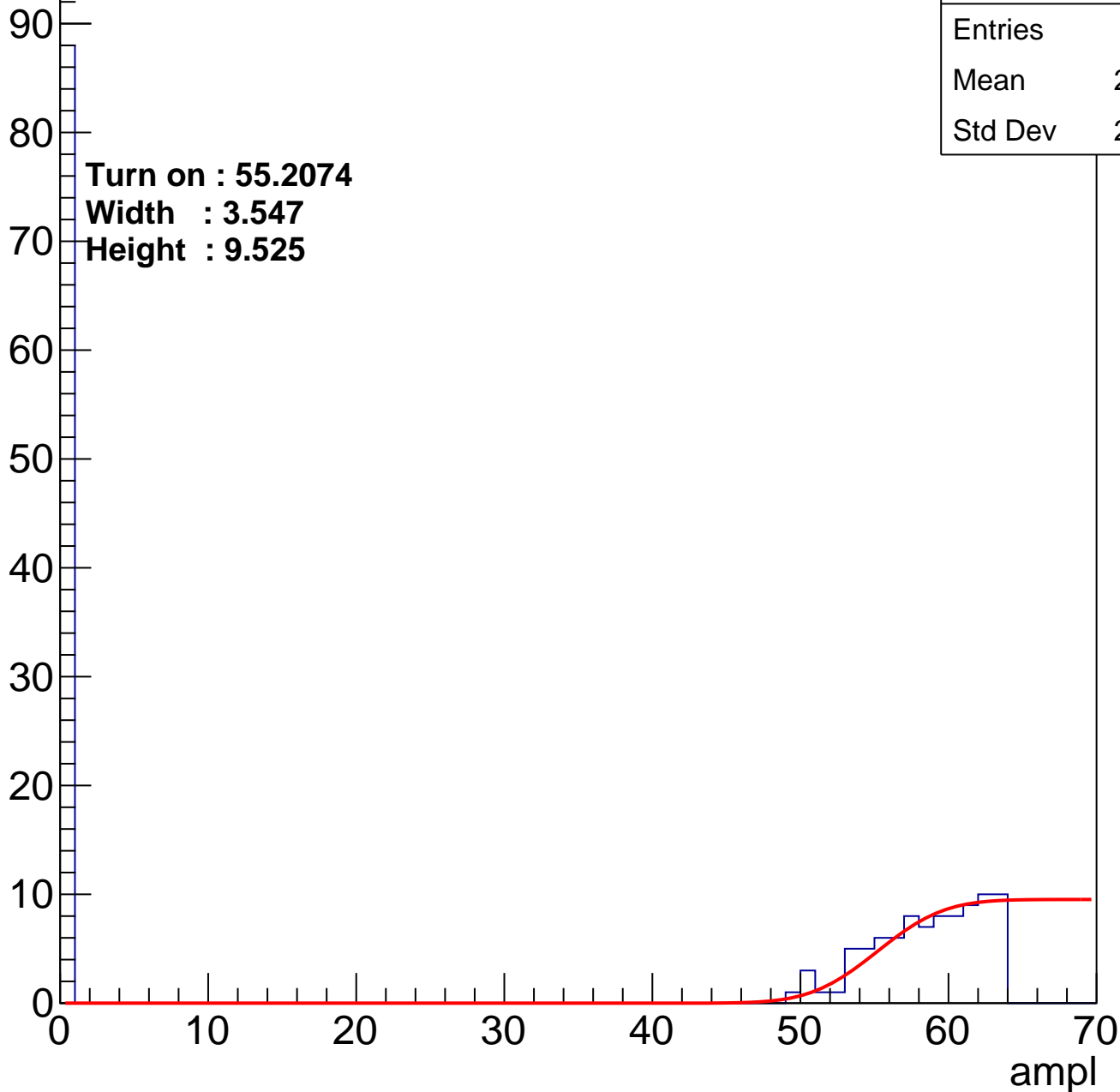
Entries	176
Mean	29.07
Std Dev	29.18

Turn on : 55.2074

Width : 3.547

Height : 9.525

Entry



B1L104S, U8-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	34.75
Std Dev	28.22

Turn on : 52.9207

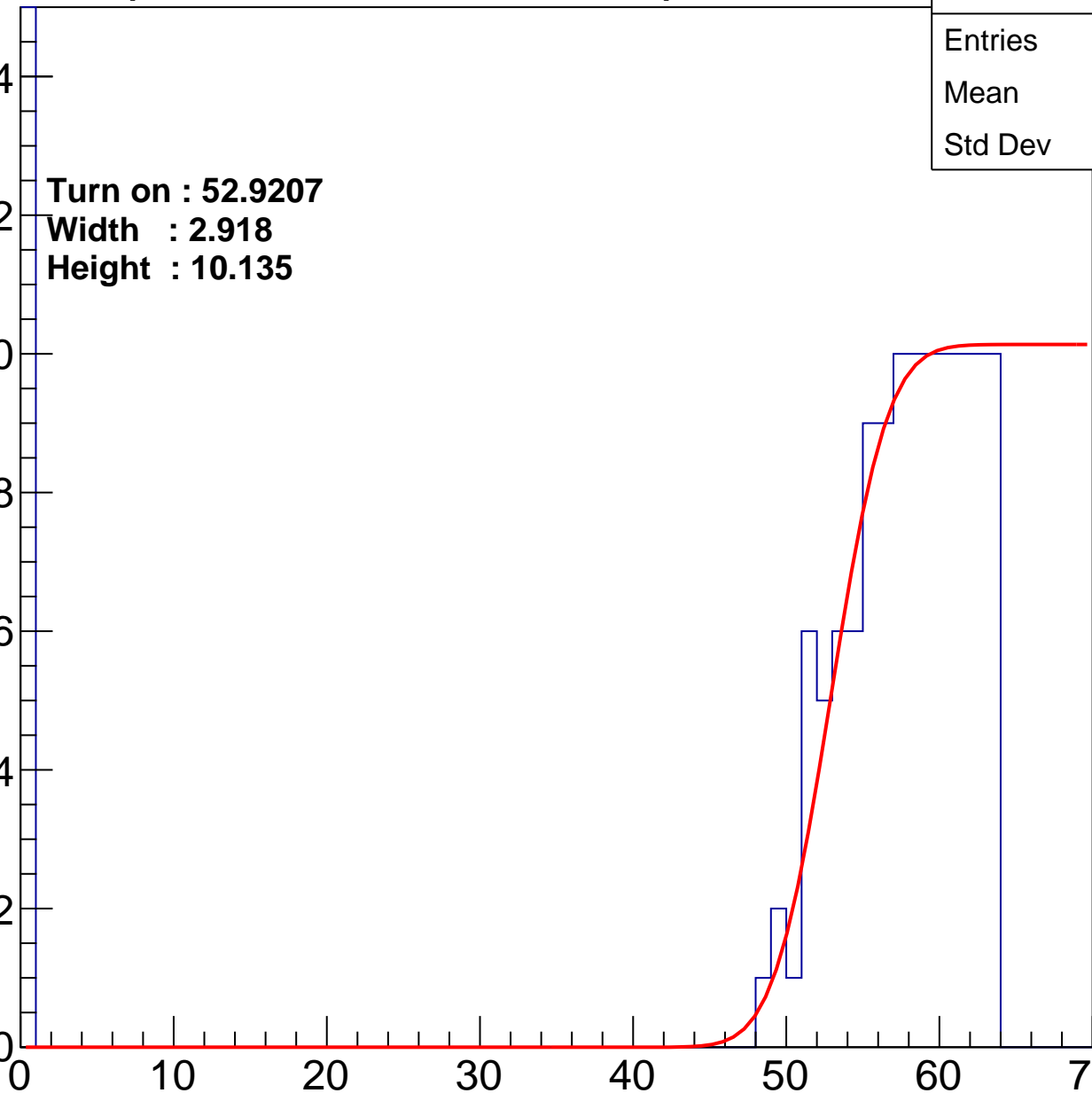
Width : 2.918

Height : 10.135

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	30.86
Std Dev	28.77

Turn on : 54.5414

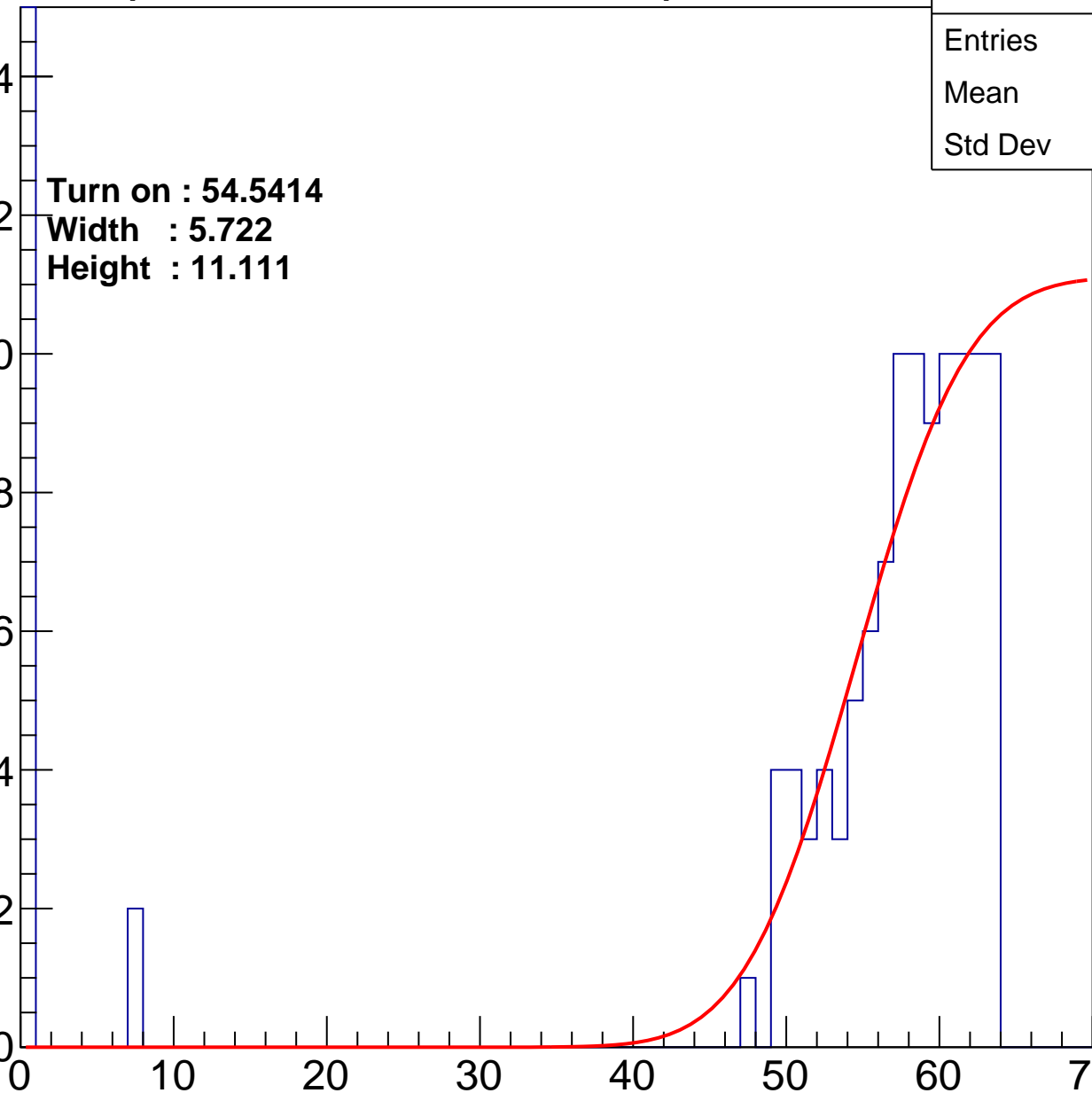
Width : 5.722

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.49
Std Dev	28.96

Turn on : 54.0609

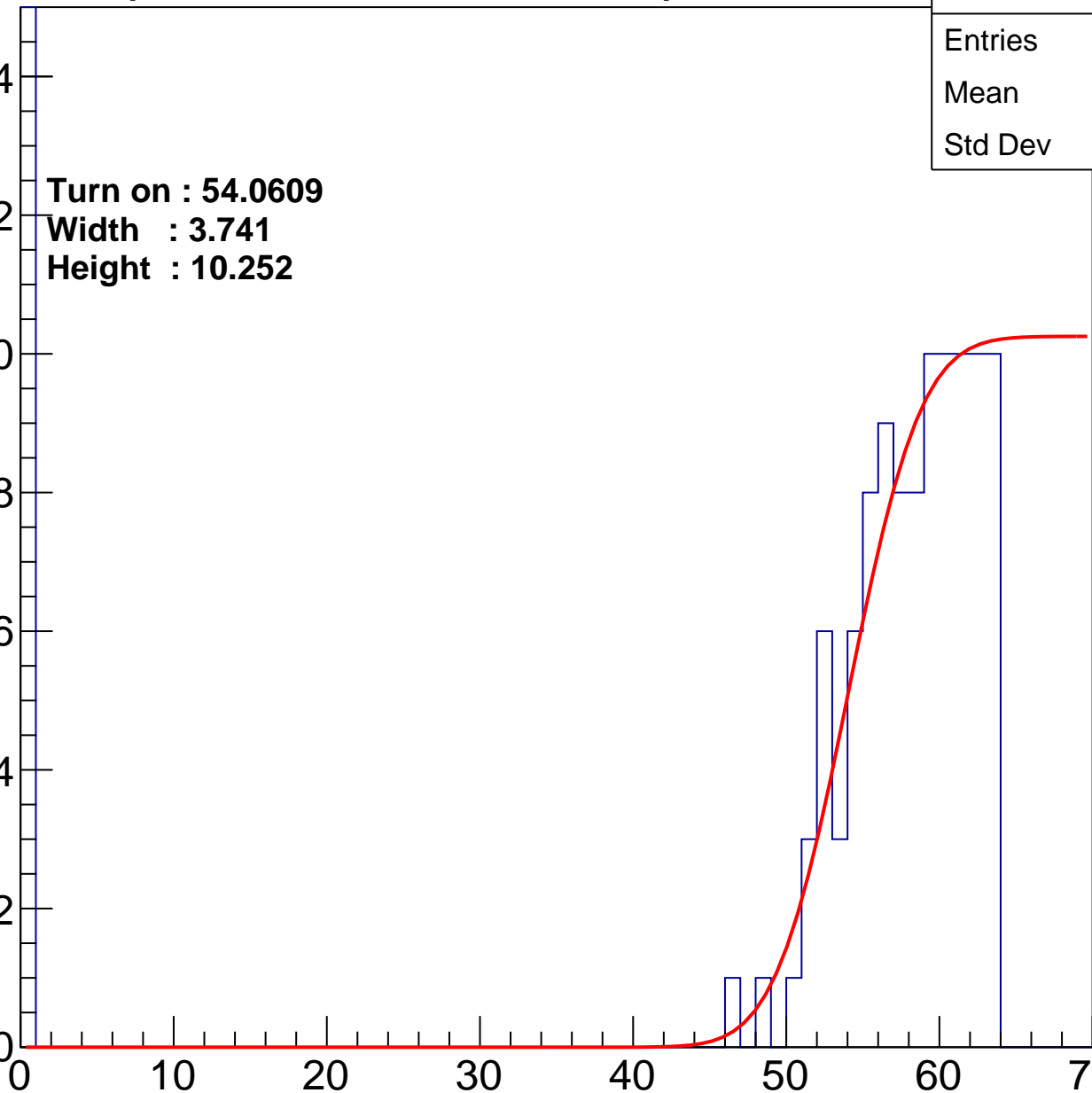
Width : 3.741

Height : 10.252

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.57
Std Dev	28.21

Turn on : 53.1306

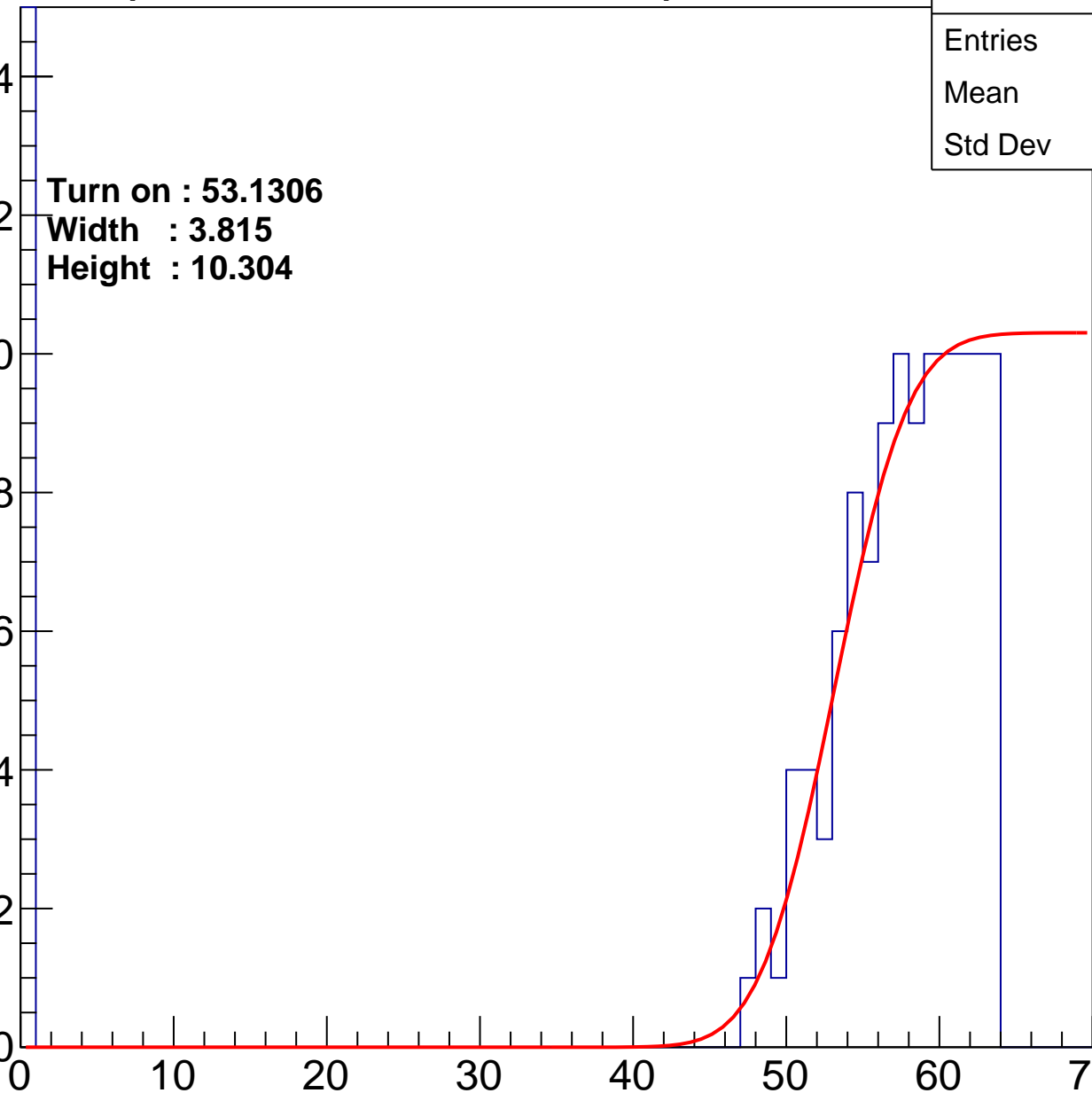
Width : 3.815

Height : 10.304

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	27.63
Std Dev	29.5

Turn on : 56.1402

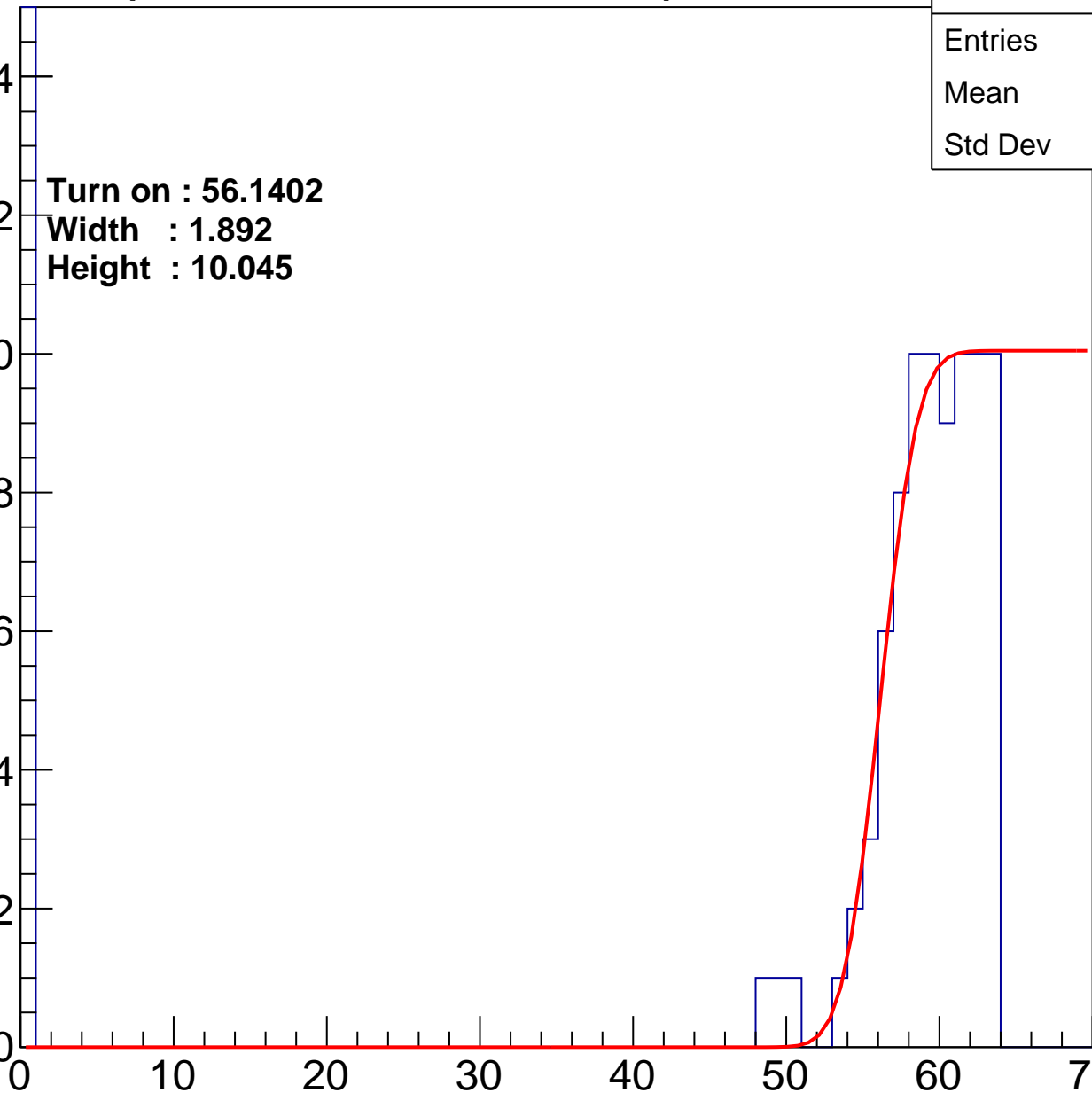
Width : 1.892

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch26

calib_packv5_033123_0516.root, FC#4, port A1

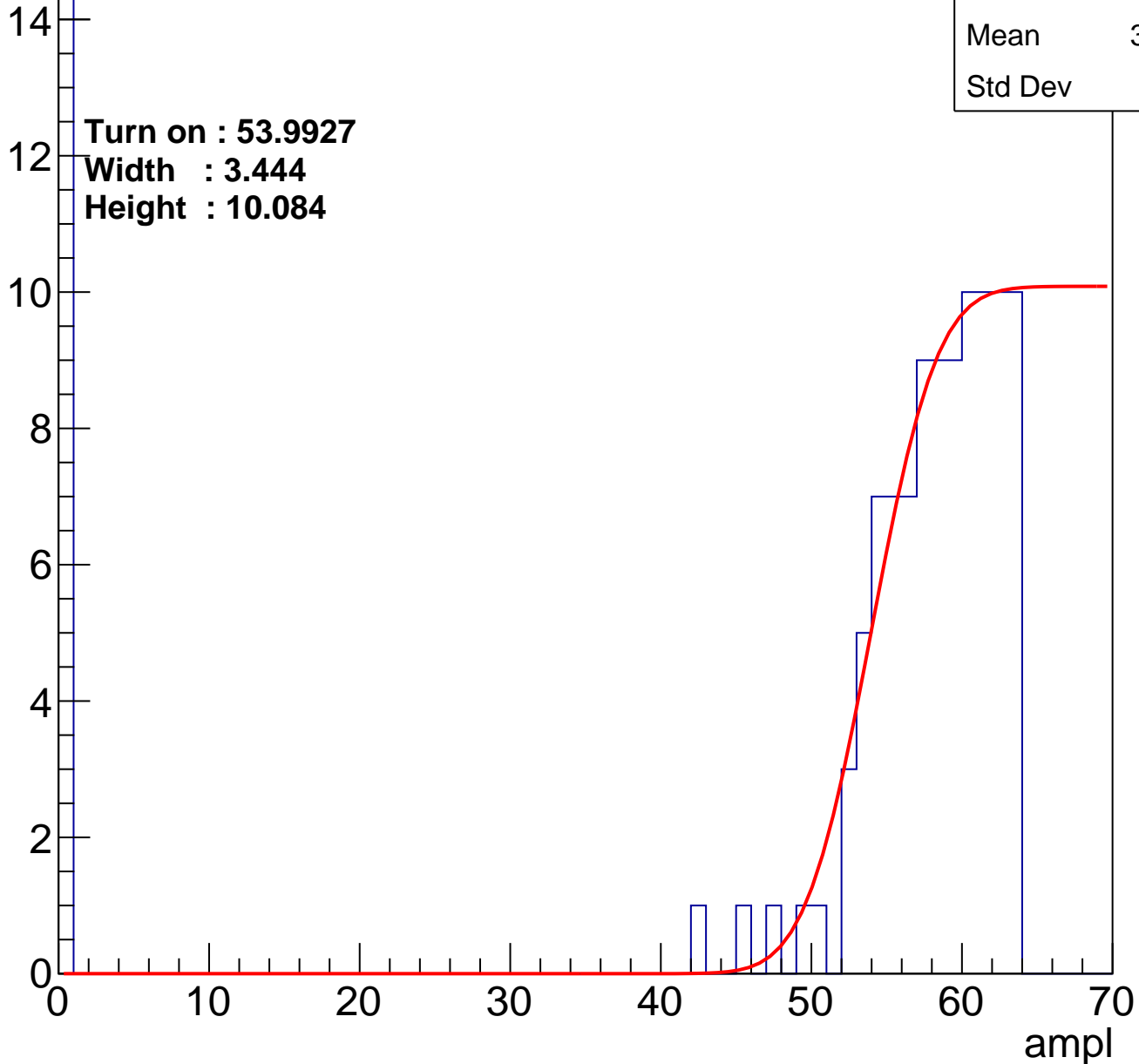
Entry

Entries	192
Mean	30.39
Std Dev	29

Turn on : 53.9927

Width : 3.444

Height : 10.084



B1L104S, U8-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	34.08
Std Dev	28.6

Turn on : 53.3825

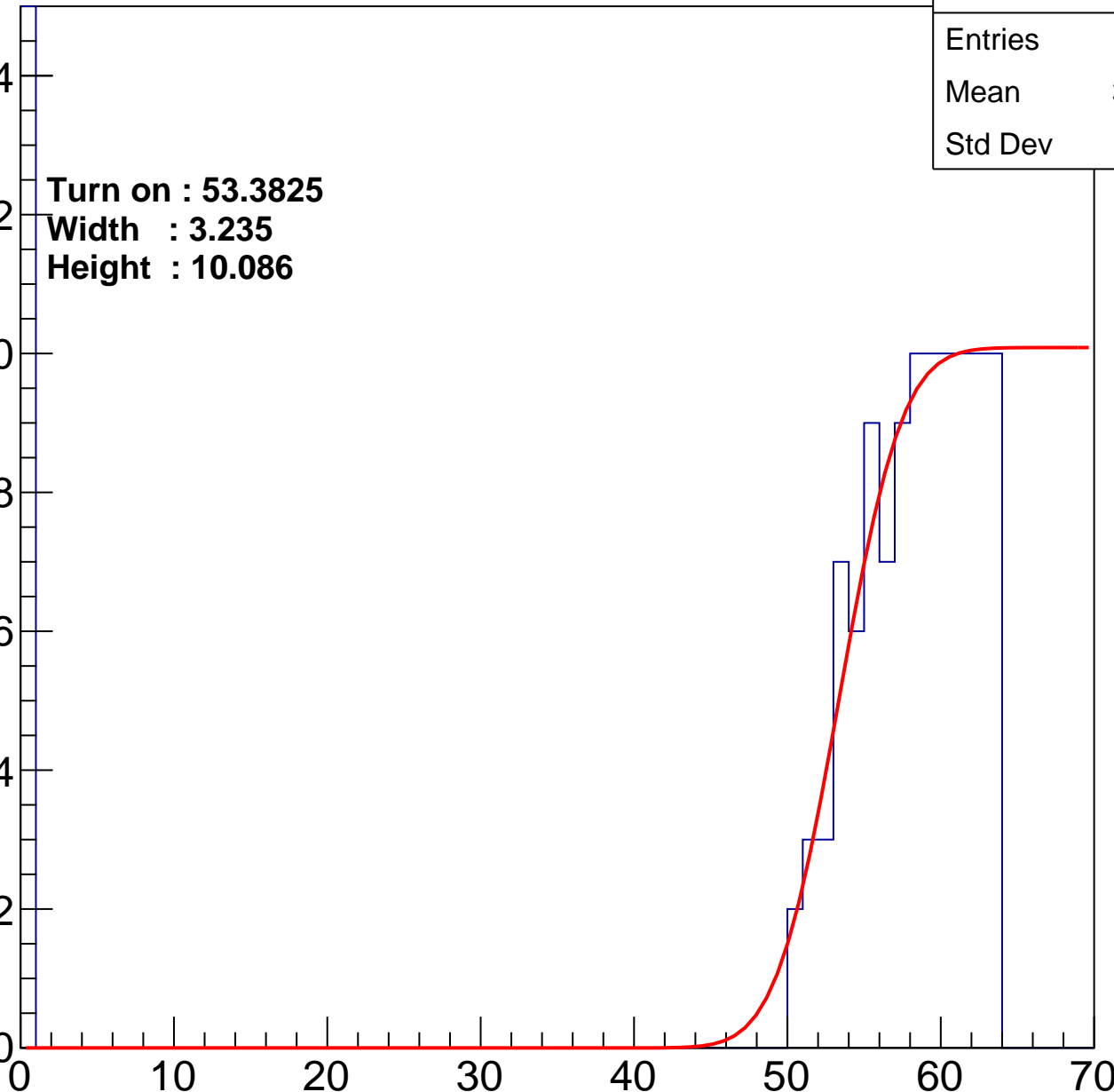
Width : 3.235

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



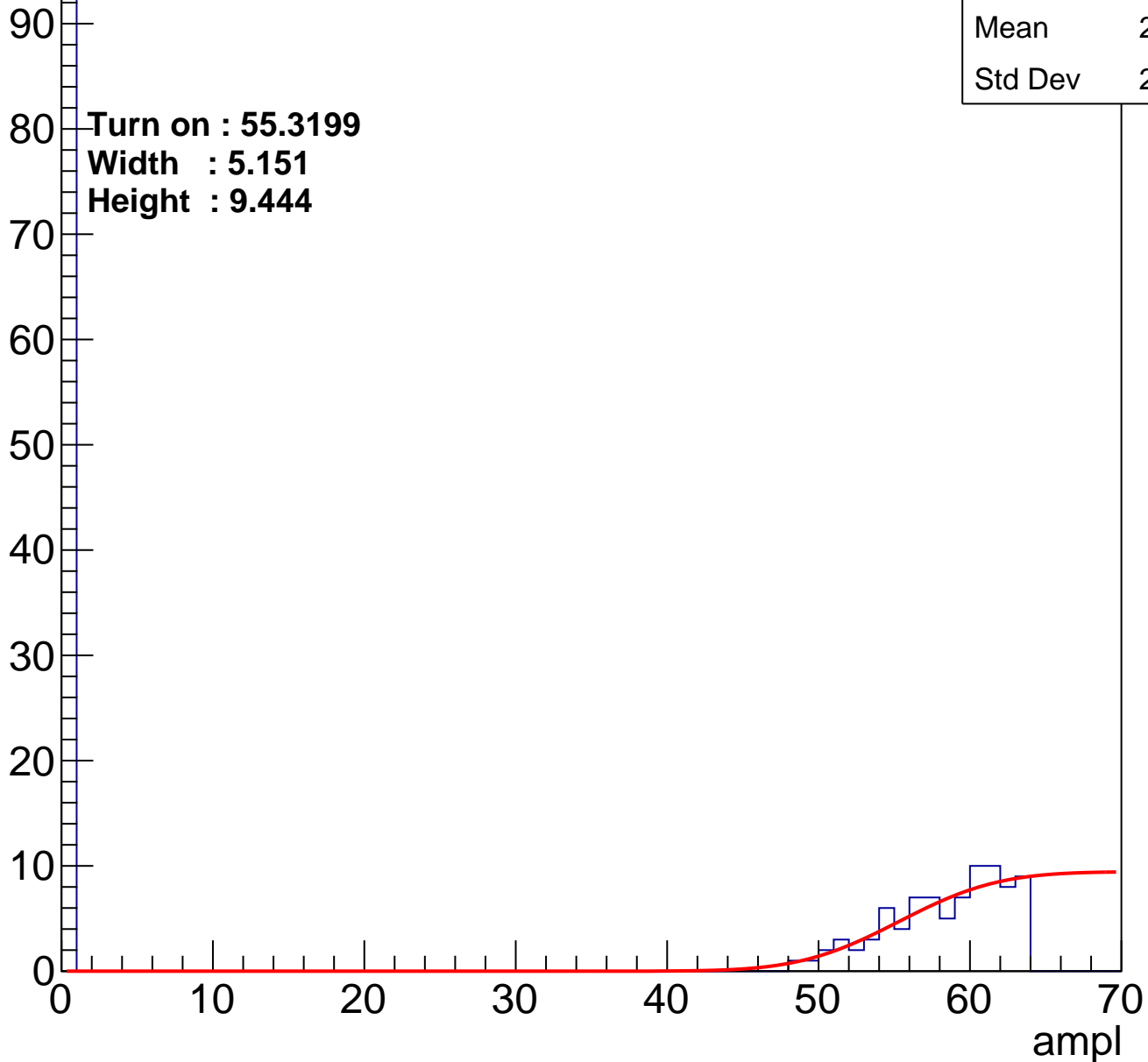
B1L104S, U8-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	27.66
Std Dev	29.06

Turn on : 55.3199
Width : 5.151
Height : 9.444

Entry



B1L104S, U8-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	32.28
Std Dev	28.64

Turn on : 54.4631

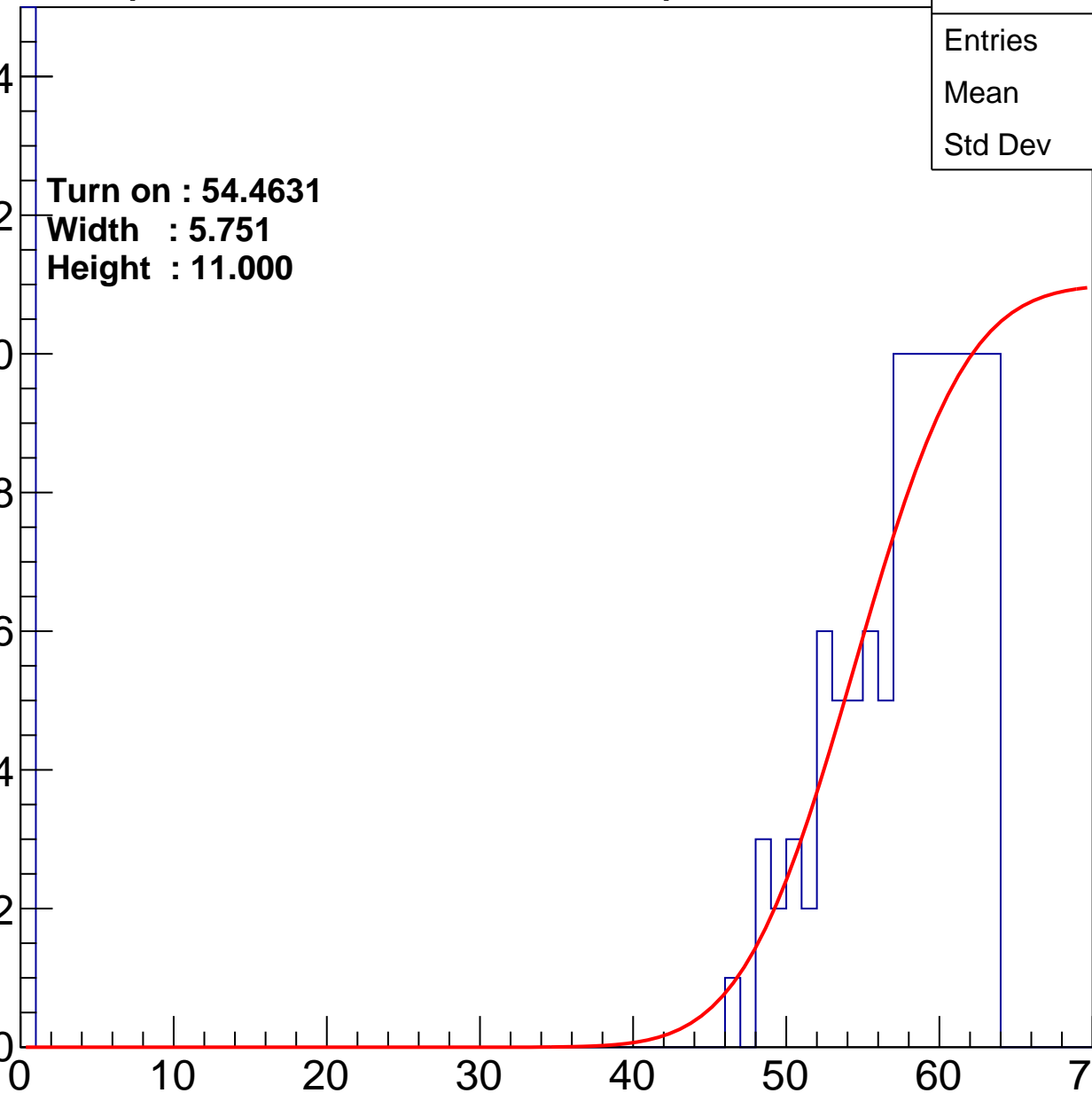
Width : 5.751

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	32.7
Std Dev	28.63

Turn on : 54.9262

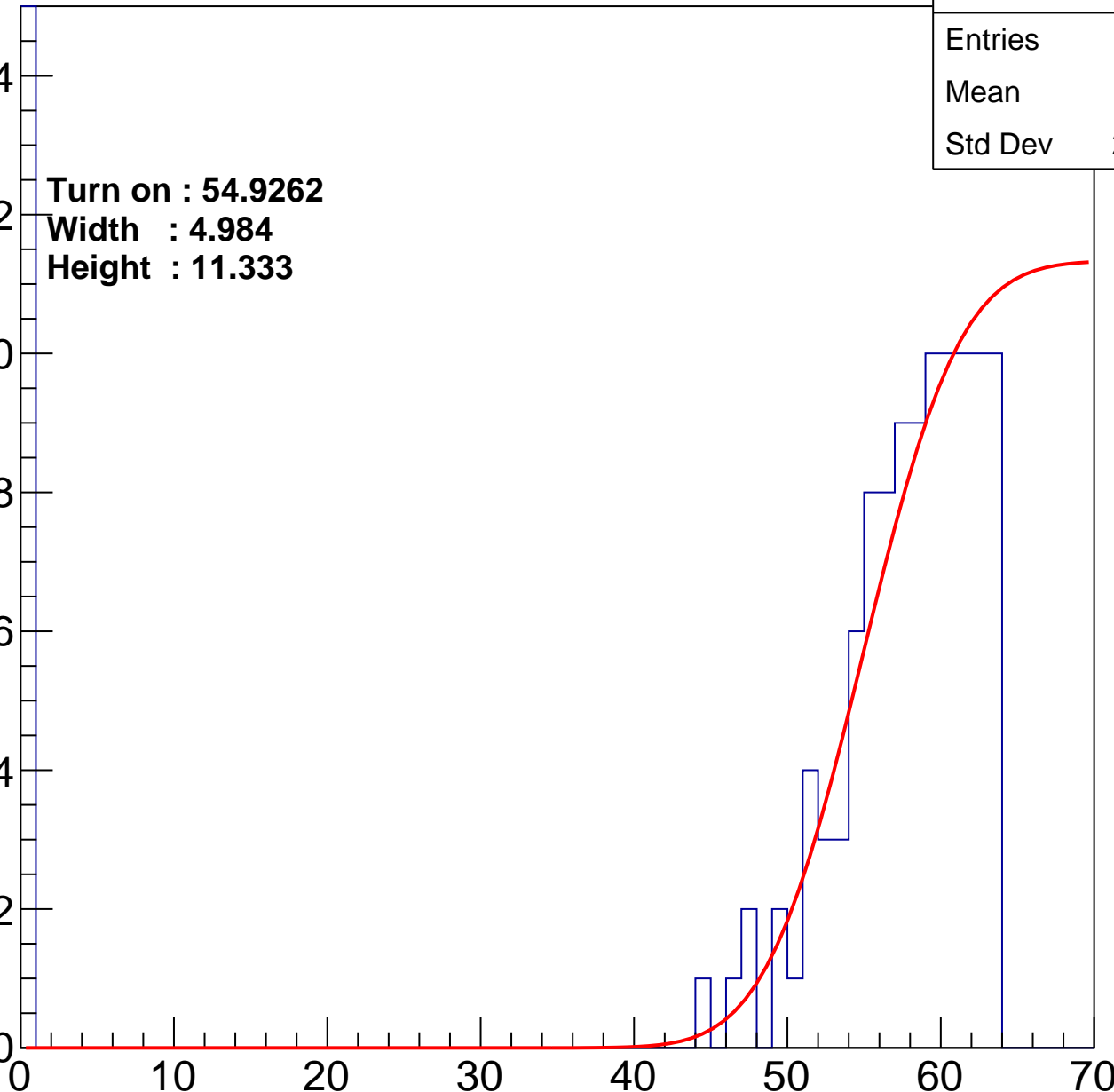
Width : 4.984

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	31.74
Std Dev	29.17

Turn on : 54.4190

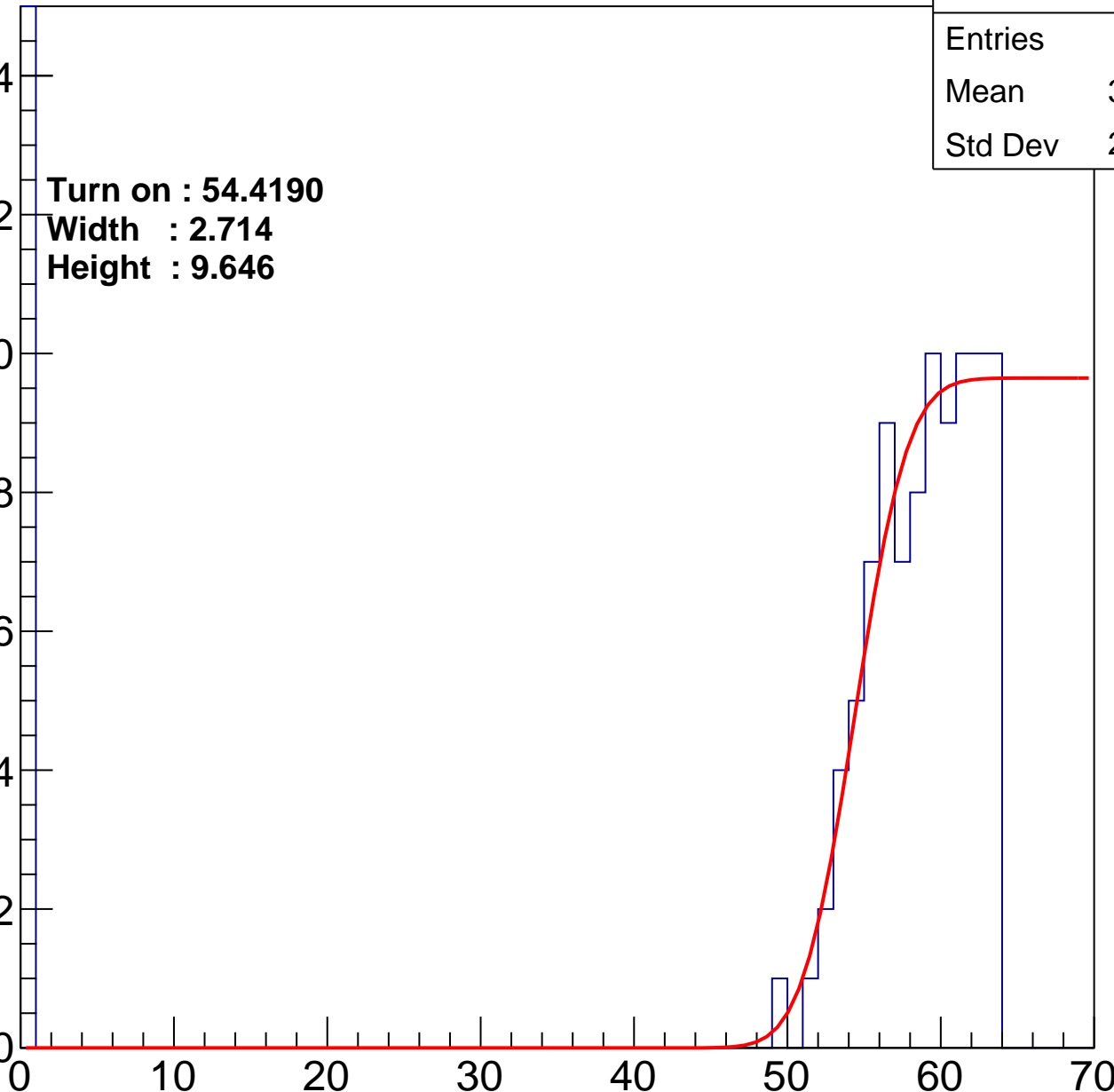
Width : 2.714

Height : 9.646

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	31.22
Std Dev	28.81

Turn on : 54.4484

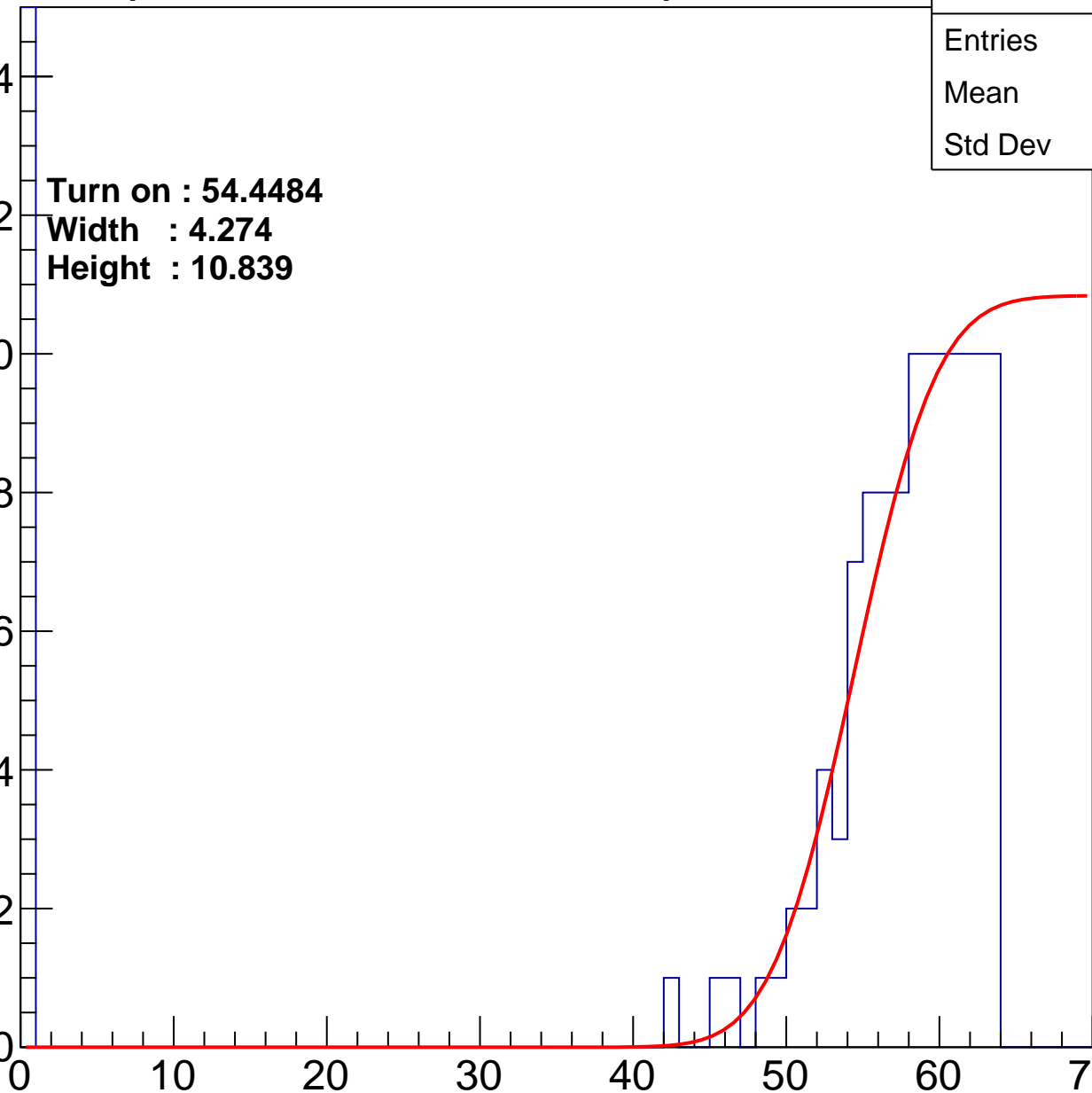
Width : 4.274

Height : 10.839

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	32.28
Std Dev	28.83

Turn on : 54.1905

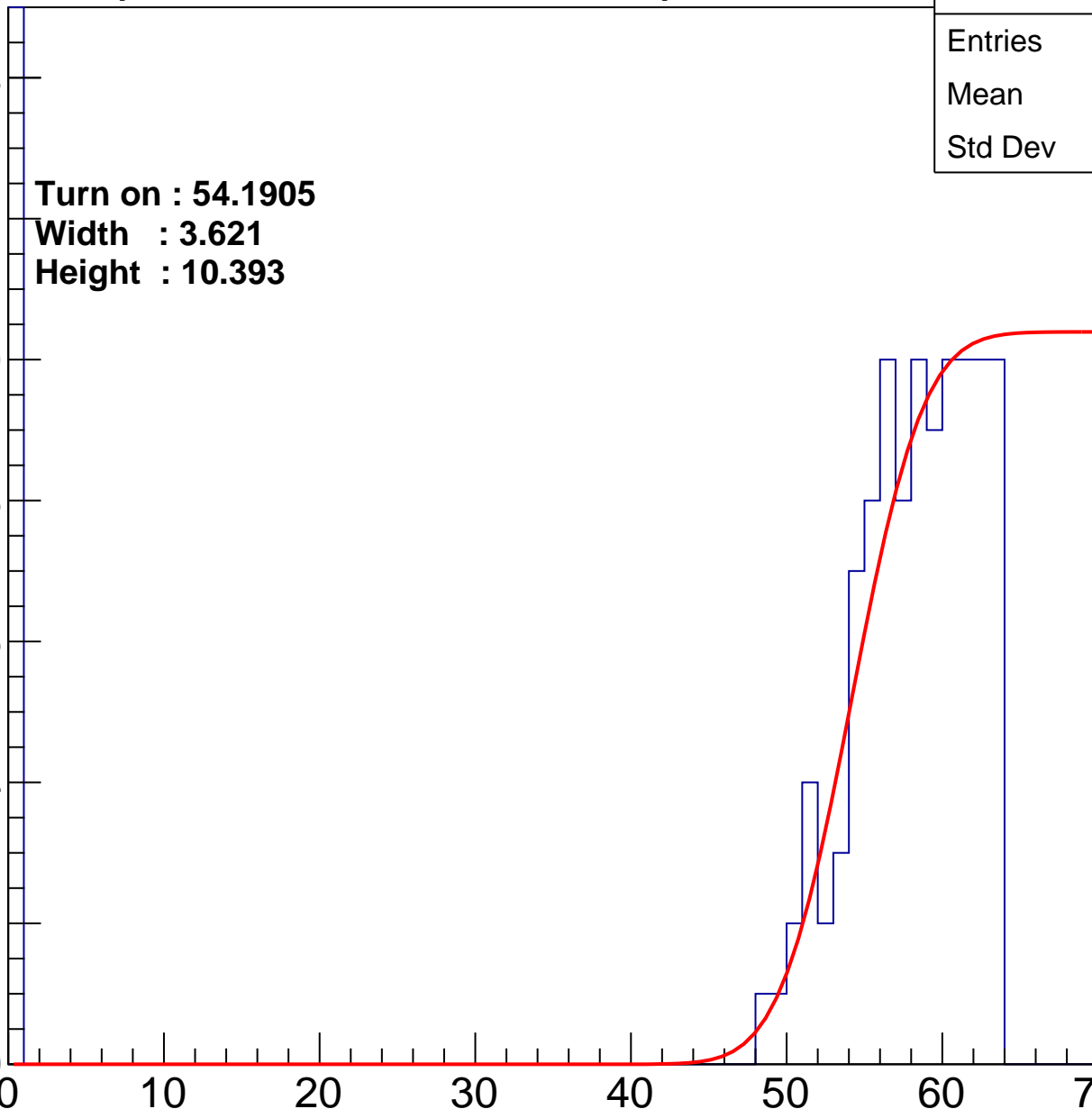
Width : 3.621

Height : 10.393

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	28.3
Std Dev	29.03

Turn on : 54.4895

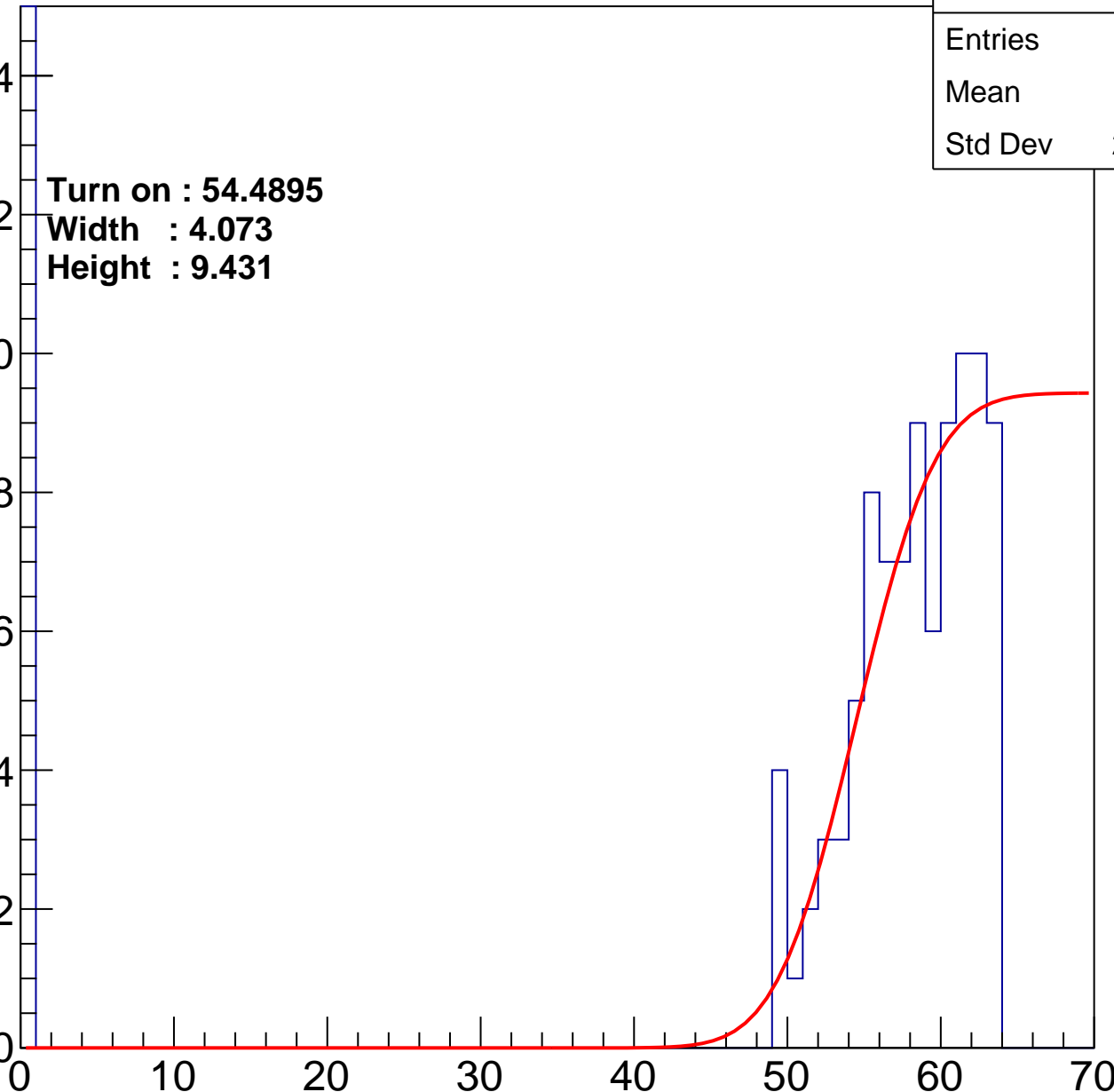
Width : 4.073

Height : 9.431

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.33
Std Dev	28.73

Turn on : 53.6658

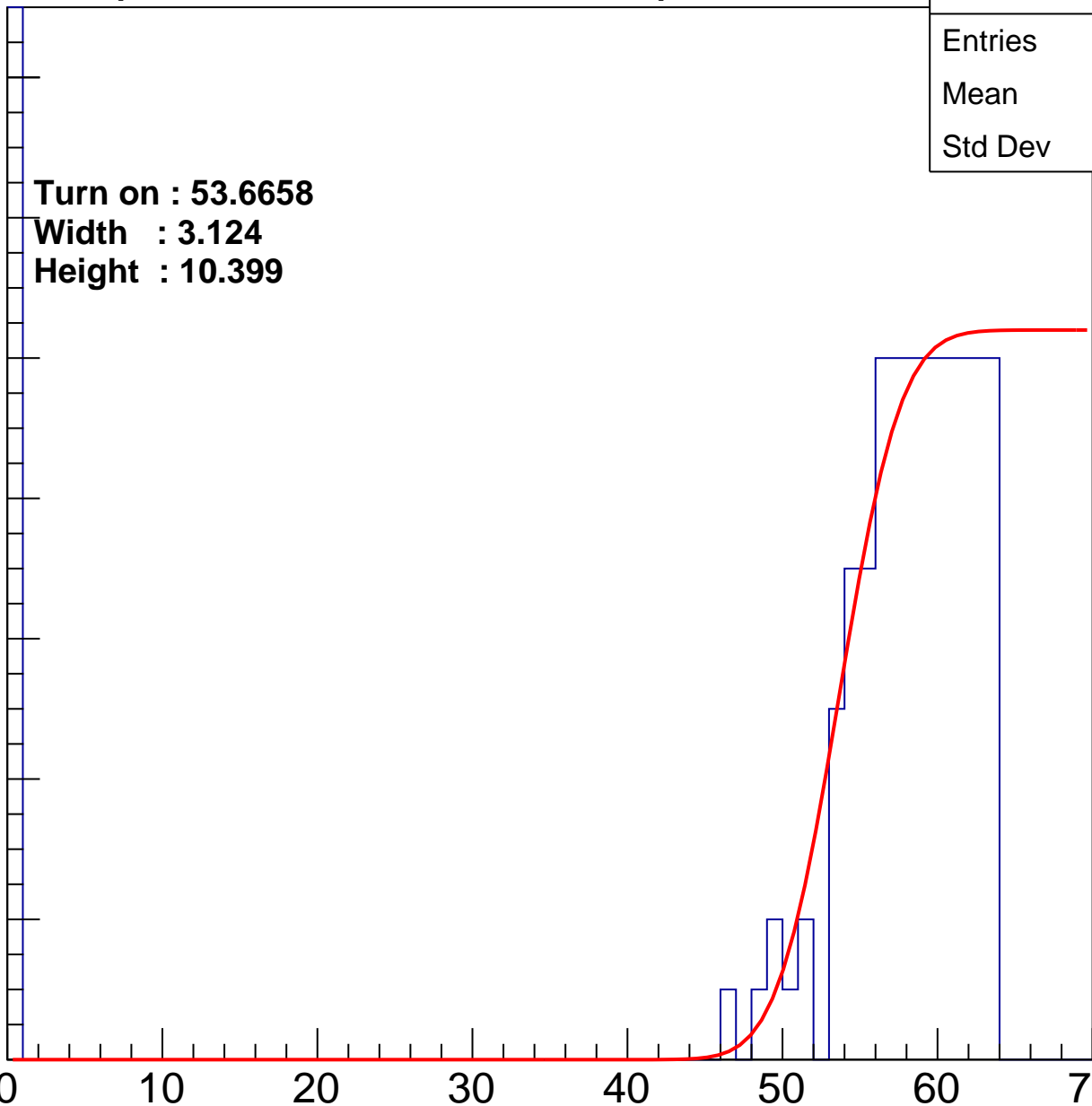
Width : 3.124

Height : 10.399

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.11
Std Dev	28.92

Turn on : 54.8355

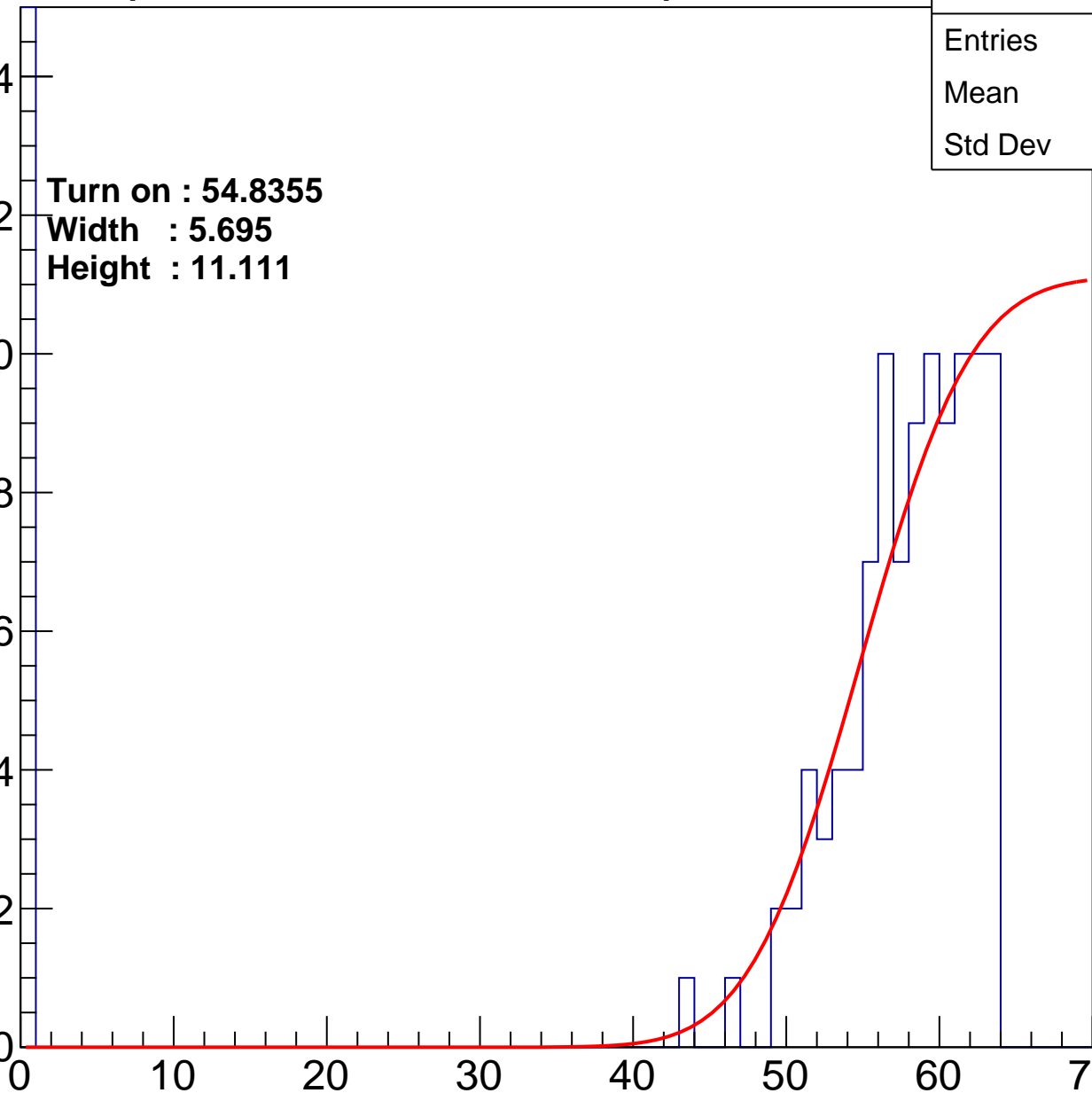
Width : 5.695

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch37

calib_packv5_033123_0516.root, FC#4, port A1

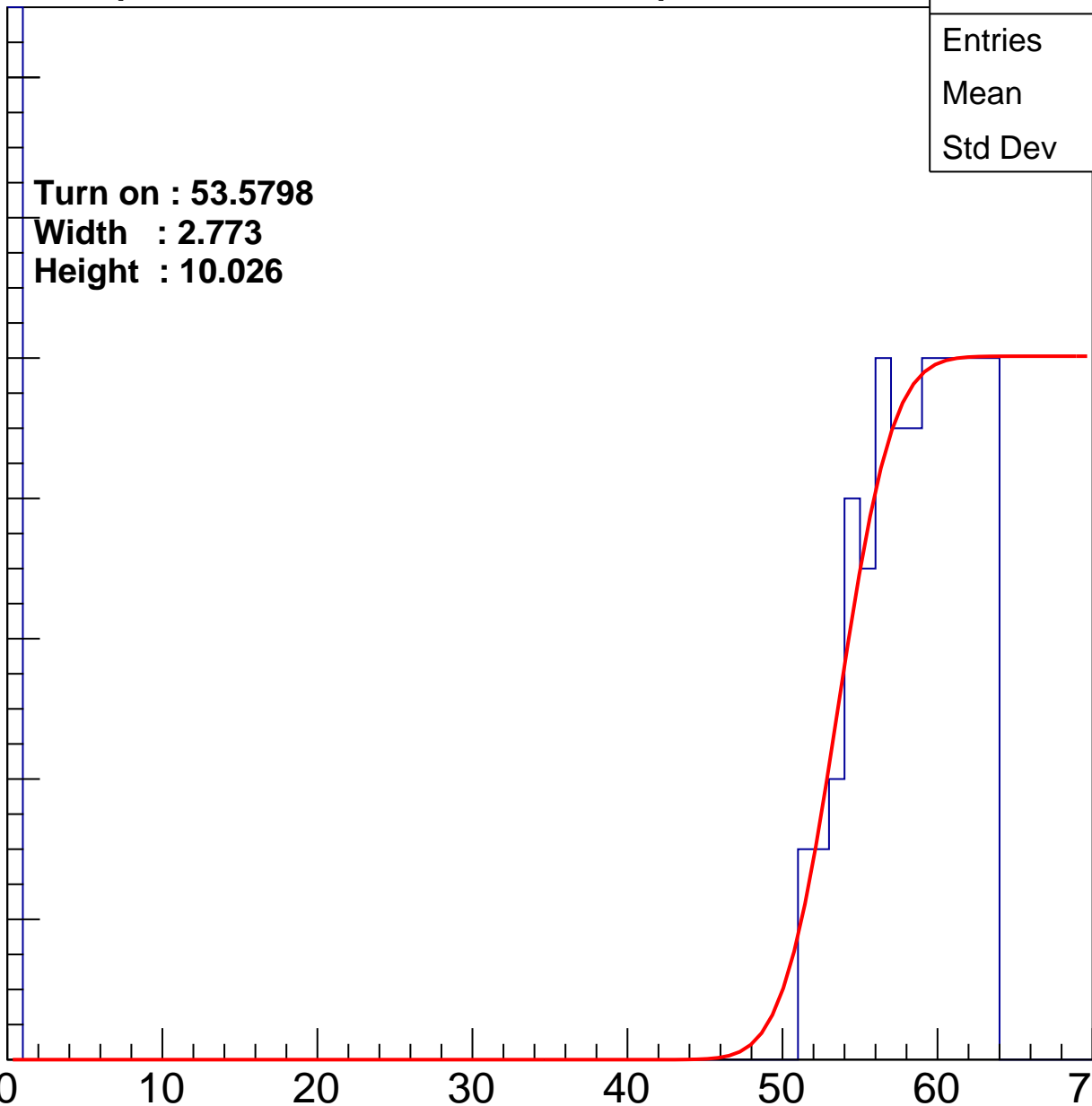
Entry

14
12
10
8
6
4
2
0

Turn on : 53.5798
Width : 2.773
Height : 10.026

Entries	171
Mean	34.99
Std Dev	28.55

ampl



B1L104S, U8-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	32.29
Std Dev	28.34

Turn on : 53.6430

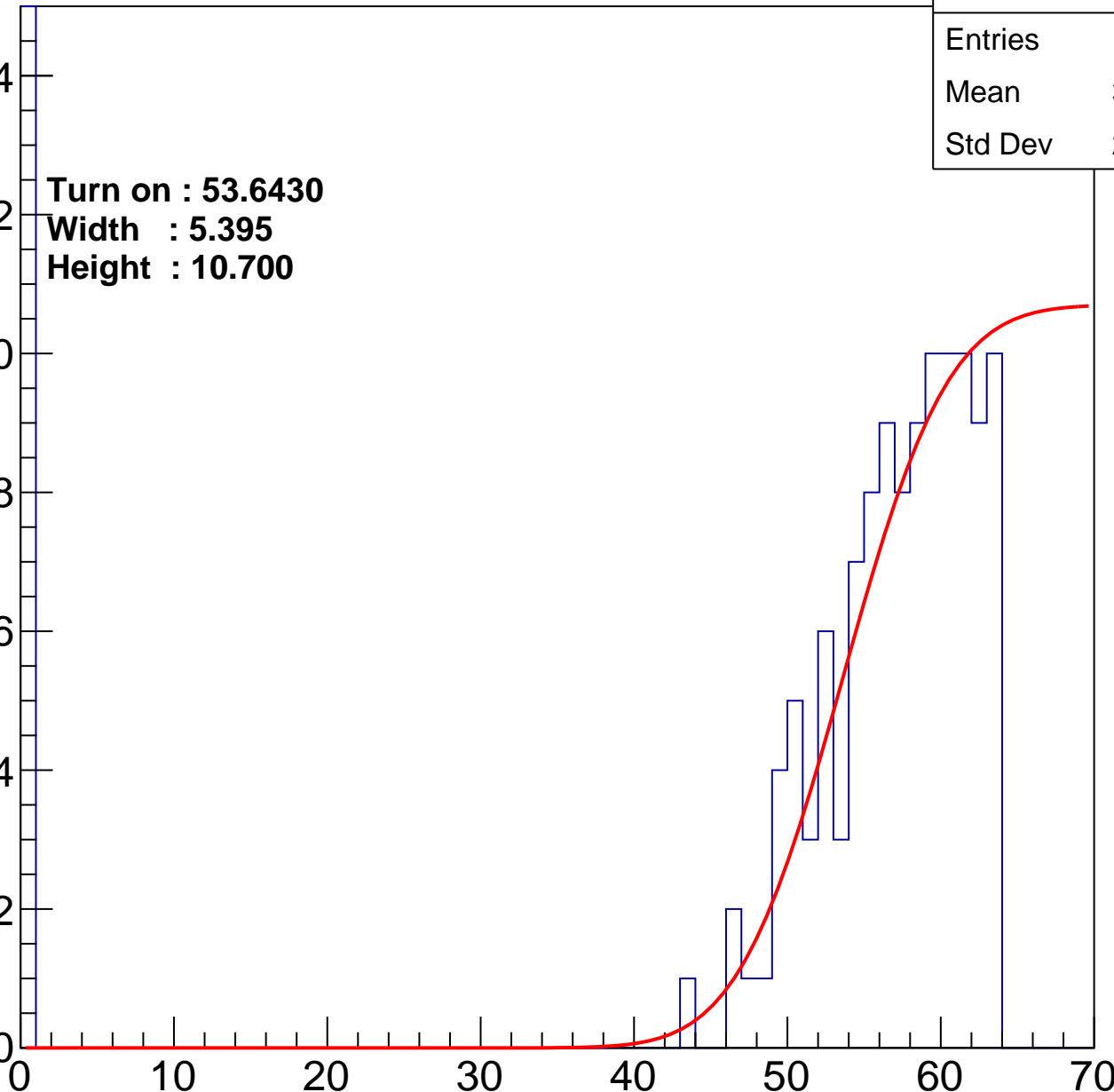
Width : 5.395

Height : 10.700

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch39

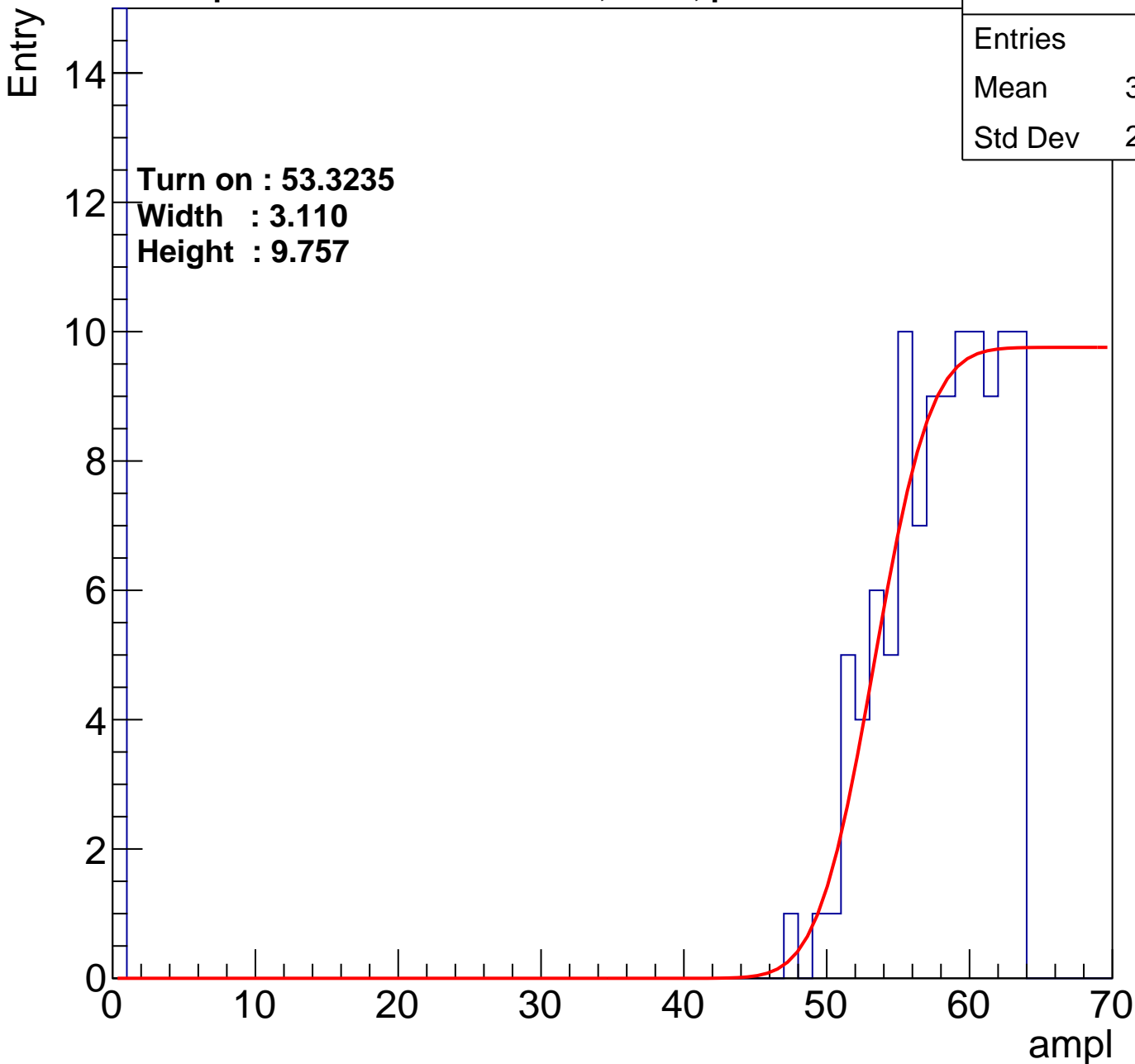
calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	34.05
Std Dev	28.47

Turn on : 53.3235

Width : 3.110

Height : 9.757



B1L104S, U8-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	26.68
Std Dev	28.94

Turn on : 55.7917

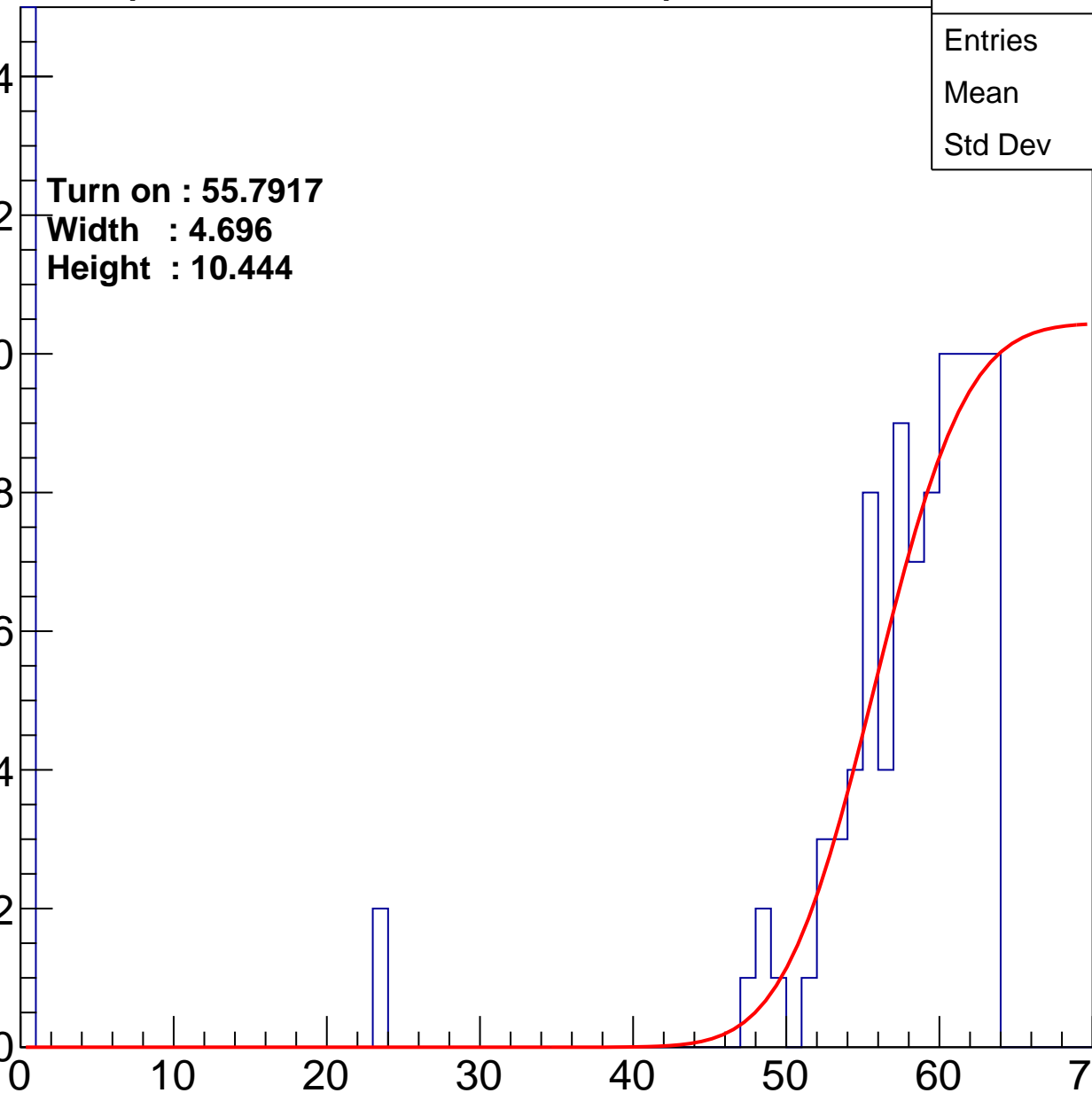
Width : 4.696

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	25.78
Std Dev	29.16

Turn on : 54.3533

Width : 3.580

Height : 9.790

Entry

14

12

10

8

6

4

2

0

0

10

20

30

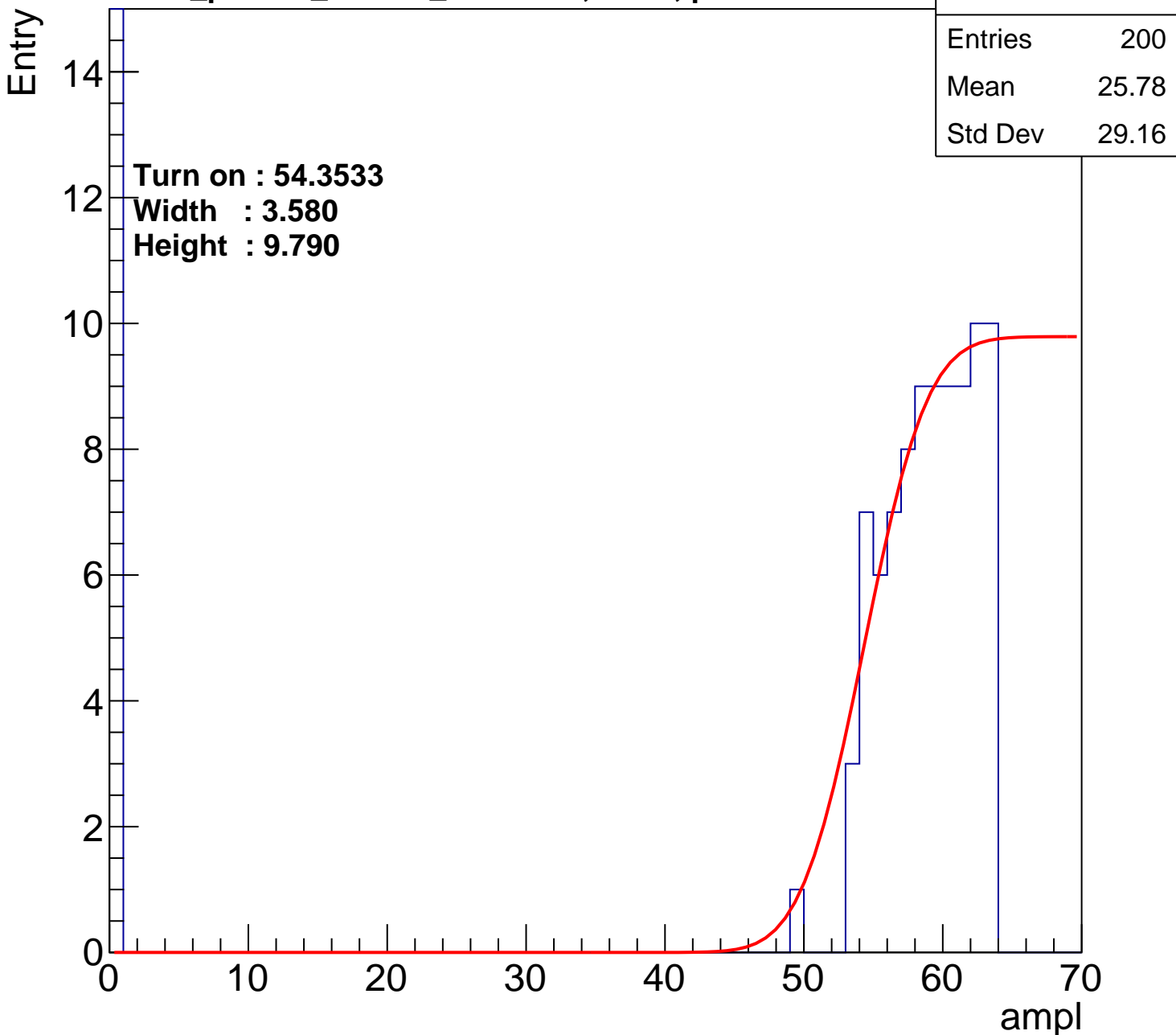
40

50

60

70

ampl



B1L104S, U8-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	31.28
Std Dev	28.41

Turn on : 54.4167

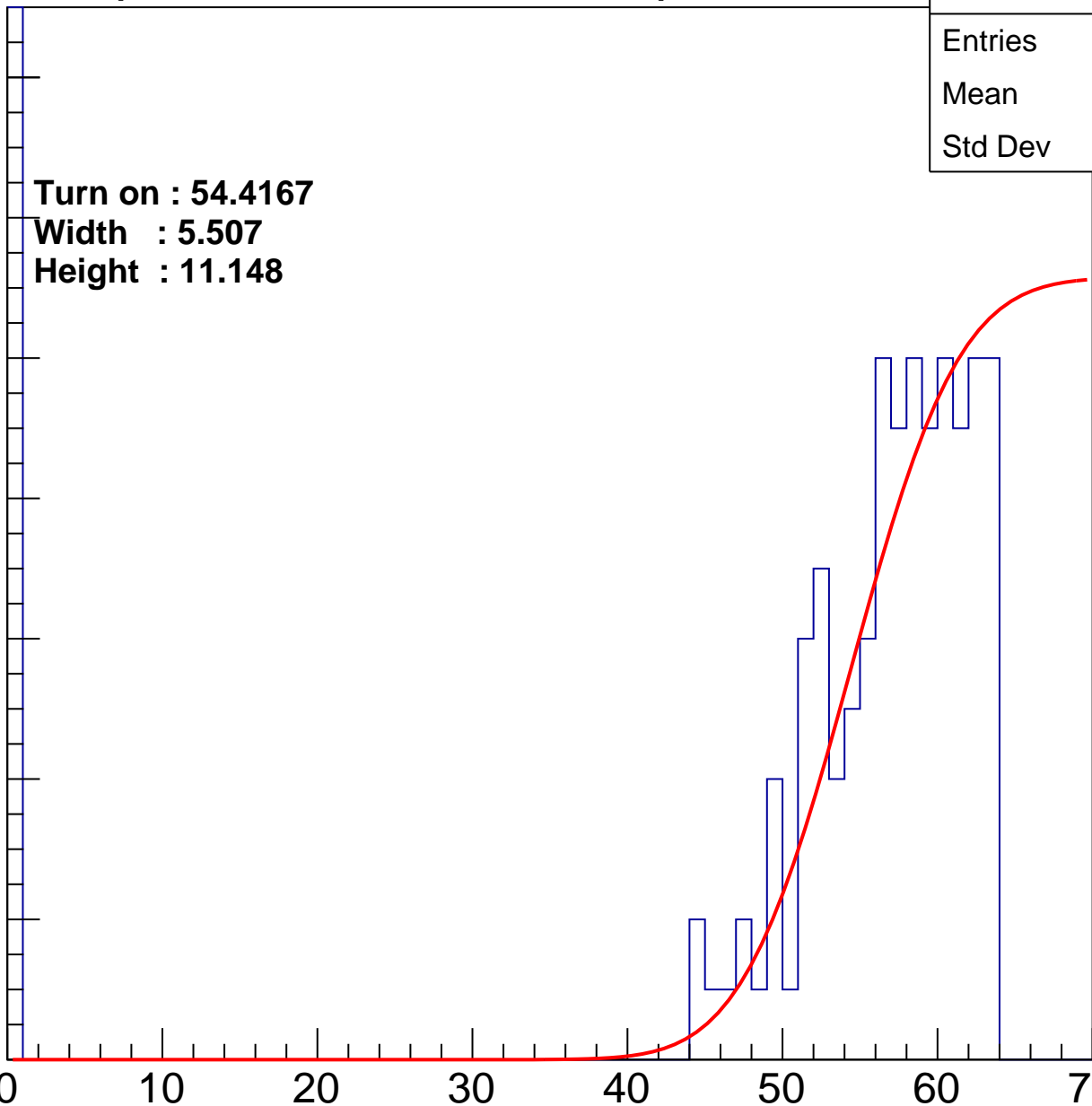
Width : 5.507

Height : 11.148

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	31.02
Std Dev	29.13

Turn on : 54.4016

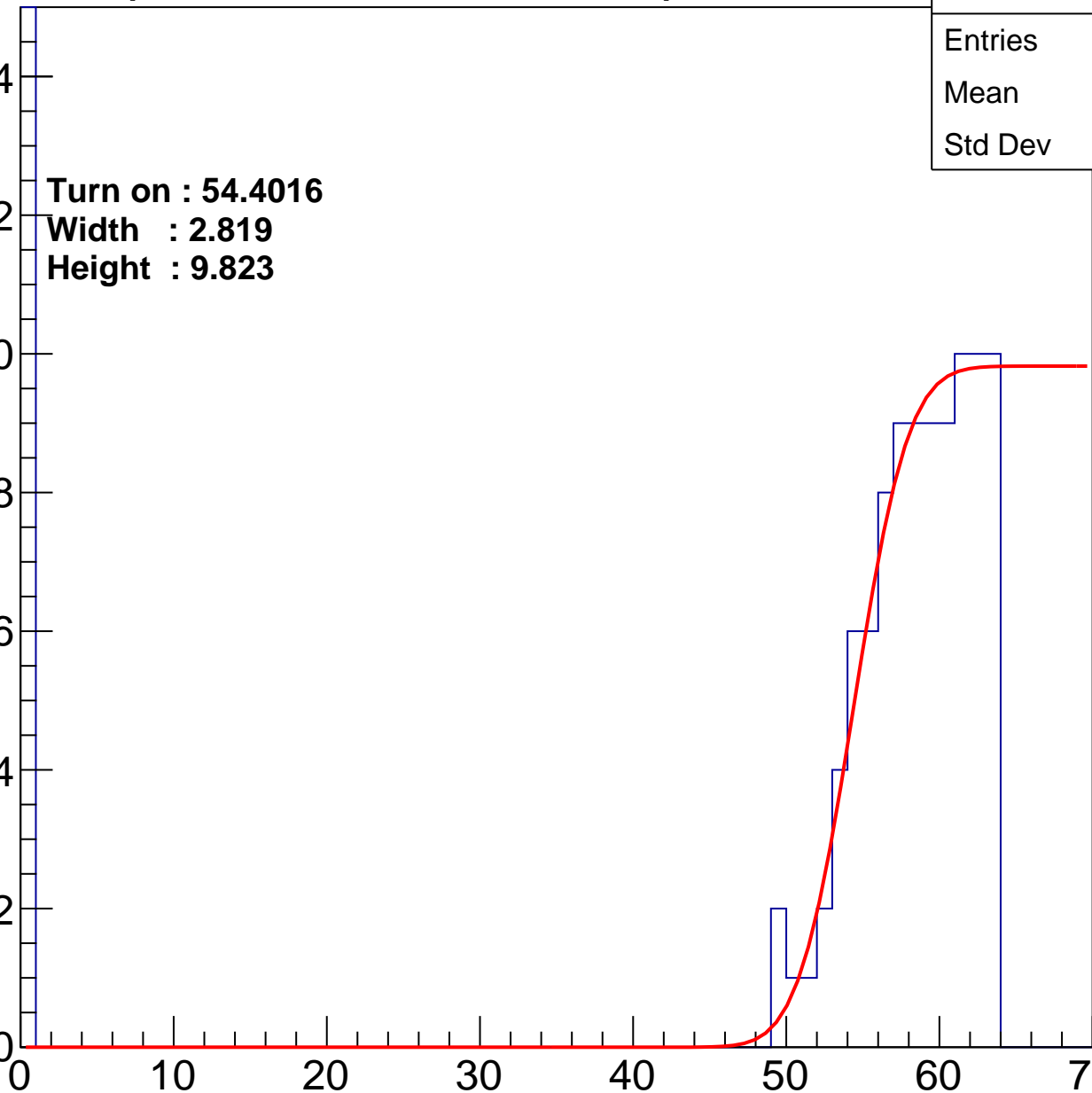
Width : 2.819

Height : 9.823

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch44

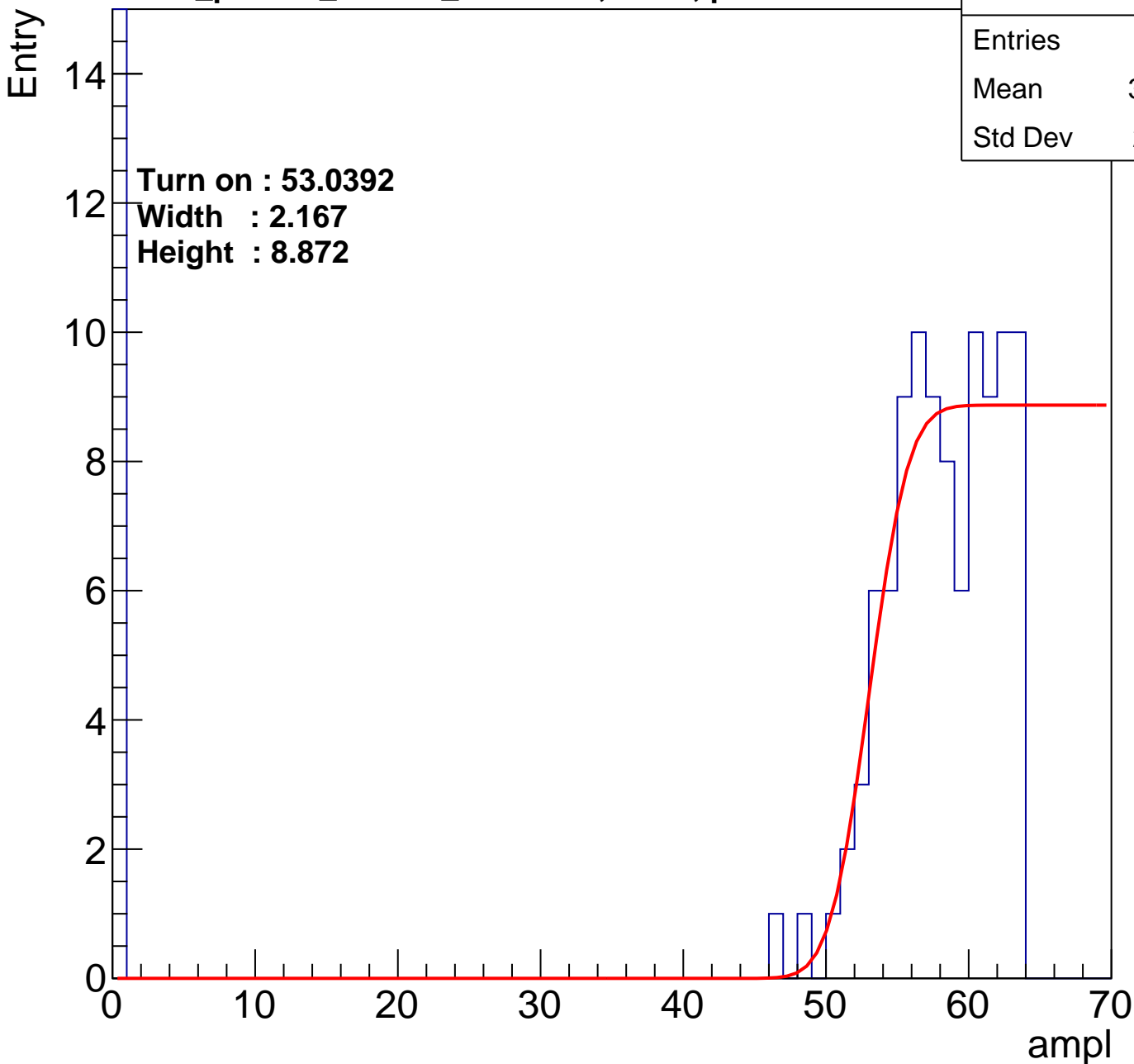
calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	32.93
Std Dev	28.71

Turn on : 53.0392

Width : 2.167

Height : 8.872



B1L104S, U8-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	30.19
Std Dev	28.83

Turn on : 52.8330

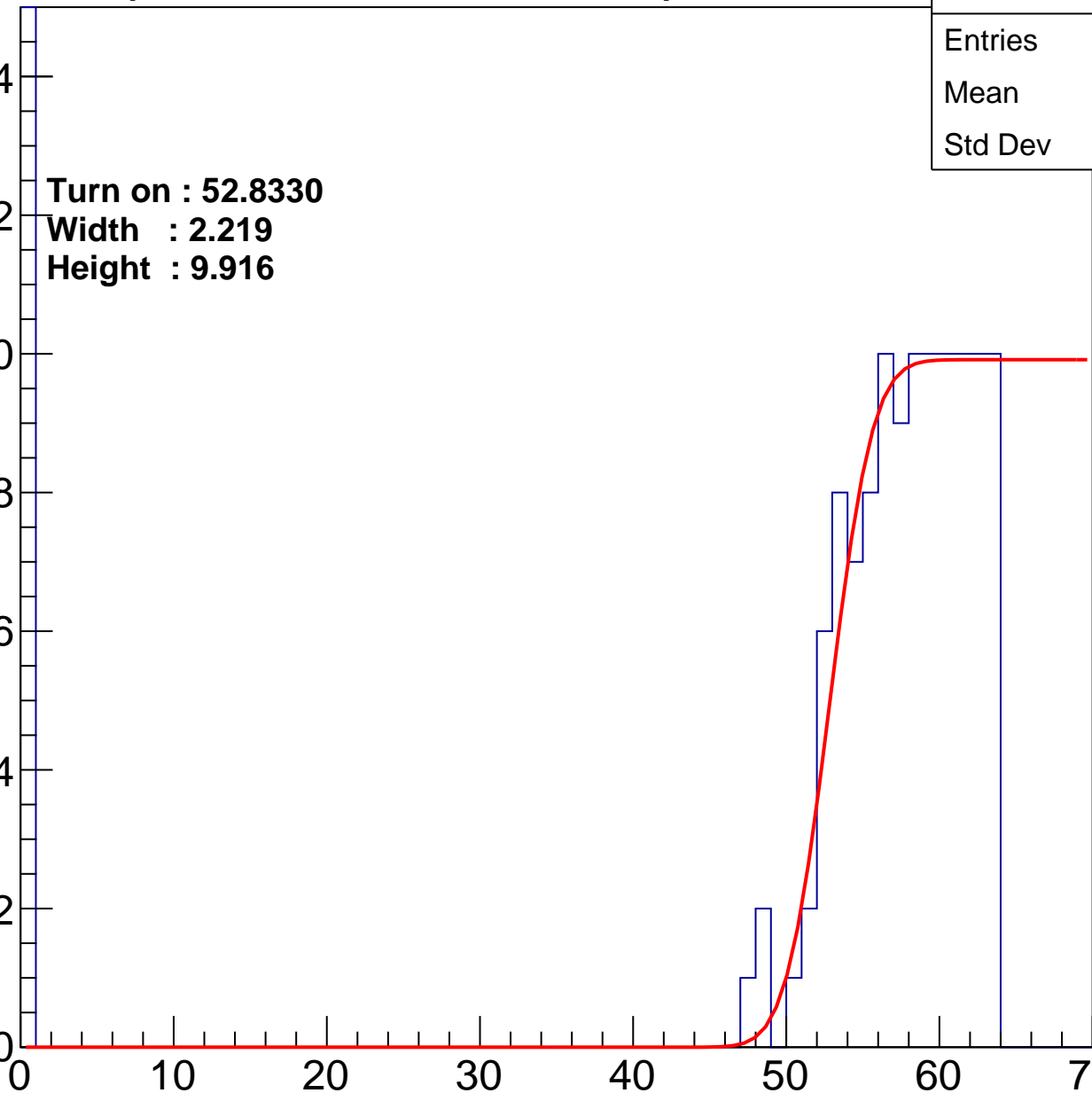
Width : 2.219

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	32.27
Std Dev	28.44

Turn on : 54.4125

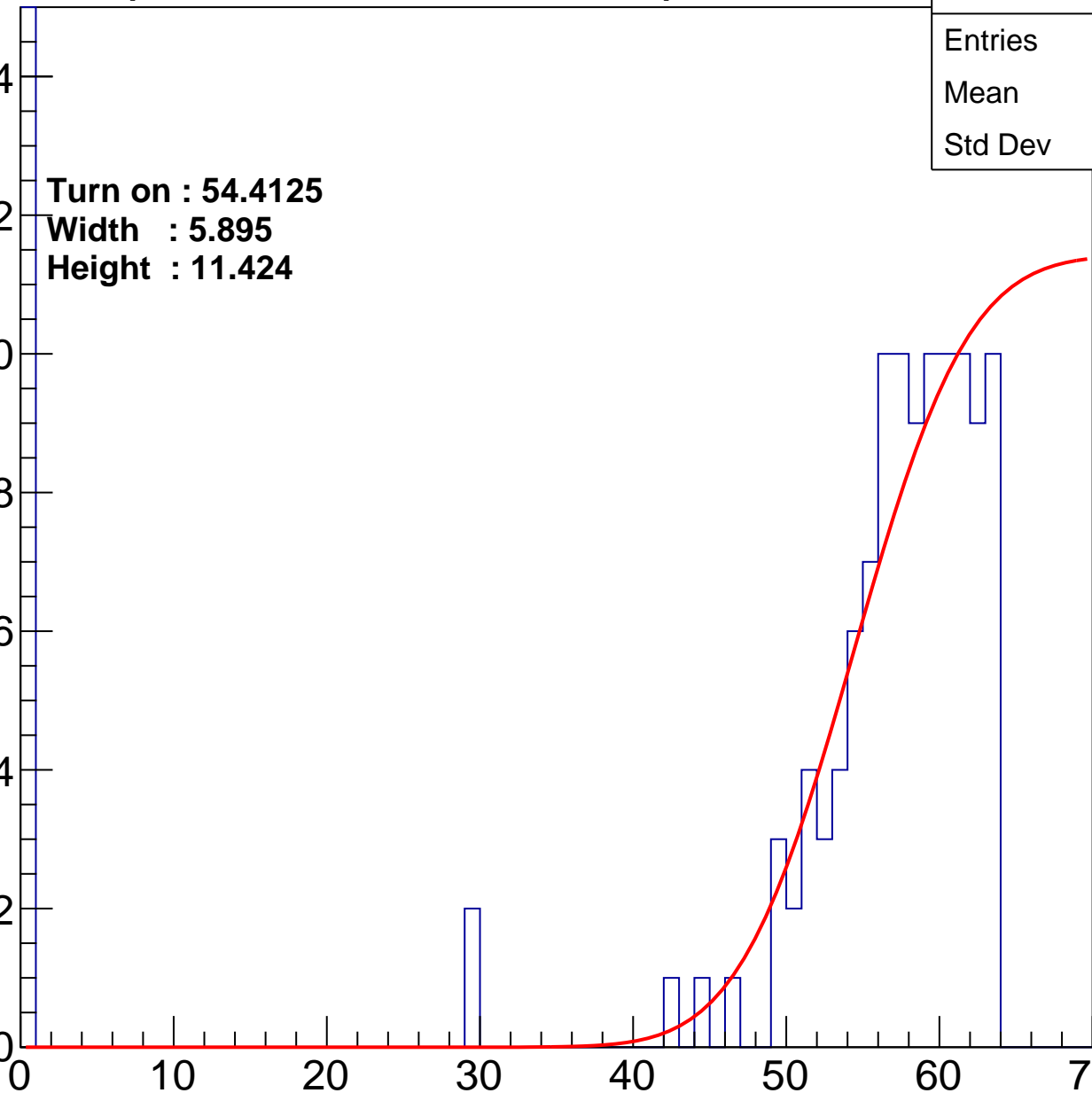
Width : 5.895

Height : 11.424

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch47

calib_packv5_033123_0516.root, FC#4, port A1

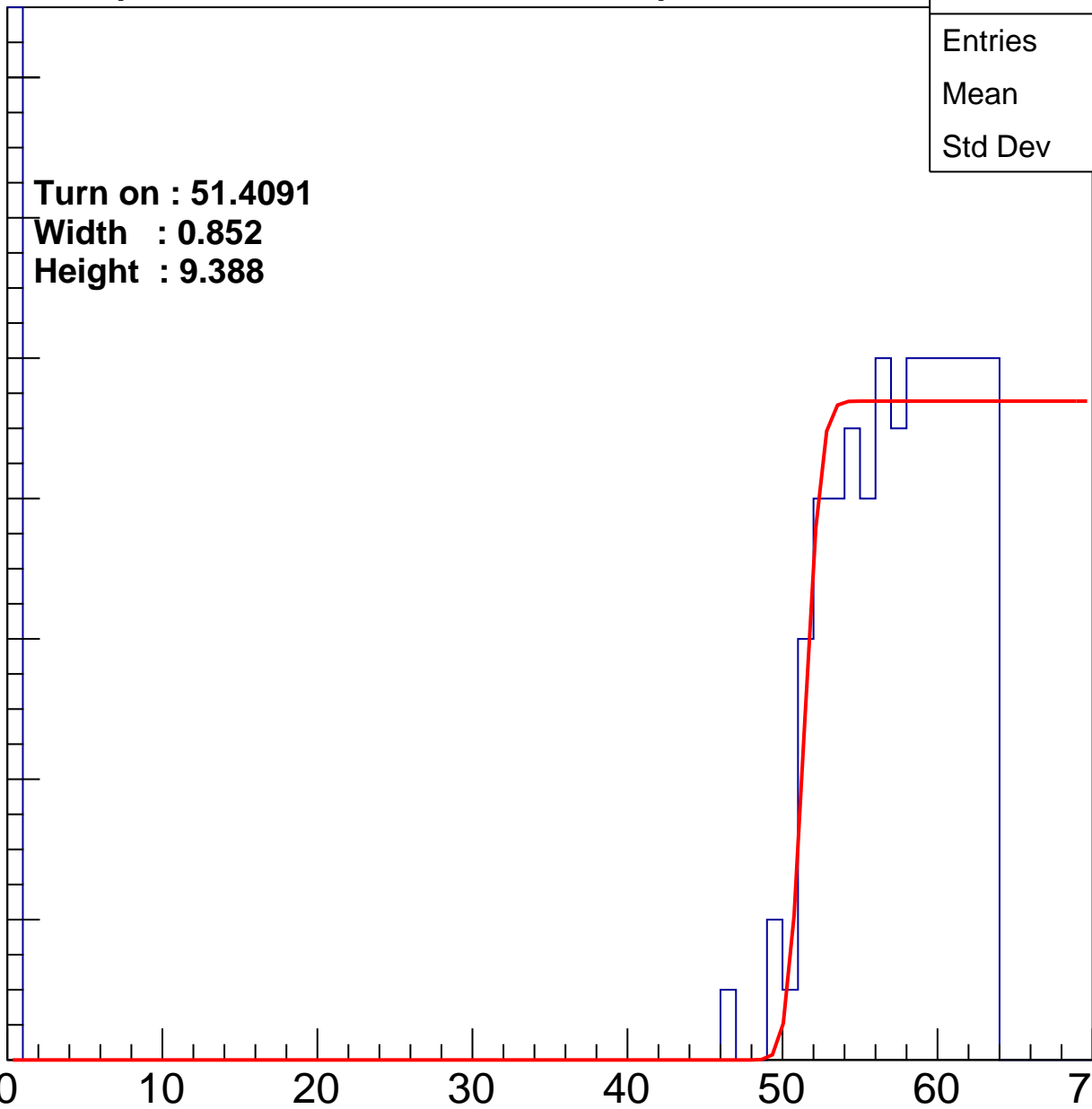
Entry

14
12
10
8
6
4
2
0

Turn on : 51.4091
Width : 0.852
Height : 9.388

Entries	209
Mean	33.34
Std Dev	28.32

ampl



B1L104S, U8-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	33.17
Std Dev	27.91

Turn on : 50.8437

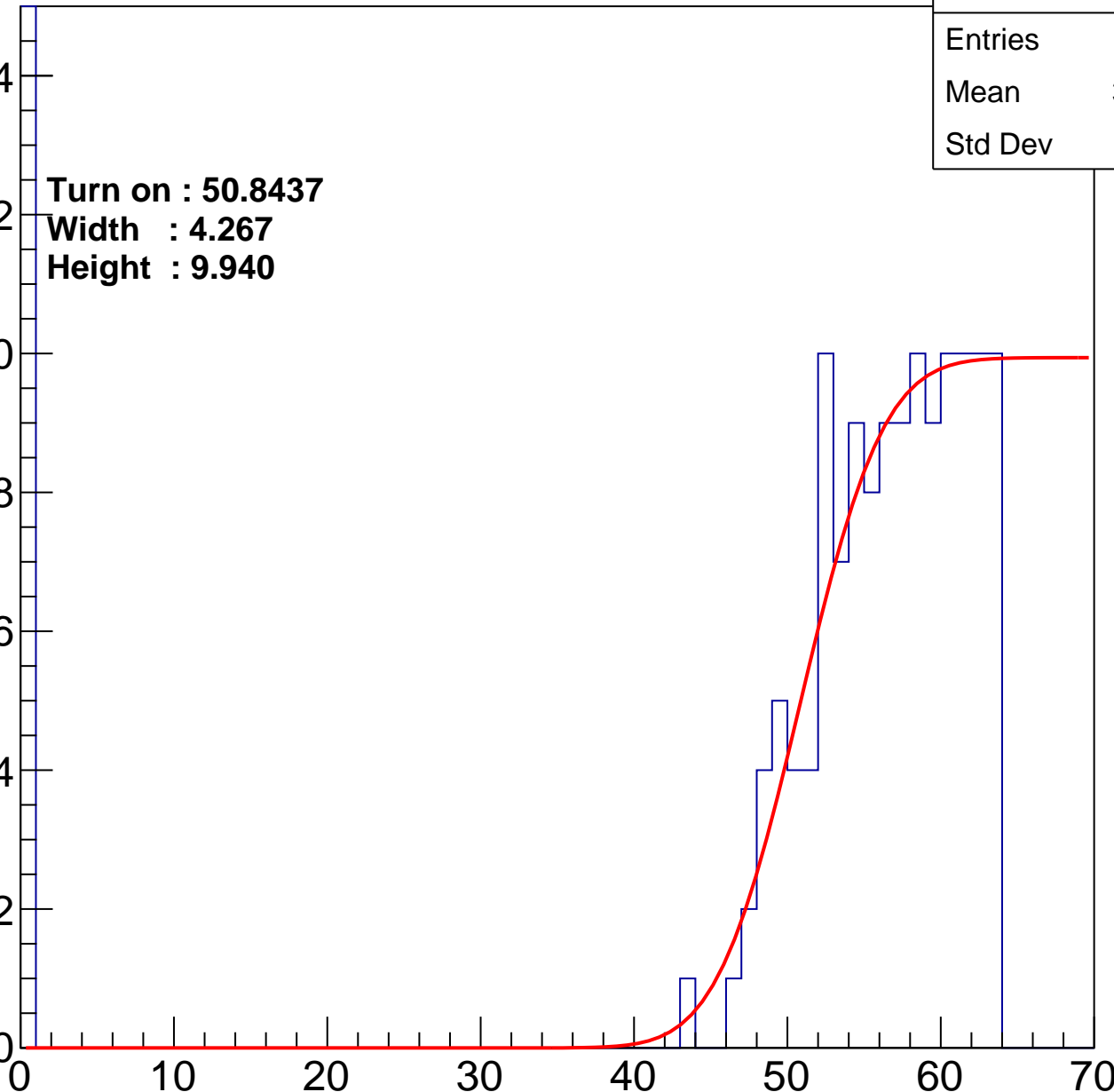
Width : 4.267

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch49

calib_packv5_033123_0516.root, FC#4, port A1

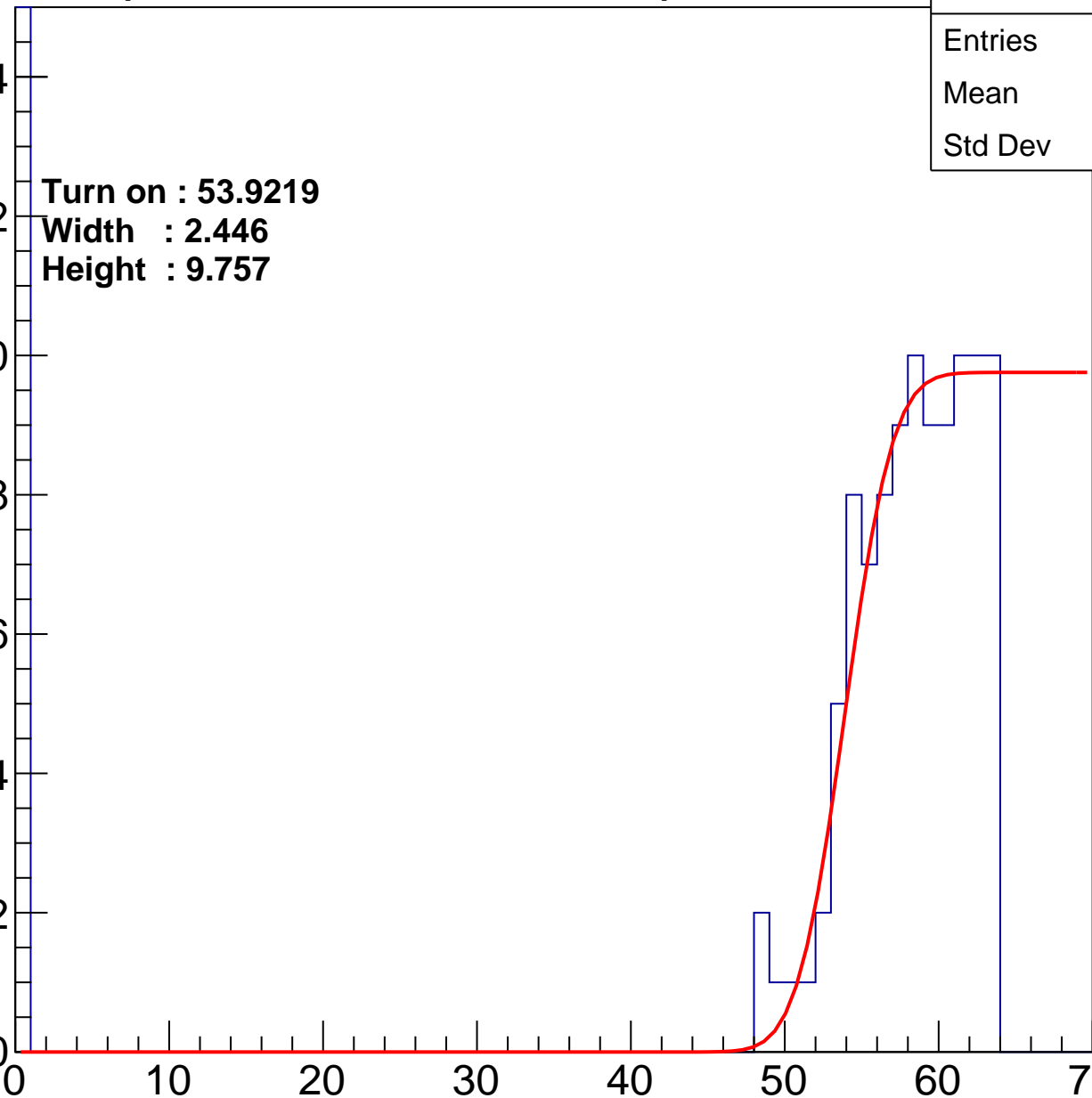
Entry

14
12
10
8
6
4
2
0

Turn on : 53.9219
Width : 2.446
Height : 9.757

Entries	203
Mean	29.08
Std Dev	29.06

ampl



B1L104S, U8-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	33.19
Std Dev	28.06

Turn on : 51.4111

Width : 4.949

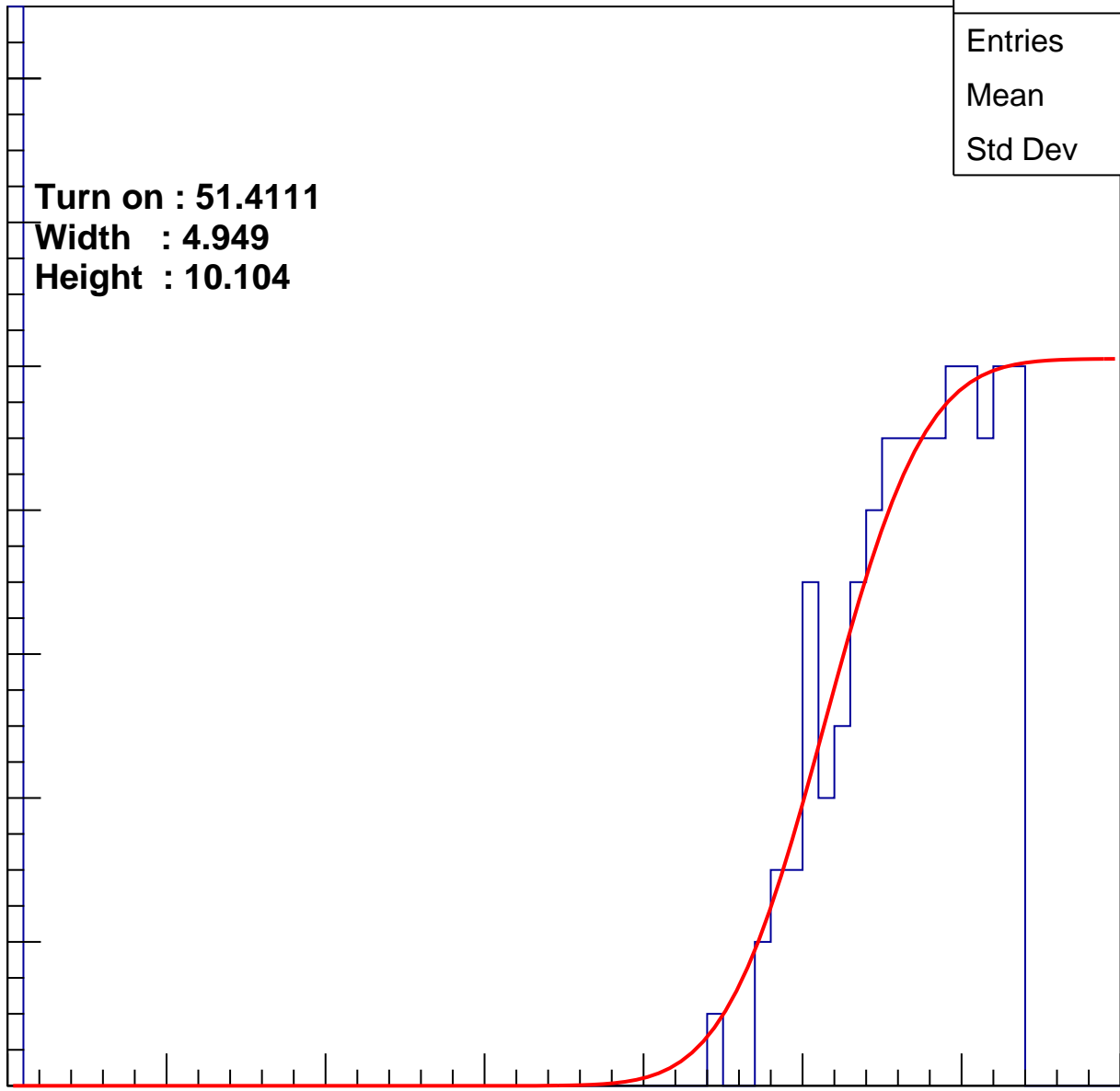
Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U8-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.81
Std Dev	28.82

Turn on : 54.0098

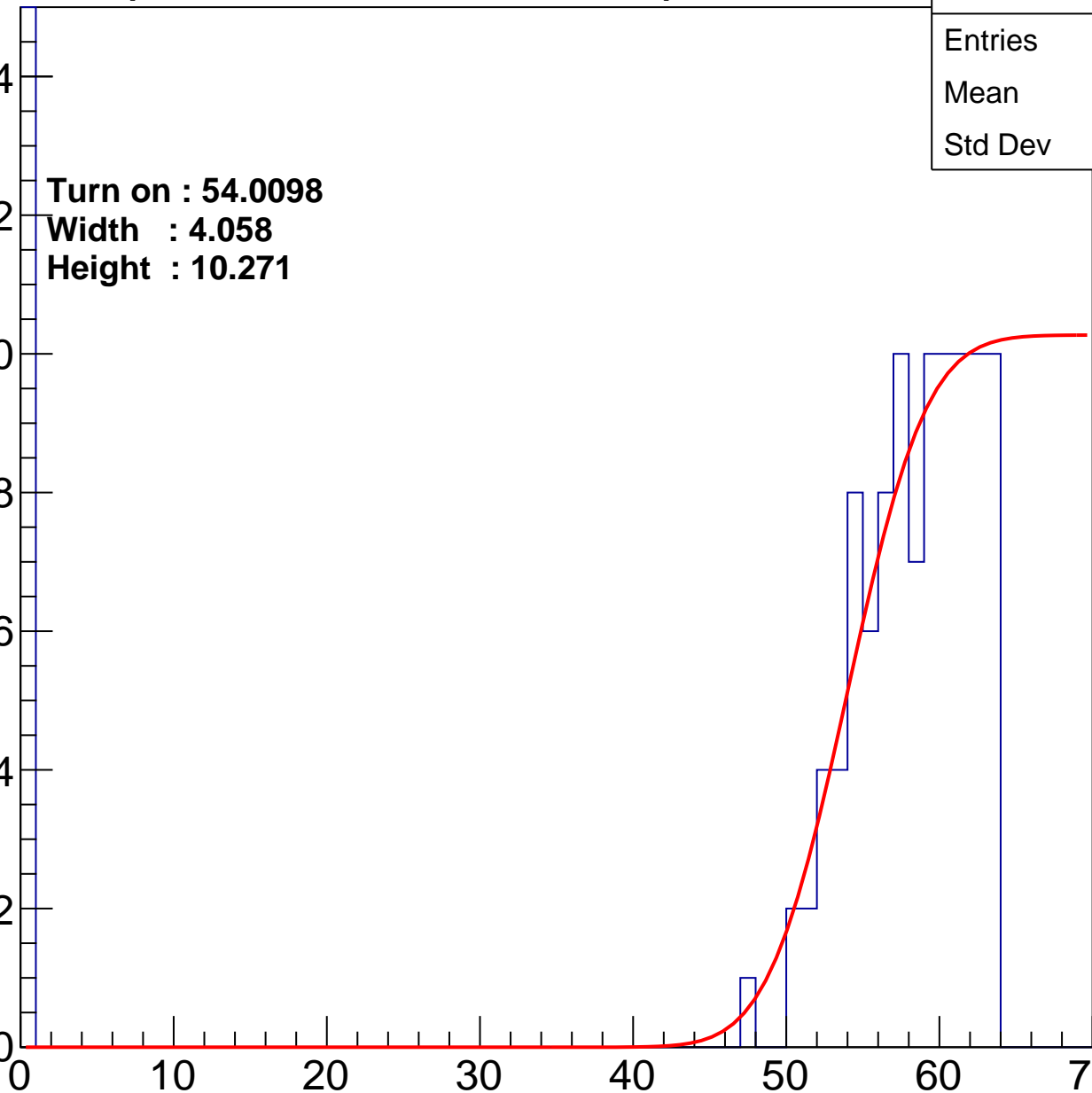
Width : 4.058

Height : 10.271

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	29.83
Std Dev	28.69

Turn on : 53.2723

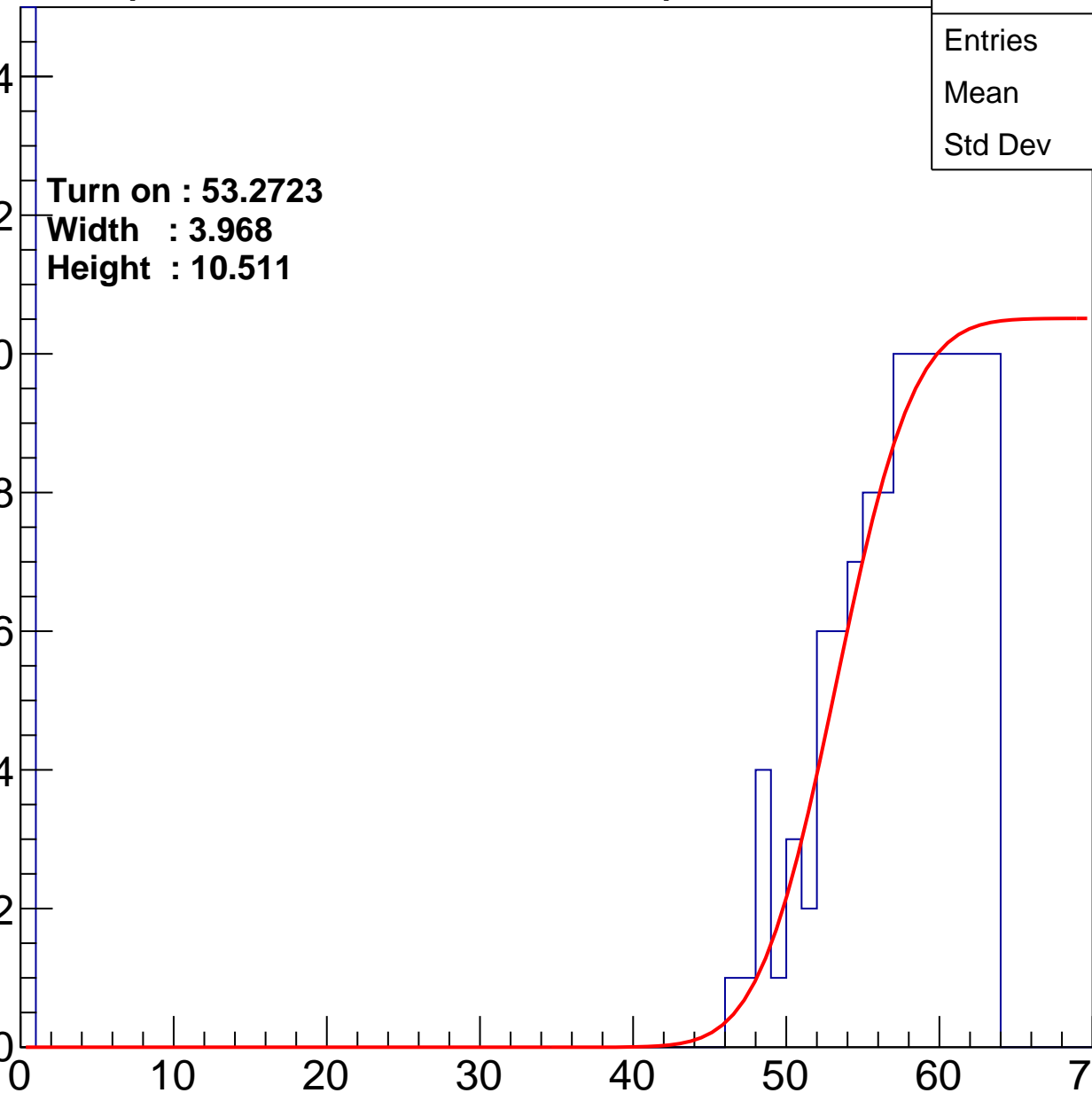
Width : 3.968

Height : 10.511

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	34.39
Std Dev	28.49

Turn on : 53.7855

Width : 3.966

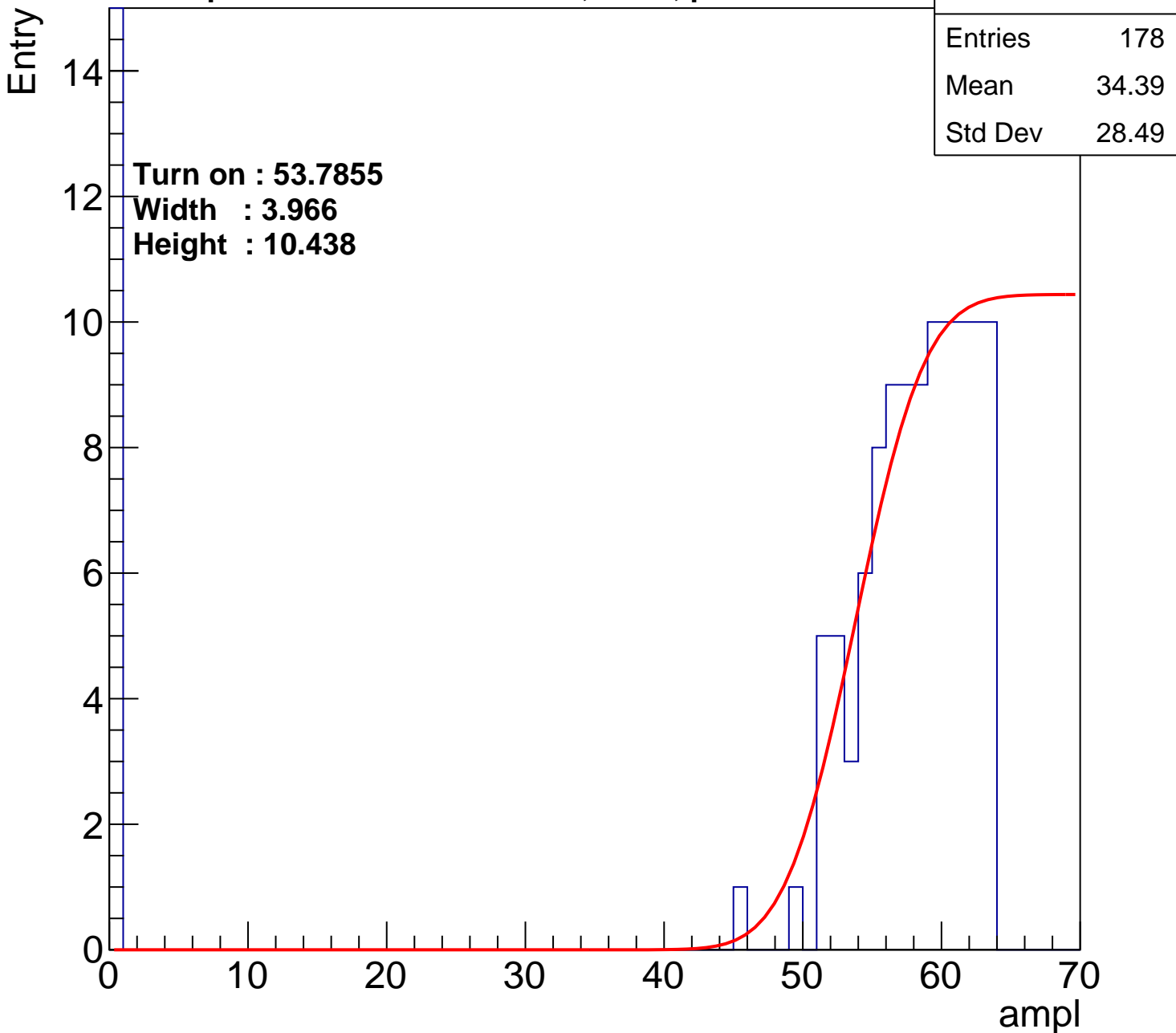
Height : 10.438

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U8-ch54

calib_packv5_033123_0516.root, FC#4, port A1

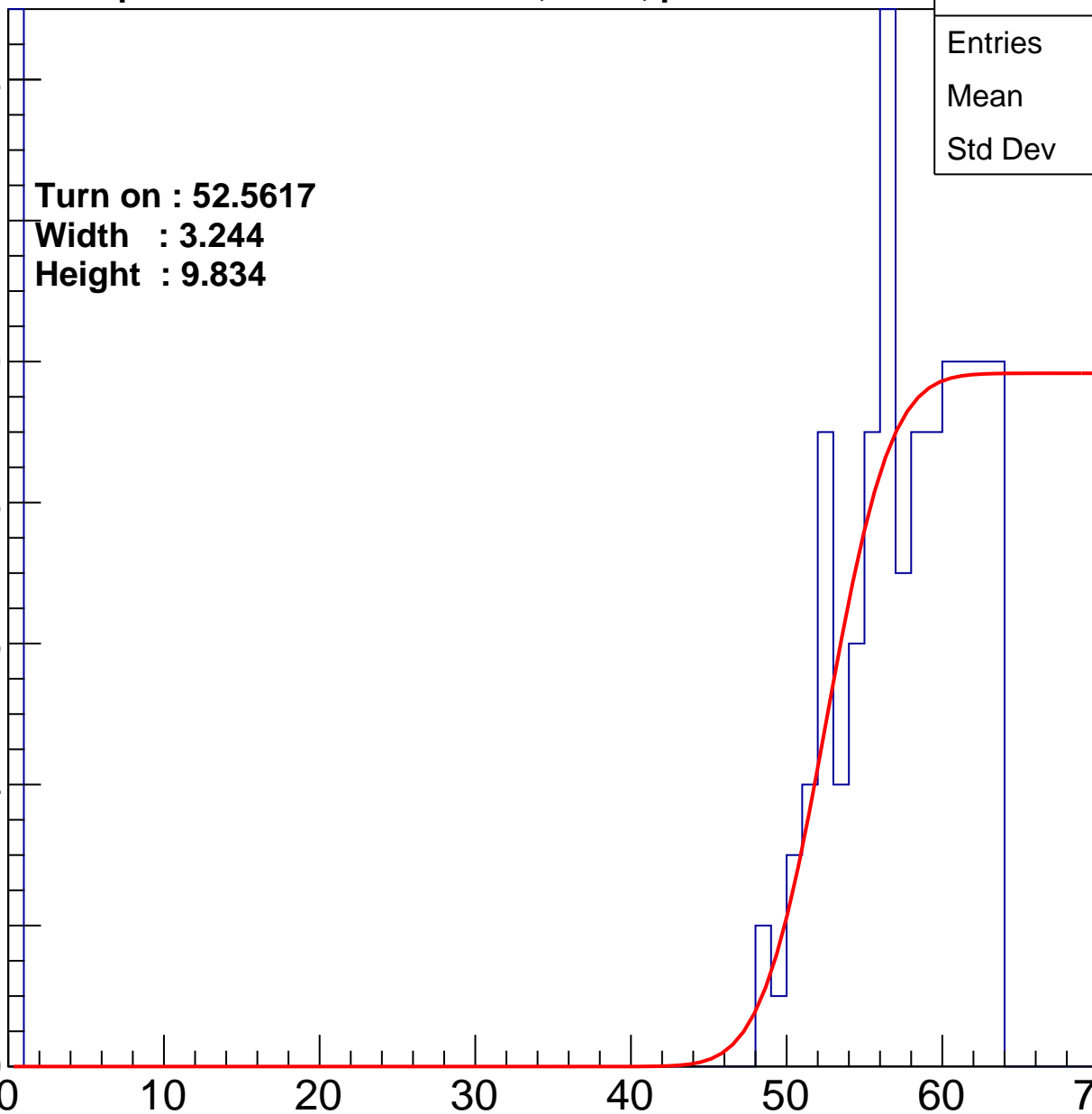
Entry

14
12
10
8
6
4
2
0

Turn on : 52.5617
Width : 3.244
Height : 9.834

Entries	202
Mean	34.52
Std Dev	28.12

ampl



B1L104S, U8-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	32.78
Std Dev	28.93

Turn on : 54.0759

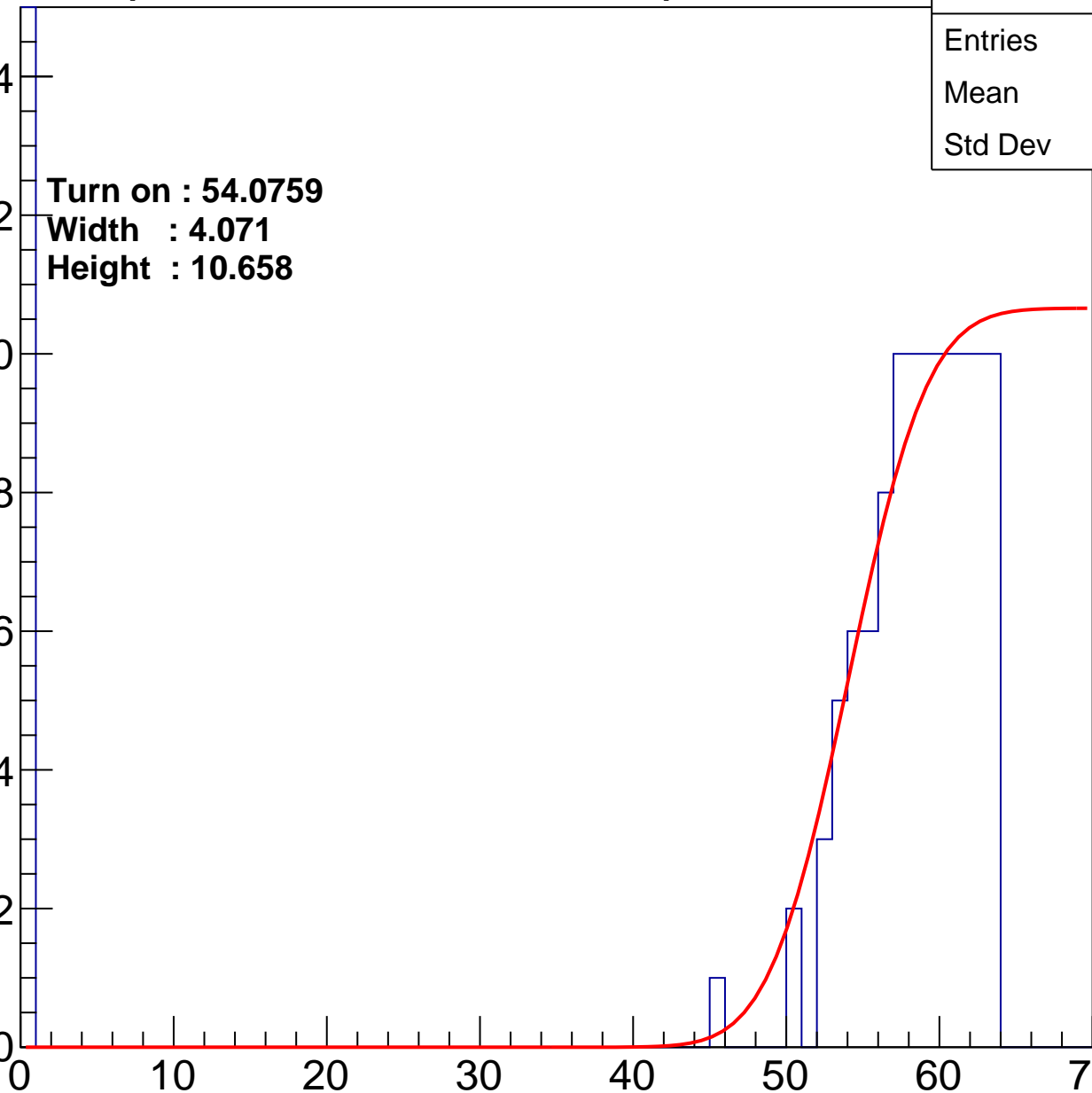
Width : 4.071

Height : 10.658

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	226
Mean	29.34
Std Dev	28.72

Turn on : 52.3921

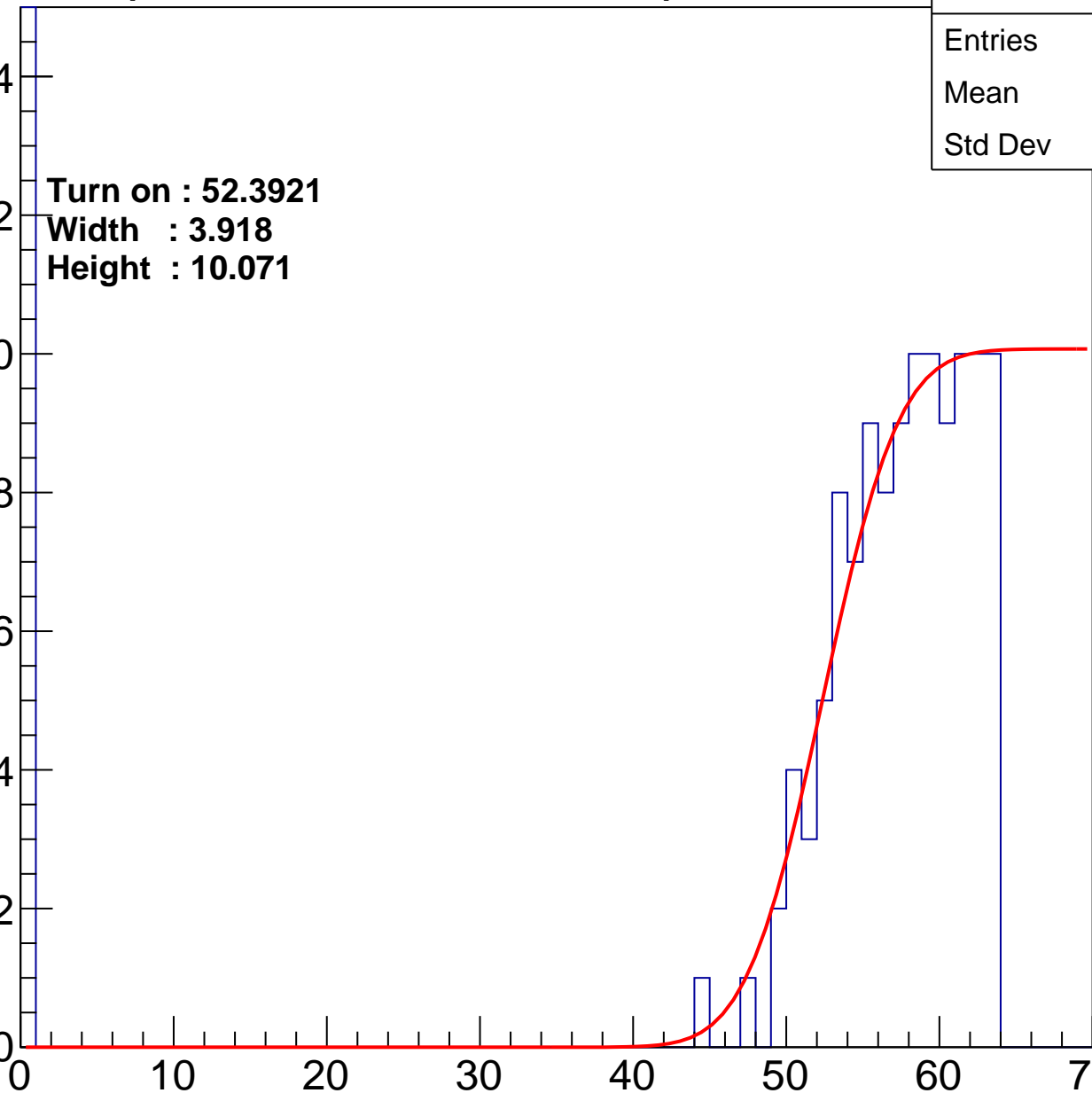
Width : 3.918

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch57

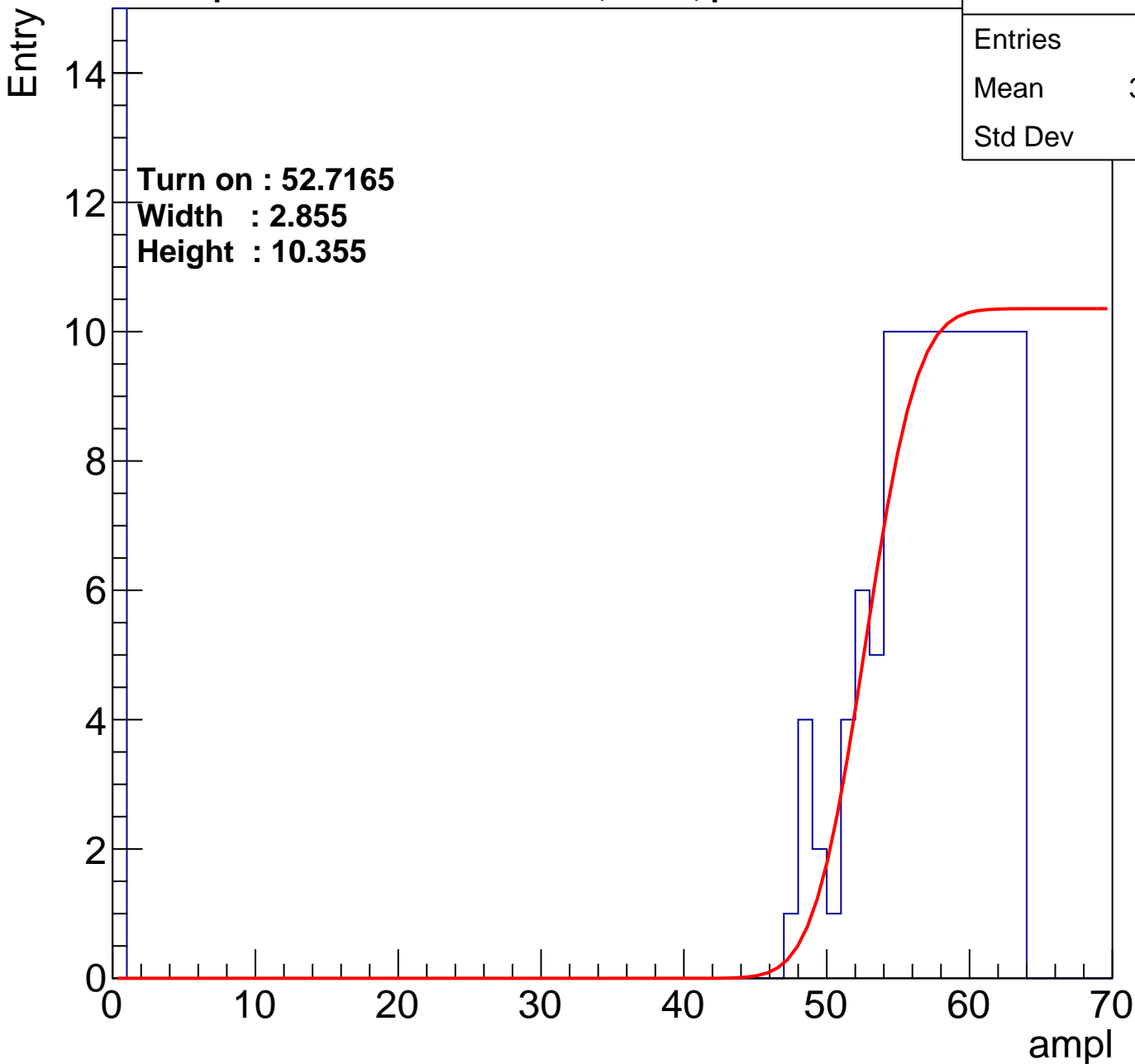
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	35.27
Std Dev	27.9

Turn on : 52.7165

Width : 2.855

Height : 10.355



B1L104S, U8-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	244
Mean	27.82
Std Dev	28.66

Turn on : 52.6867

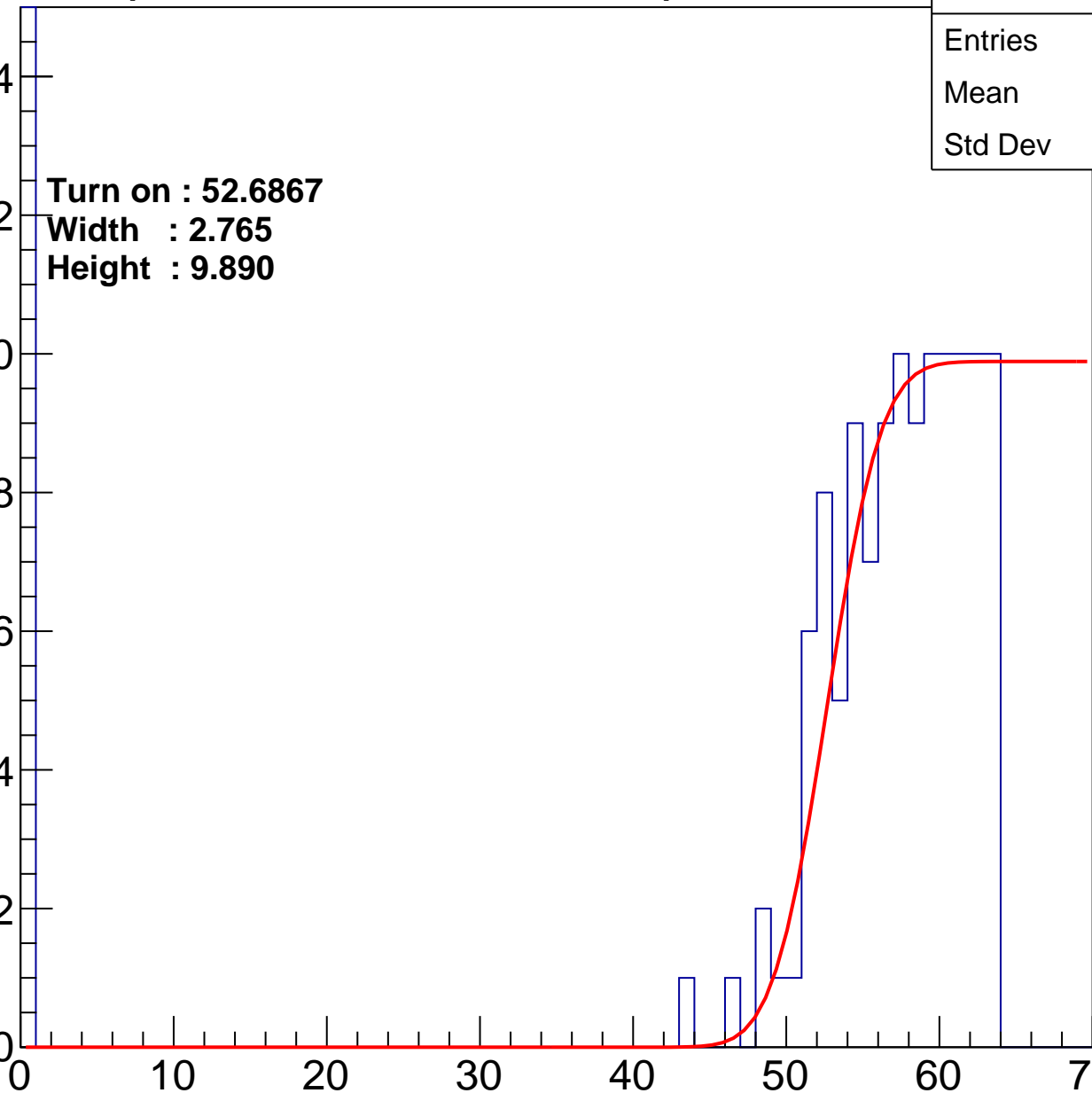
Width : 2.765

Height : 9.890

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	34.51
Std Dev	28.58

Turn on : 54.2589

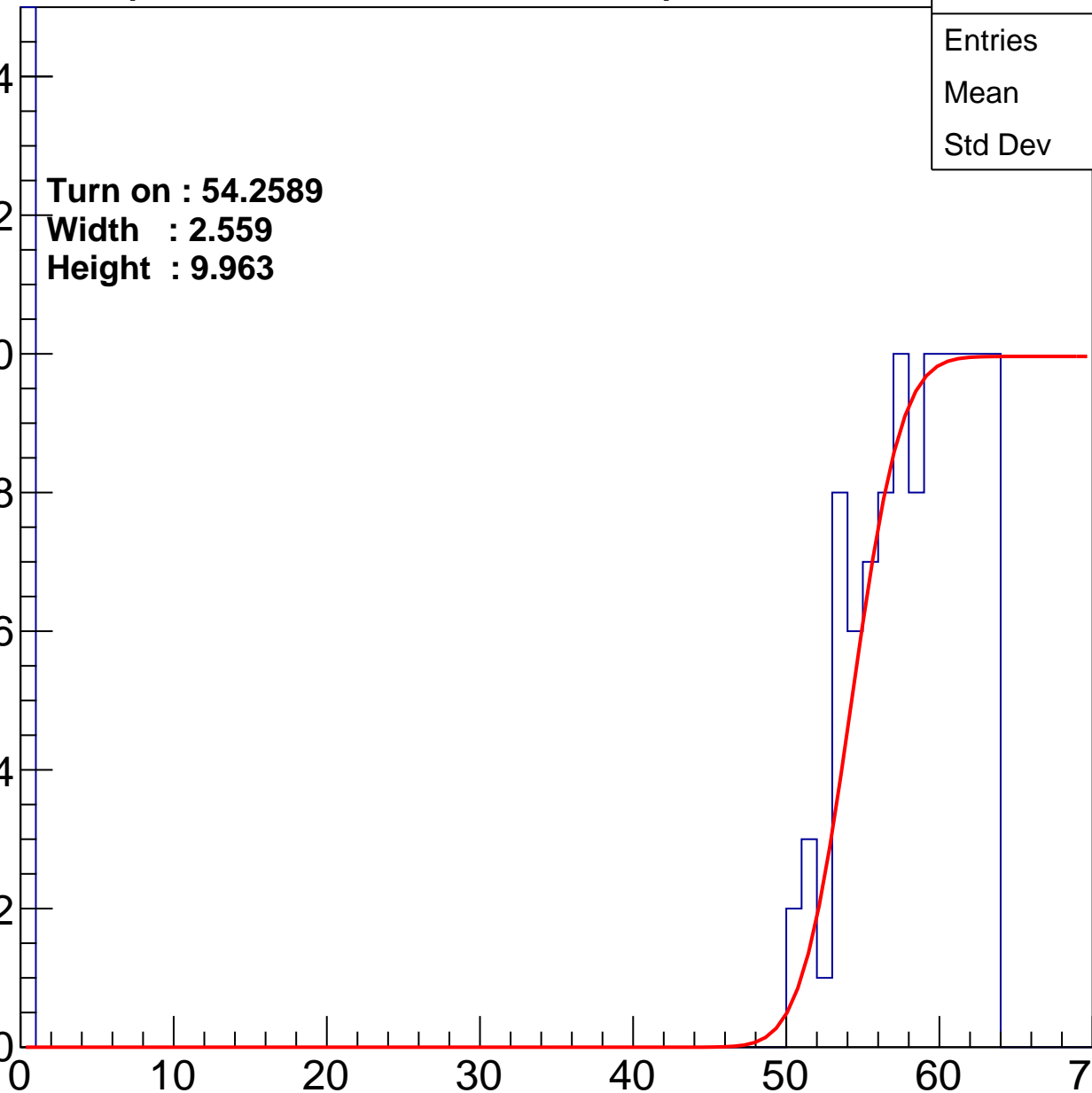
Width : 2.559

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	35.89
Std Dev	27.85

Turn on : 53.7920

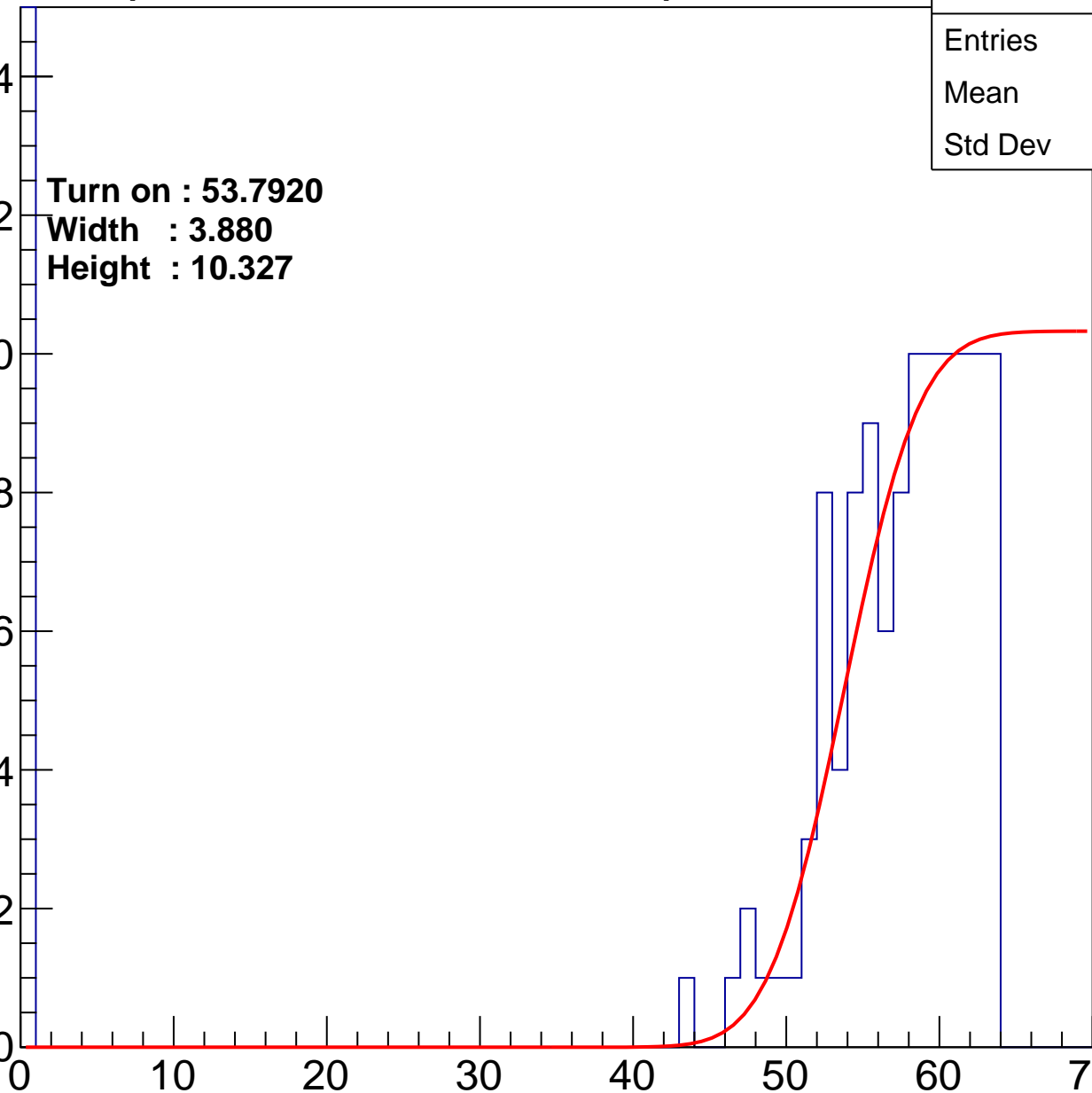
Width : 3.880

Height : 10.327

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	36.56
Std Dev	28.14

Turn on : 53.5722

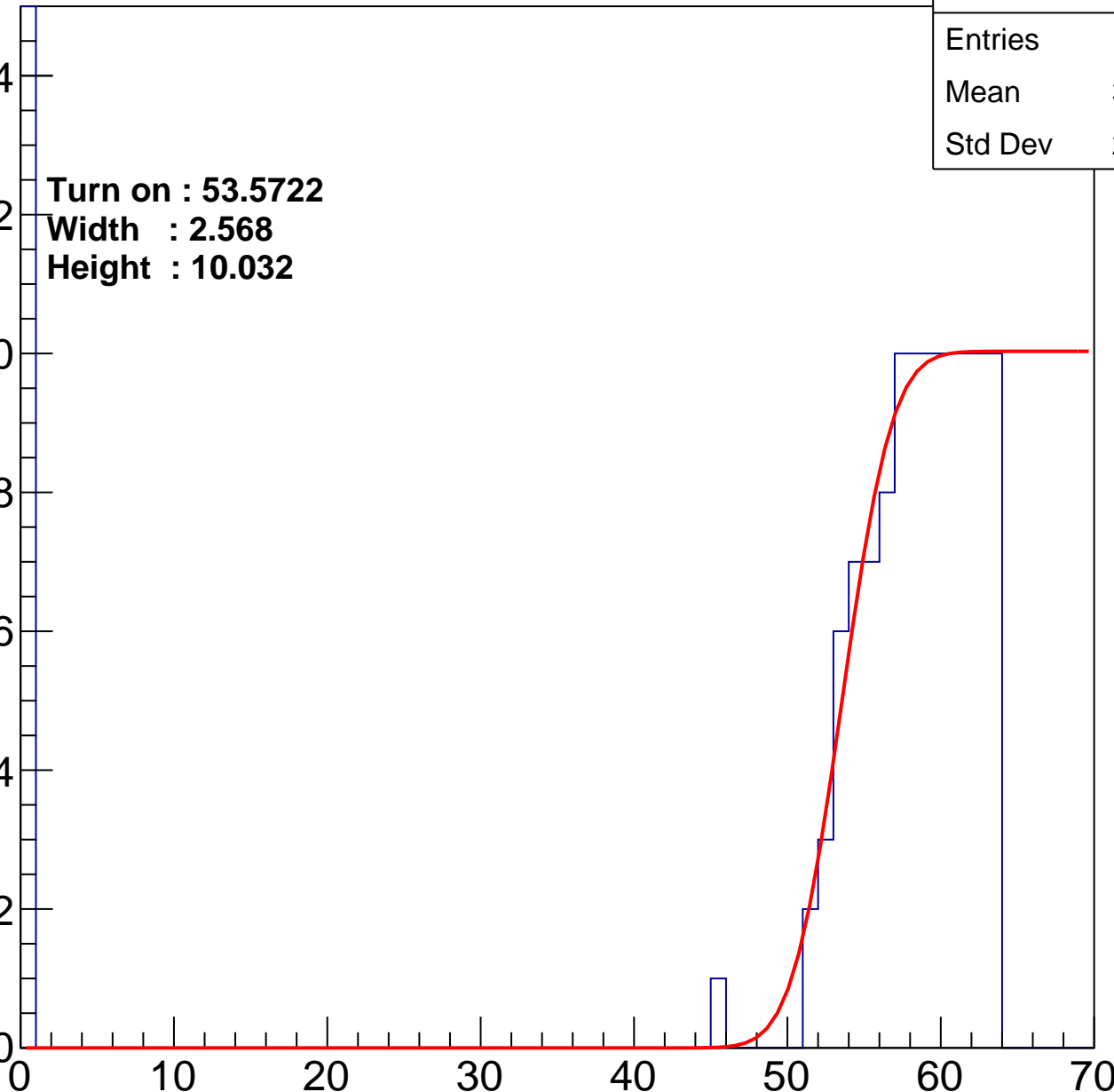
Width : 2.568

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	240
Mean	31.77
Std Dev	28.02

Turn on : 51.1528

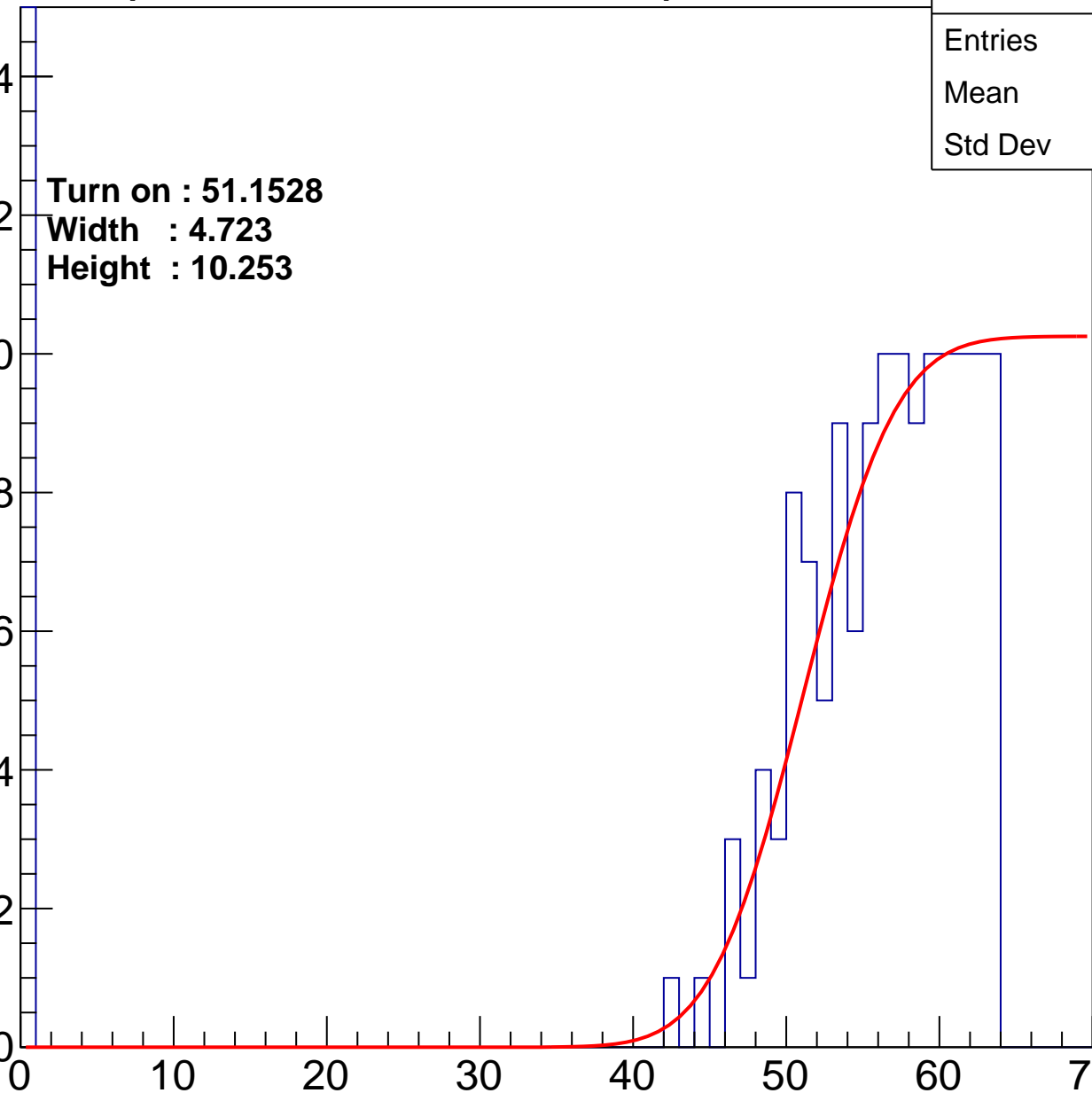
Width : 4.723

Height : 10.253

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.83
Std Dev	28.69

Turn on : 52.6216

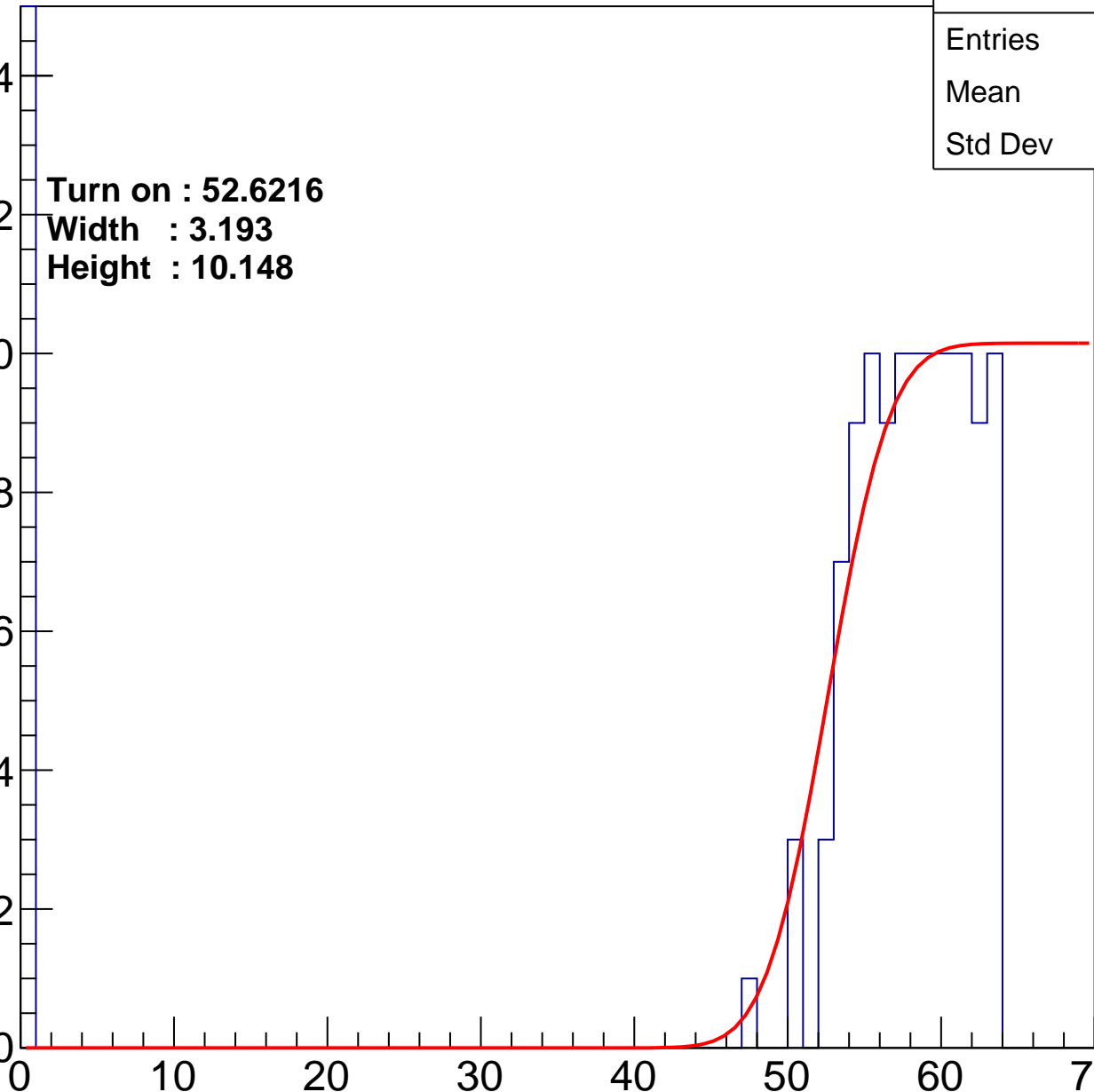
Width : 3.193

Height : 10.148

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	33.14
Std Dev	27.9

Turn on : 51.5918

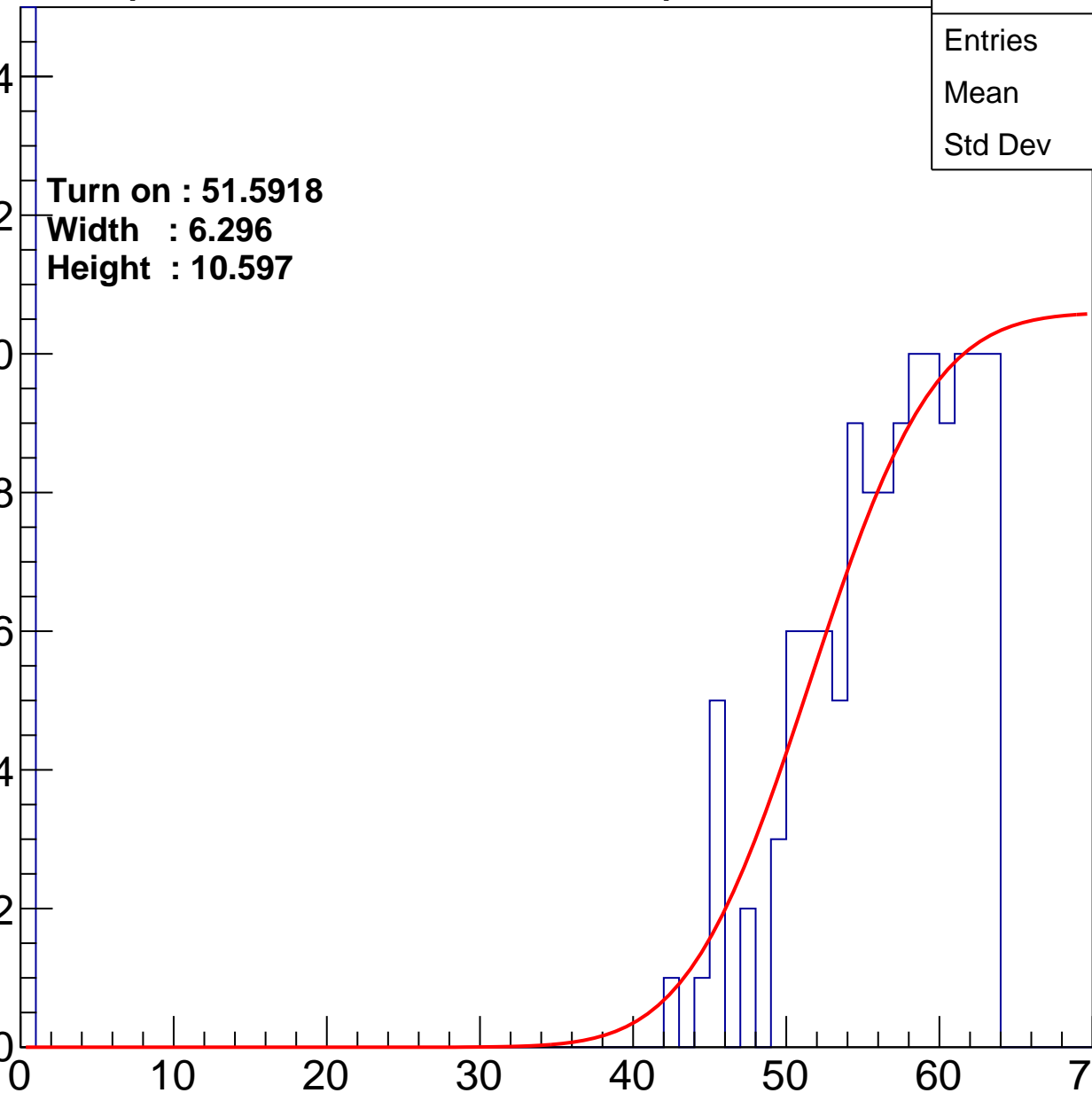
Width : 6.296

Height : 10.597

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch65

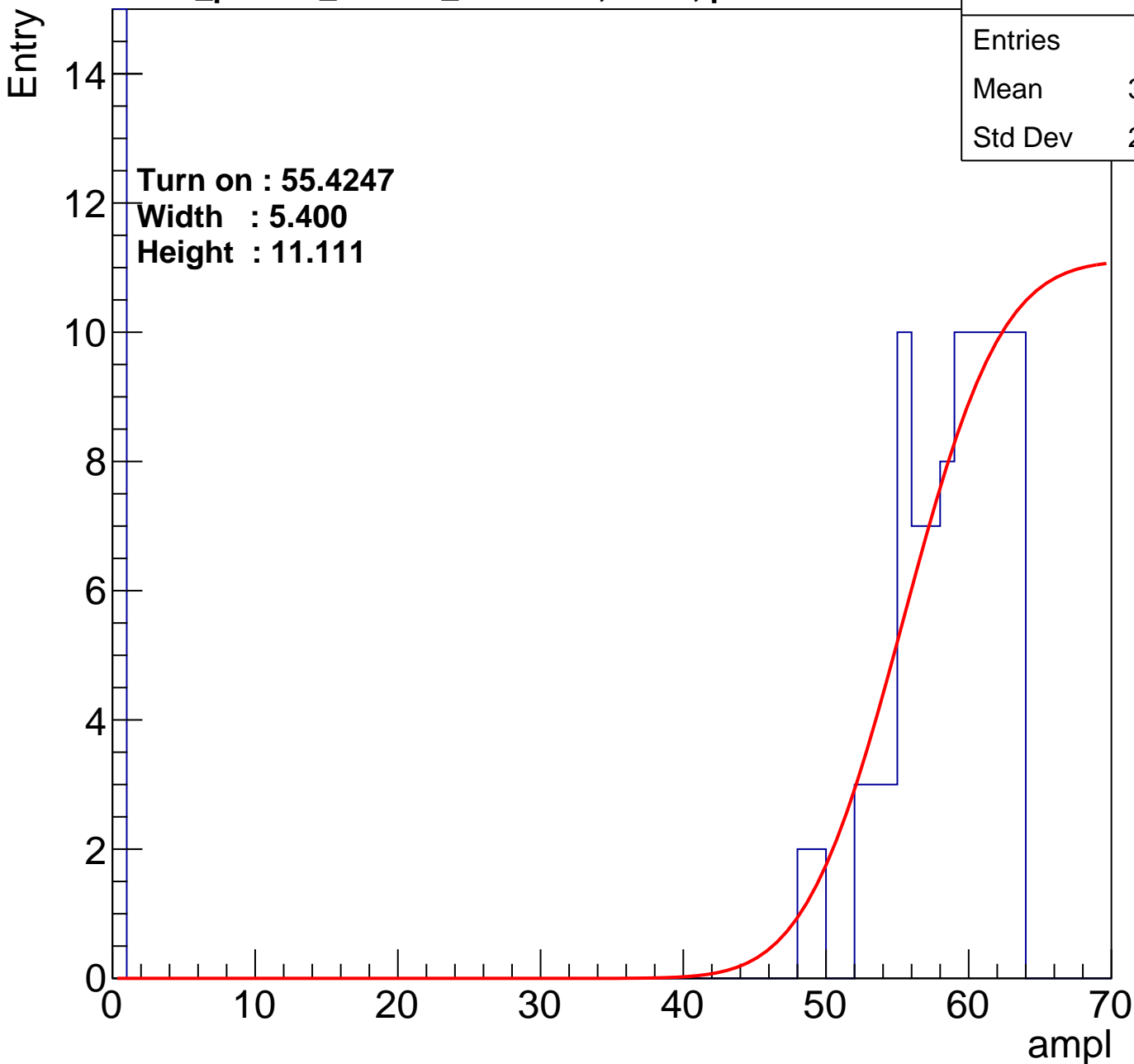
calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	31.94
Std Dev	29.07

Turn on : 55.4247

Width : 5.400

Height : 11.111



B1L104S, U8-ch66

calib_packv5_033123_0516.root, FC#4, port A1

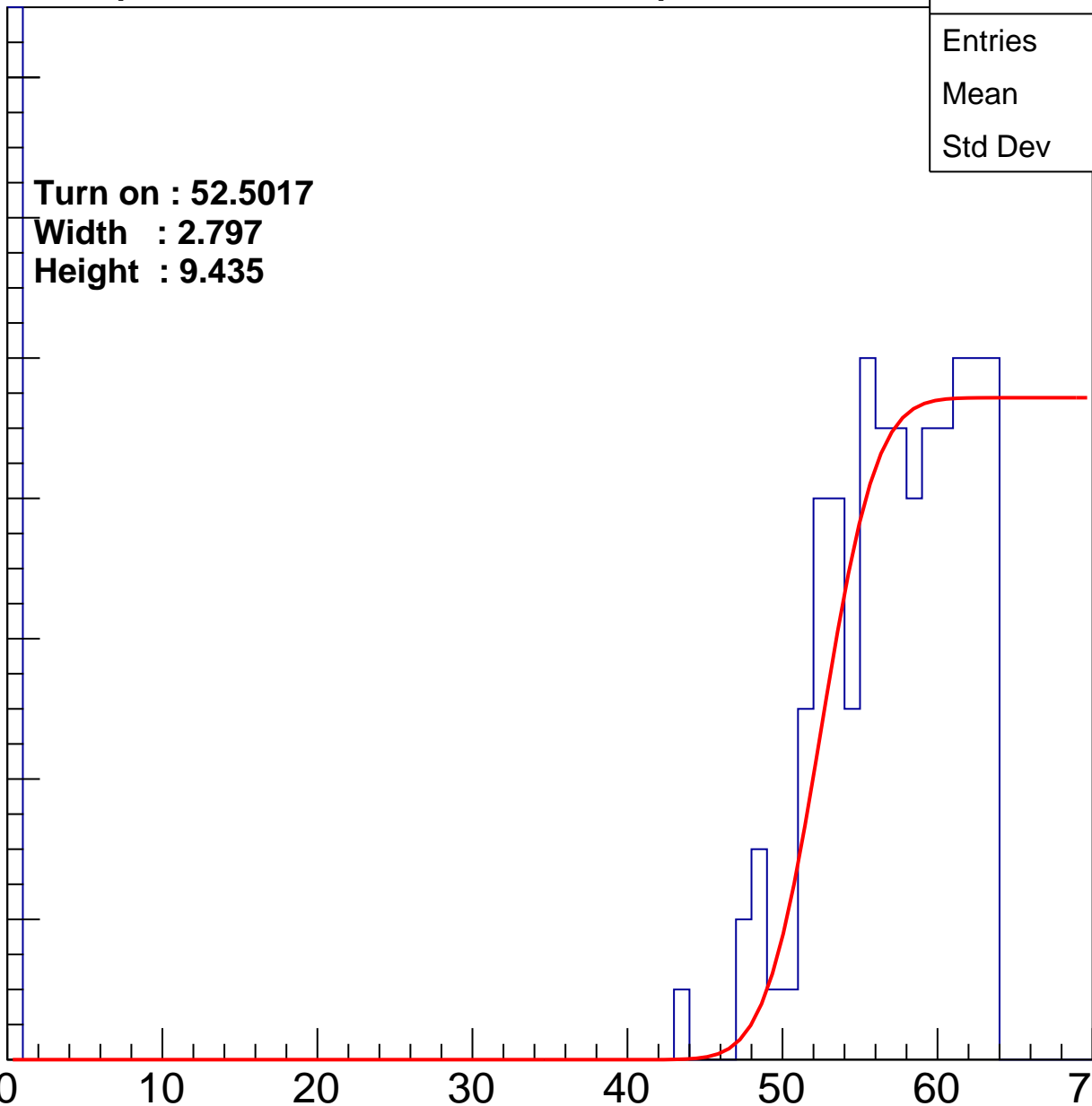
Entry

14
12
10
8
6
4
2
0

Turn on : 52.5017
Width : 2.797
Height : 9.435

Entries	227
Mean	29.55
Std Dev	28.57

ampl



B1L104S, U8-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	30.44
Std Dev	29.14

Turn on : 54.6034

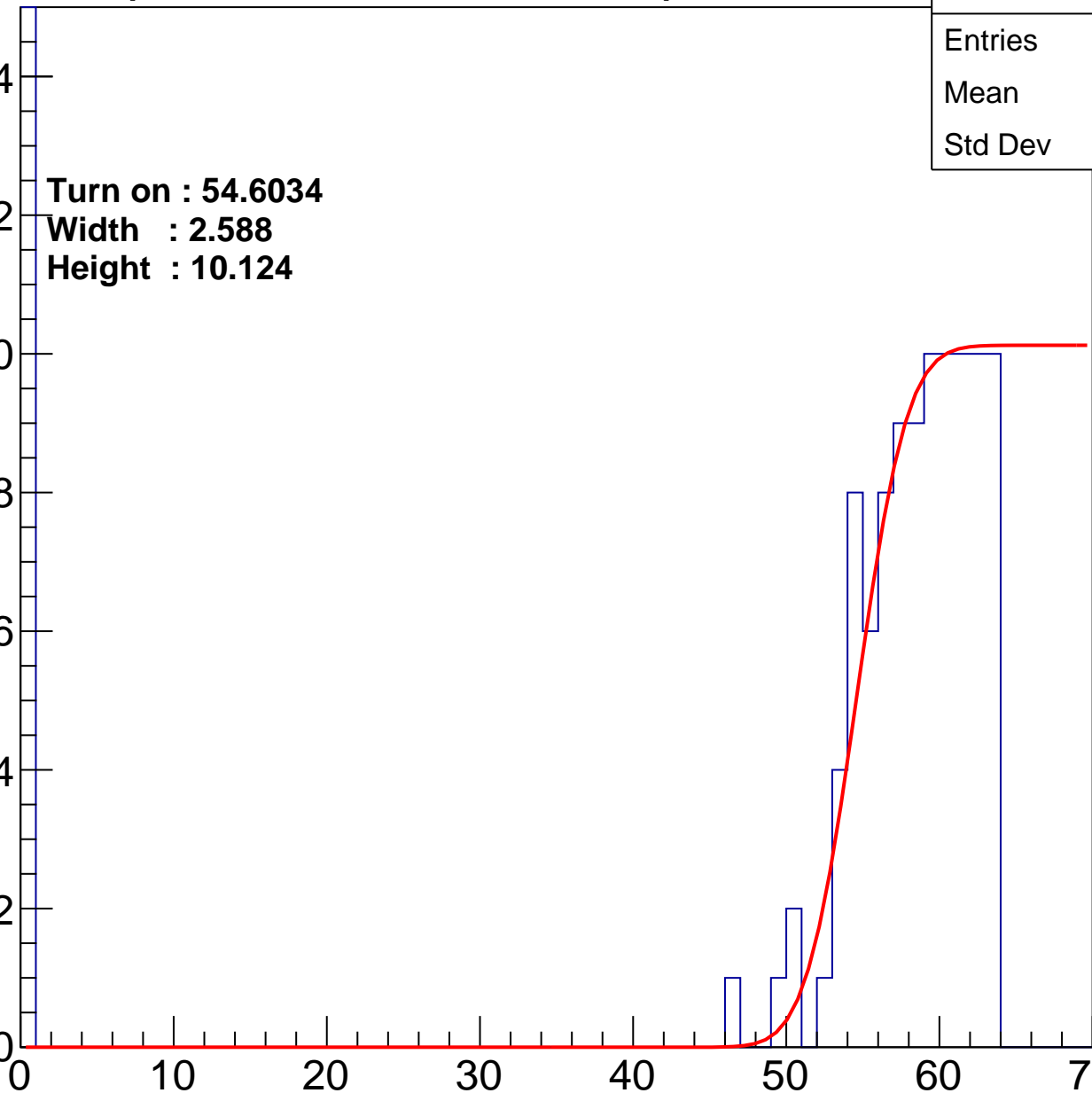
Width : 2.588

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	30.24
Std Dev	28.83

Turn on : 52.7976

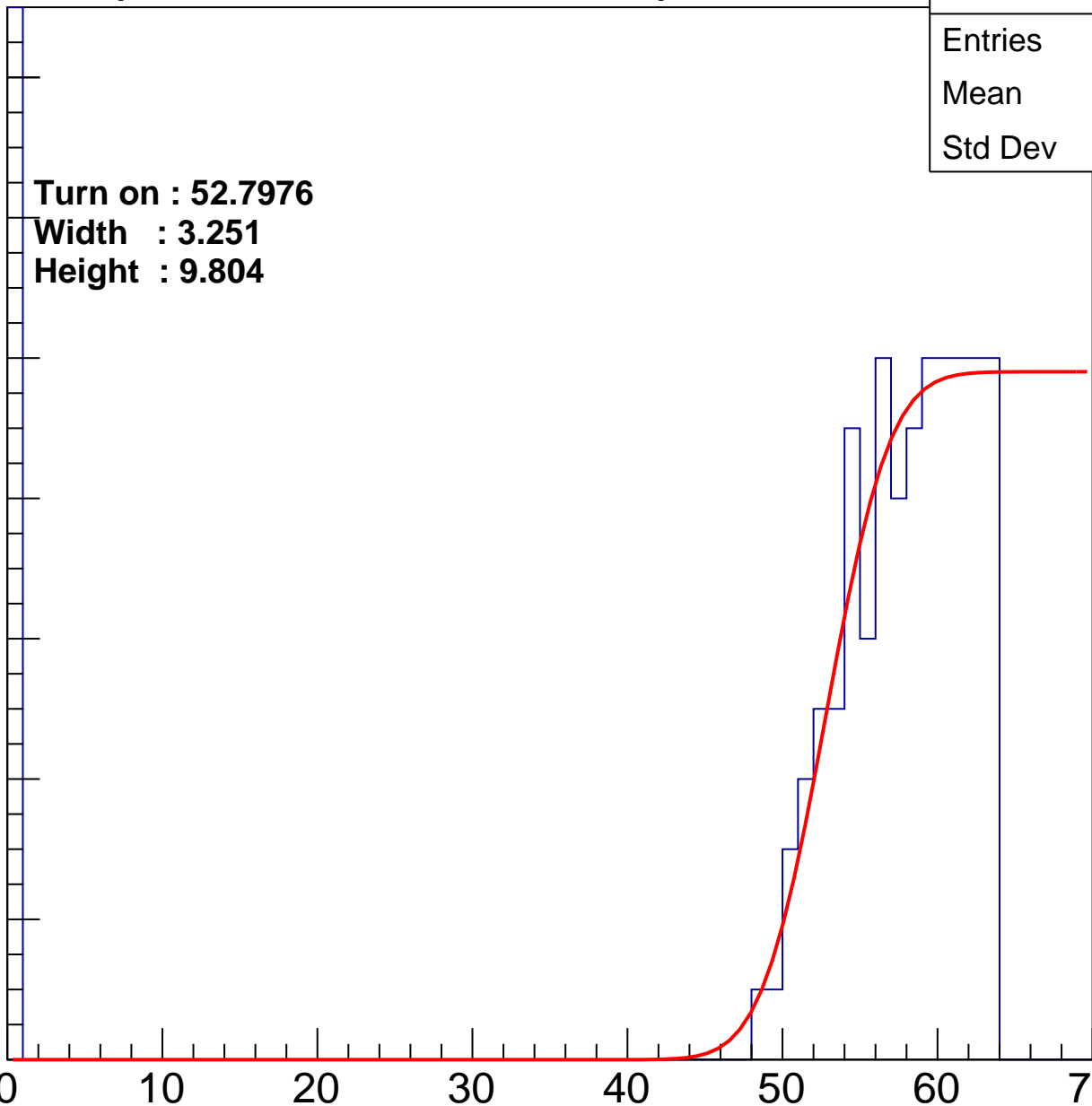
Width : 3.251

Height : 9.804

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	29.59
Std Dev	29.35

Turn on : 55.1922

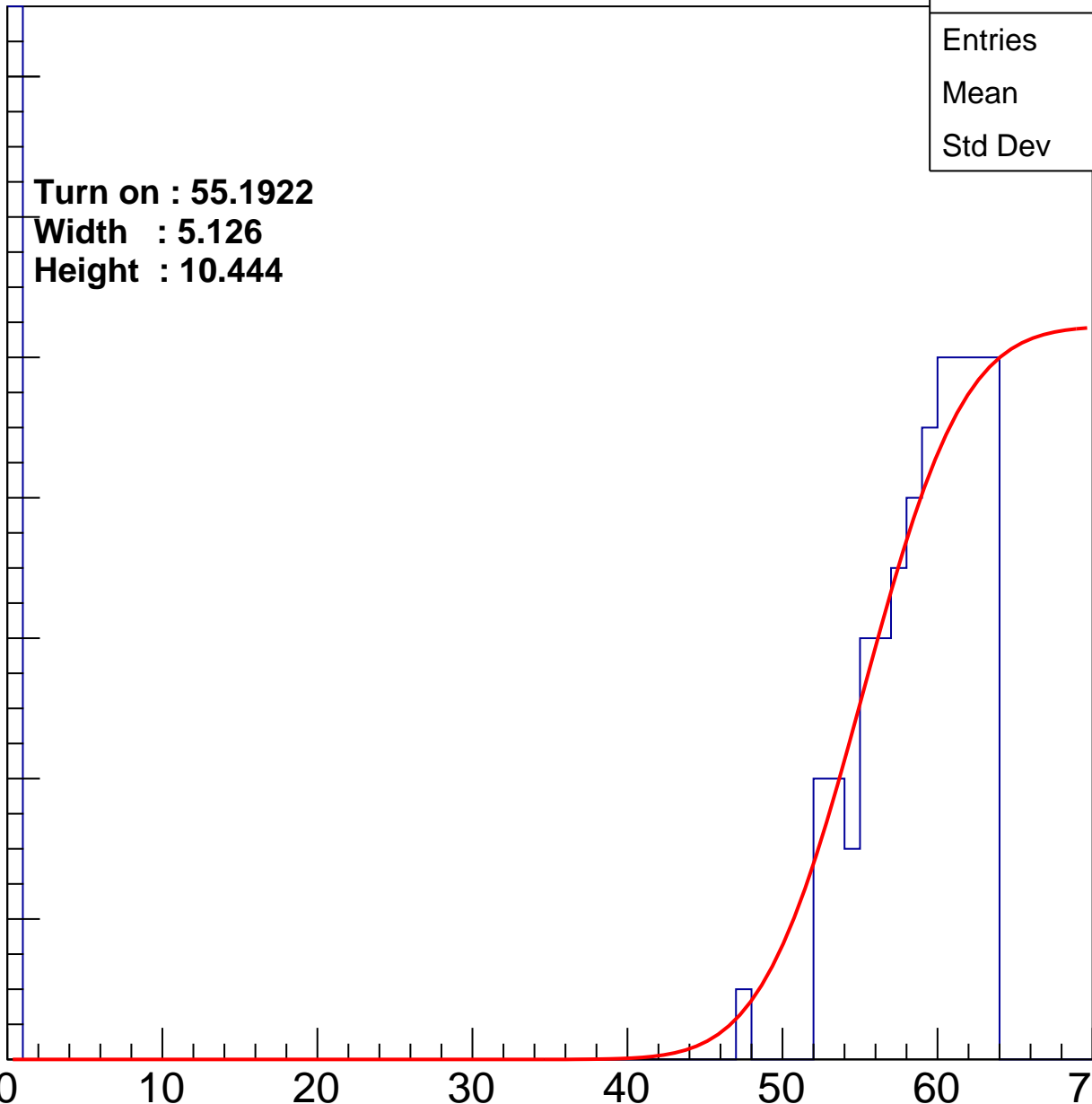
Width : 5.126

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch70

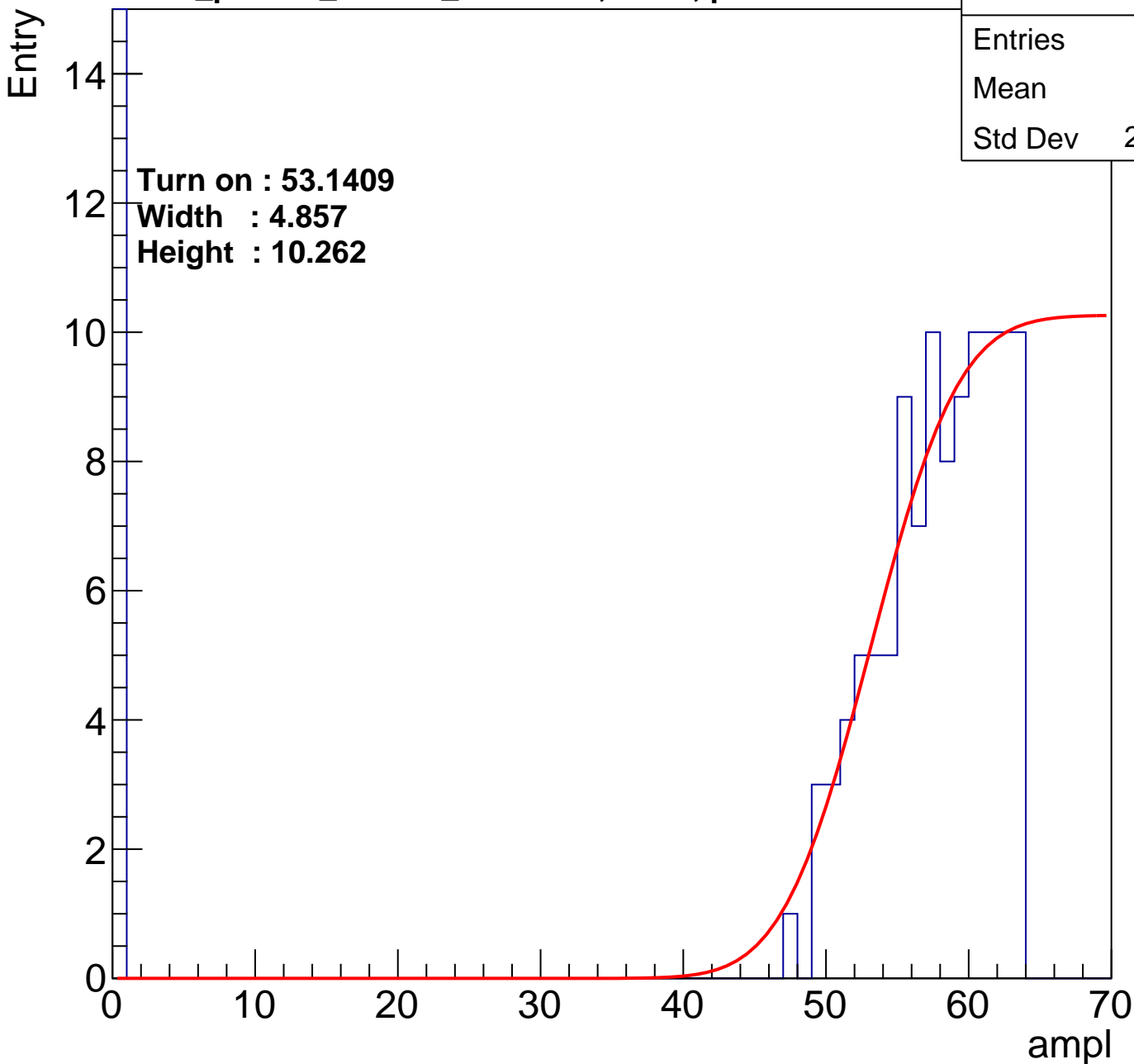
calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	28.3
Std Dev	28.83

Turn on : 53.1409

Width : 4.857

Height : 10.262



B1L104S, U8-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	30.24
Std Dev	28.85

Turn on : 52.9937

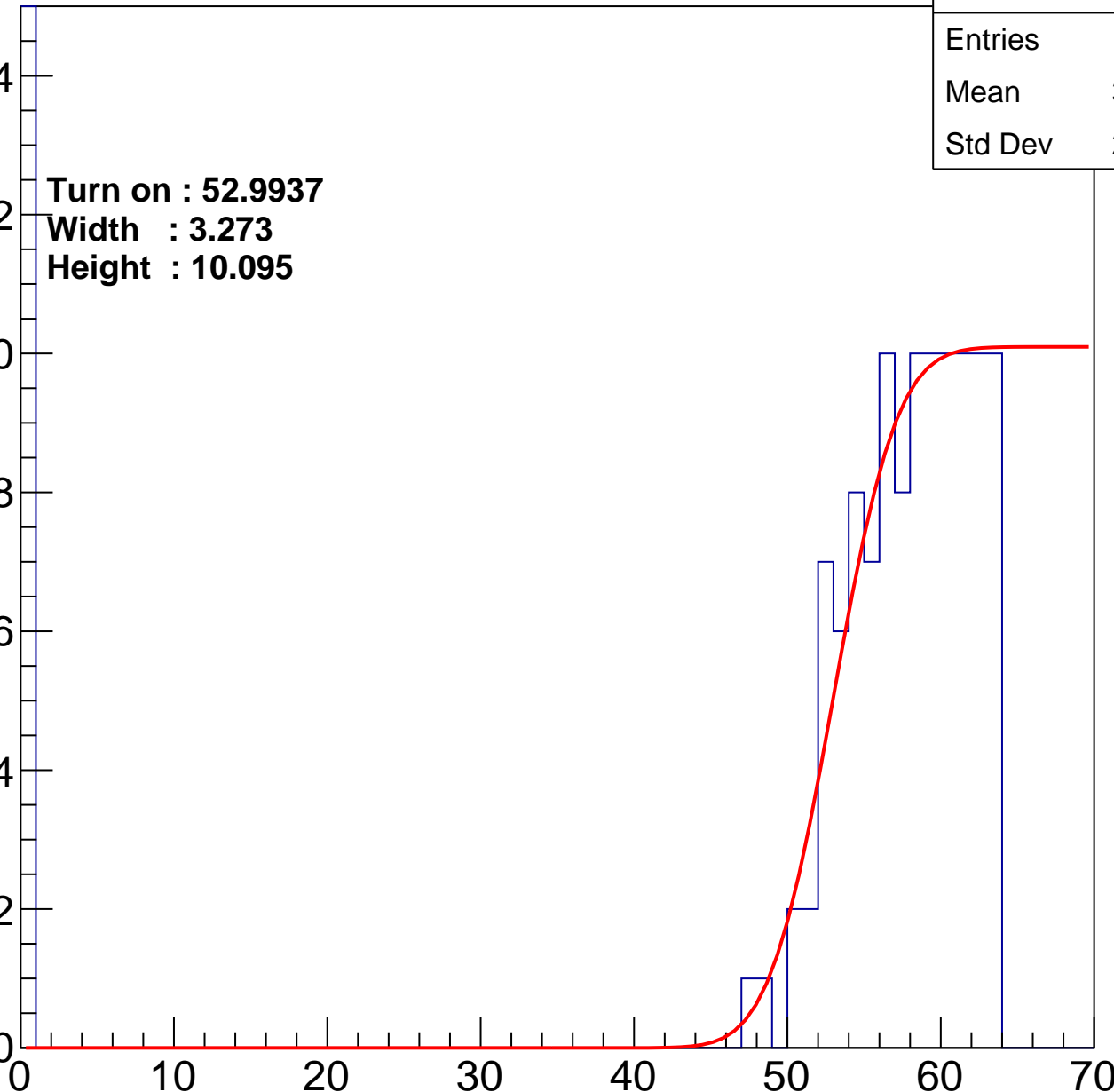
Width : 3.273

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	30.43
Std Dev	28.94

Turn on : 55.0526

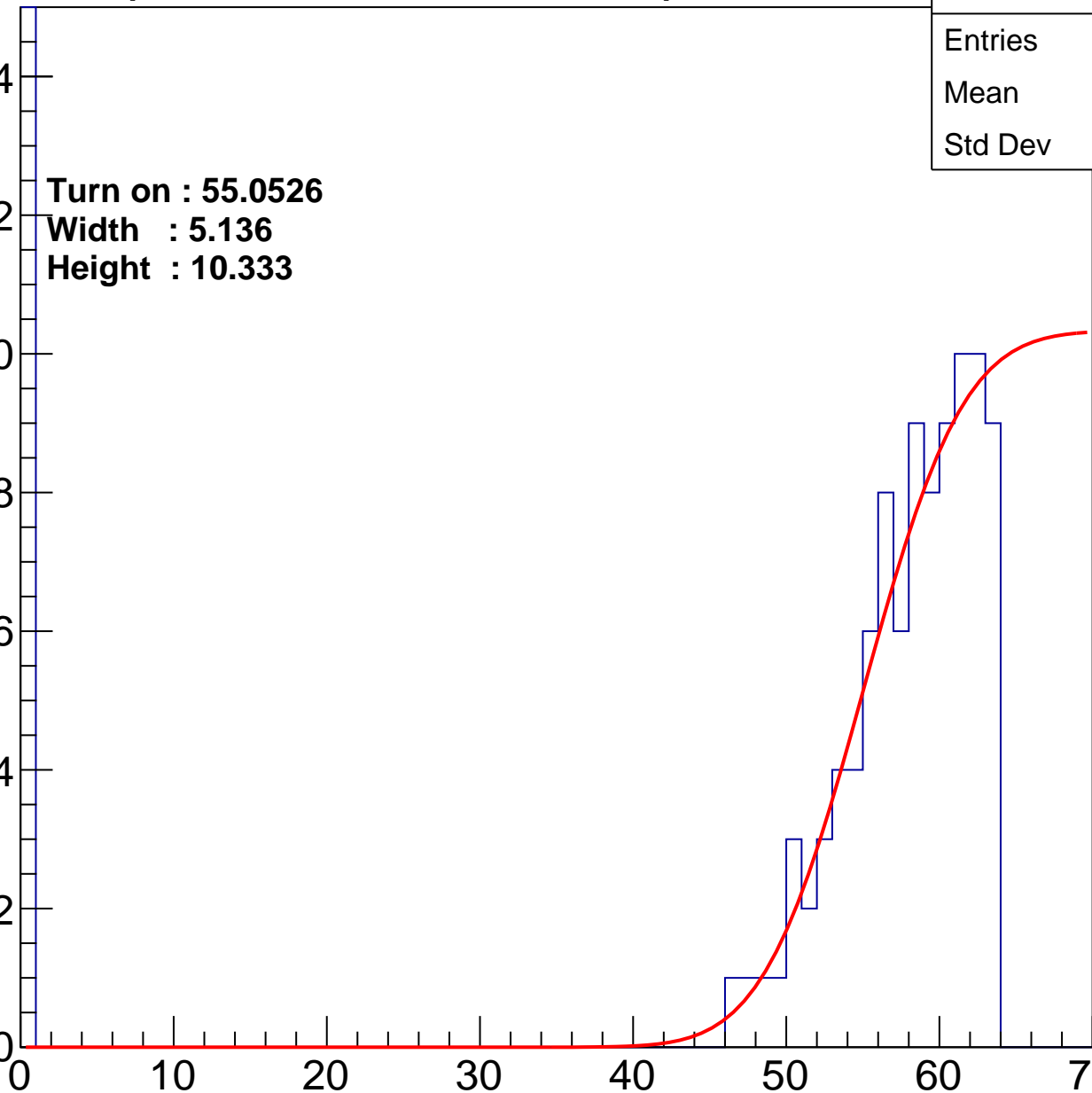
Width : 5.136

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	25.86
Std Dev	29.07

Turn on : 54.1250

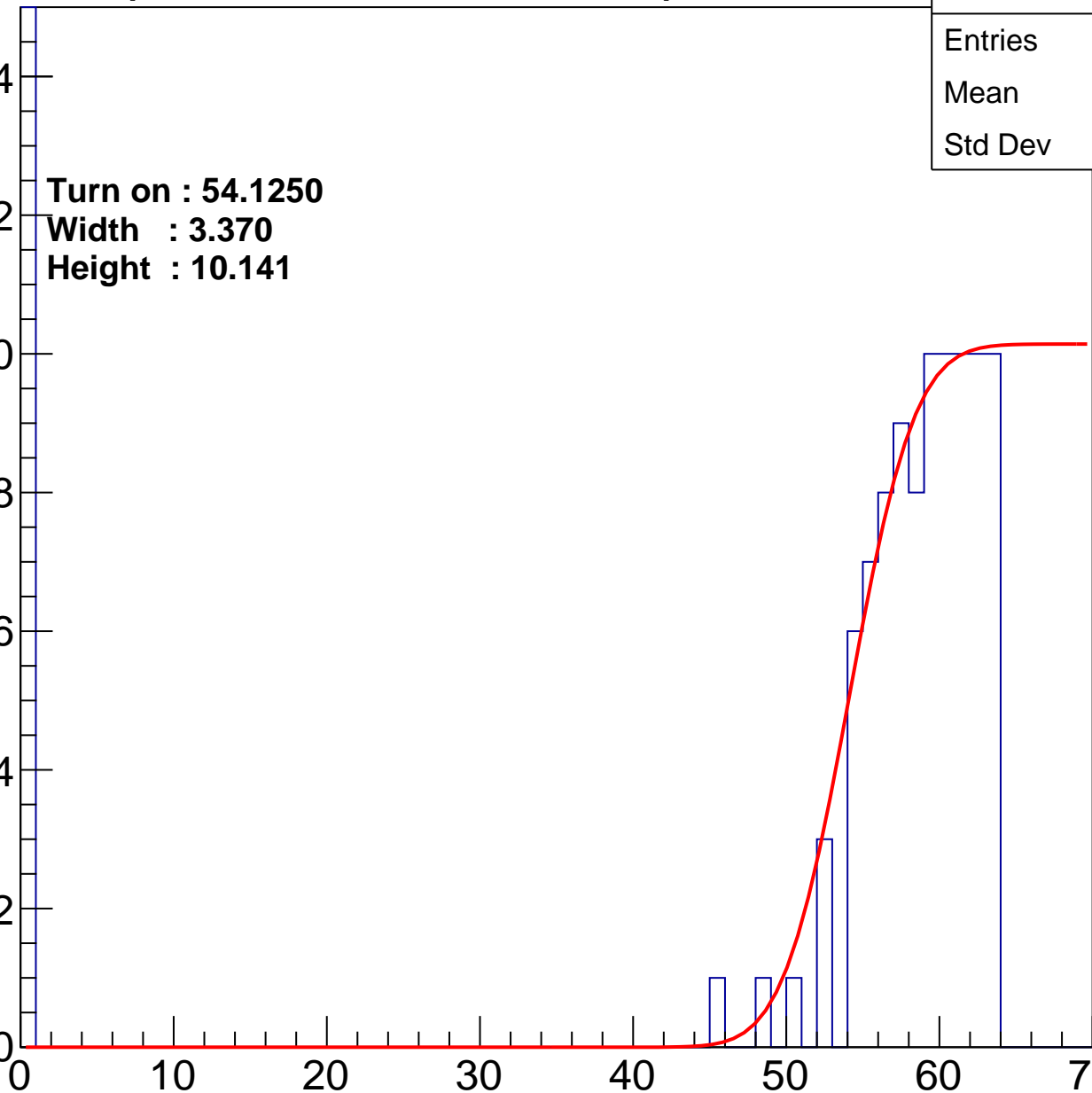
Width : 3.370

Height : 10.141

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	28.13
Std Dev	29.09

Turn on : 54.6756

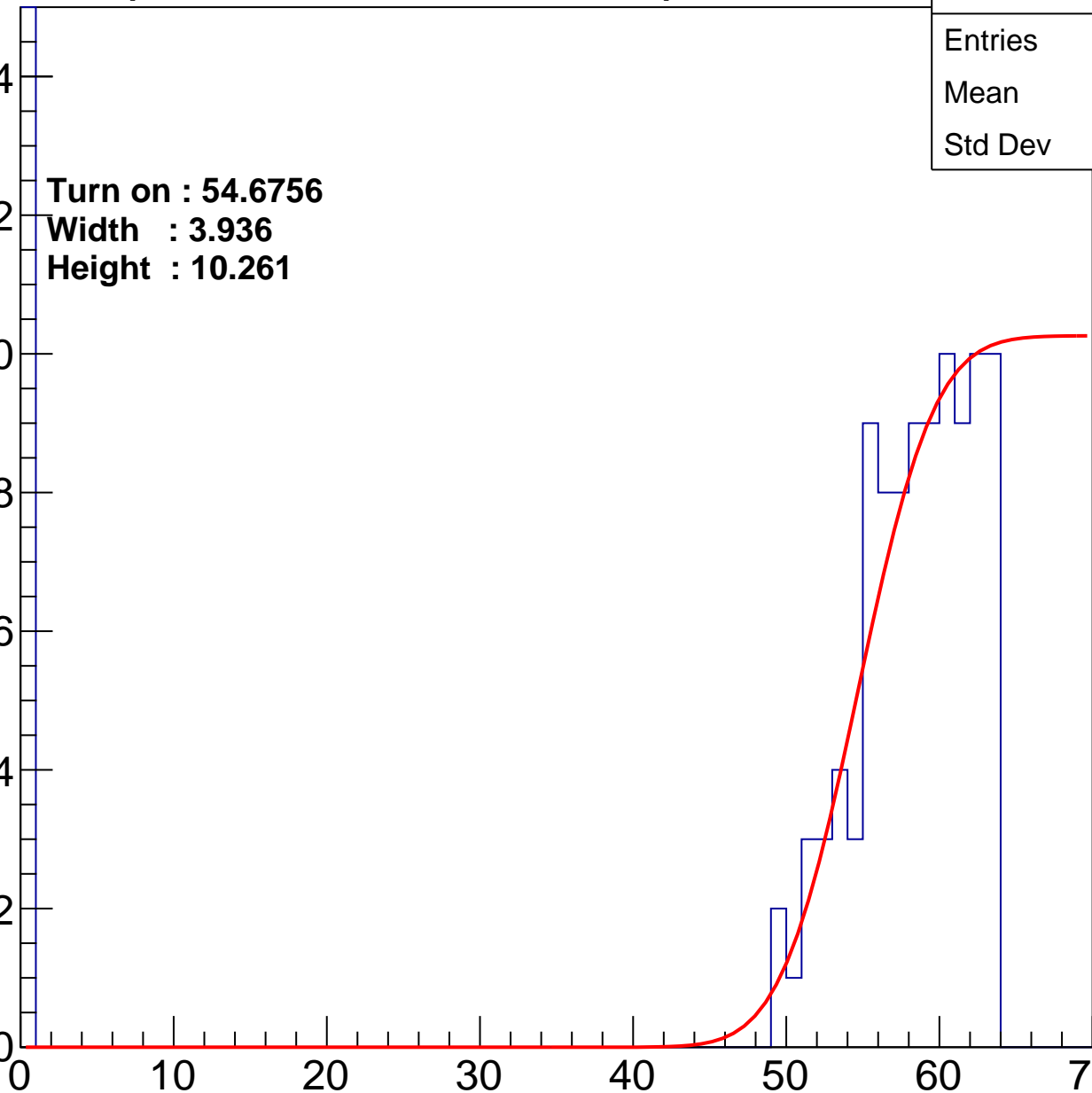
Width : 3.936

Height : 10.261

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	33.97
Std Dev	28.49

Turn on : 53.8416

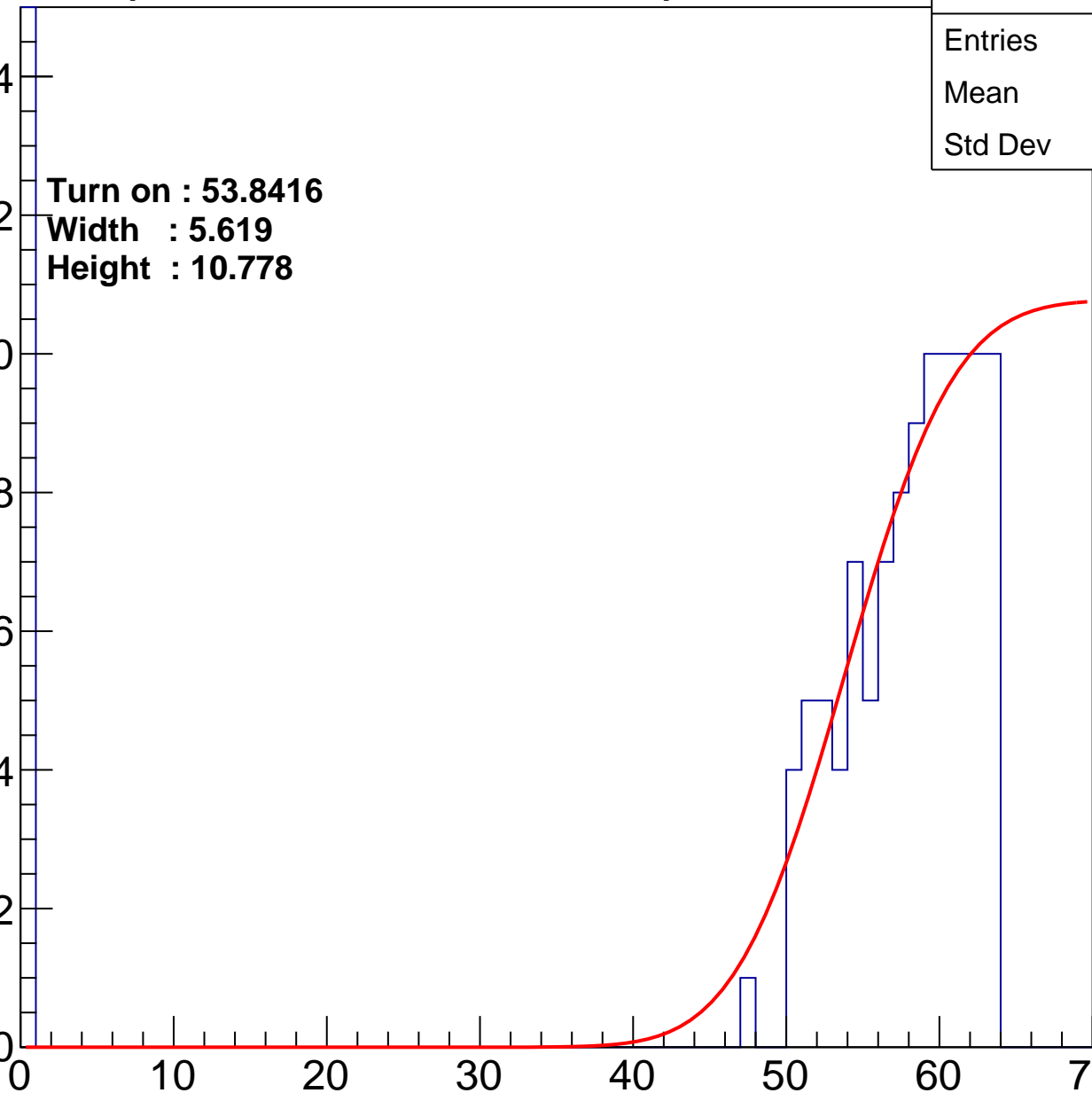
Width : 5.619

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	32.23
Std Dev	28.44

Turn on : 52.8196

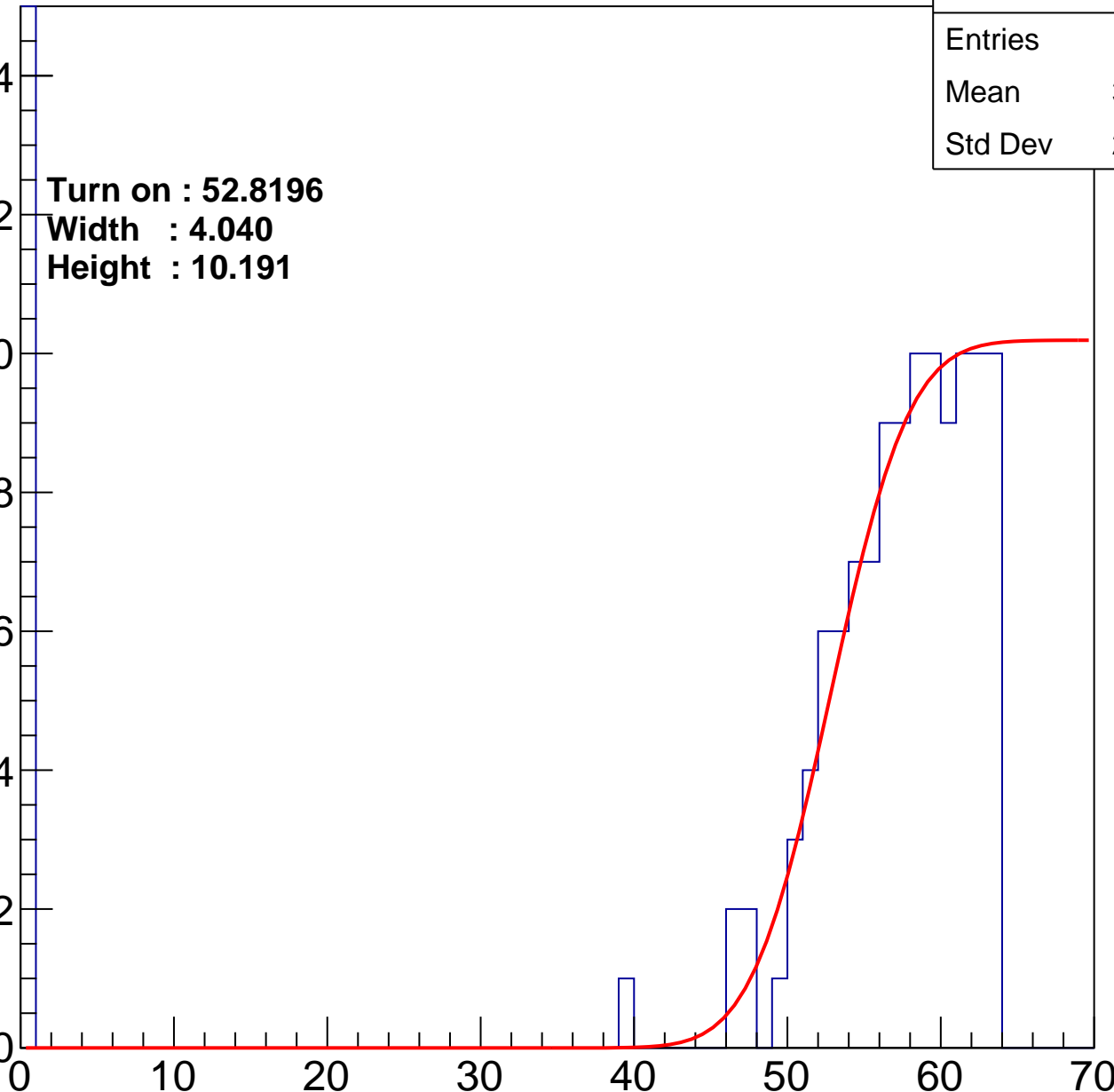
Width : 4.040

Height : 10.191

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch77

calib_packv5_033123_0516.root, FC#4, port A1

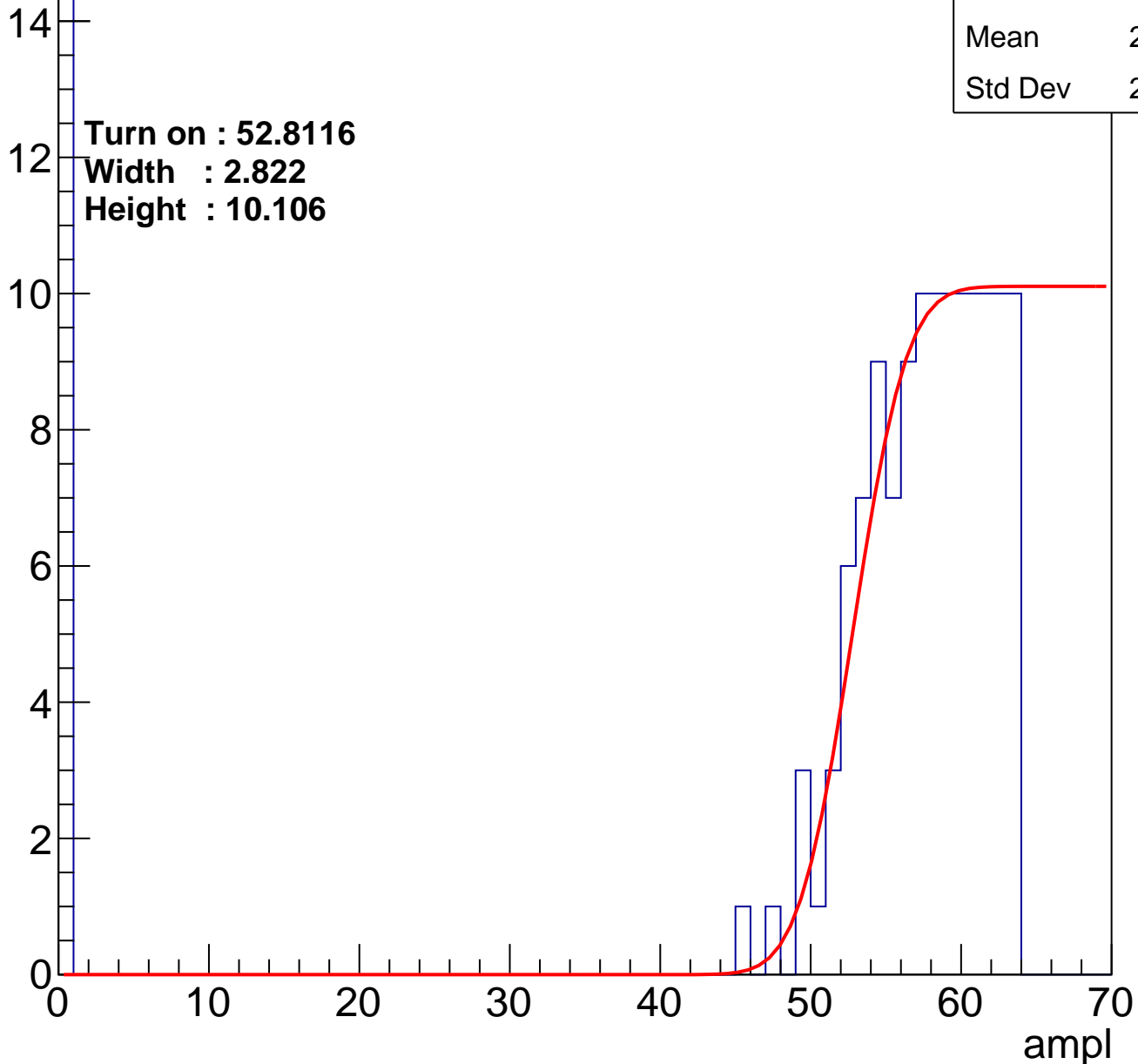
Entry

Entries	230
Mean	29.13
Std Dev	28.77

Turn on : 52.8116

Width : 2.822

Height : 10.106



B1L104S, U8-ch78

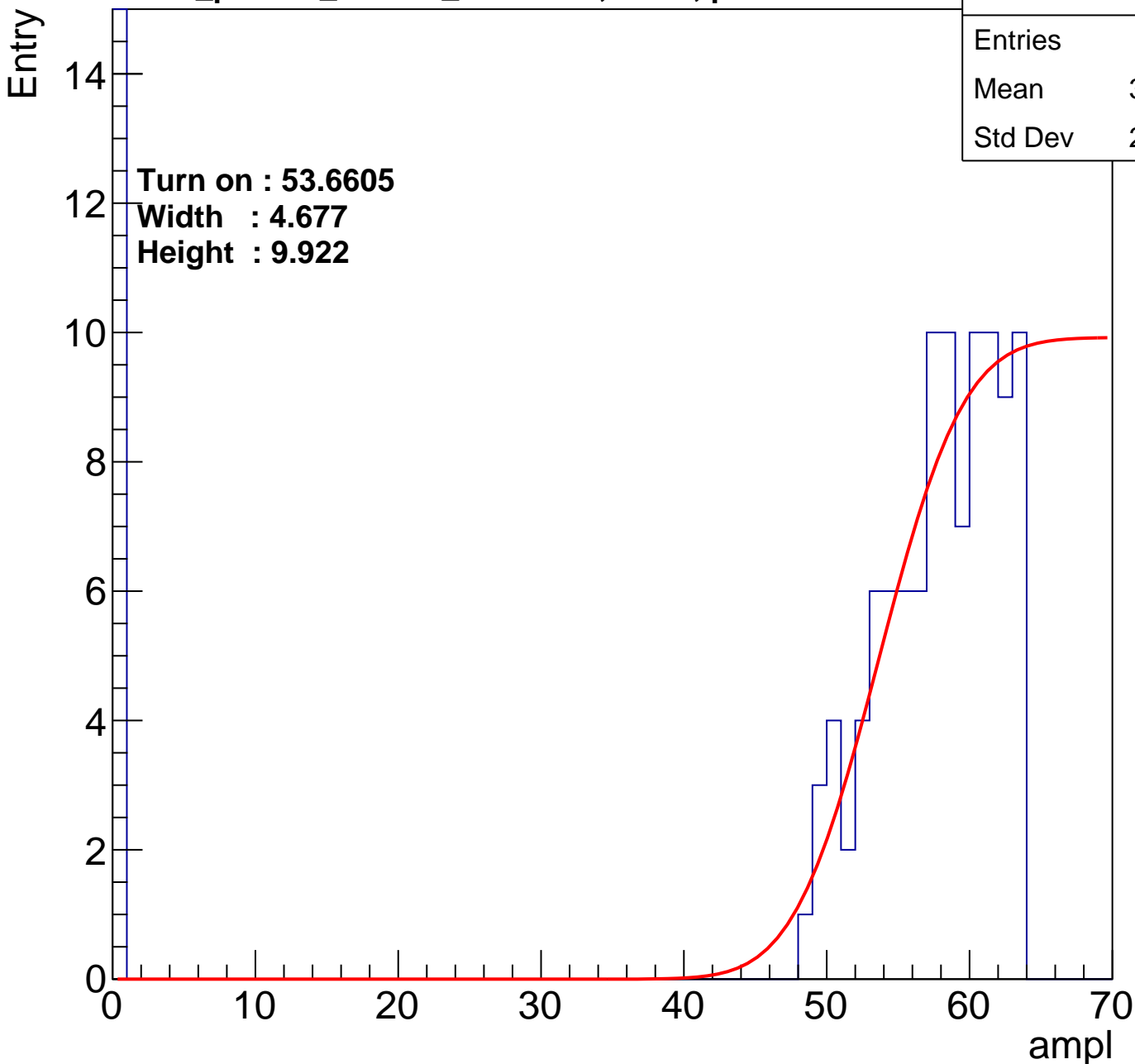
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	30.17
Std Dev	28.83

Turn on : 53.6605

Width : 4.677

Height : 9.922



B1L104S, U8-ch79

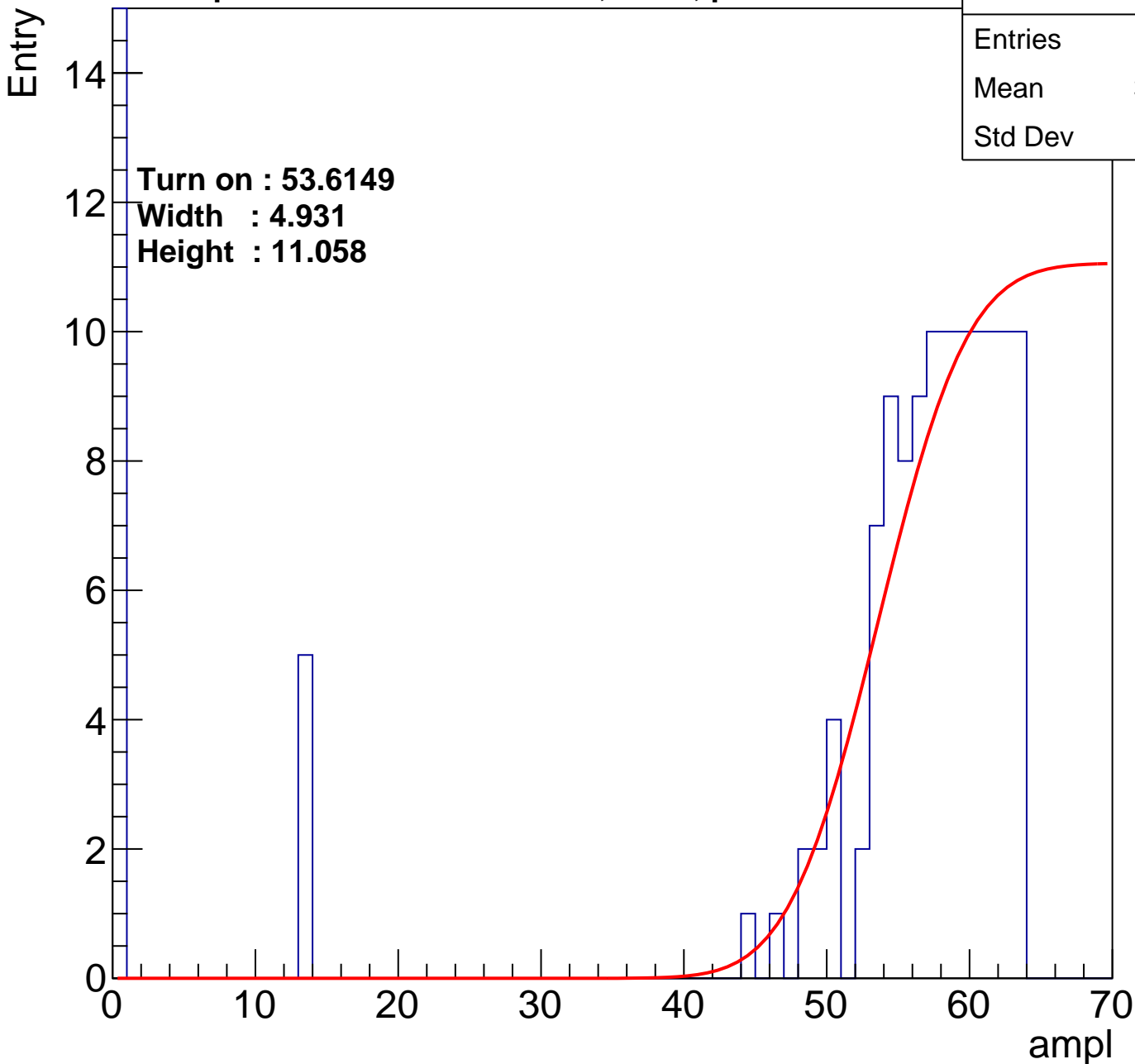
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	35.21
Std Dev	27.8

Turn on : 53.6149

Width : 4.931

Height : 11.058



B1L104S, U8-ch80

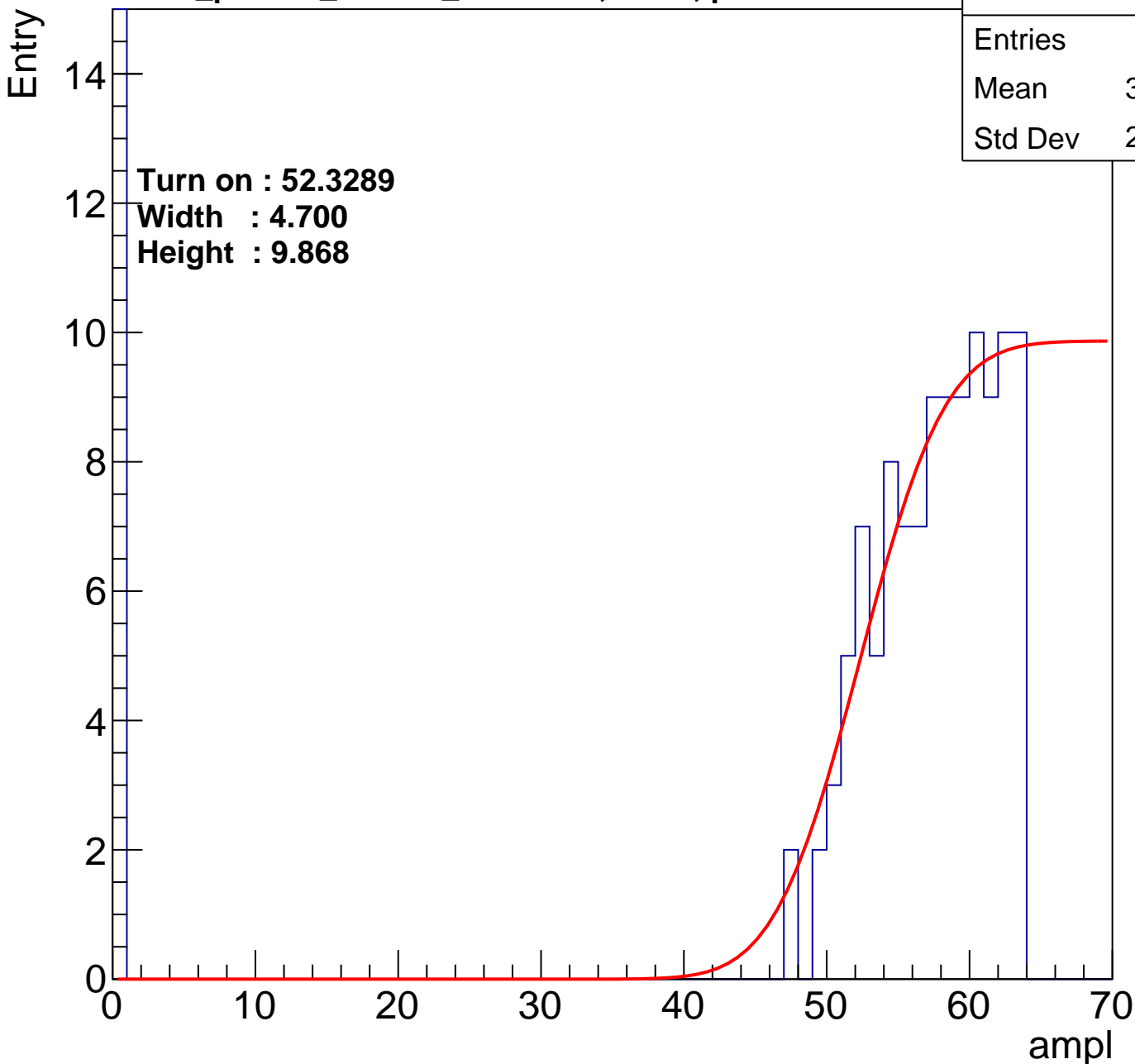
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	30.33
Std Dev	28.67

Turn on : 52.3289

Width : 4.700

Height : 9.868



B1L104S, U8-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.74
Std Dev	28.77

Turn on : 53.4029

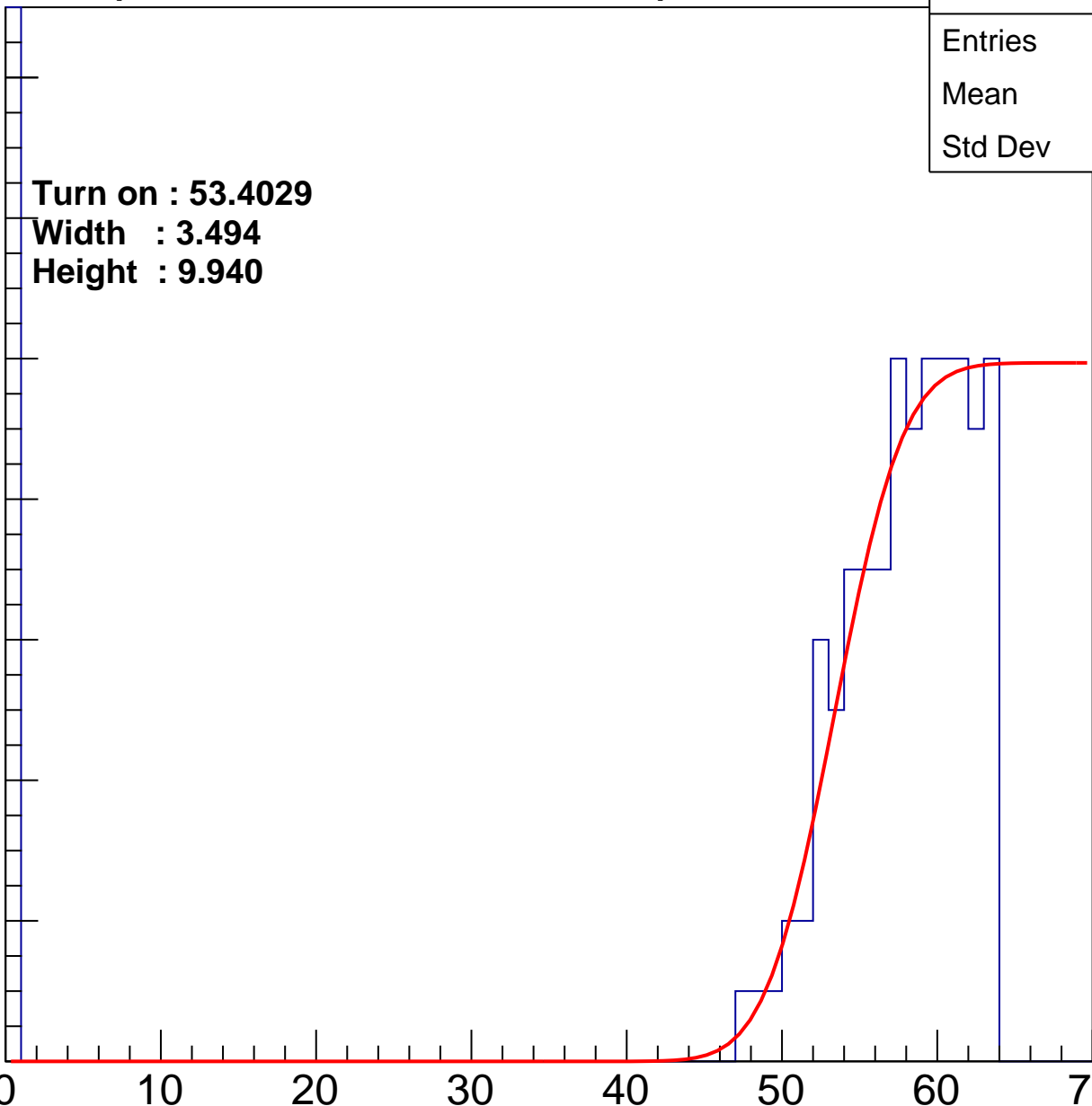
Width : 3.494

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	244
Mean	26.82
Std Dev	28.58

Turn on : 54.4834

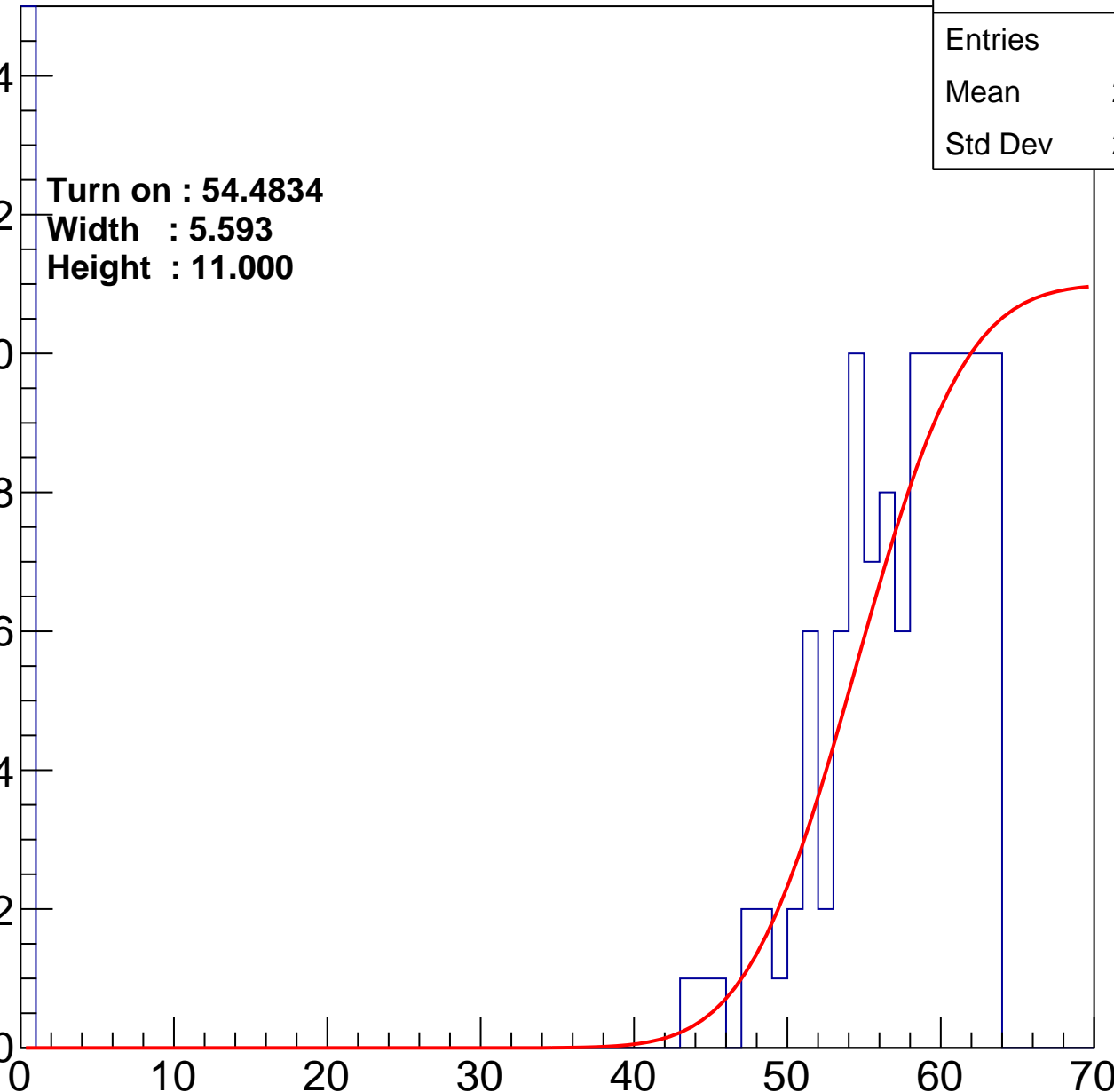
Width : 5.593

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	34.44
Std Dev	28.54

Turn on : 54.5341

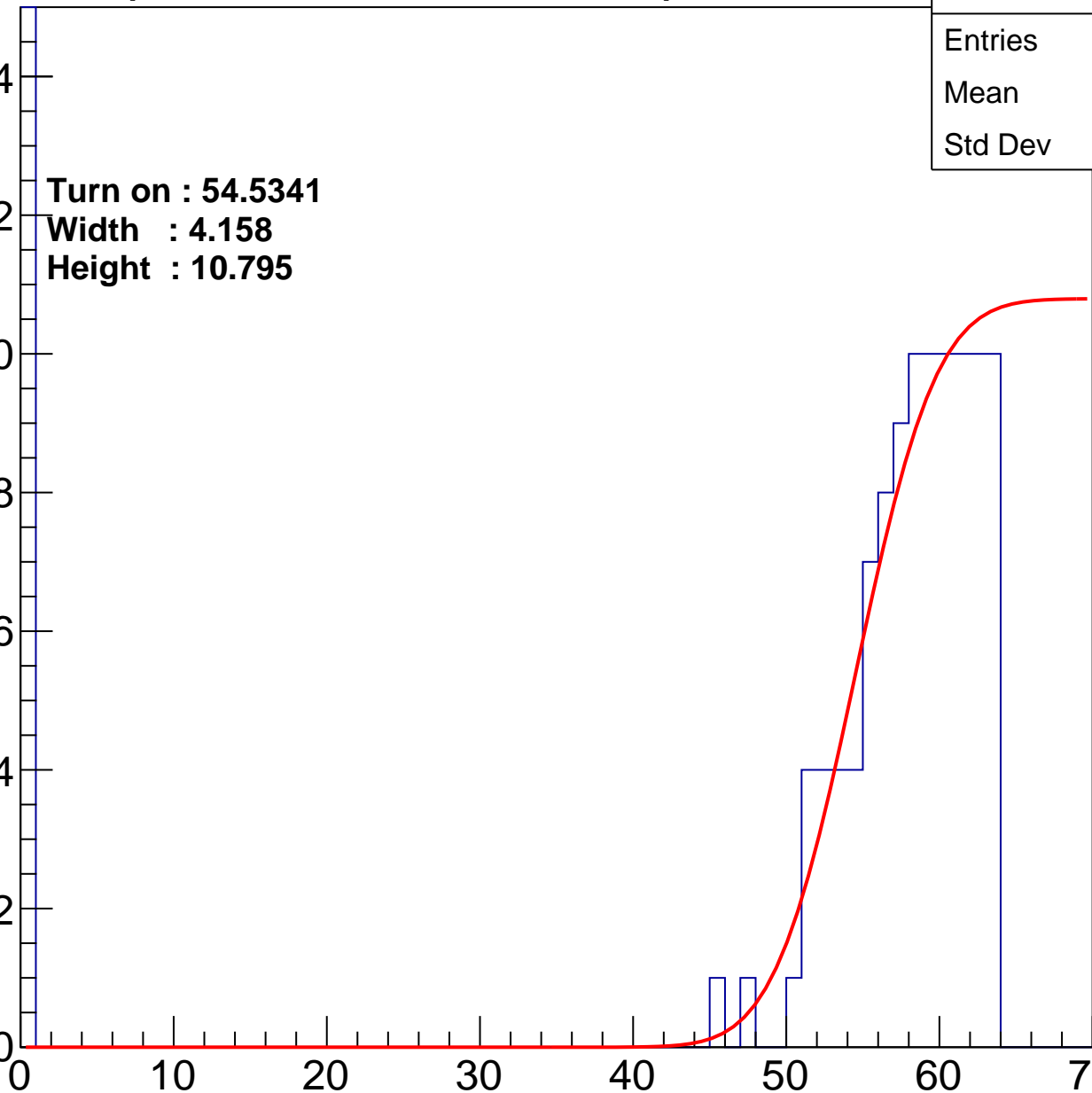
Width : 4.158

Height : 10.795

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	29.98
Std Dev	29.03

Turn on : 54.7552

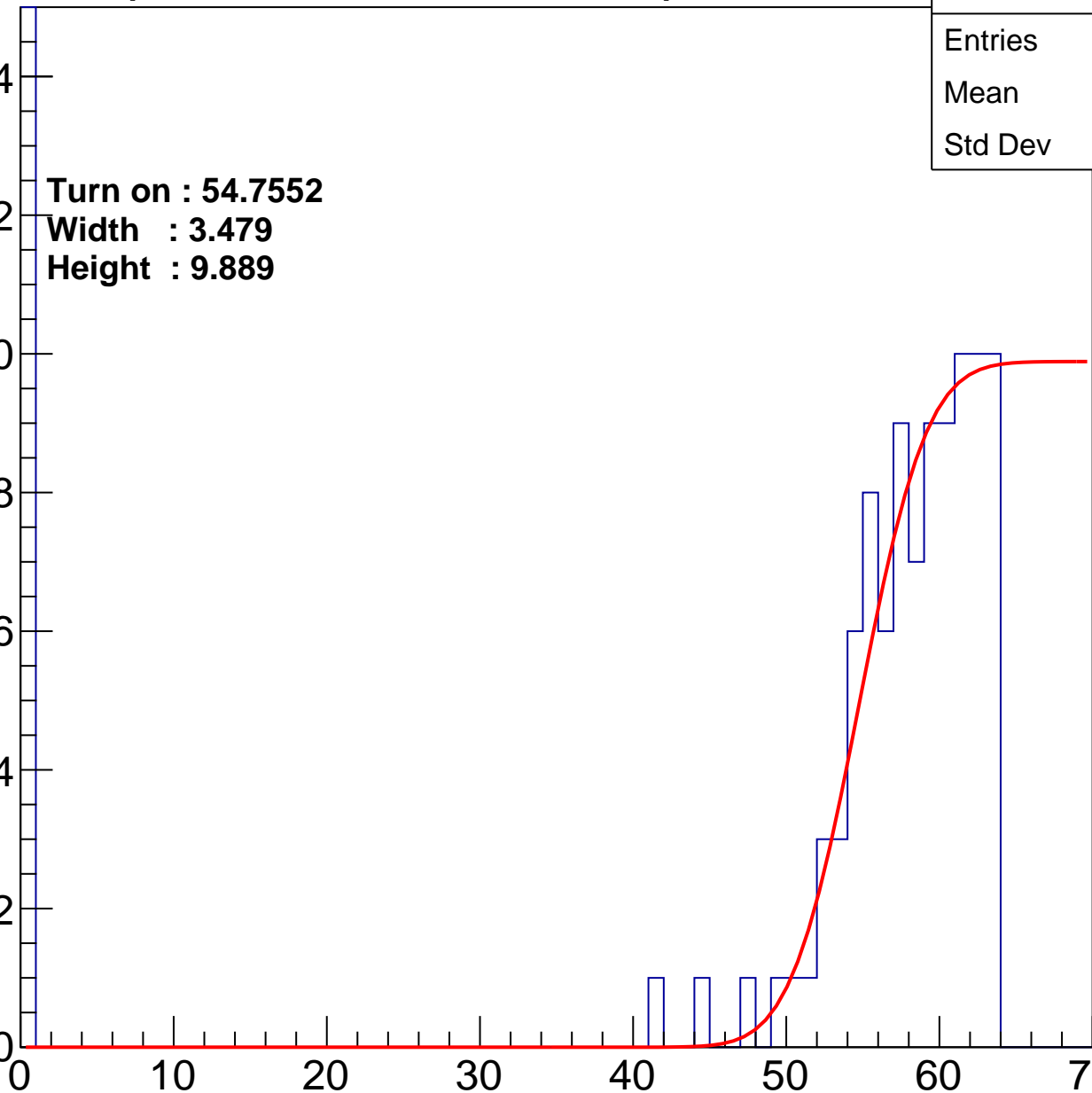
Width : 3.479

Height : 9.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	34.73
Std Dev	28.01

Turn on : 52.1006

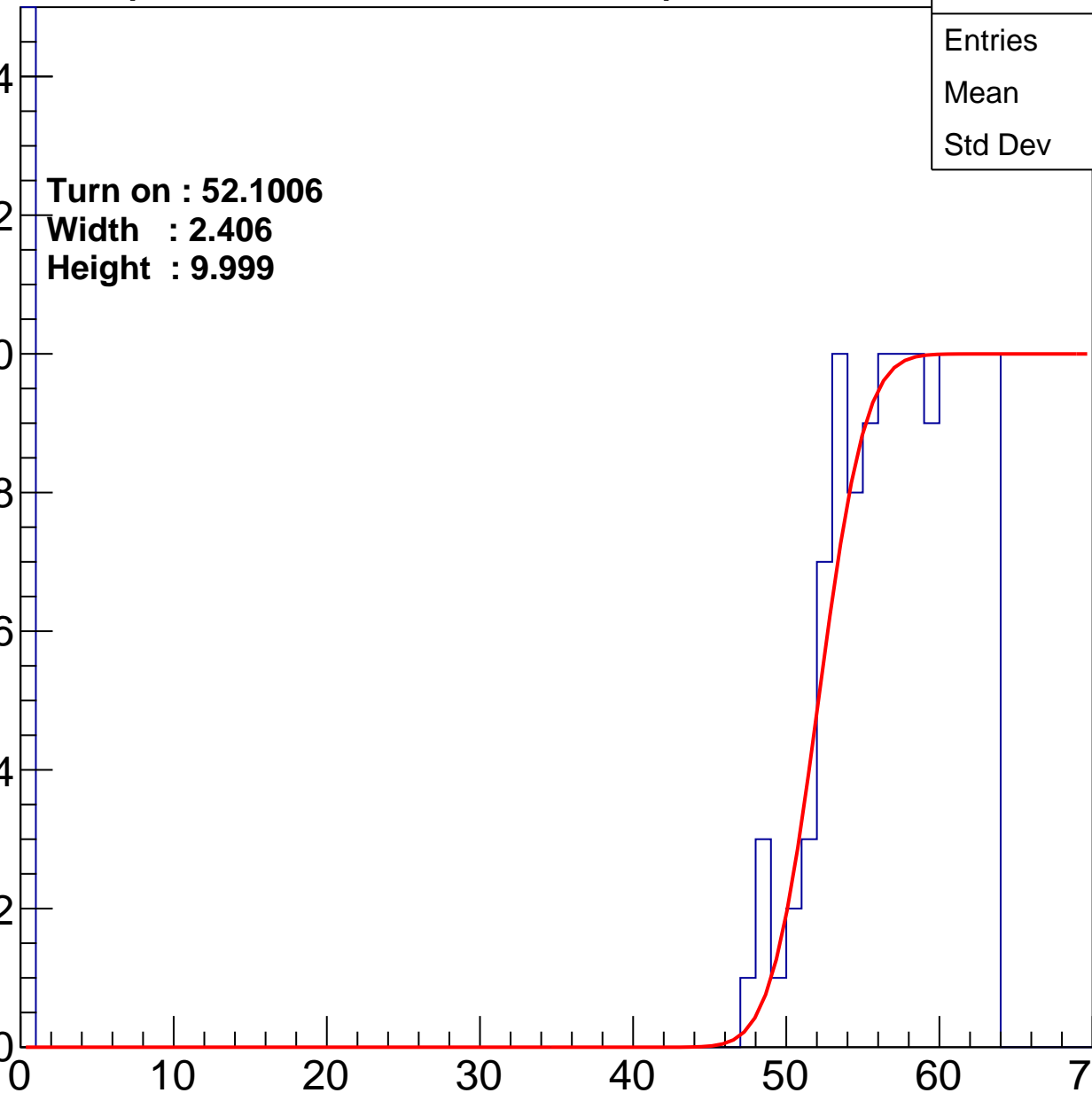
Width : 2.406

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	229
Mean	26.7
Std Dev	28.89

Turn on : 54.0937

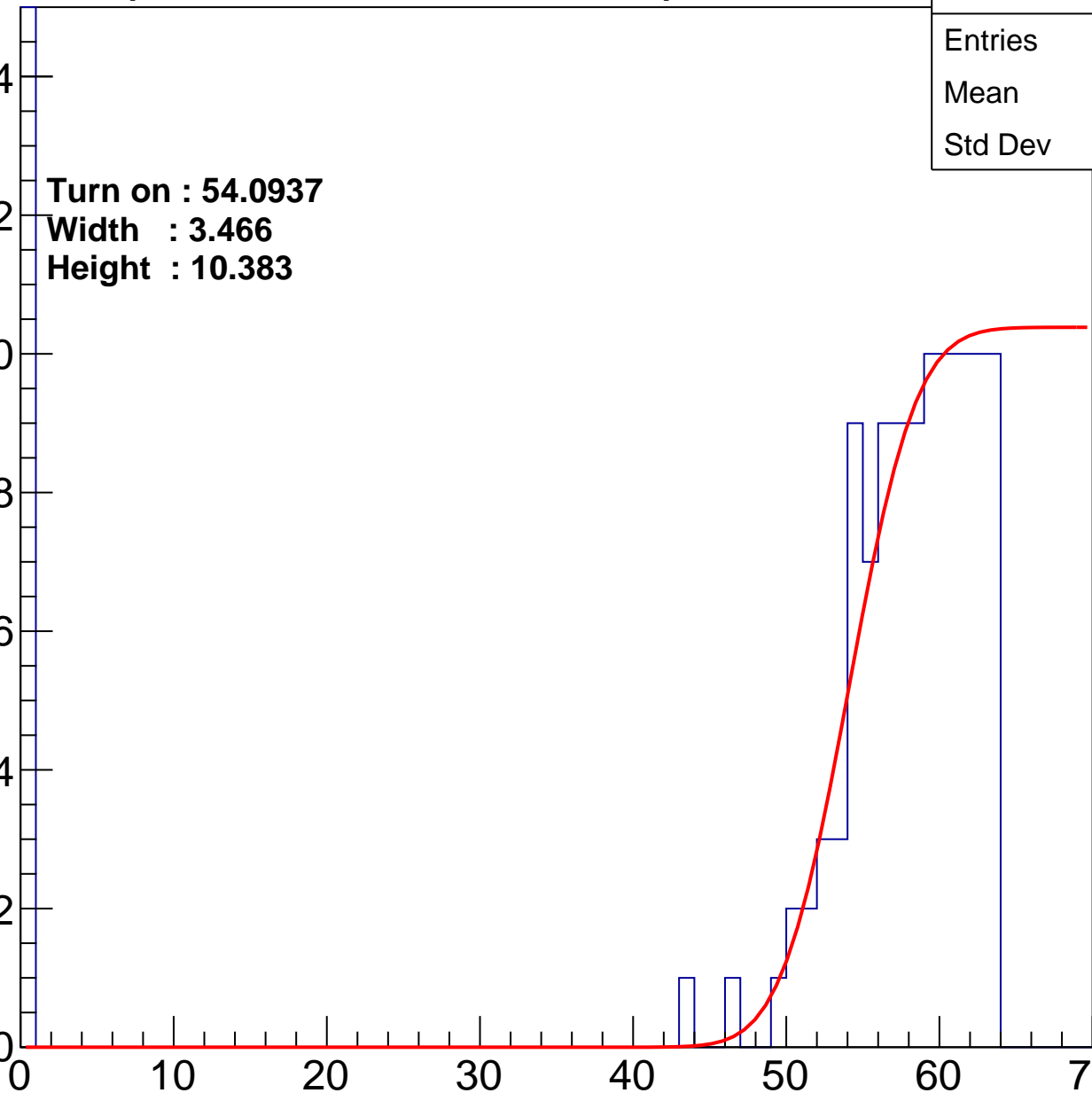
Width : 3.466

Height : 10.383

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch87

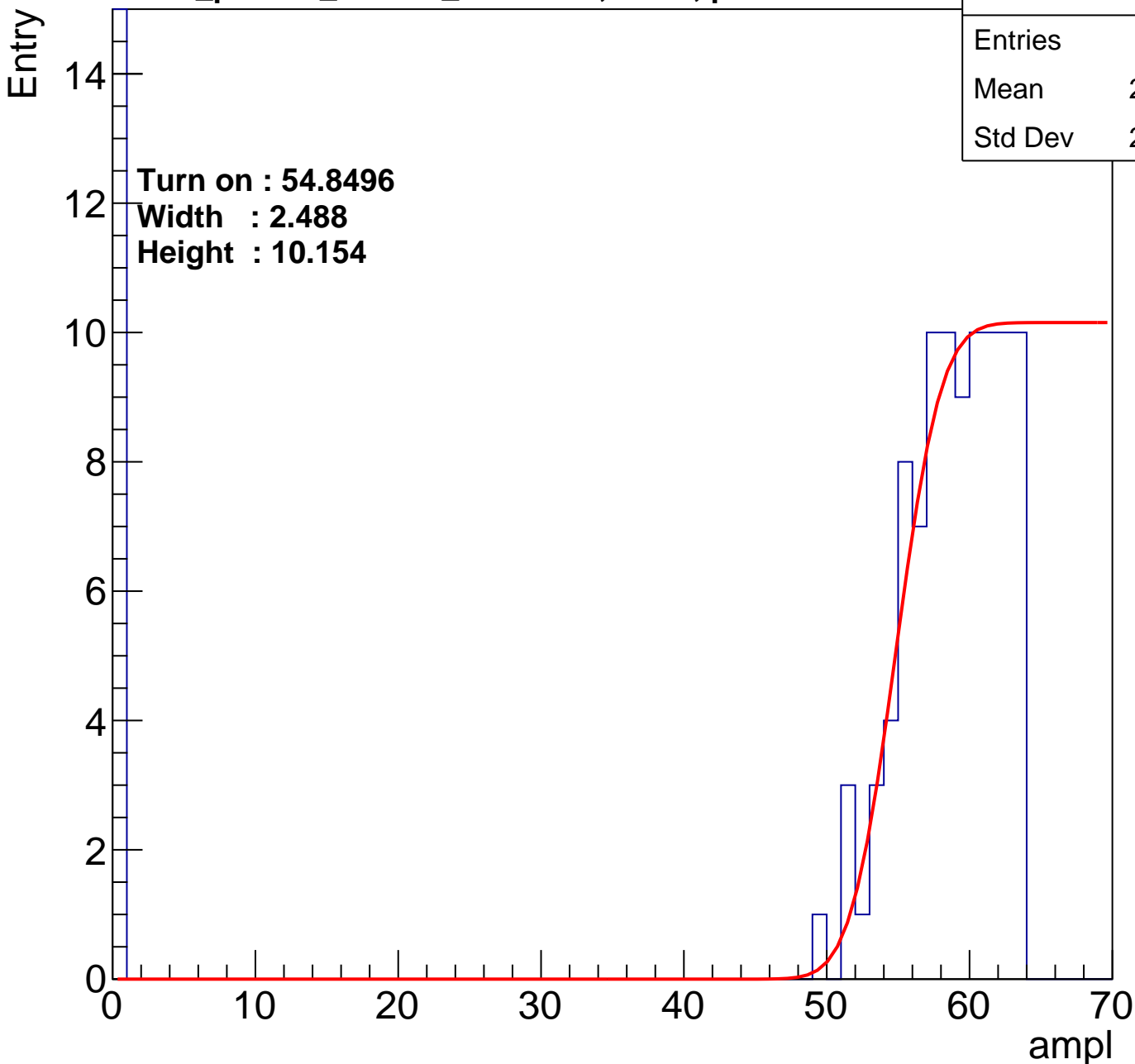
calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	29.48
Std Dev	29.27

Turn on : 54.8496

Width : 2.488

Height : 10.154



B1L104S, U8-ch88

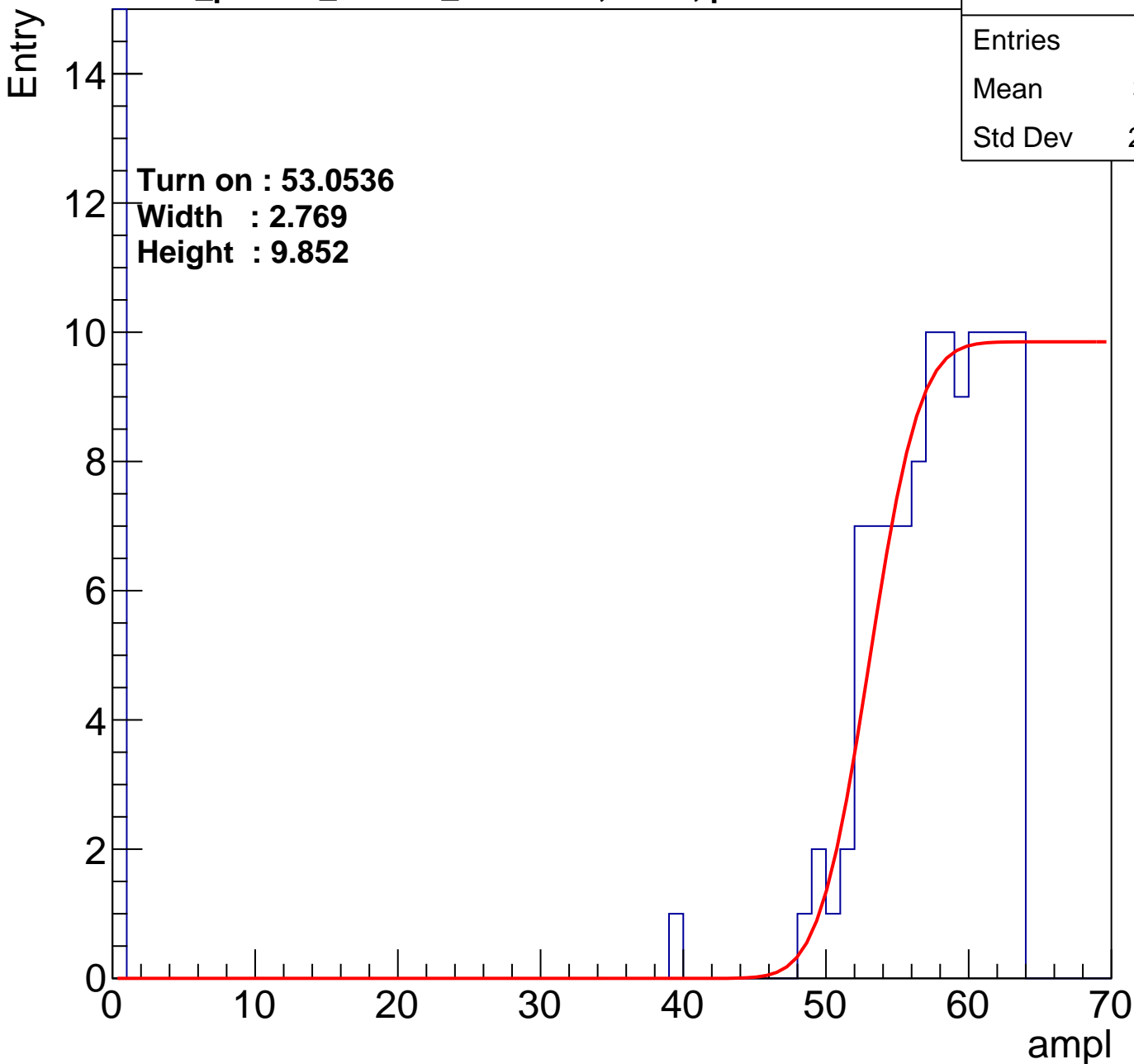
calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	32.61
Std Dev	28.58

Turn on : 53.0536

Width : 2.769

Height : 9.852



B1L104S, U8-ch89

calib_packv5_033123_0516.root, FC#4, port A1

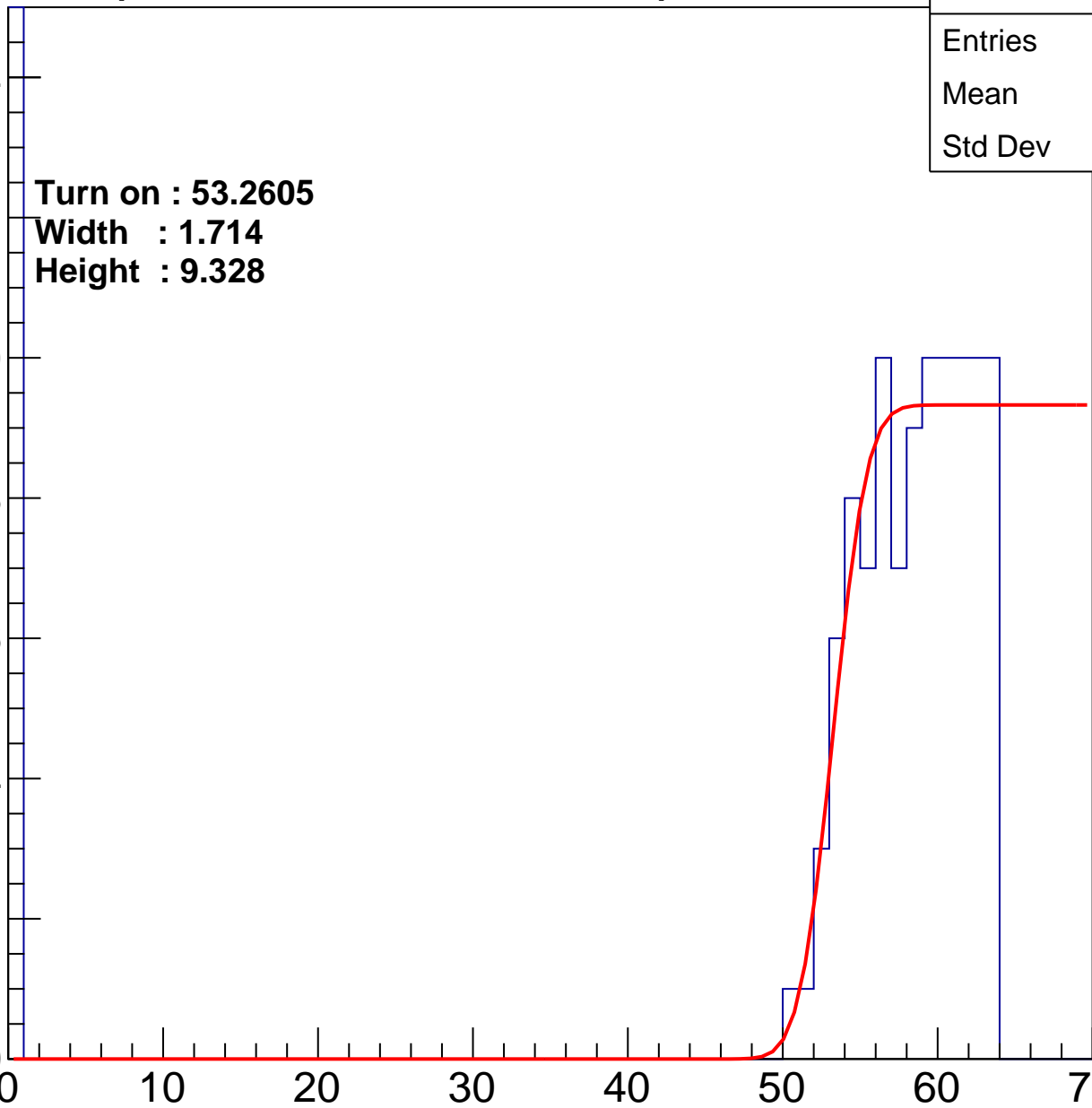
Entry

14
12
10
8
6
4
2
0

Turn on : 53.2605
Width : 1.714
Height : 9.328

Entries	182
Mean	32.54
Std Dev	28.93

ampl



B1L104S, U8-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	30.52
Std Dev	28.65

Turn on : 52.6974

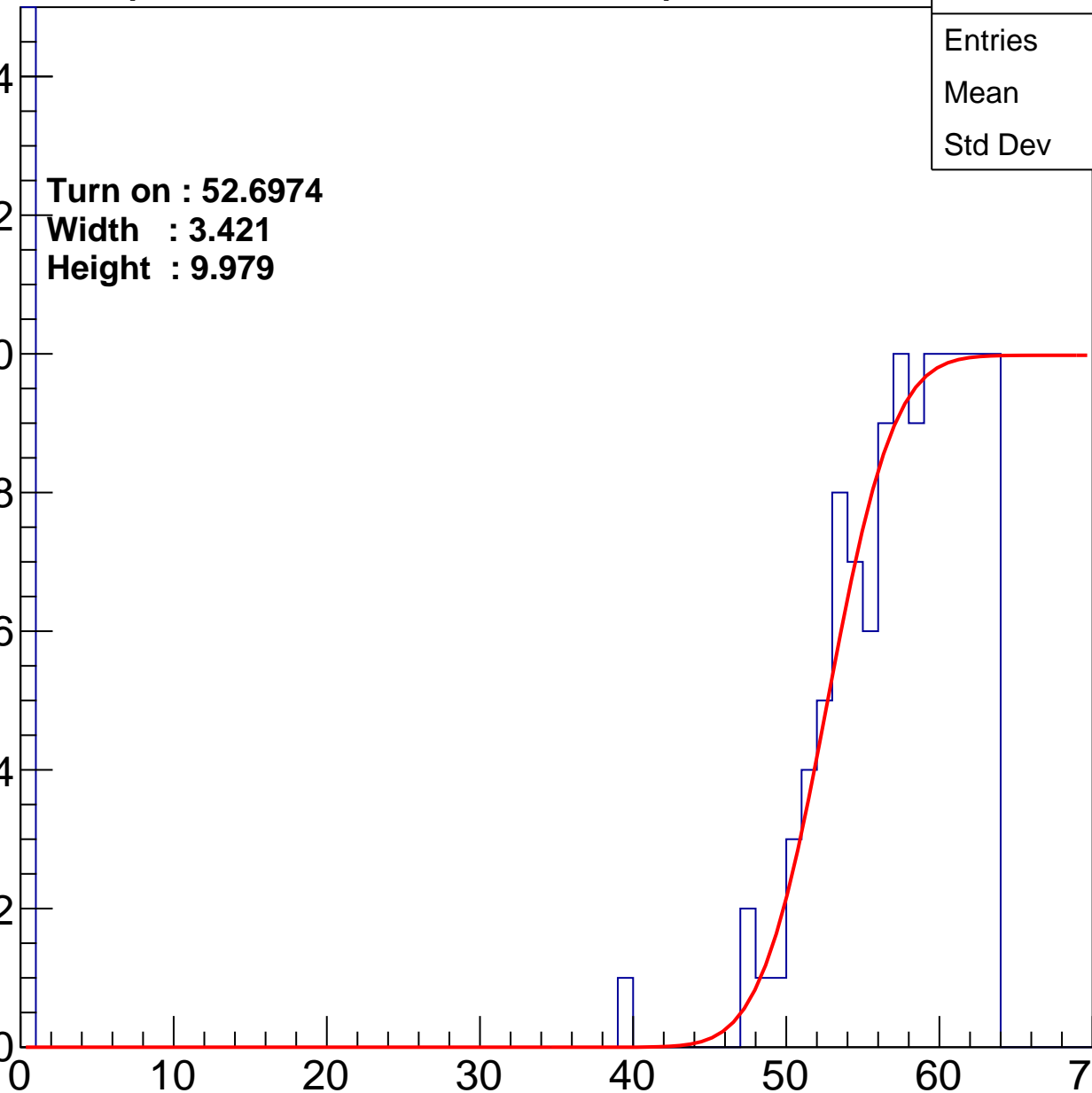
Width : 3.421

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.22
Std Dev	28.75

Turn on : 53.1331

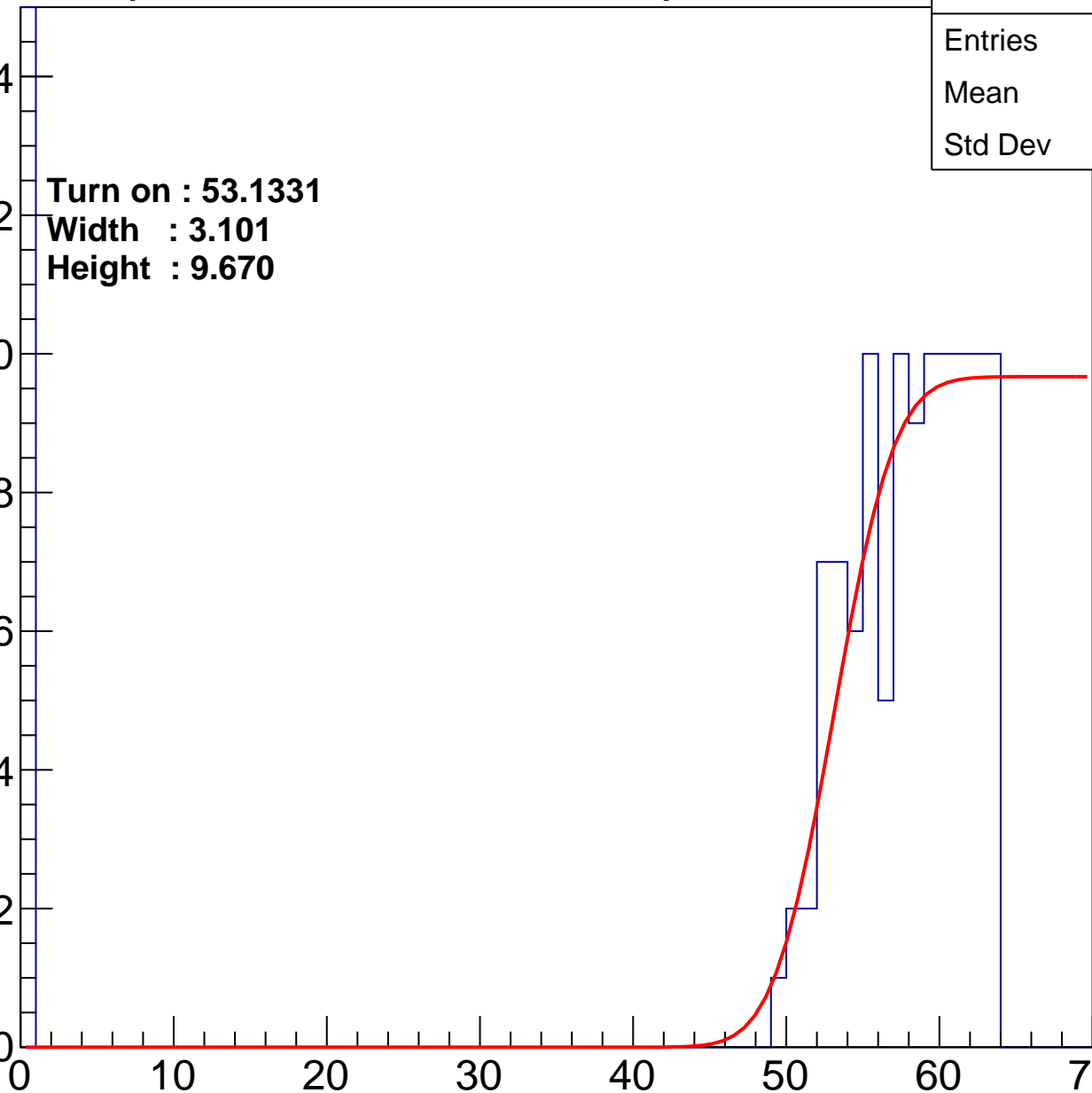
Width : 3.101

Height : 9.670

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch92

calib_packv5_033123_0516.root, FC#4, port A1

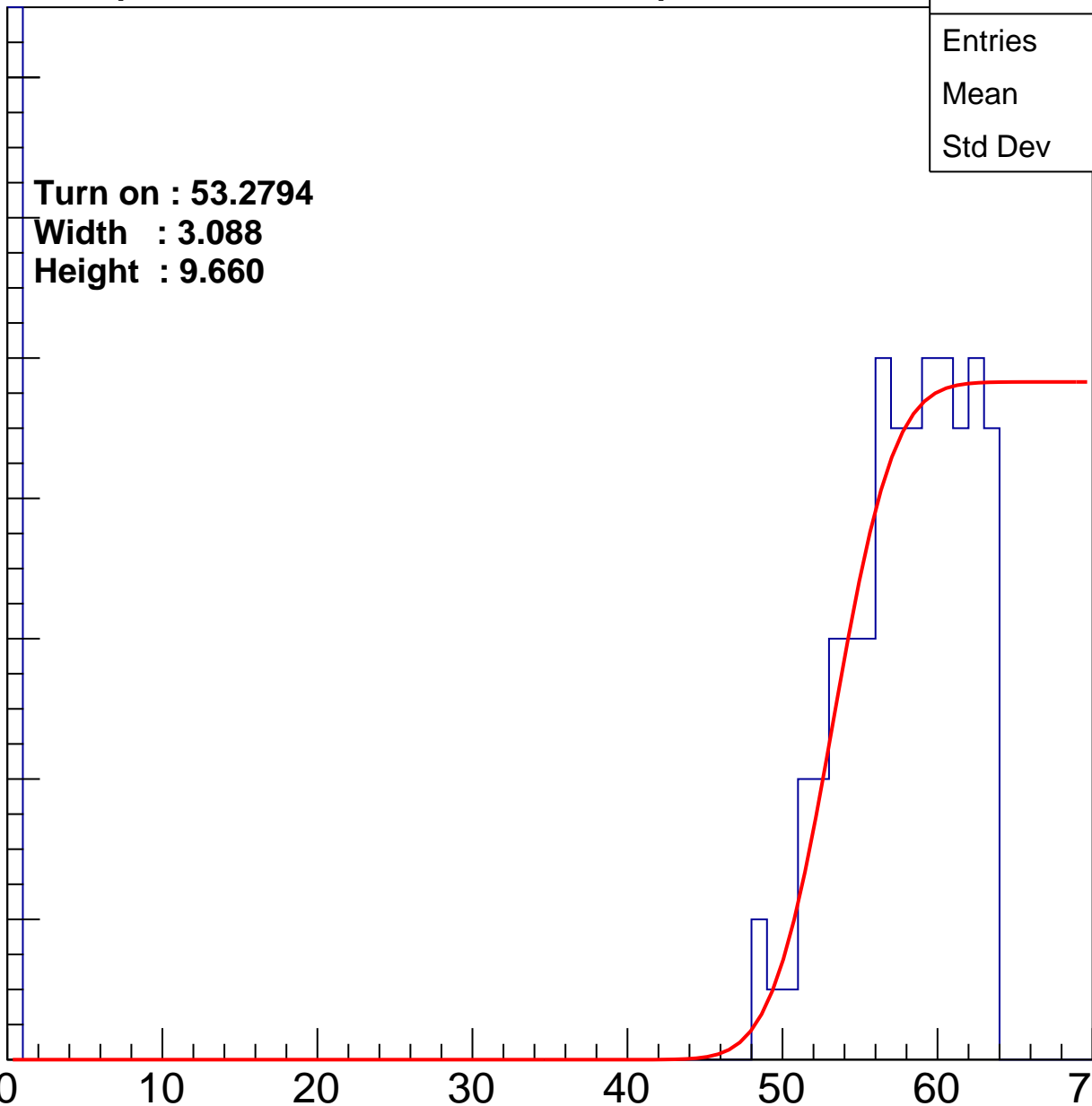
Entry

14
12
10
8
6
4
2
0

Turn on : 53.2794
Width : 3.088
Height : 9.660

Entries	217
Mean	28.11
Std Dev	28.89

ampl



B1L104S, U8-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	28.96
Std Dev	28.95

Turn on : 54.0068

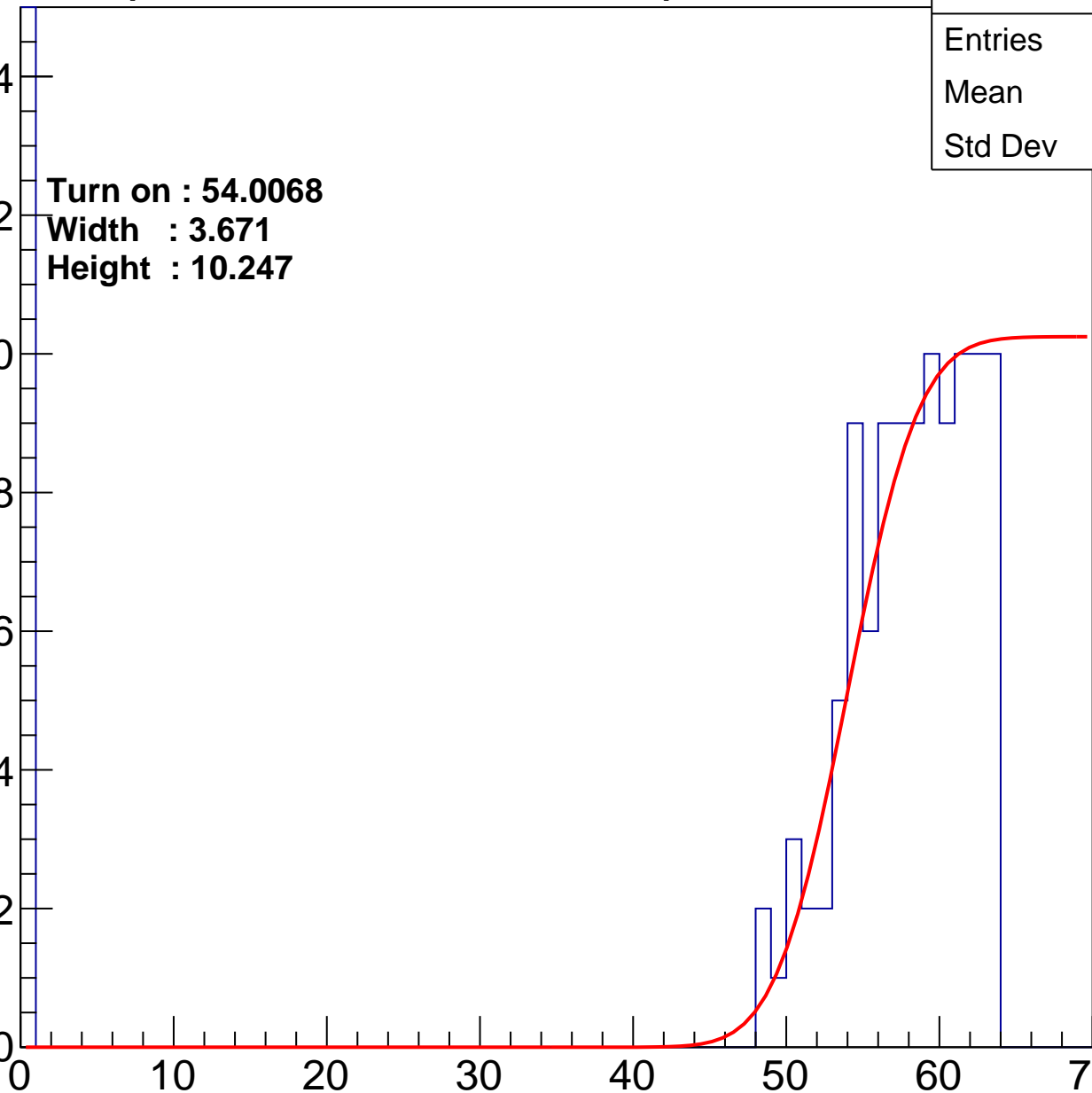
Width : 3.671

Height : 10.247

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	32.6
Std Dev	28.39

Turn on : 53.0686

Width : 3.477

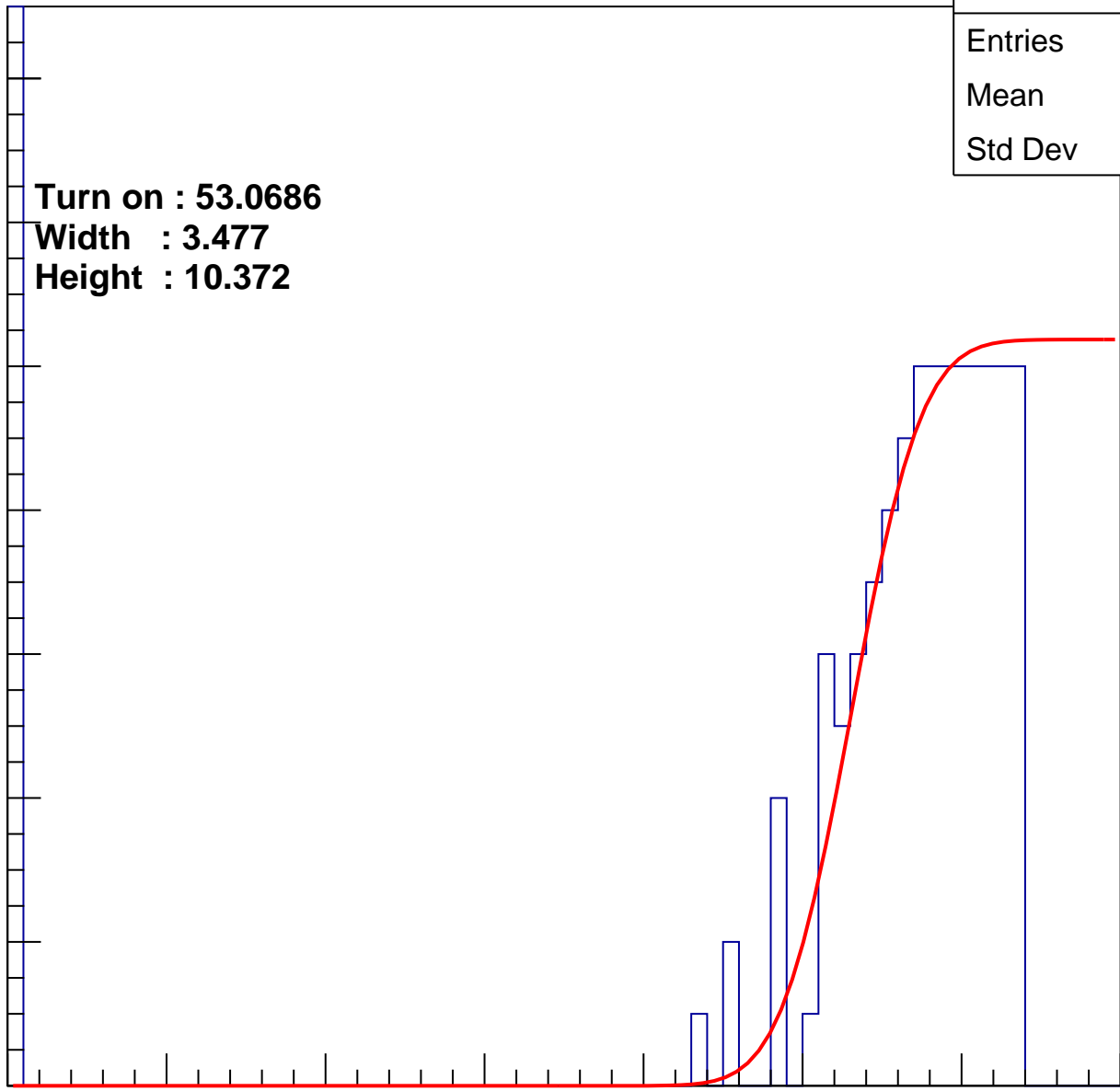
Height : 10.372

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U8-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	39.57
Std Dev	26.51

Turn on : 52.3642

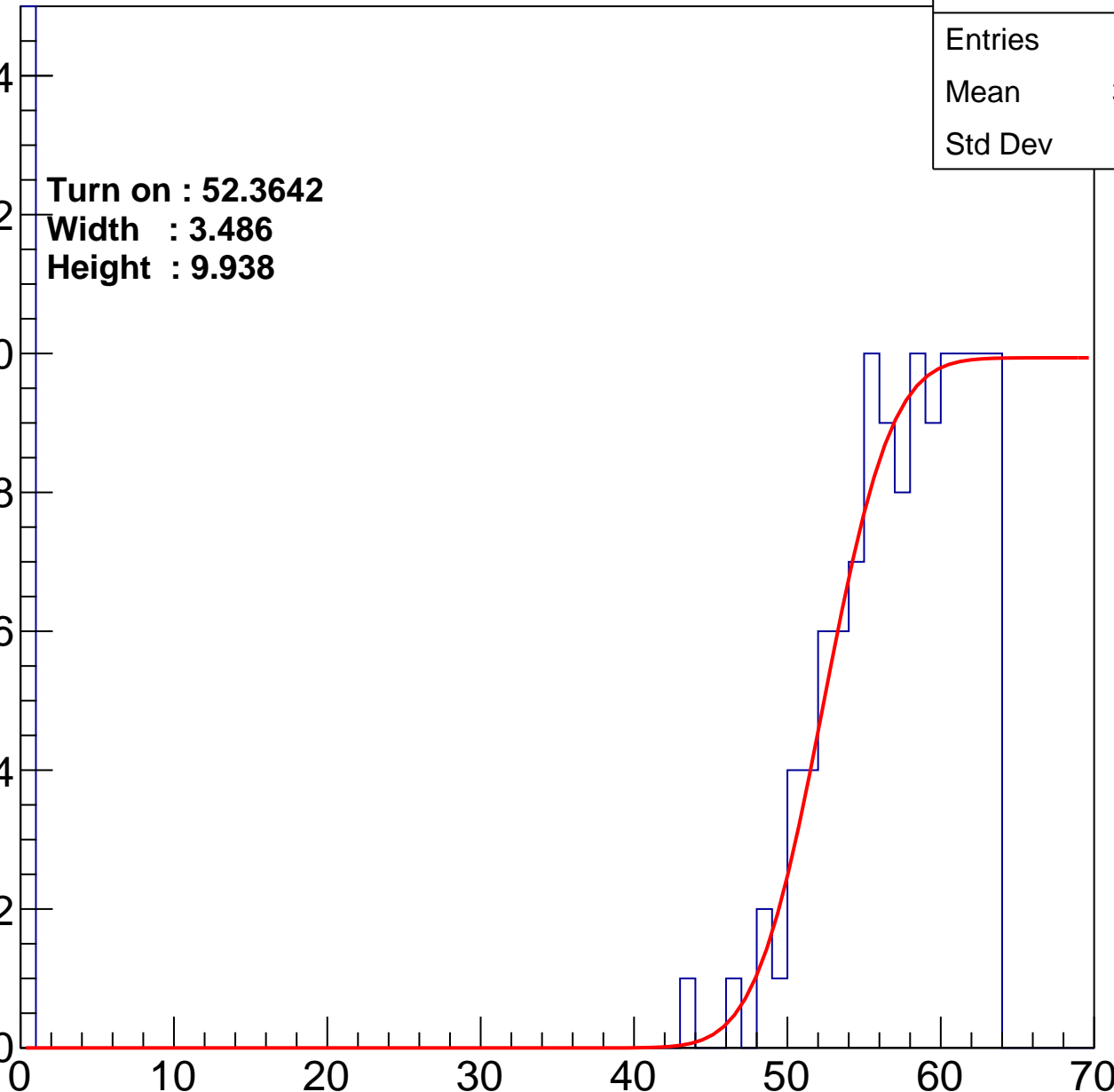
Width : 3.486

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	229
Mean	27.83
Std Dev	28.84

Turn on : 52.8804

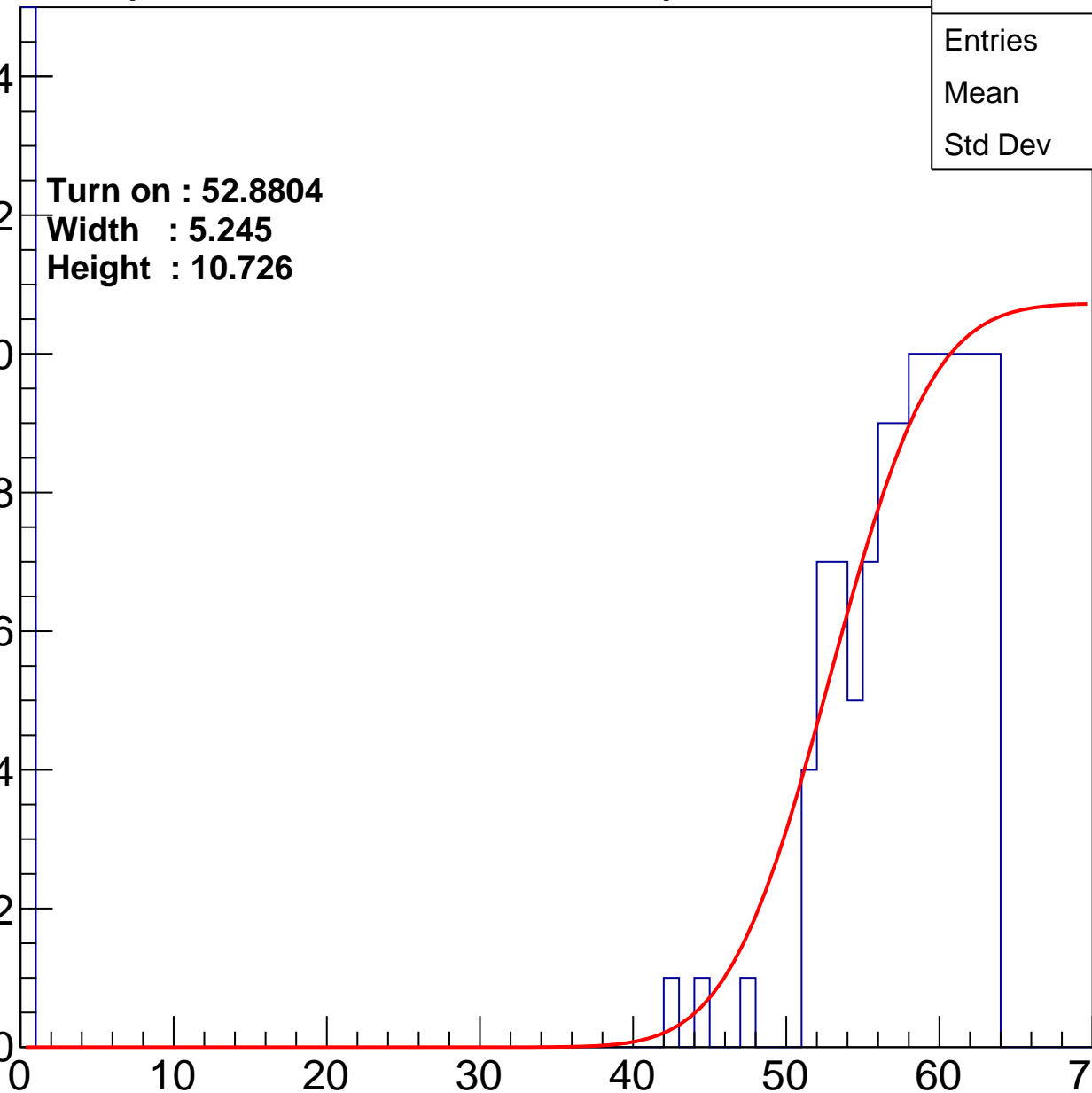
Width : 5.245

Height : 10.726

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	32.26
Std Dev	29.22

Turn on : 55.3674

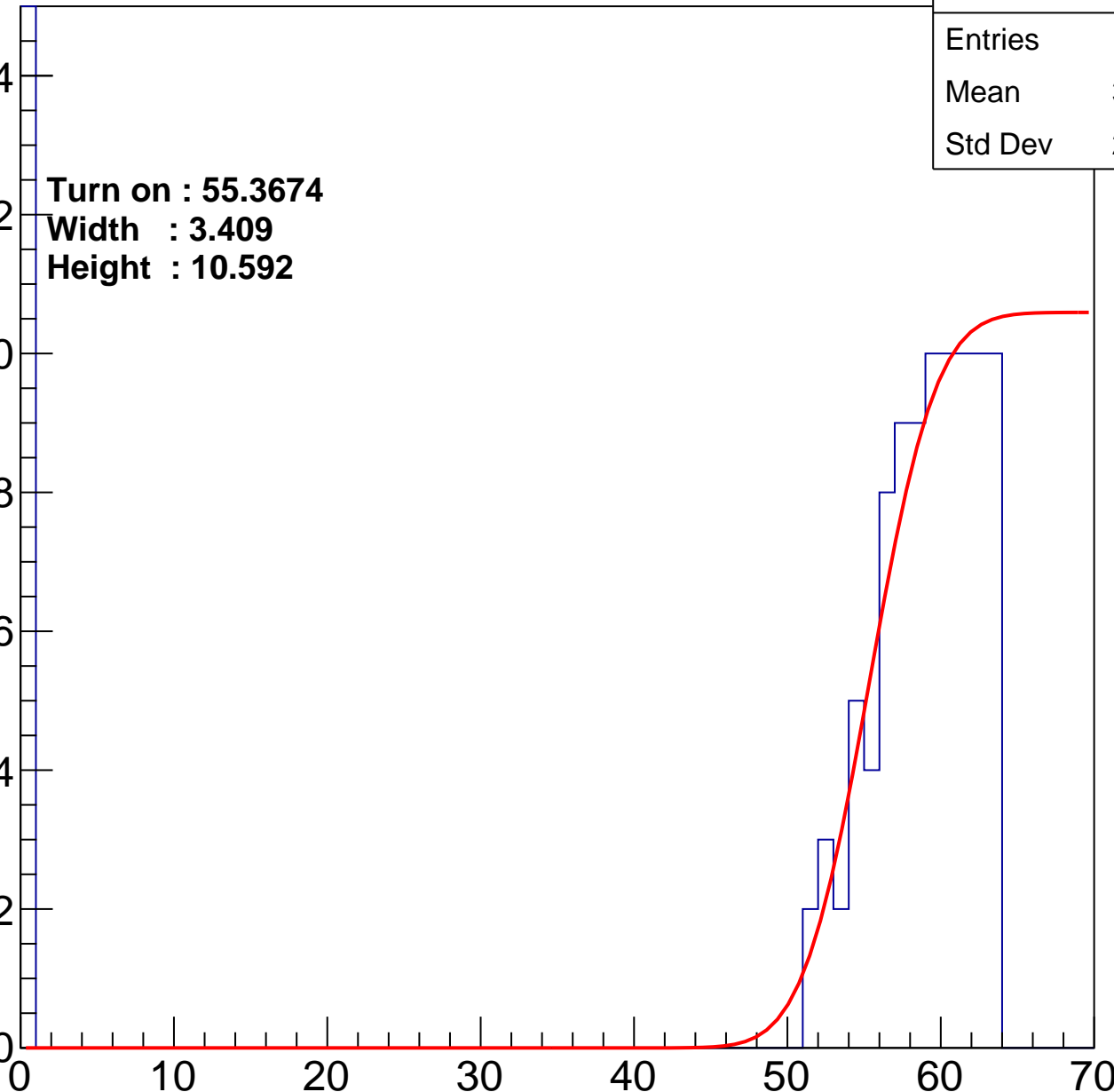
Width : 3.409

Height : 10.592

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	31.66
Std Dev	28.59

Turn on : 53.9099

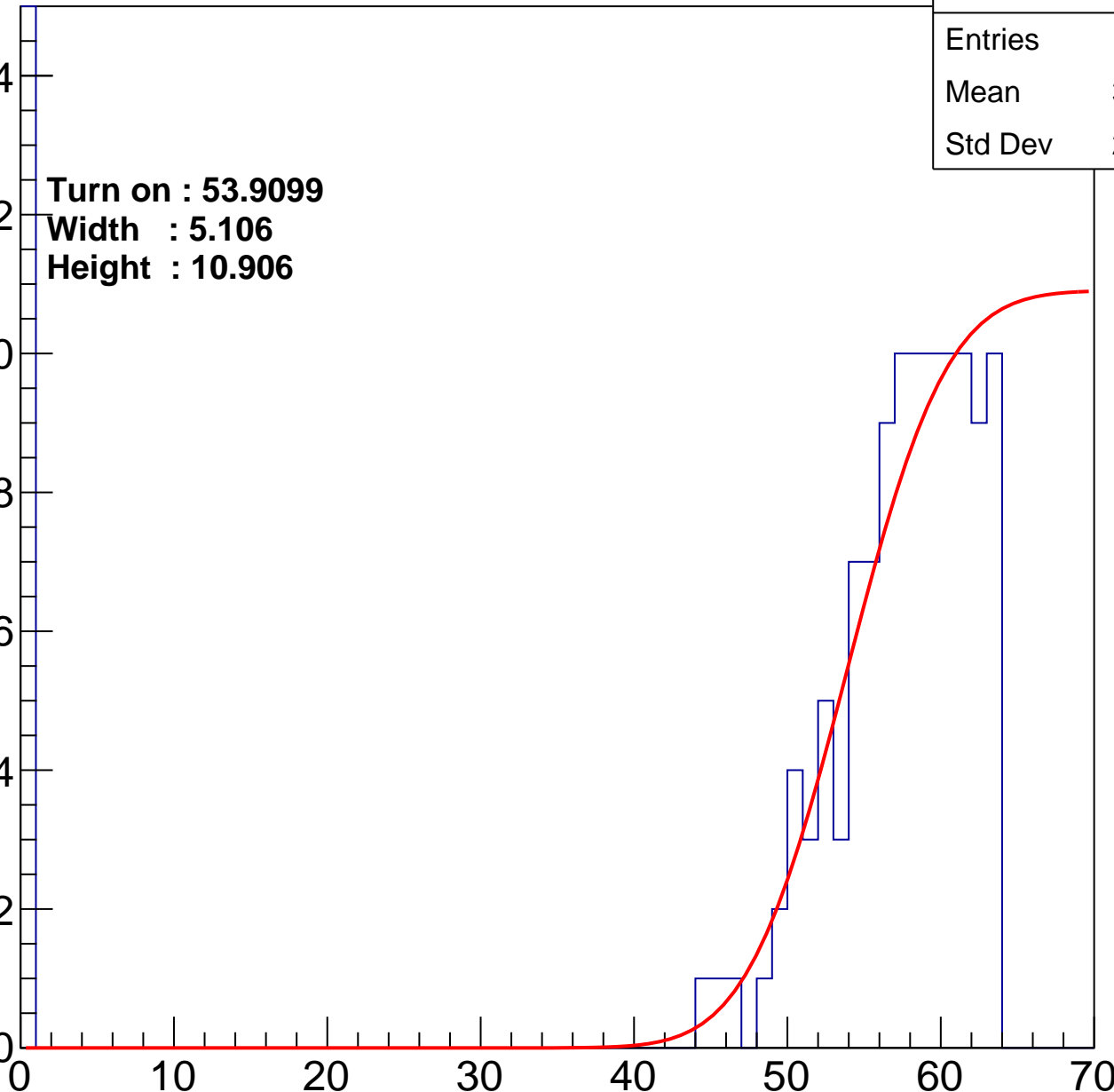
Width : 5.106

Height : 10.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	28.01
Std Dev	29.25

Turn on : 55.1071

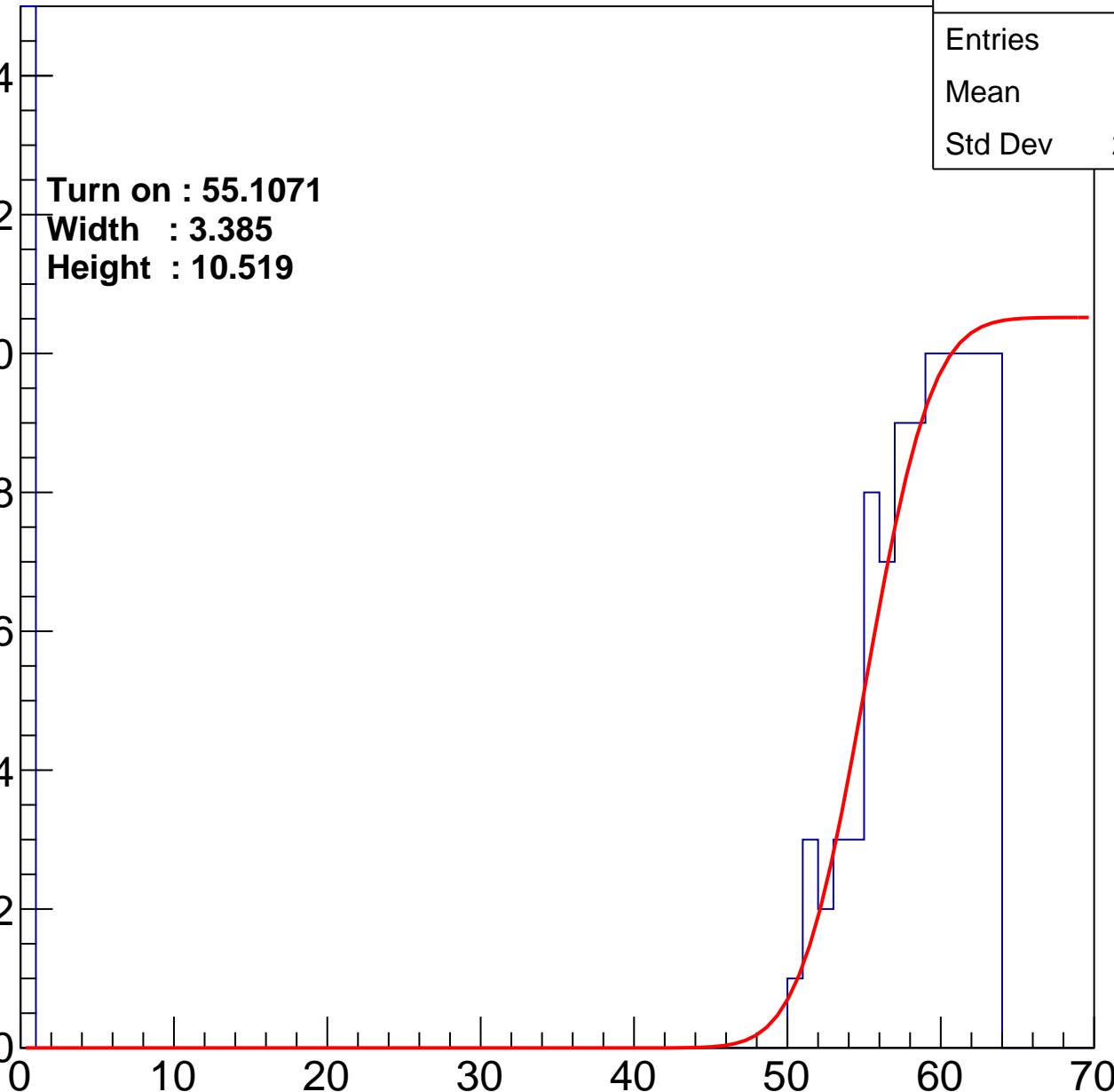
Width : 3.385

Height : 10.519

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch100

calib_packv5_033123_0516.root, FC#4, port A1

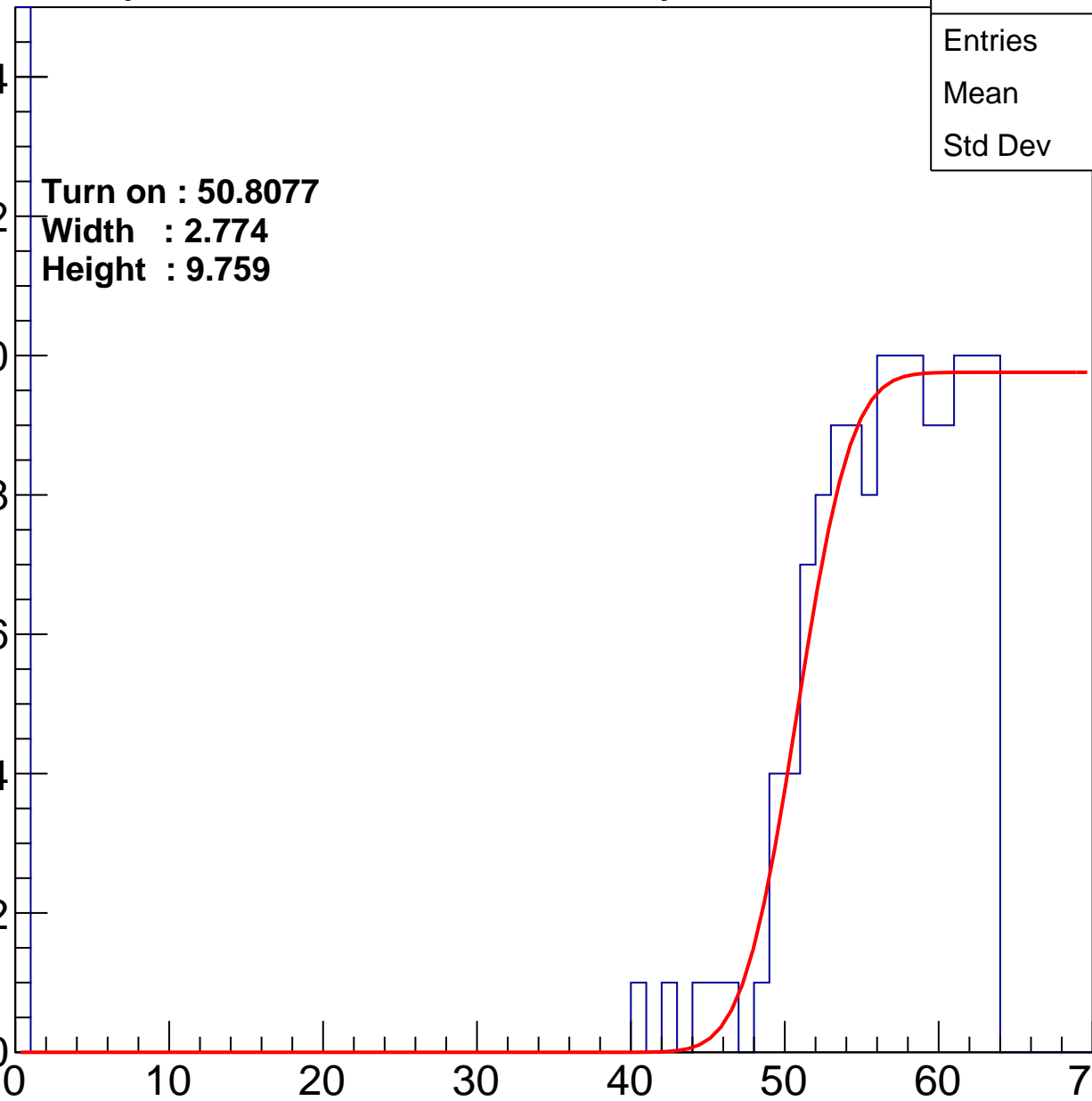
Entry

14
12
10
8
6
4
2
0

Turn on : 50.8077
Width : 2.774
Height : 9.759

Entries	239
Mean	31.29
Std Dev	28.16

ampl



B1L104S, U8-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	225
Mean	29.94
Std Dev	28.67

Turn on : 52.8411

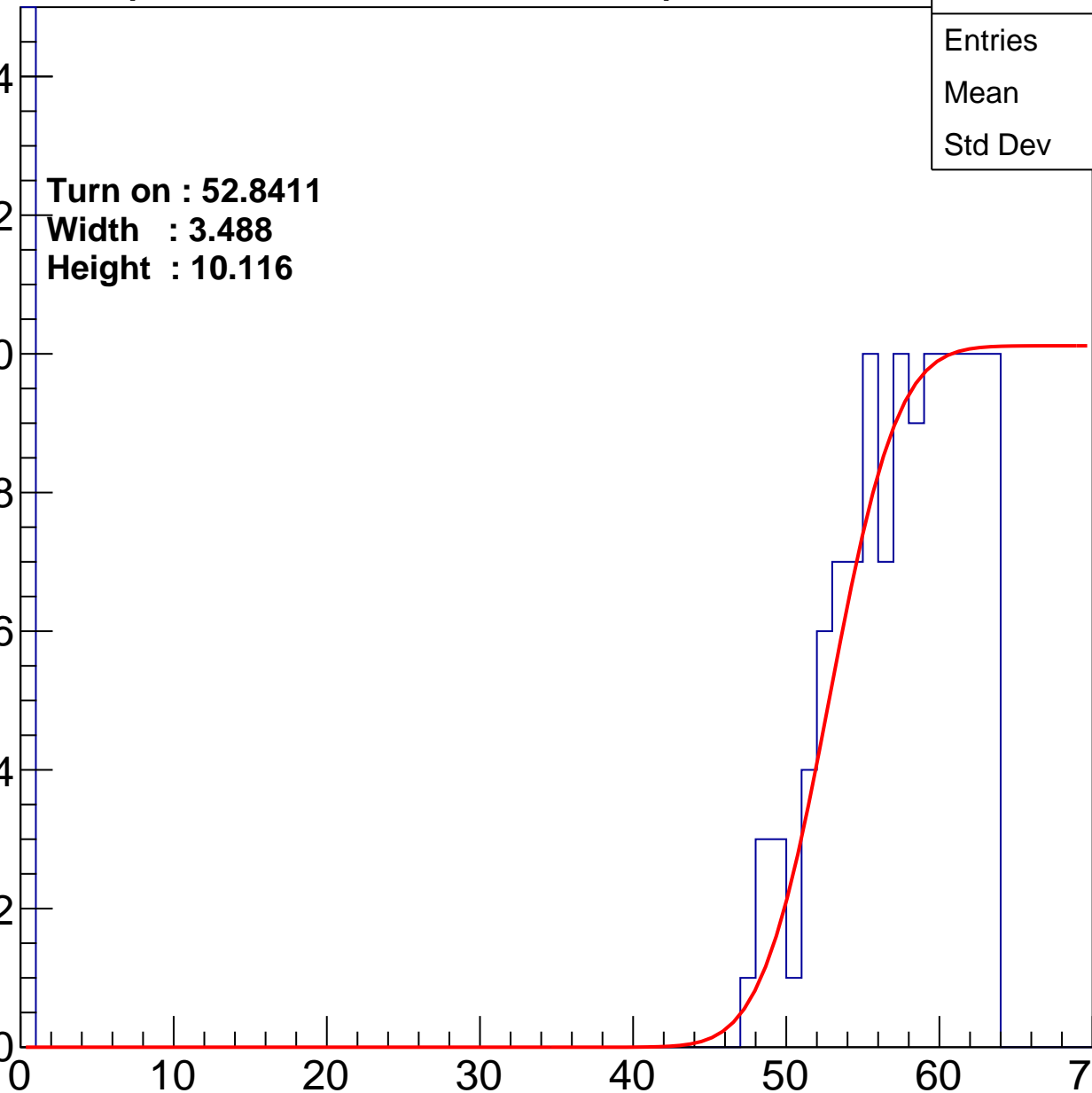
Width : 3.488

Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	33.08
Std Dev	28.01

Turn on : 52.1817

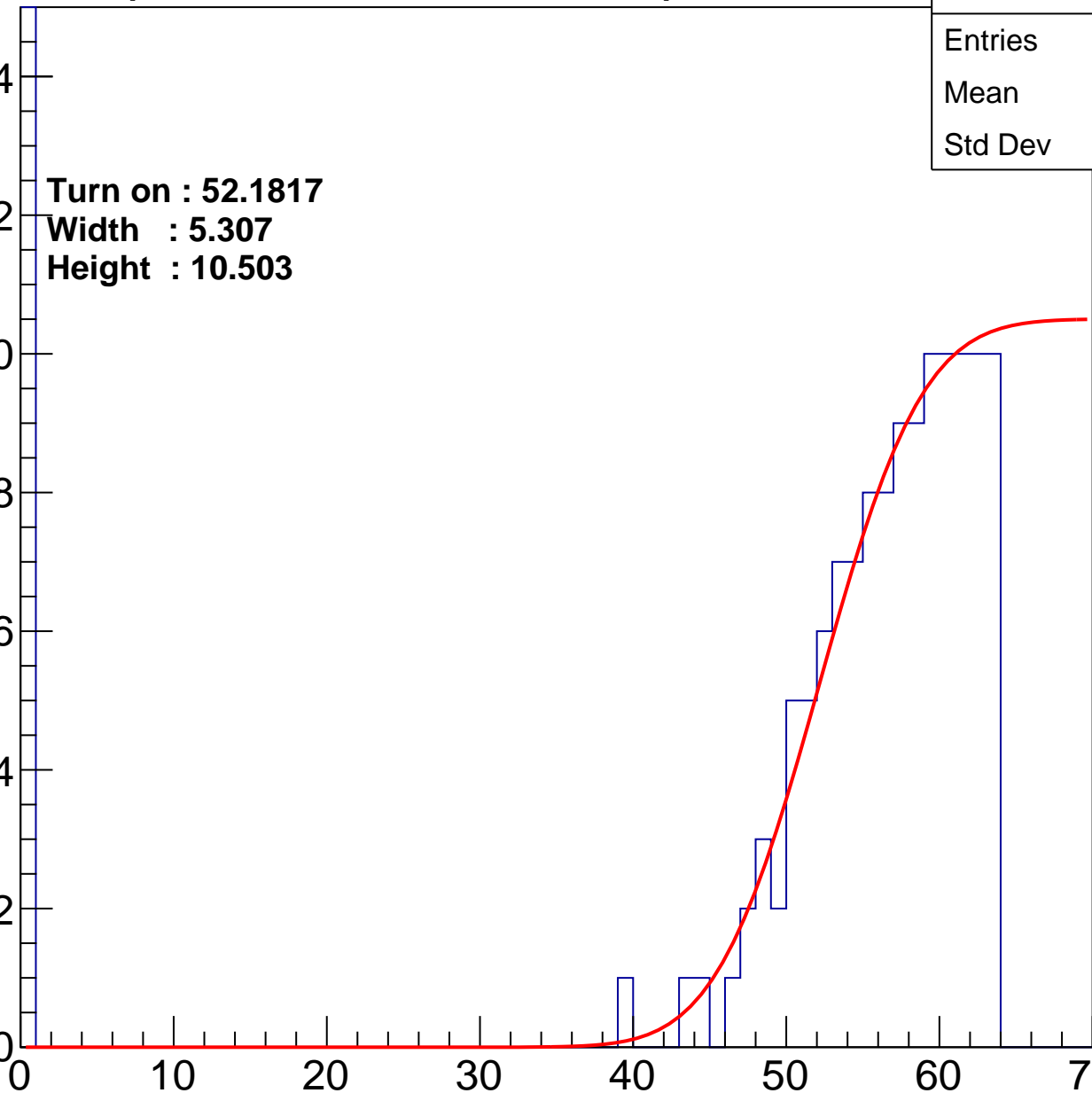
Width : 5.307

Height : 10.503

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	35.52
Std Dev	27.42

Turn on : 51.4155

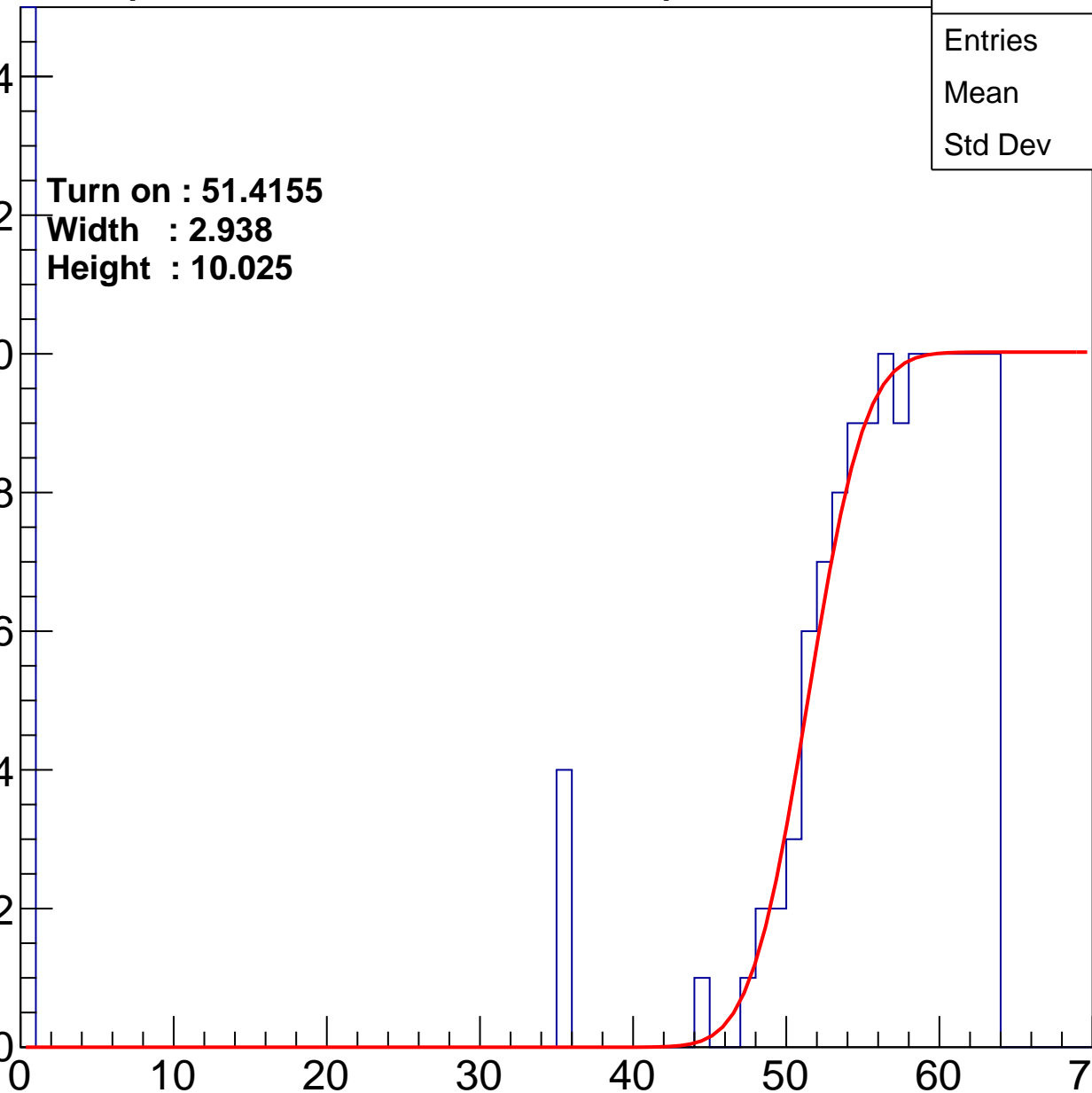
Width : 2.938

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	35.81
Std Dev	27.52

Turn on : 52.2198

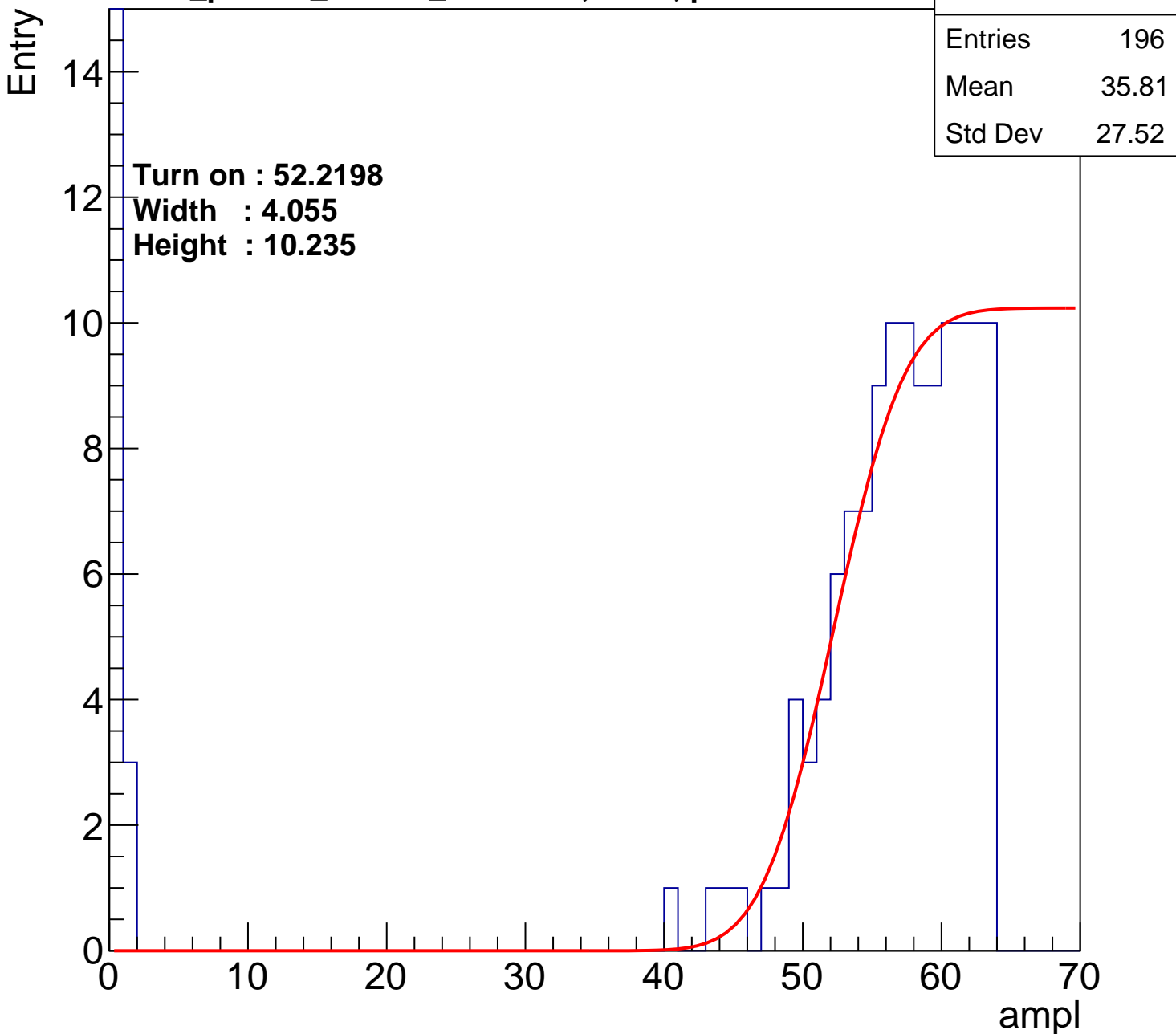
Width : 4.055

Height : 10.235

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	32.94
Std Dev	29.02

Turn on : 55.6894

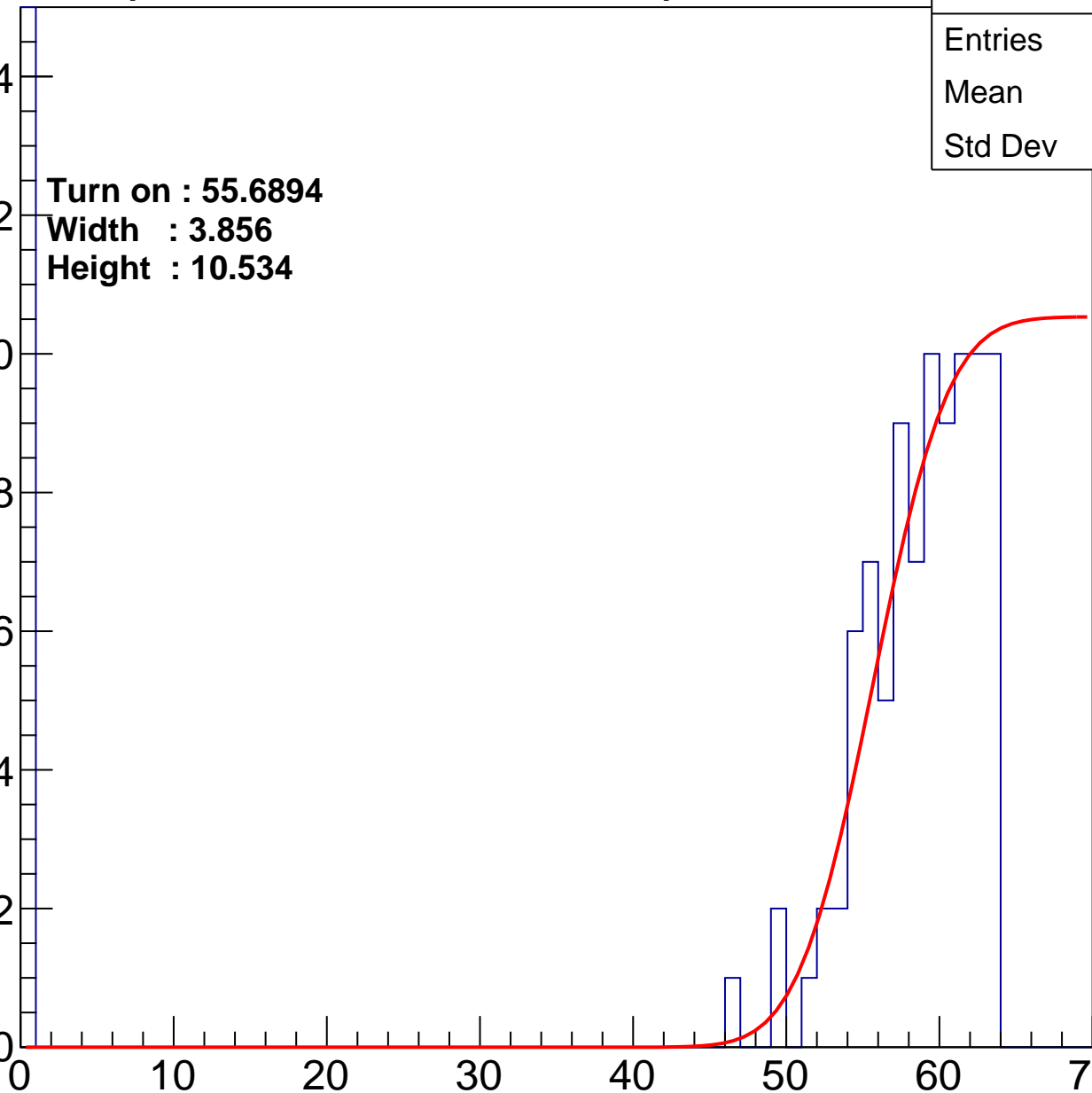
Width : 3.856

Height : 10.534

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	33.99
Std Dev	28.51

Turn on : 53.4652

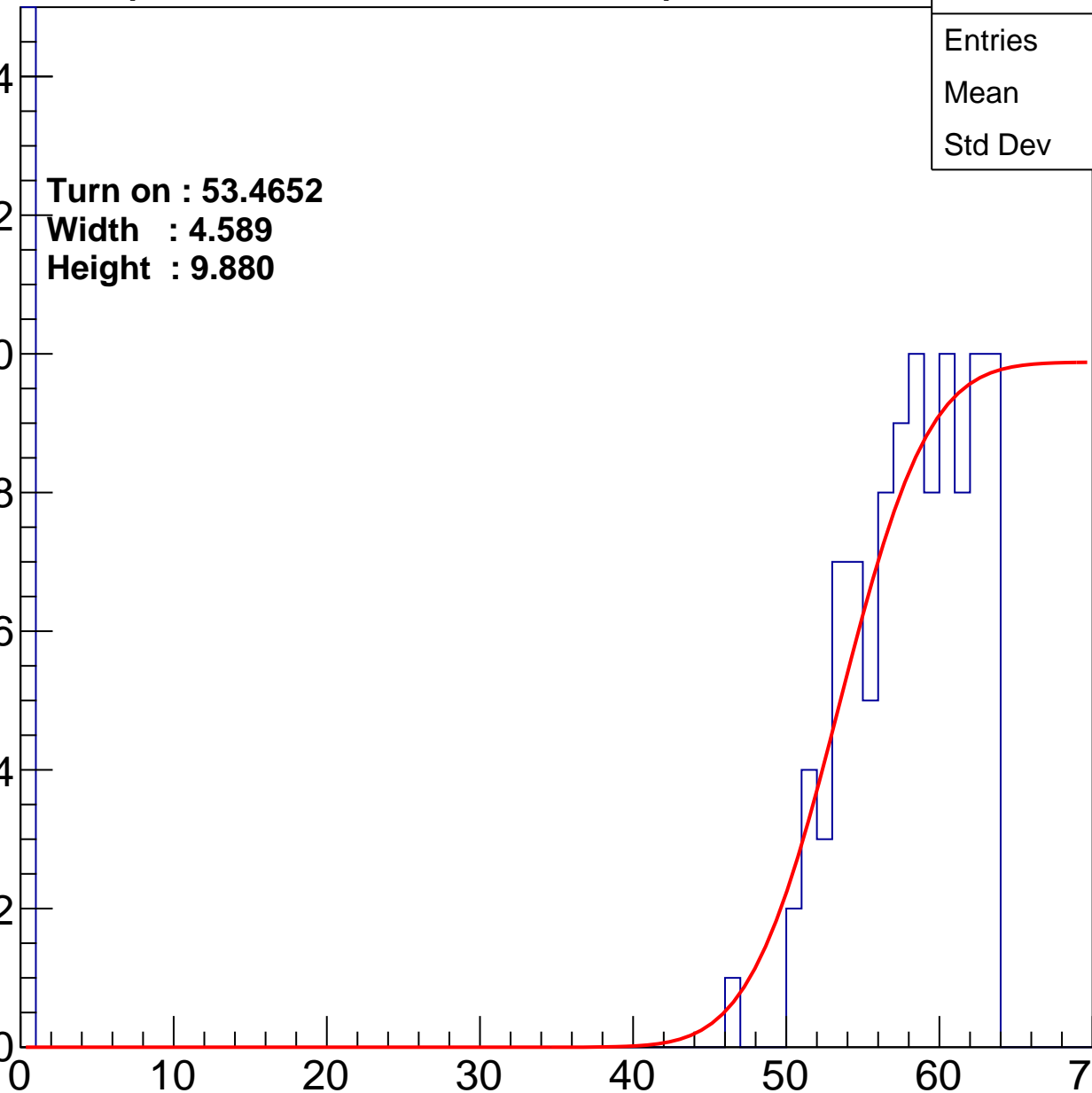
Width : 4.589

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch107

calib_packv5_033123_0516.root, FC#4, port A1

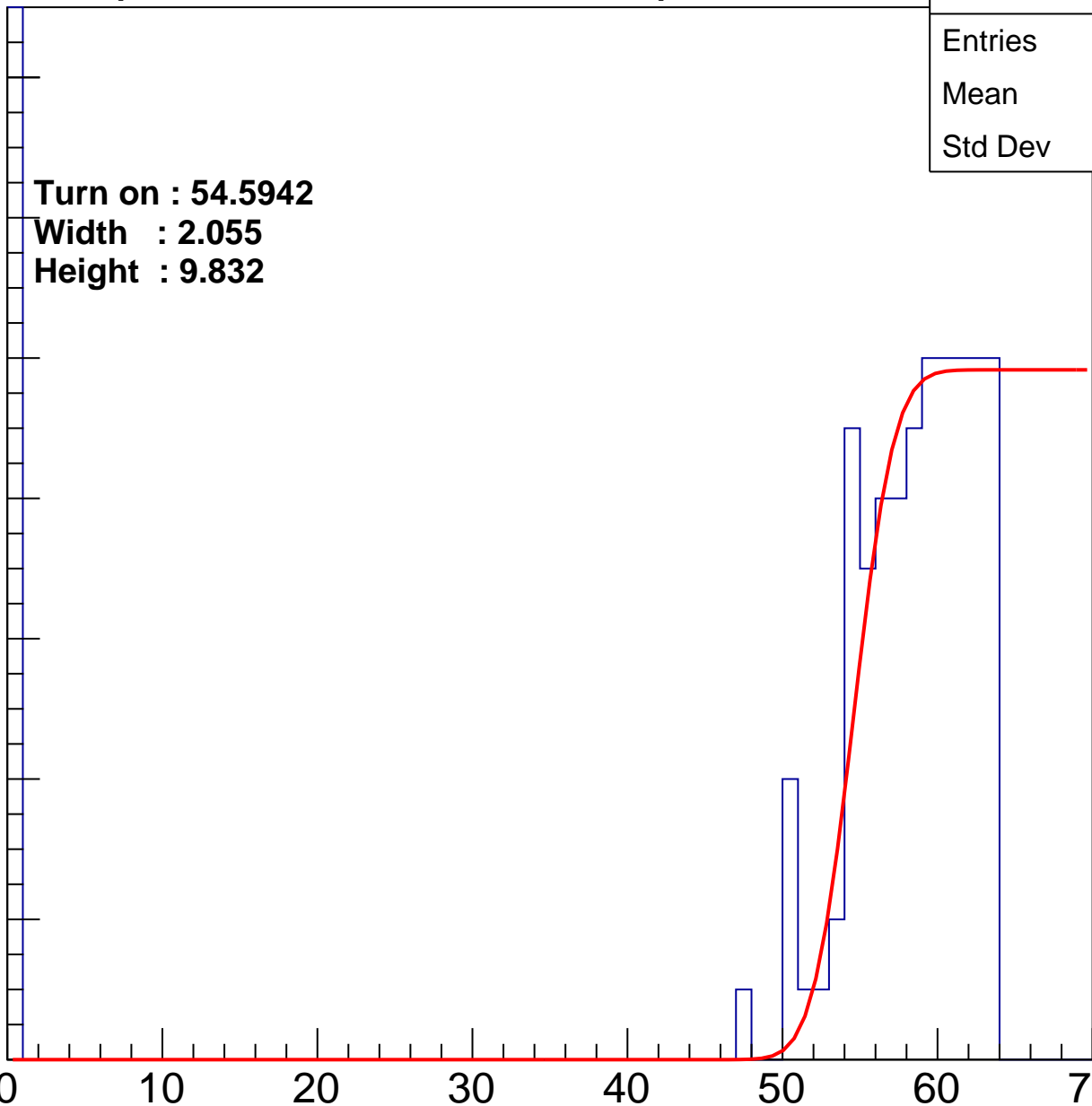
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5942
Width : 2.055
Height : 9.832

Entries	184
Mean	31.54
Std Dev	29.03

ampl



B1L104S, U8-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	30.32
Std Dev	28.76

Turn on : 54.6751

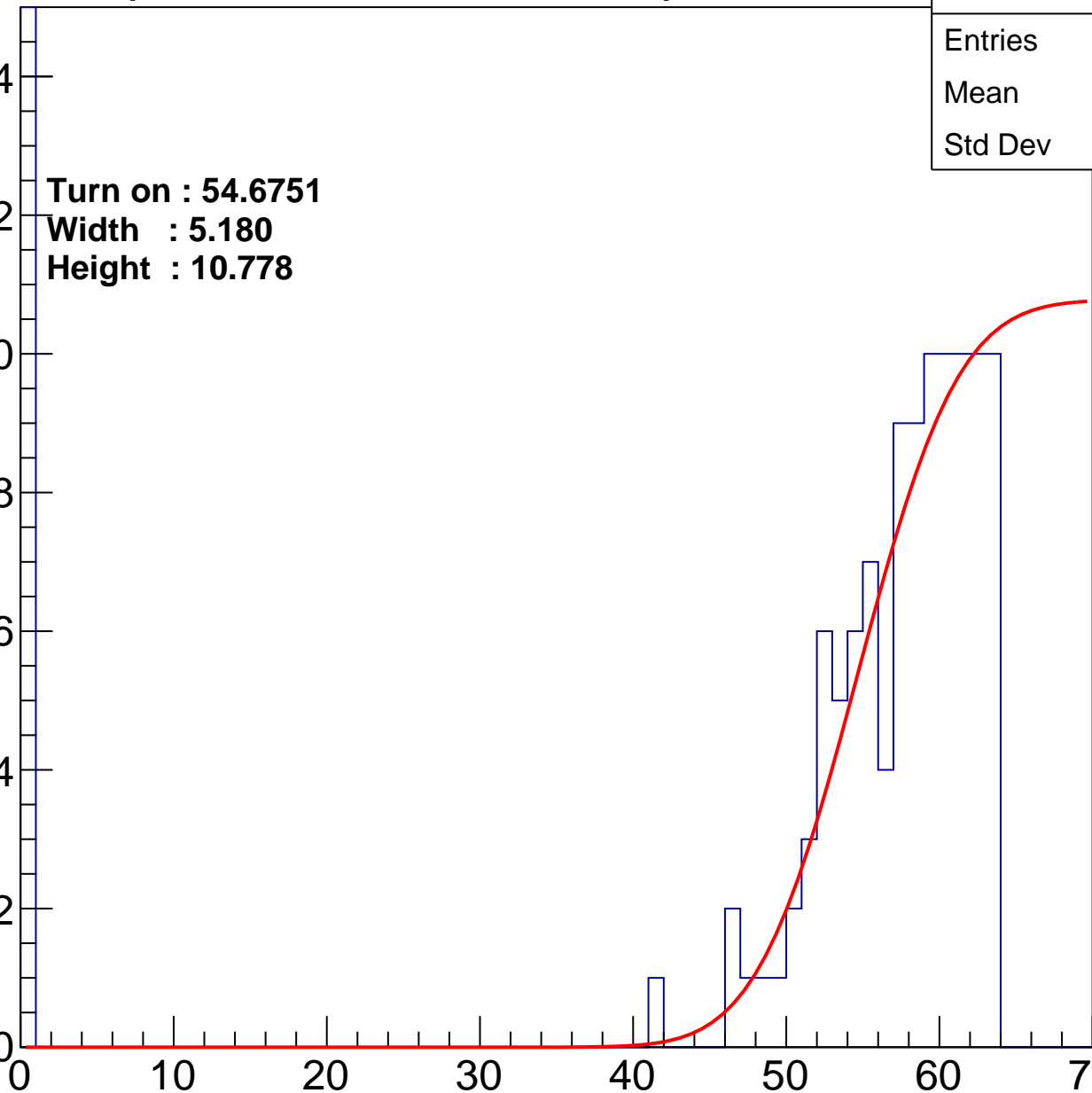
Width : 5.180

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch109

calib_packv5_033123_0516.root, FC#4, port A1

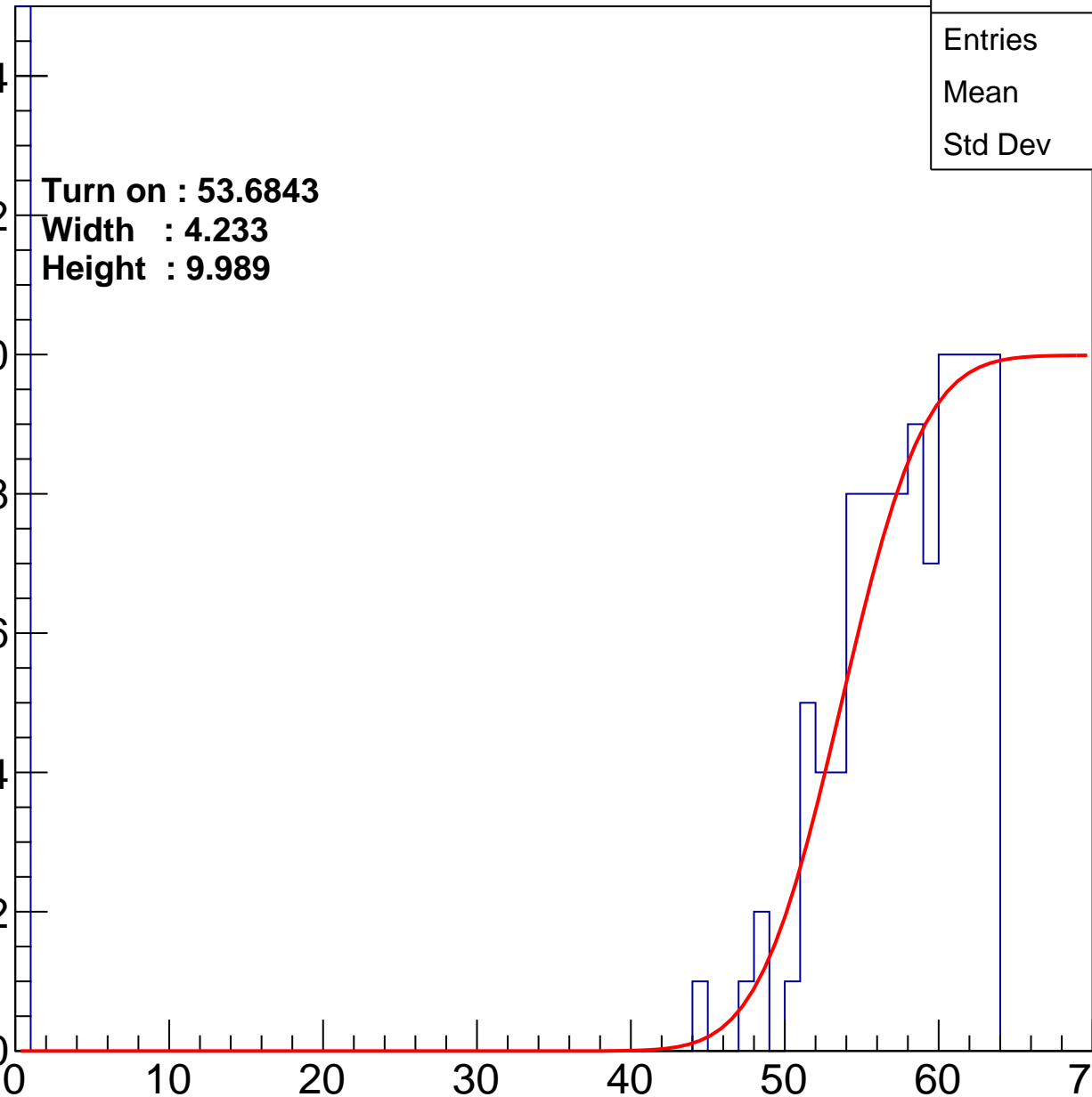
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6843
Width : 4.233
Height : 9.989

Entries	193
Mean	31.52
Std Dev	28.72

ampl



B1L104S, U8-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	230
Mean	30.43
Std Dev	28.54

Turn on : 51.5576

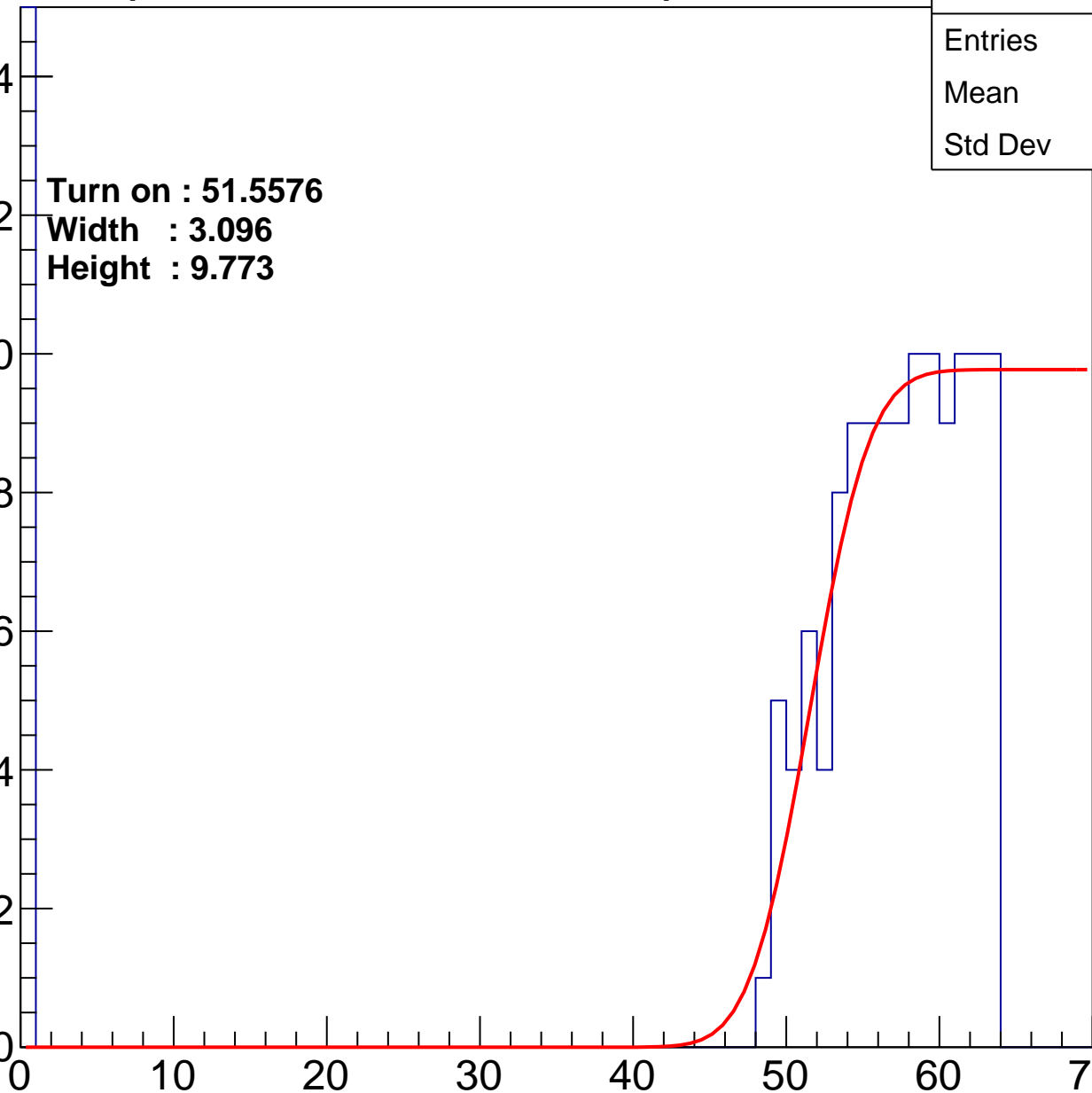
Width : 3.096

Height : 9.773

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	29.68
Std Dev	28.97

Turn on : 53.0423

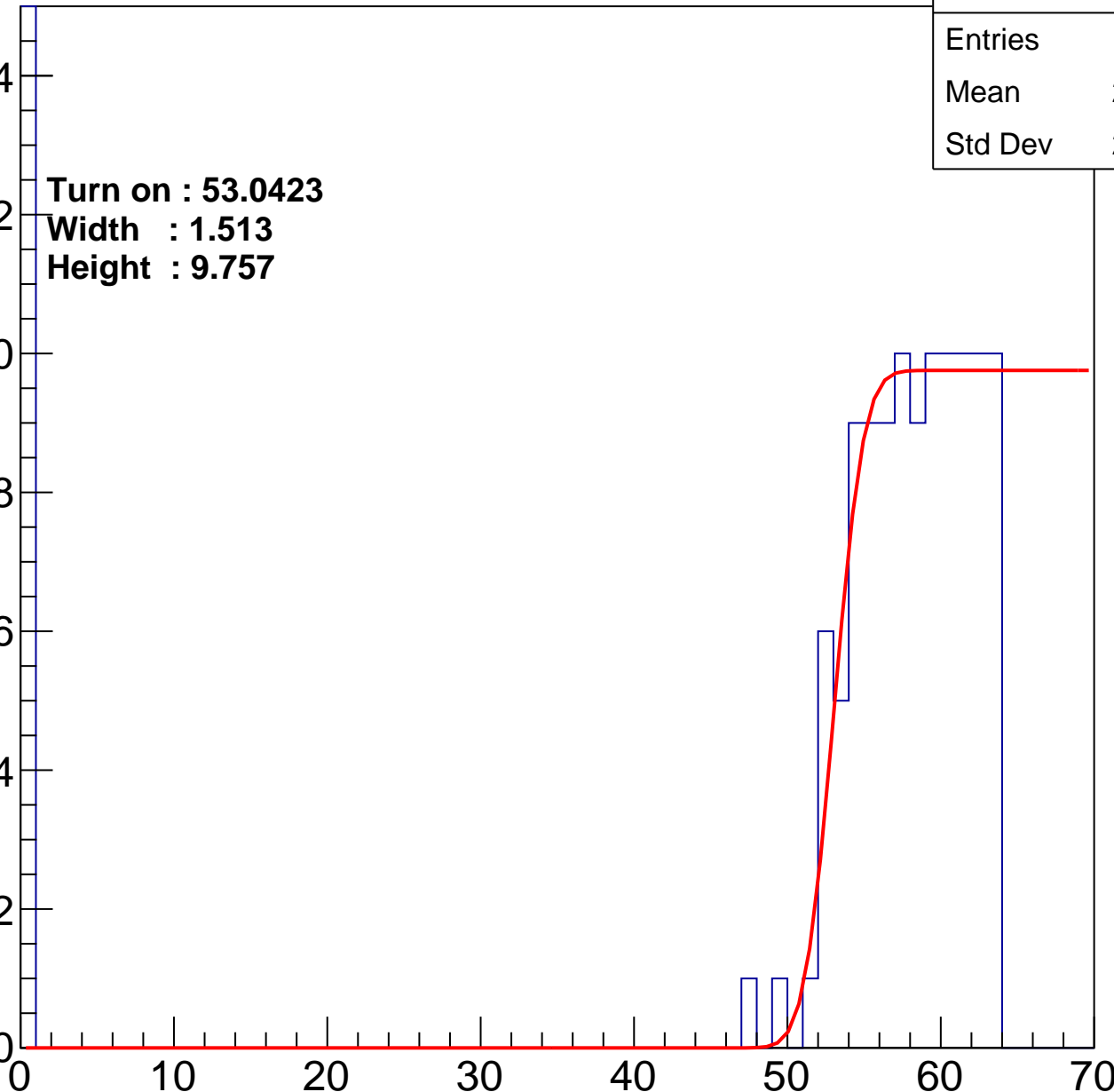
Width : 1.513

Height : 9.757

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	35.75
Std Dev	27.3

Turn on : 51.7245

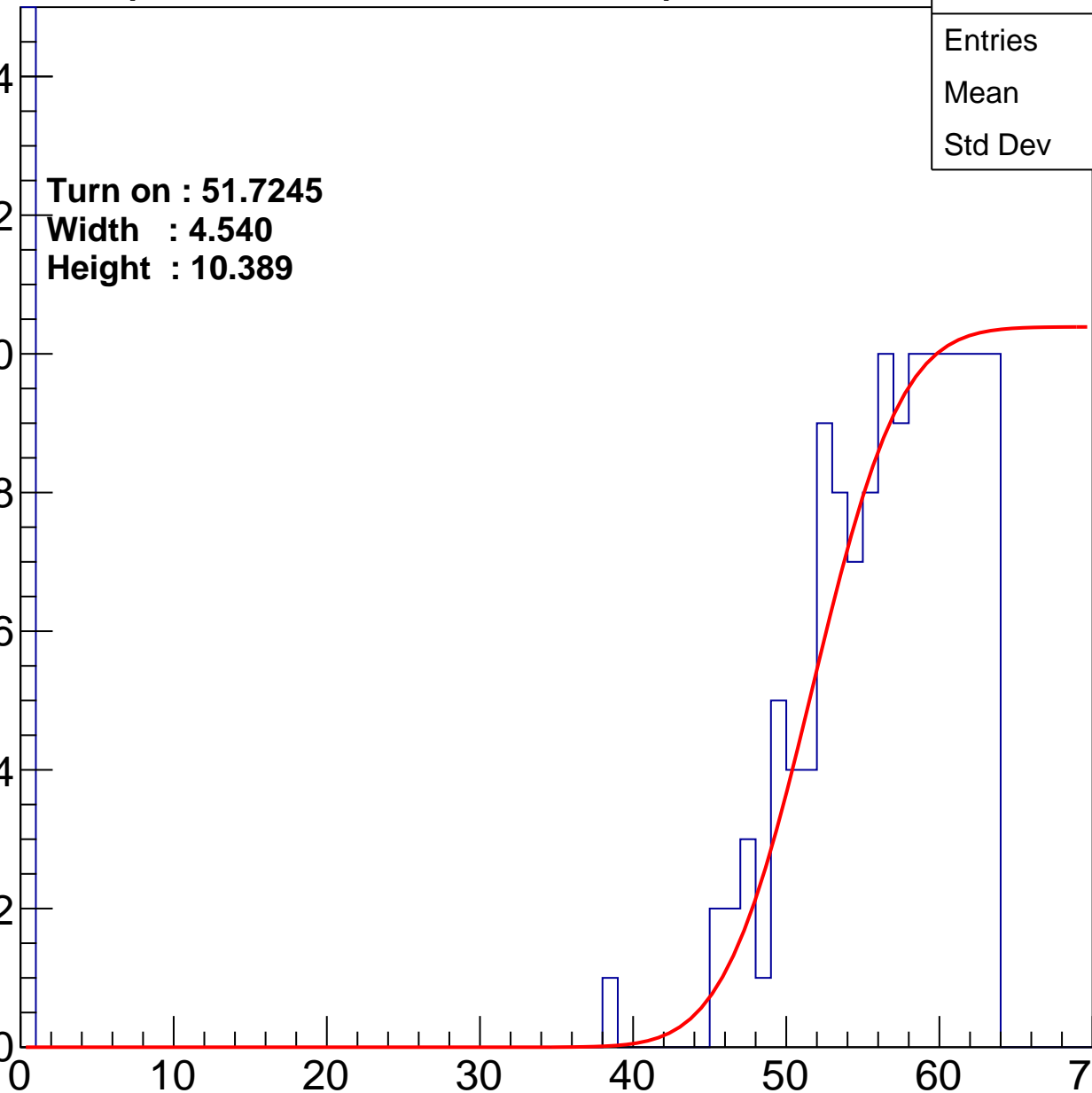
Width : 4.540

Height : 10.389

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	32.64
Std Dev	28.86

Turn on : 54.8335

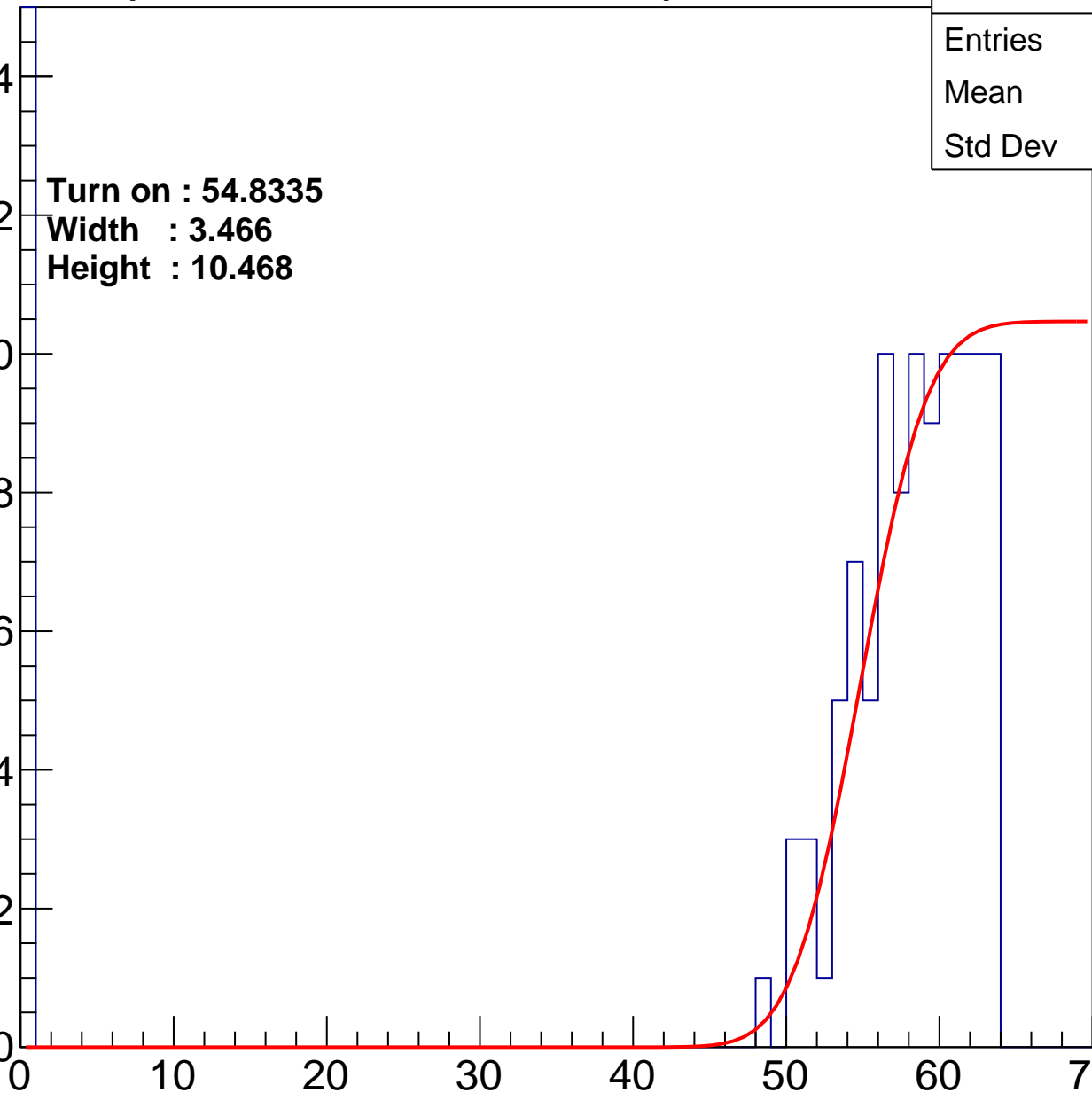
Width : 3.466

Height : 10.468

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch114

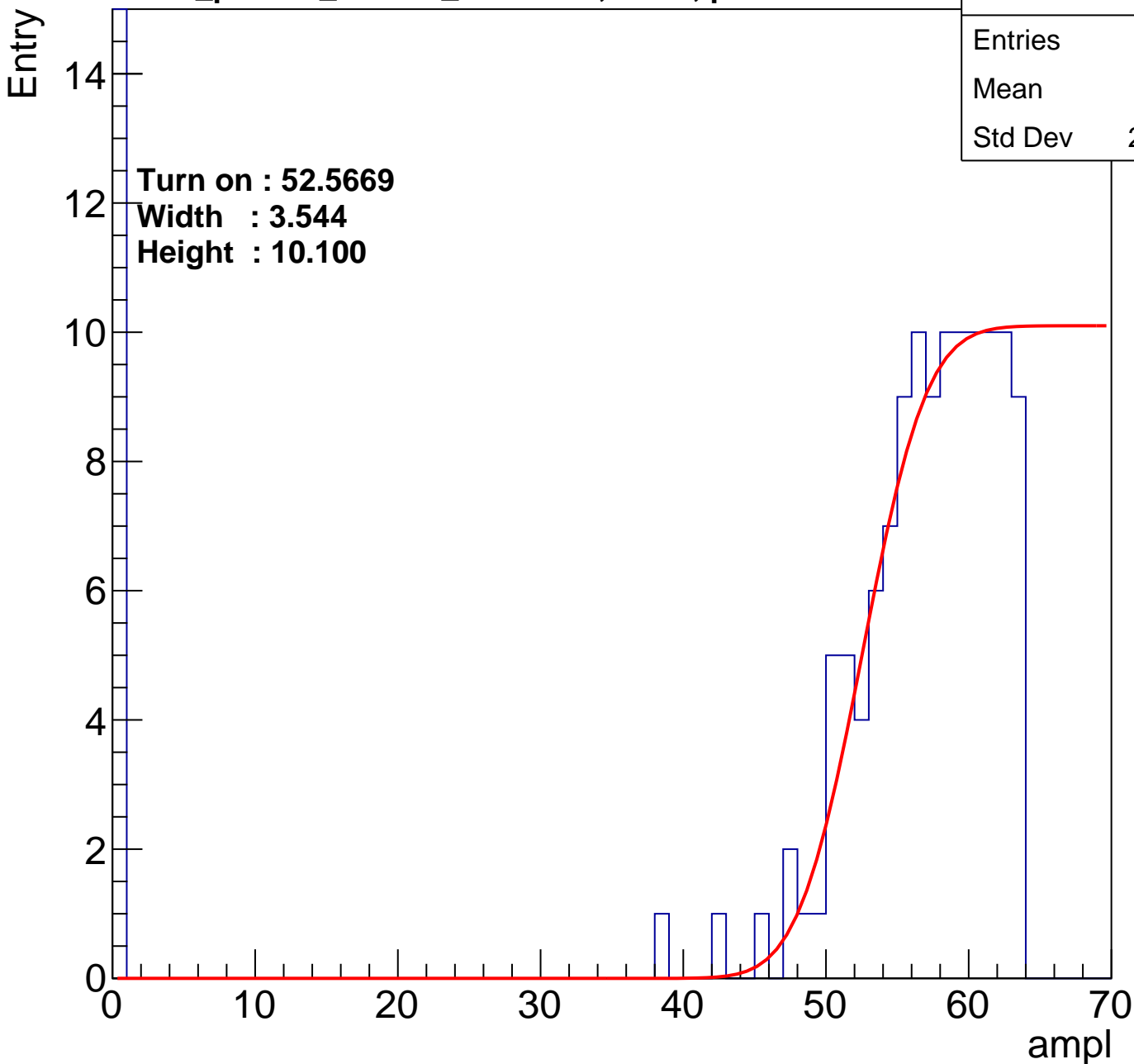
calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	34.3
Std Dev	27.95

Turn on : 52.5669

Width : 3.544

Height : 10.100



B1L104S, U8-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	33.96
Std Dev	28.09

Turn on : 51.5040

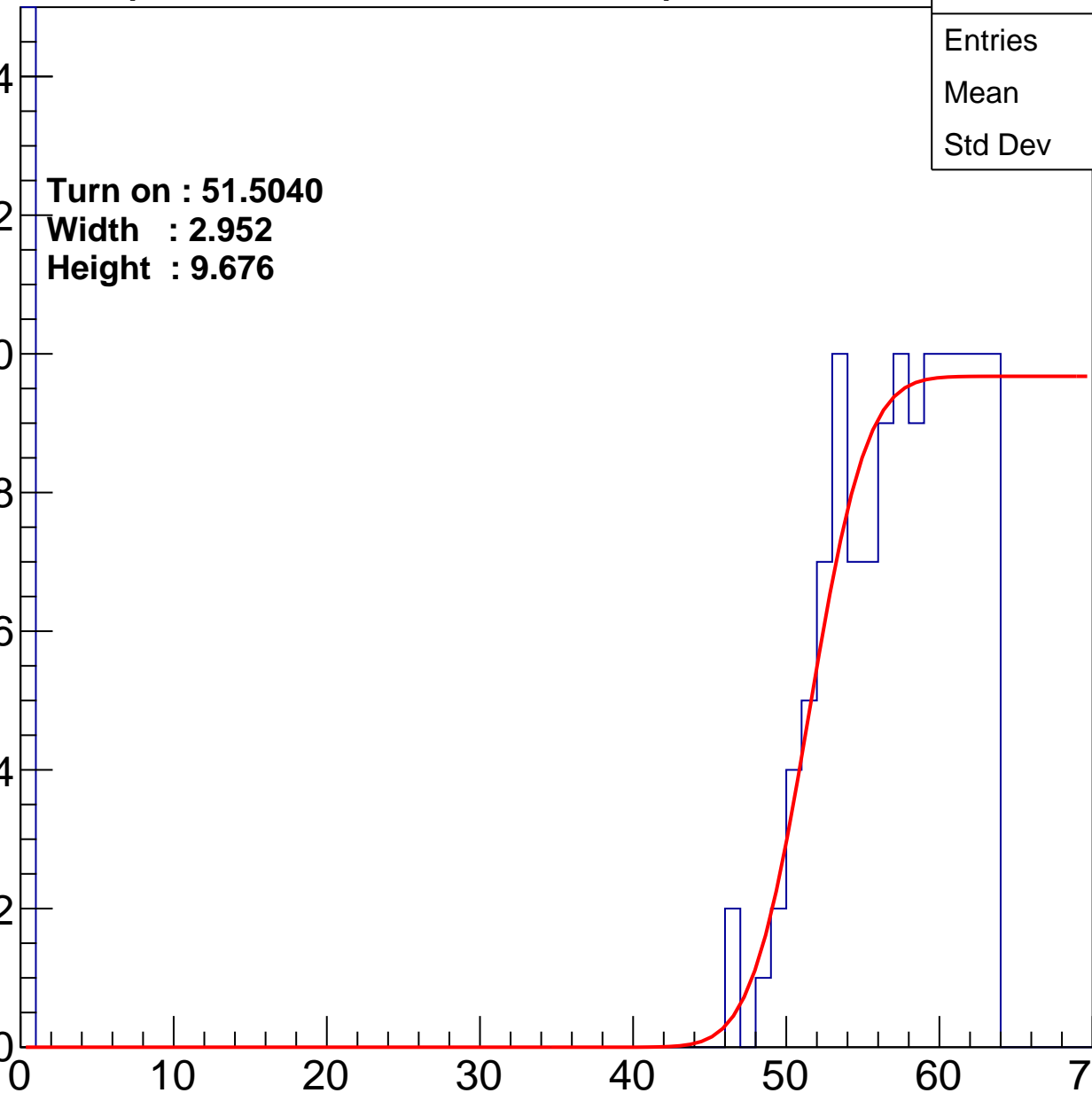
Width : 2.952

Height : 9.676

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	26.8
Std Dev	29.03

Turn on : 54.7814

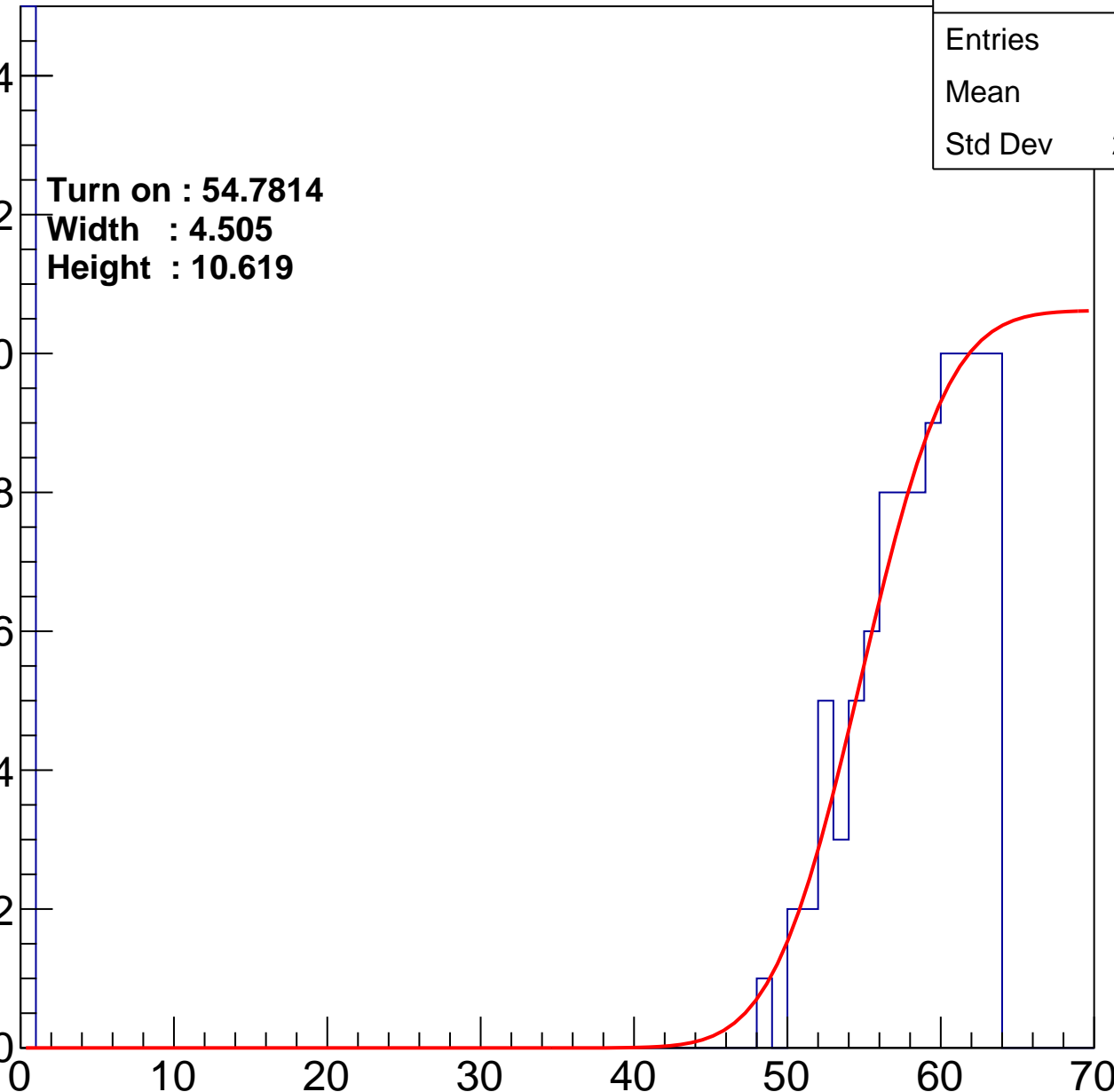
Width : 4.505

Height : 10.619

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch117

calib_packv5_033123_0516.root, FC#4, port A1

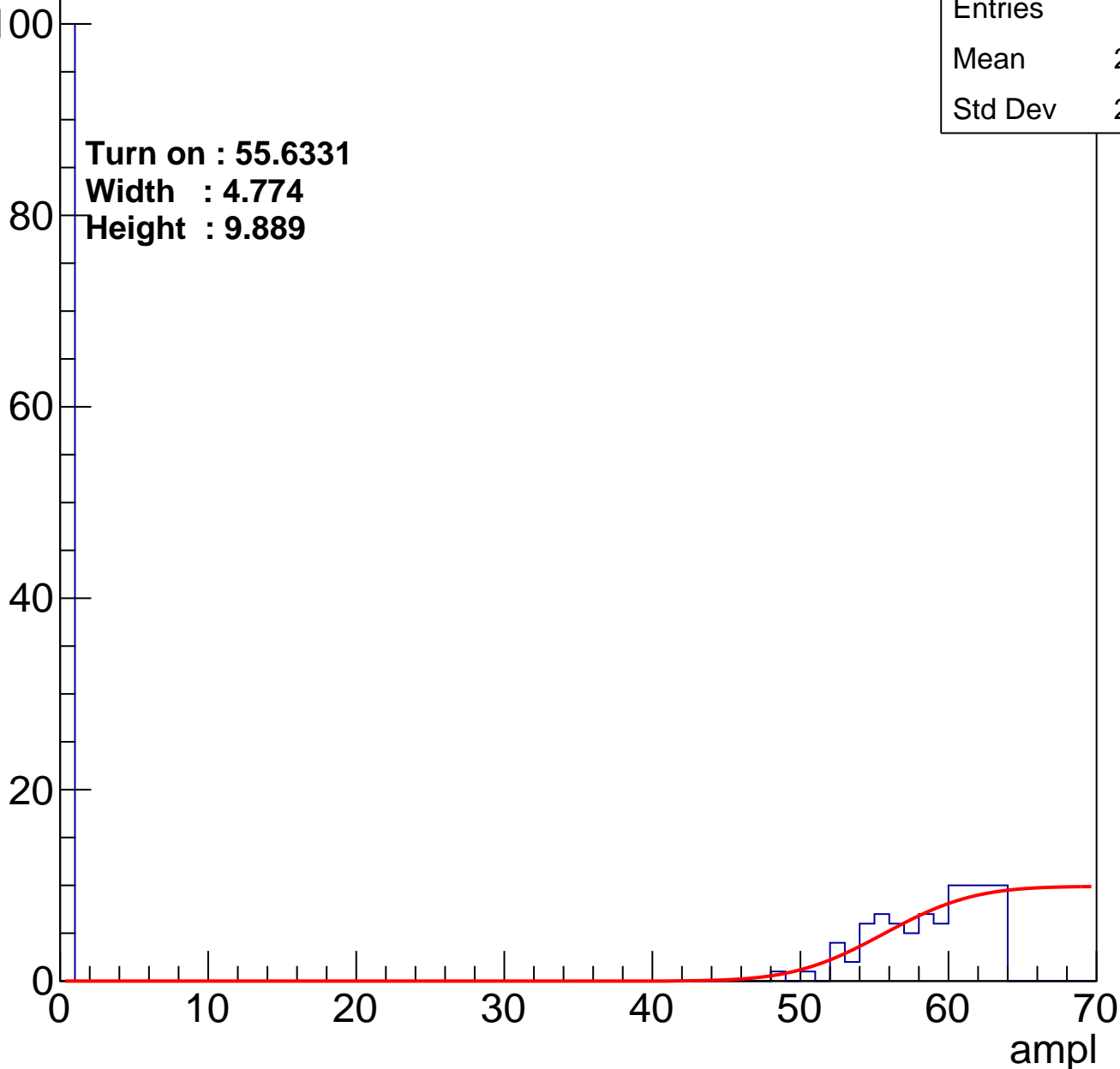
Entry

Entries	185
Mean	26.82
Std Dev	29.19

Turn on : 55.6331

Width : 4.774

Height : 9.889



B1L104S, U8-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	31.24
Std Dev	28.53

Turn on : 53.1384

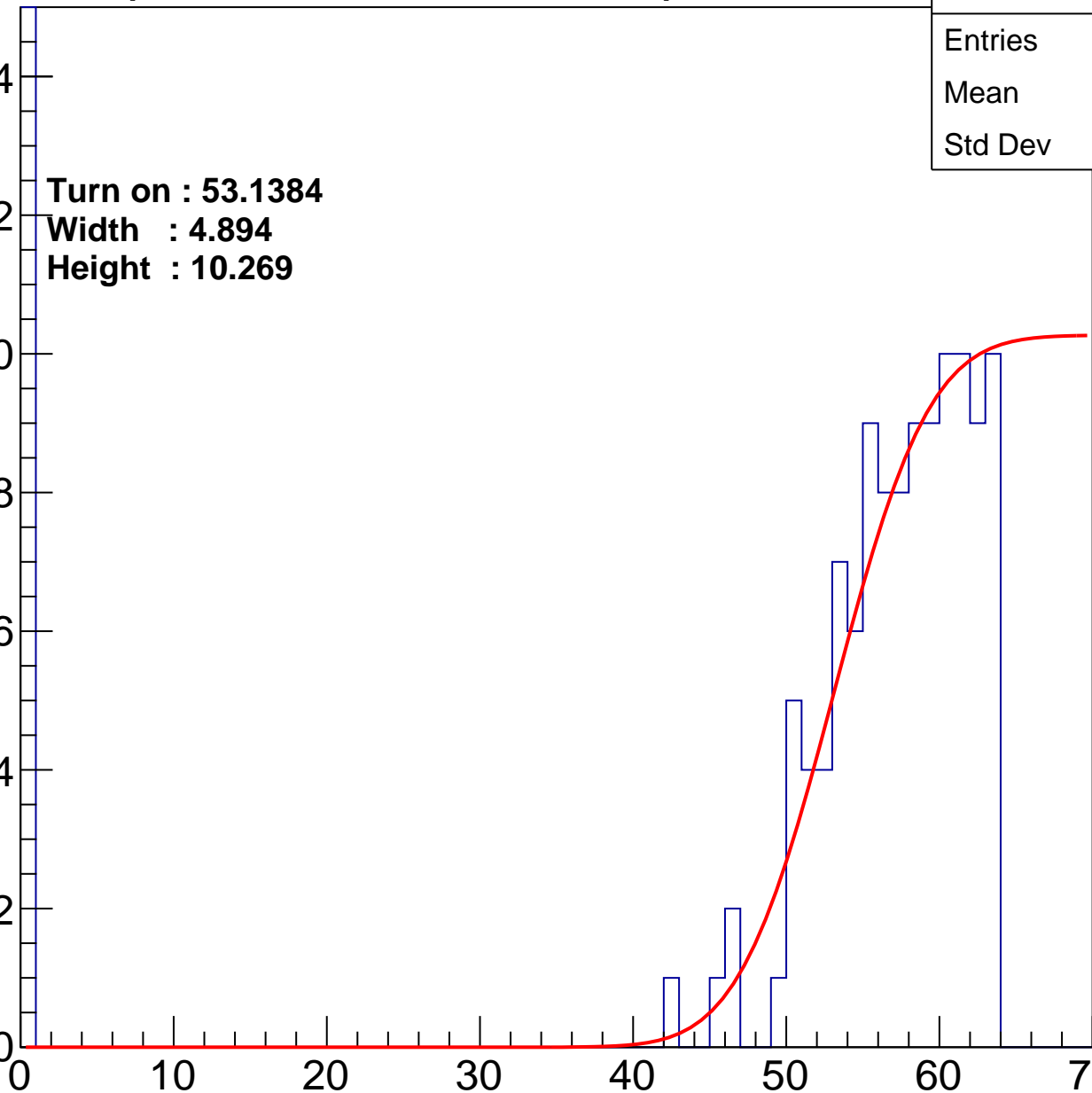
Width : 4.894

Height : 10.269

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	36.27
Std Dev	28.28

Turn on : 54.6359

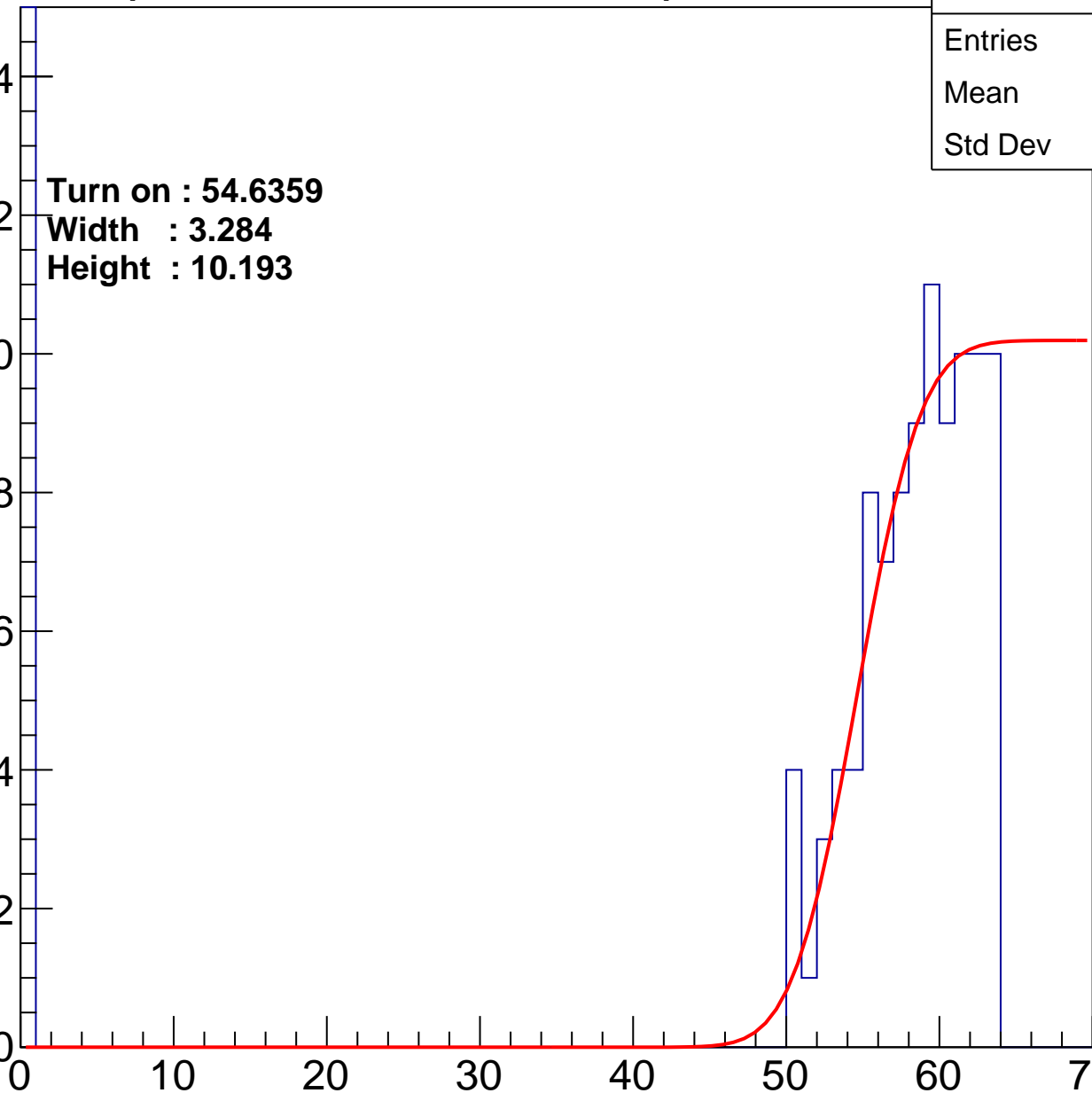
Width : 3.284

Height : 10.193

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	255
Mean	25.27
Std Dev	27.73

Turn on : 53.9773

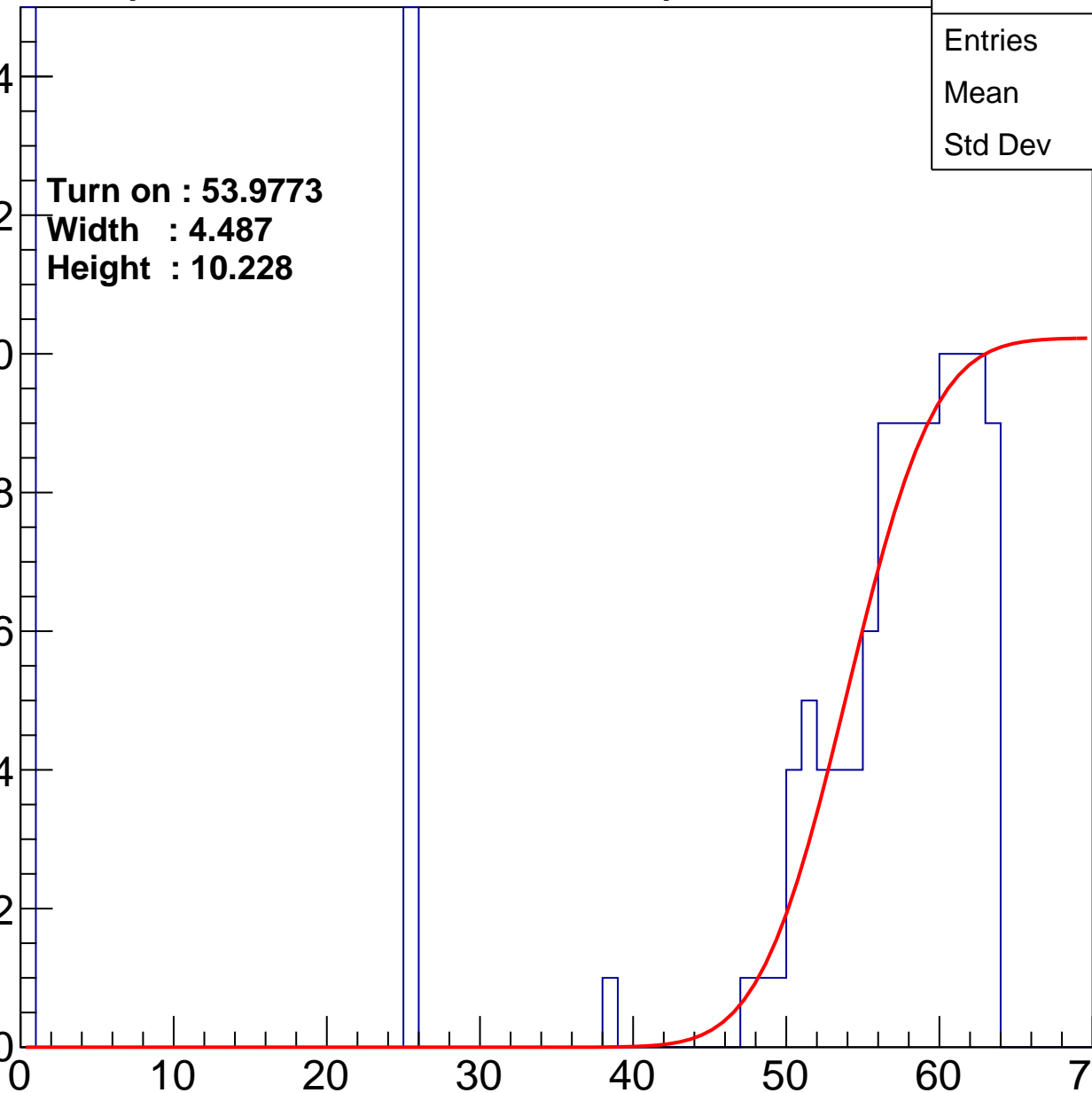
Width : 4.487

Height : 10.228

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch121

calib_packv5_033123_0516.root, FC#4, port A1

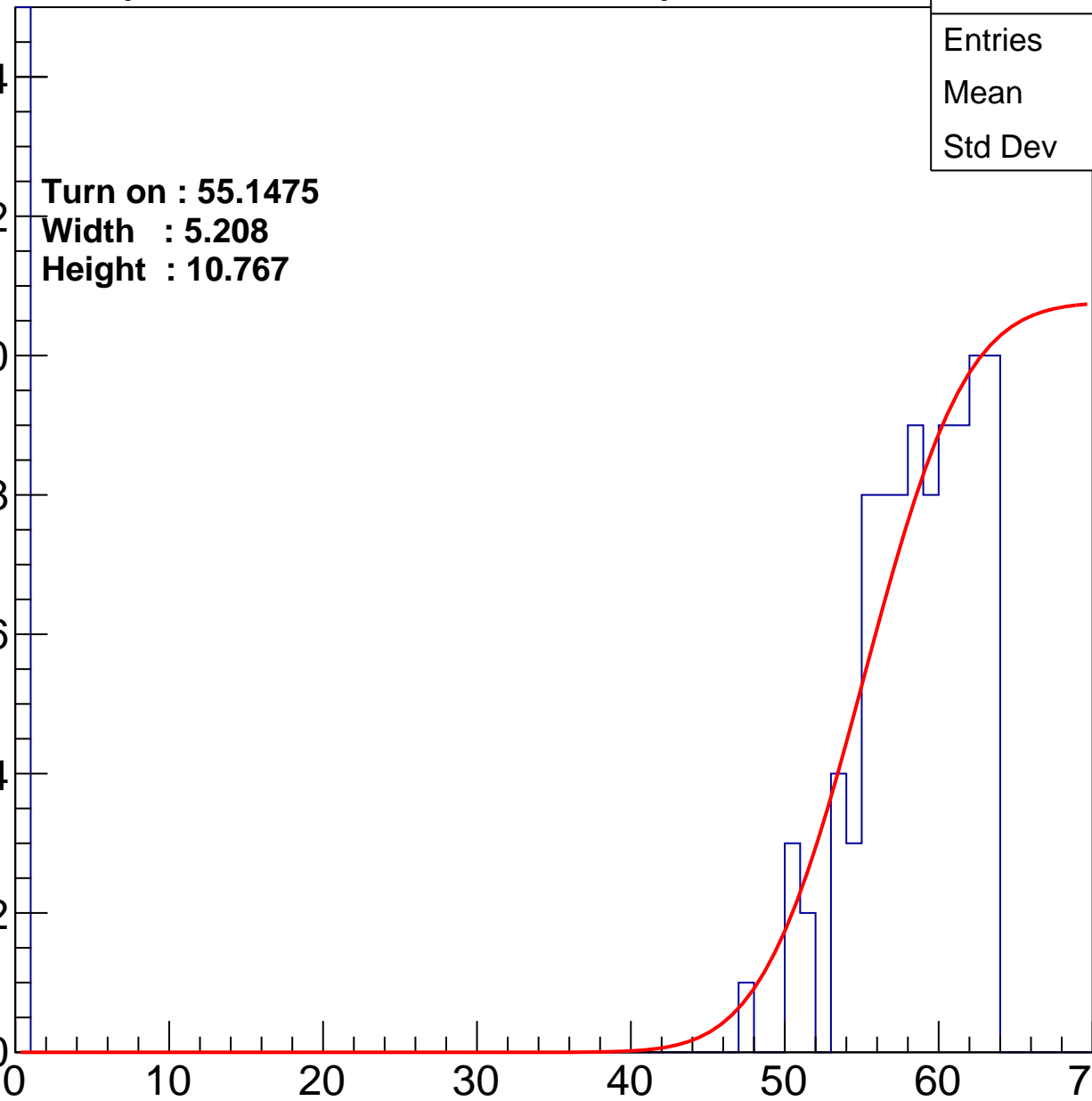
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1475
Width : 5.208
Height : 10.767

Entries	191
Mean	28.01
Std Dev	29.16

ampl



B1L104S, U8-ch122

calib_packv5_033123_0516.root, FC#4, port A1

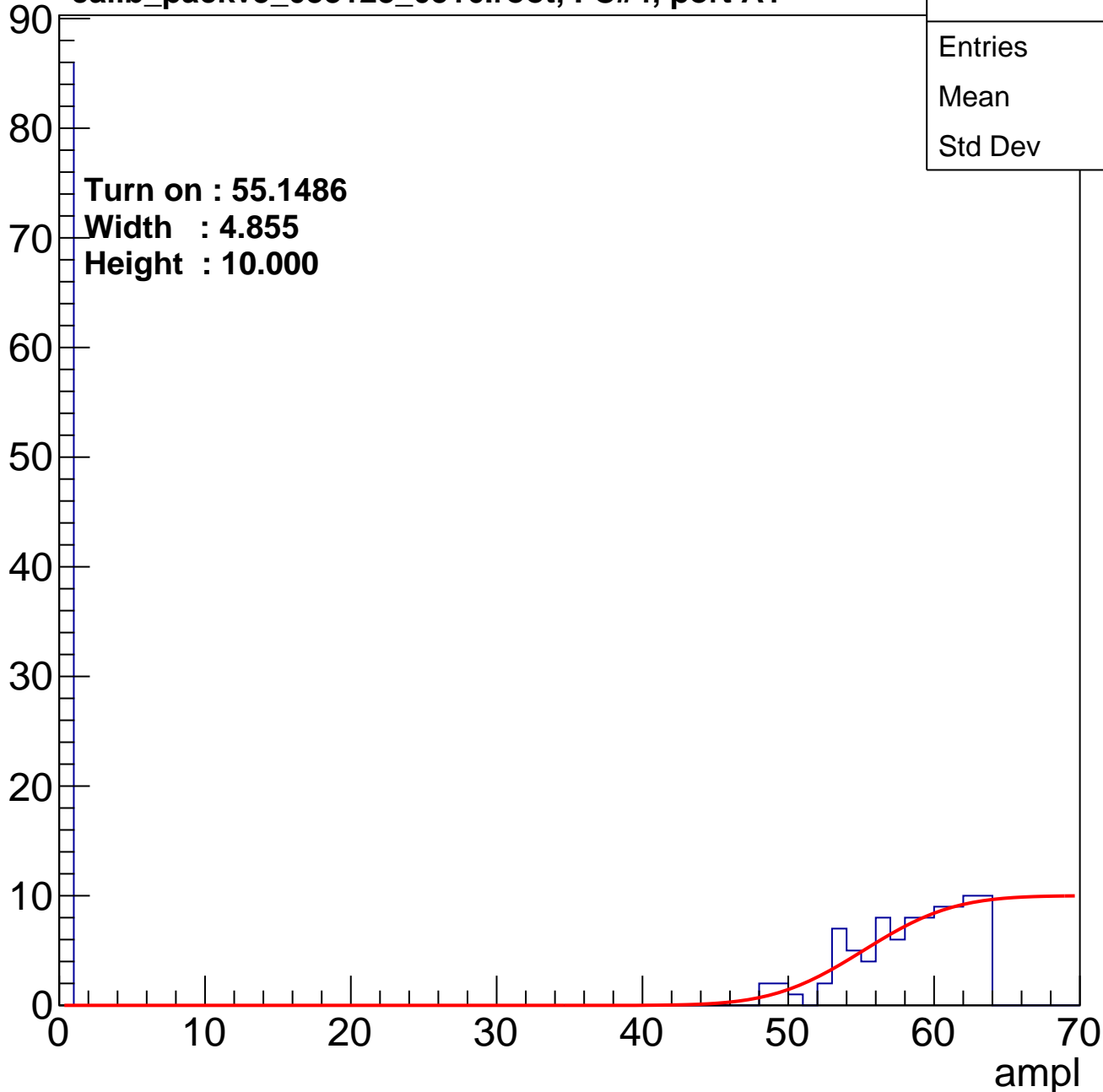
Entries	177
Mean	29.8
Std Dev	29.1

Turn on : 55.1486

Width : 4.855

Height : 10.000

Entry



B1L104S, U8-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	31.79
Std Dev	28.68

Turn on : 52.7699

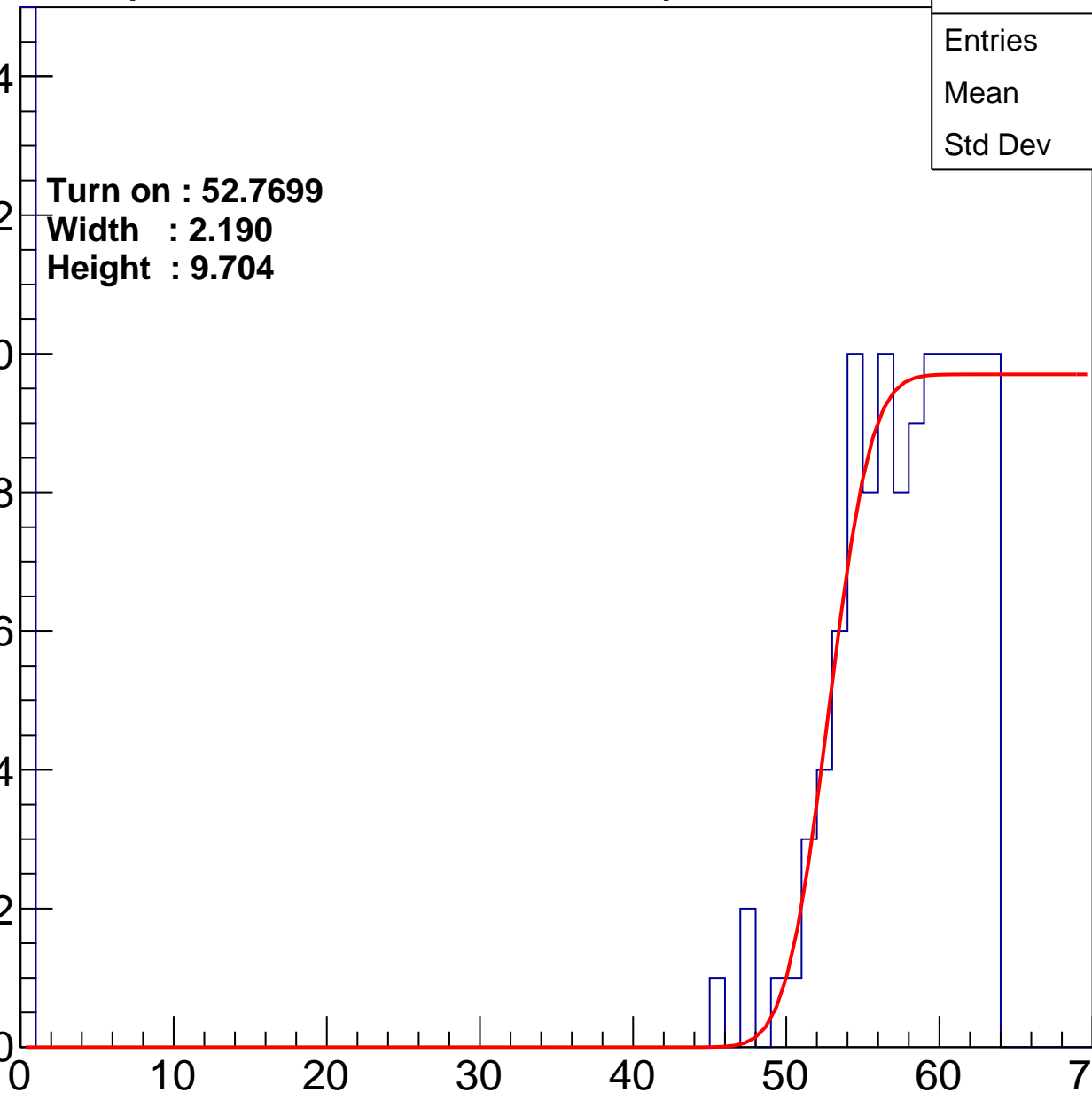
Width : 2.190

Height : 9.704

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	34.2
Std Dev	27.85

Turn on : 51.1651

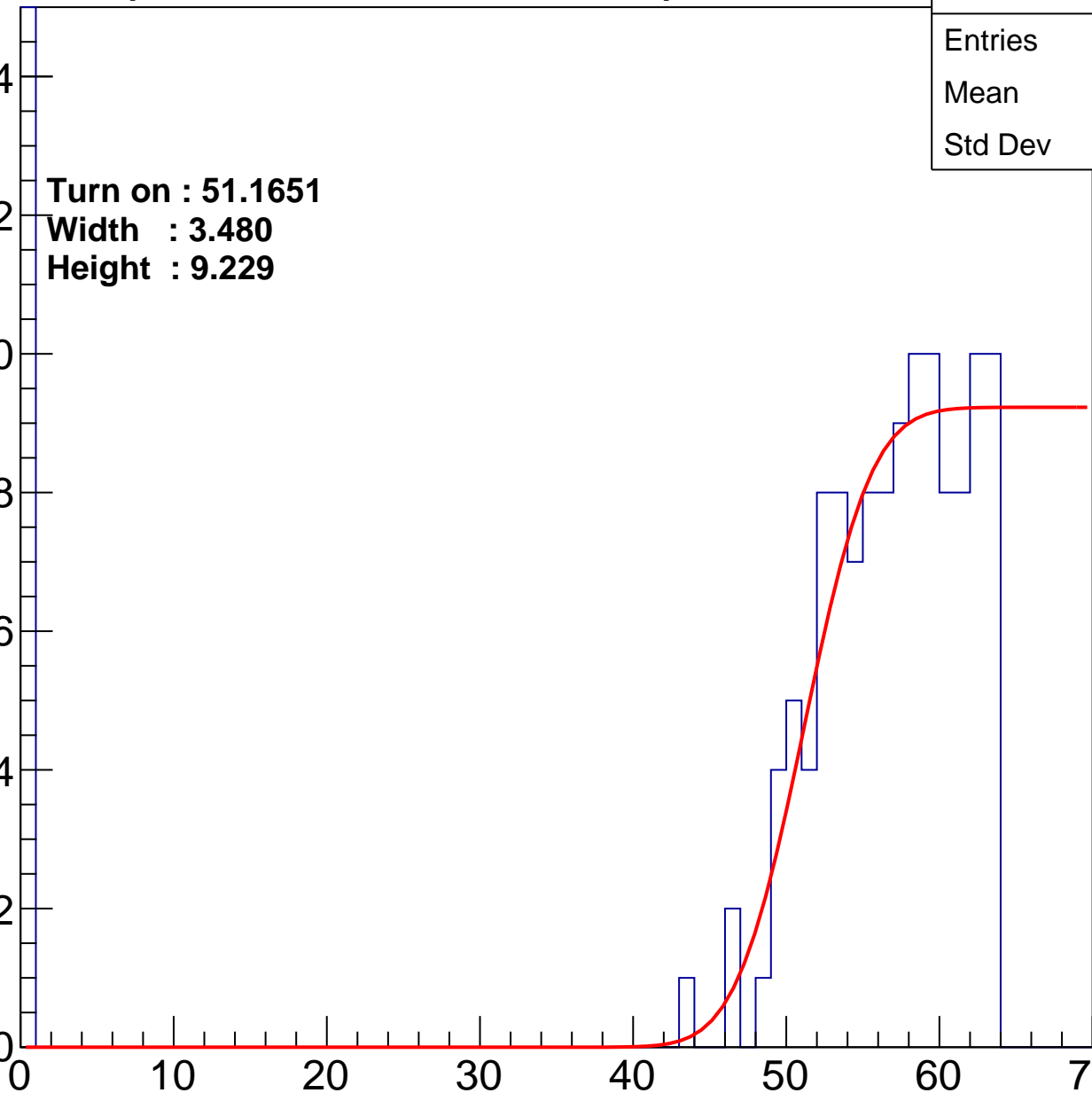
Width : 3.480

Height : 9.229

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	30.67
Std Dev	28.89

Turn on : 53.3452

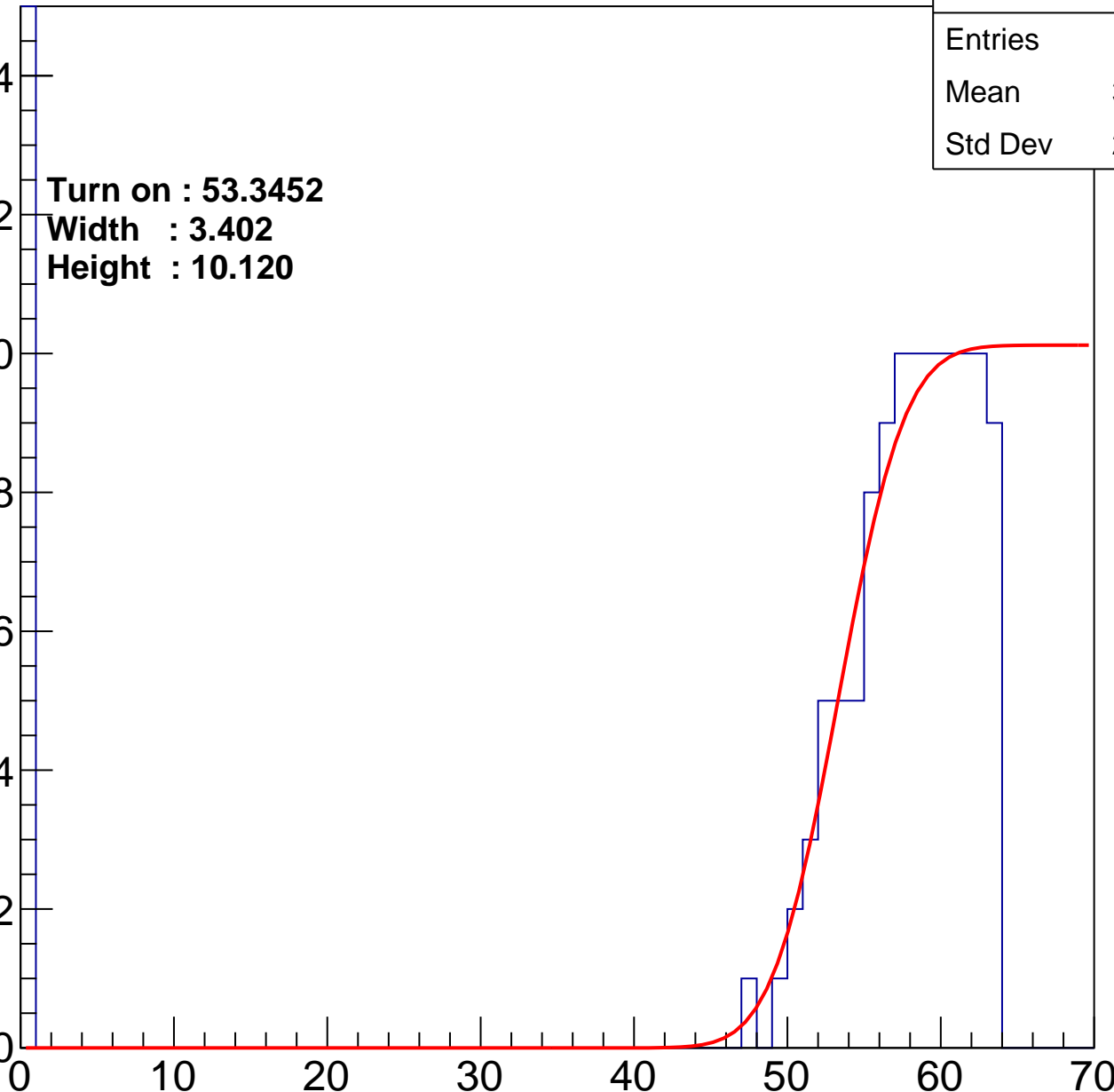
Width : 3.402

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	34.26
Std Dev	27.76

Turn on : 50.7726

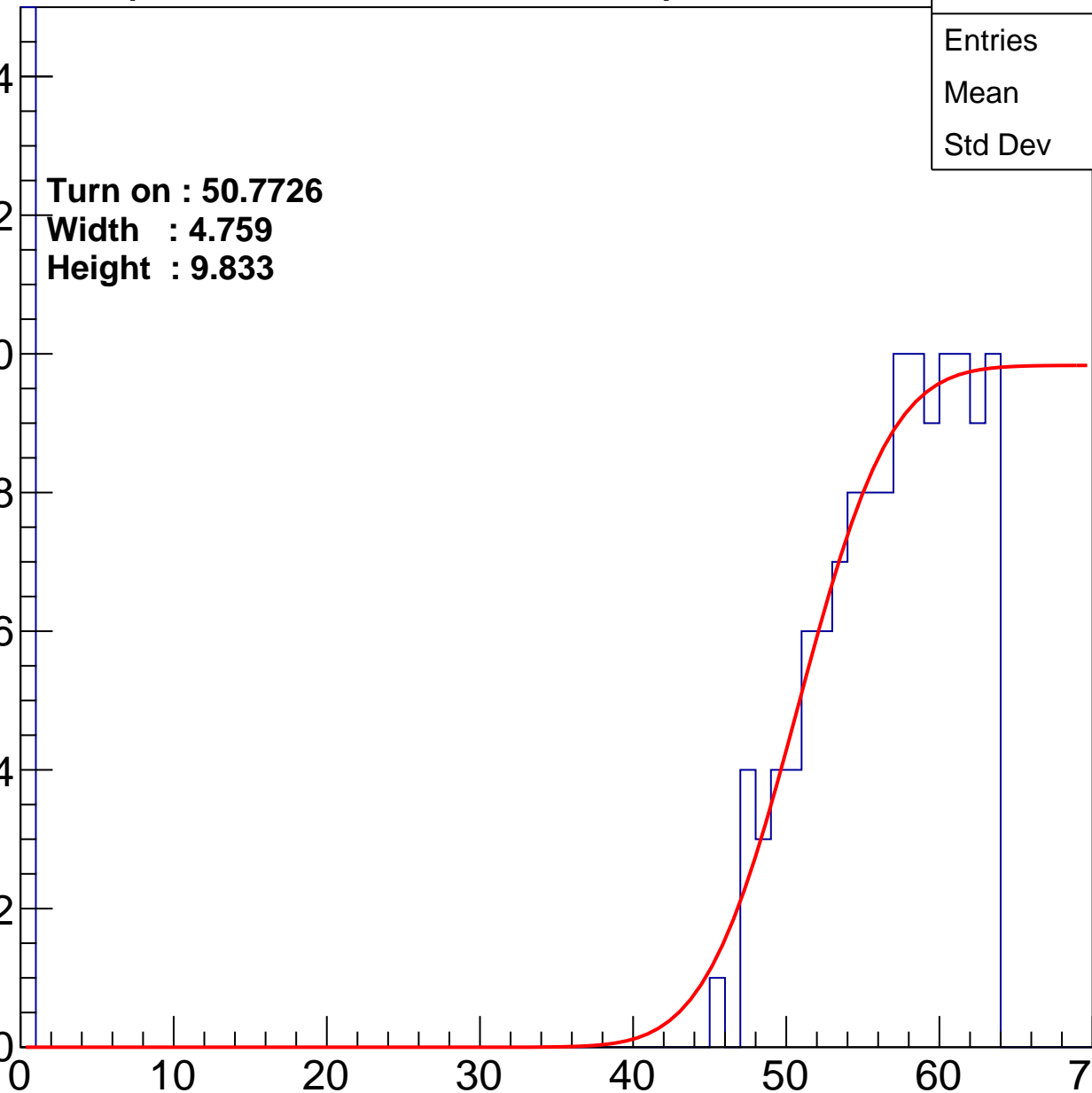
Width : 4.759

Height : 9.833

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.64
Std Dev	28.23

Turn on : 53.2020

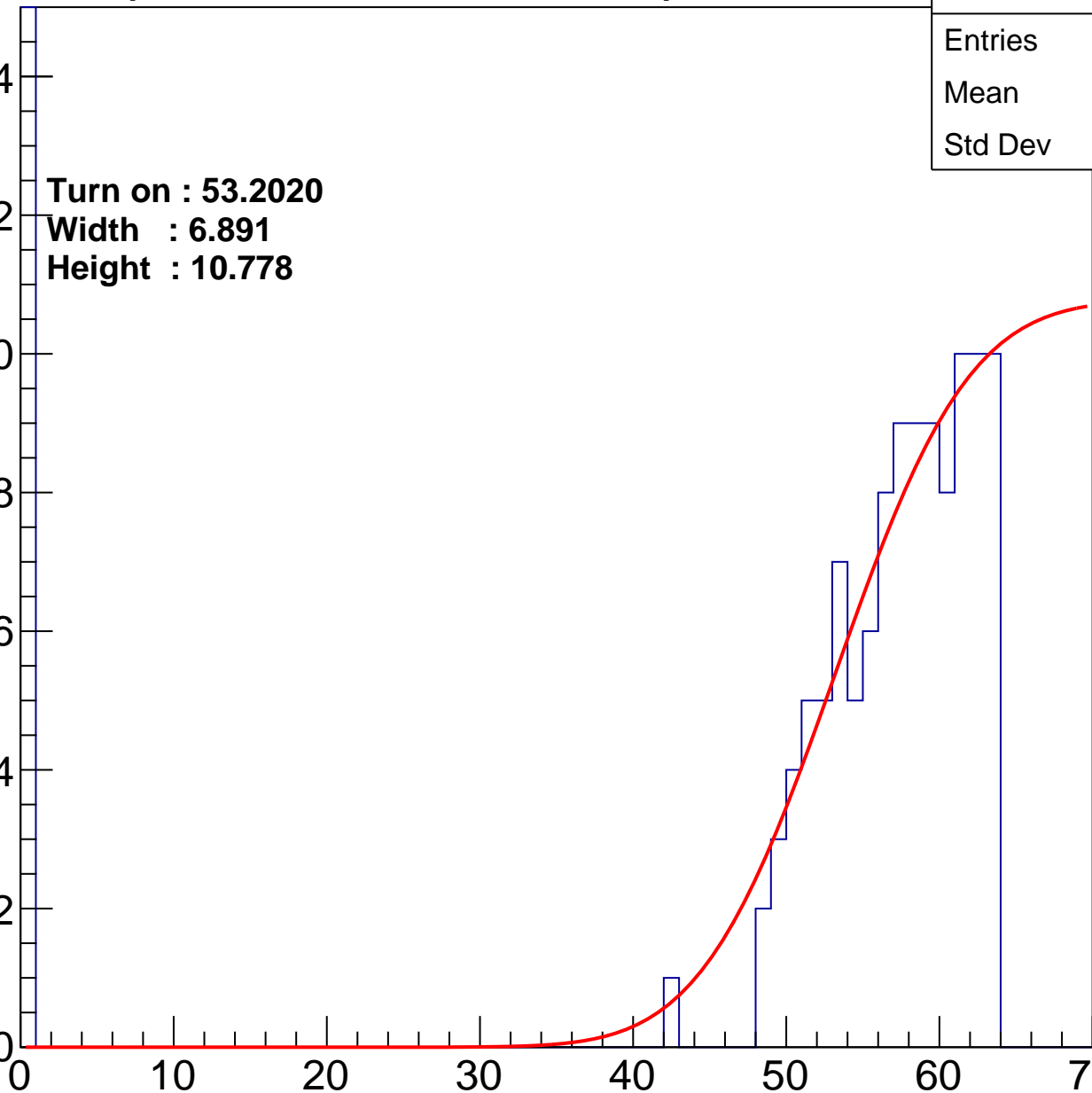
Width : 6.891

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U8-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.64
Std Dev	28.23

Turn on : 53.2020

Width : 6.891

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl

