

B1L002S, U23-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.53

Turn on : 25.5957

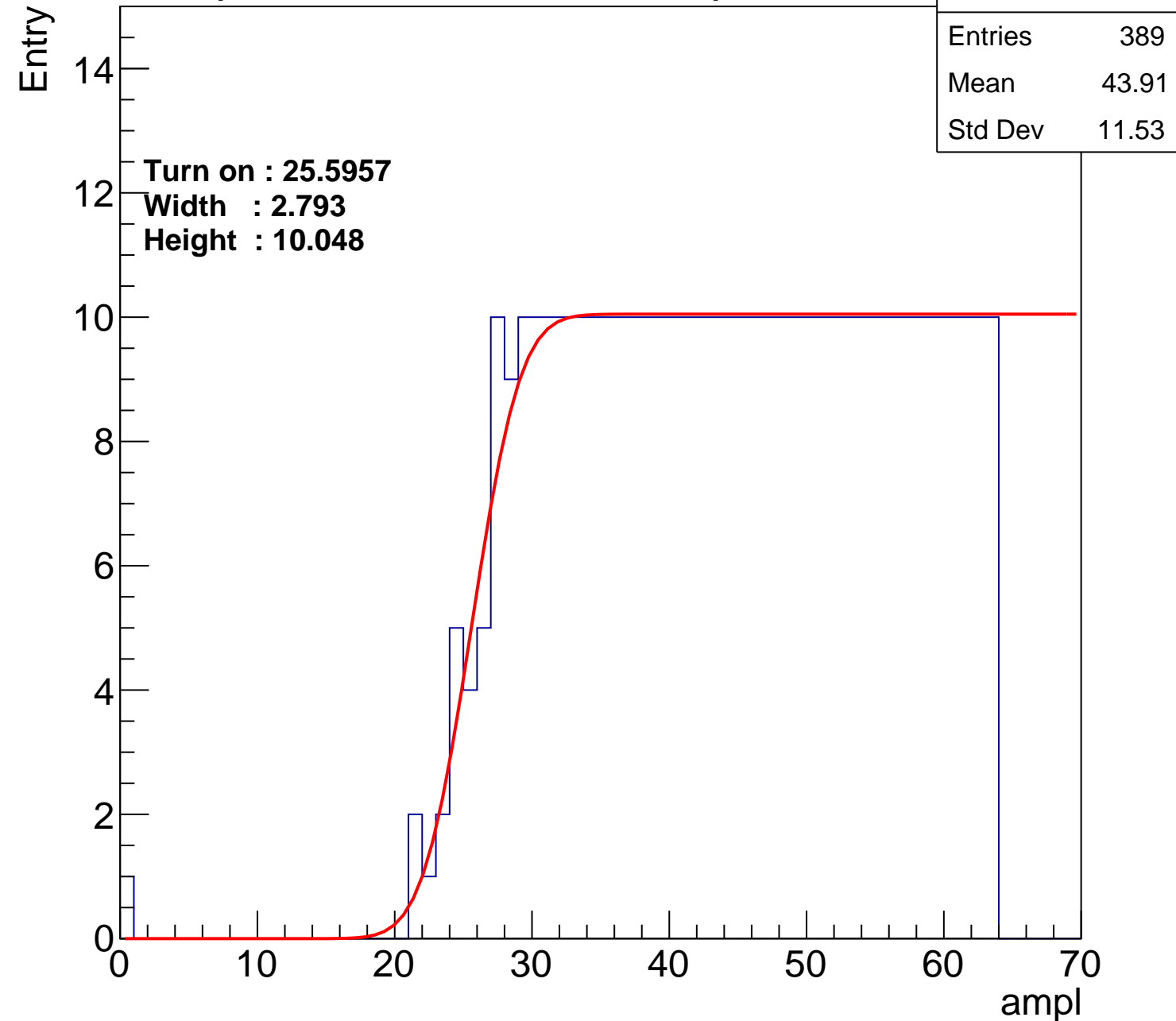
Width : 2.793

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.85
Std Dev	11.29

Turn on : 27.1577

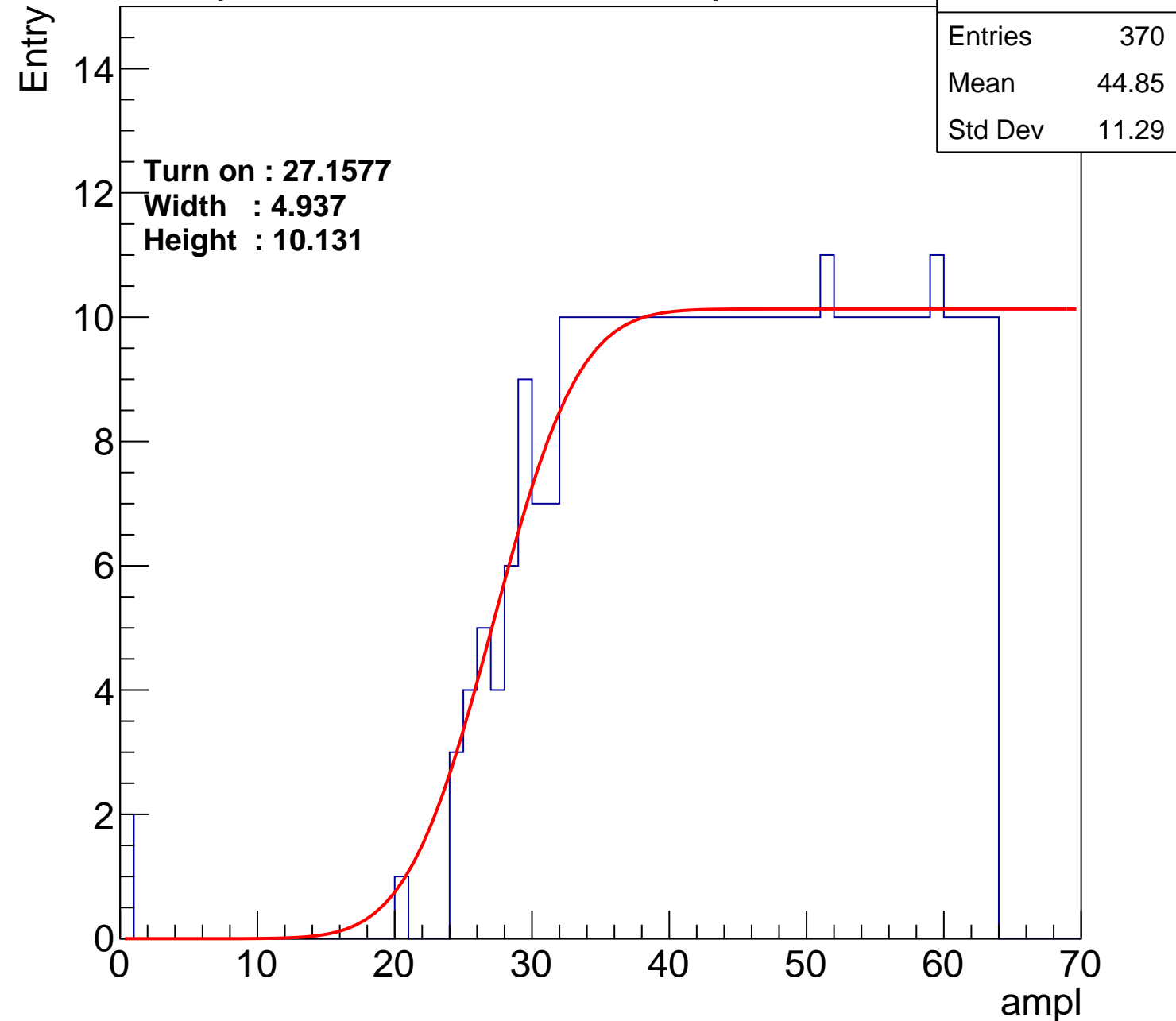
Width : 4.937

Height : 10.131

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.1461

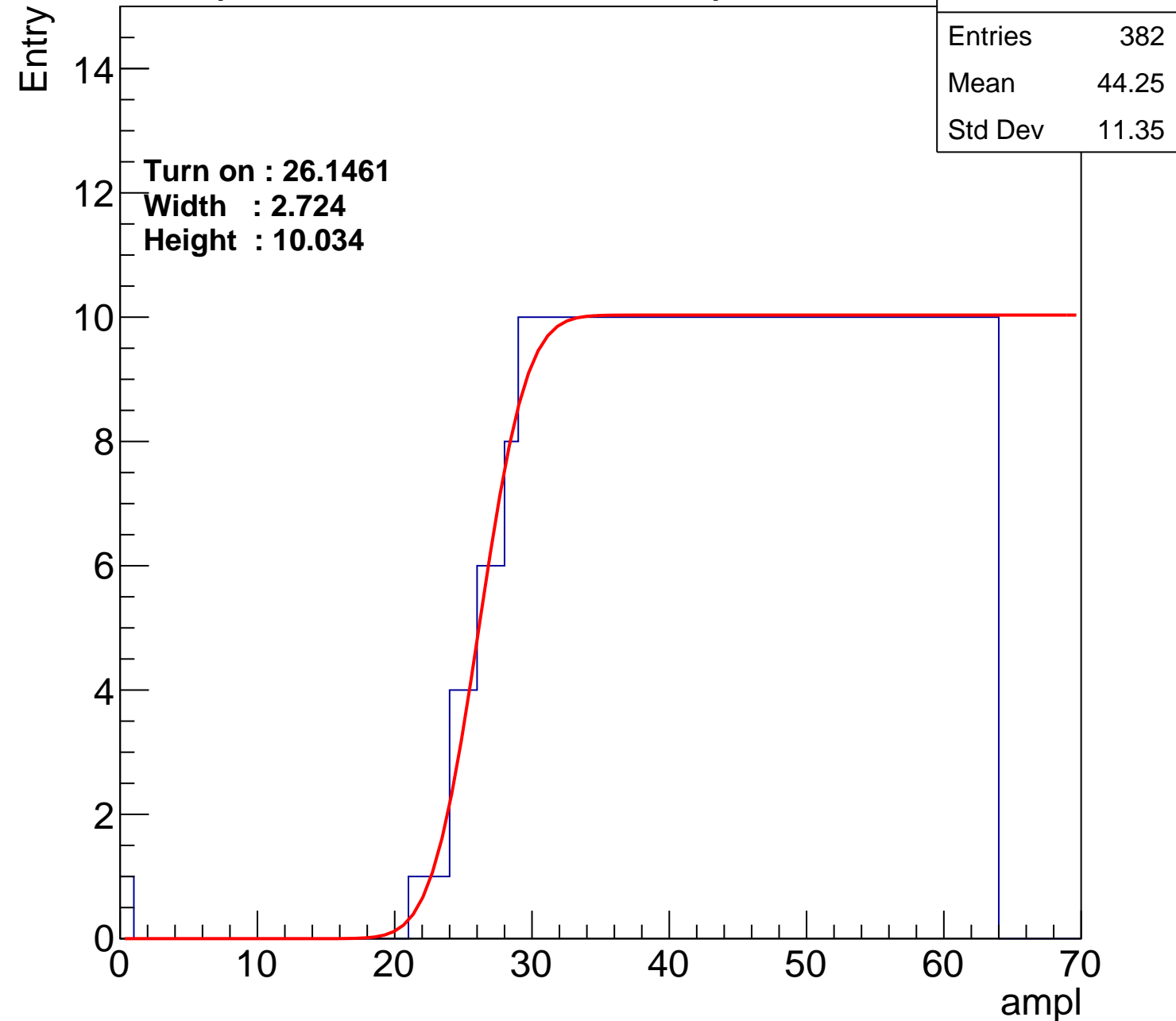
Width : 2.724

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.45
Std Dev	11.38

Turn on : 26.6603

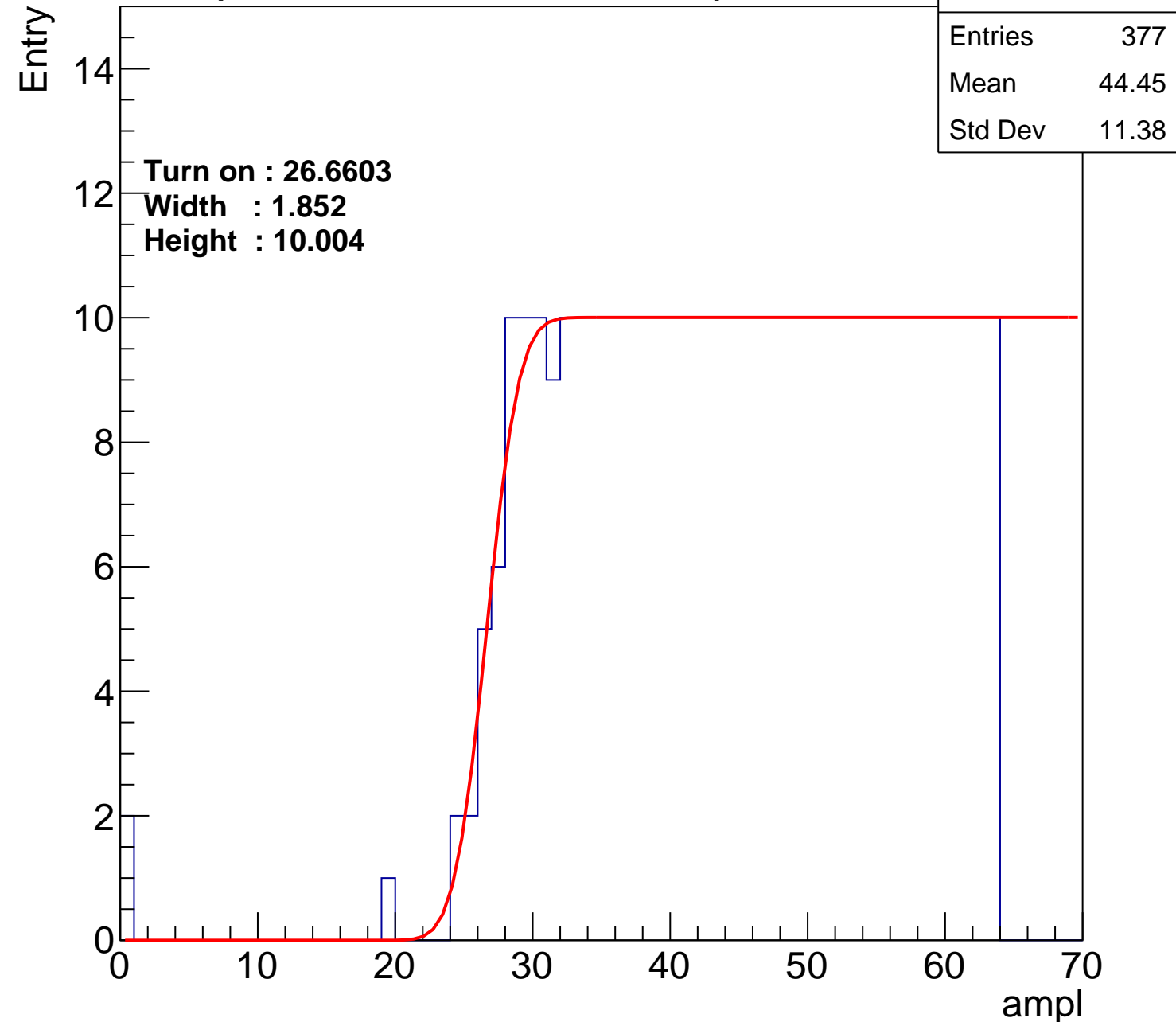
Width : 1.852

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch4

calib_packv5_042523_0143.root, FC#10, port B3

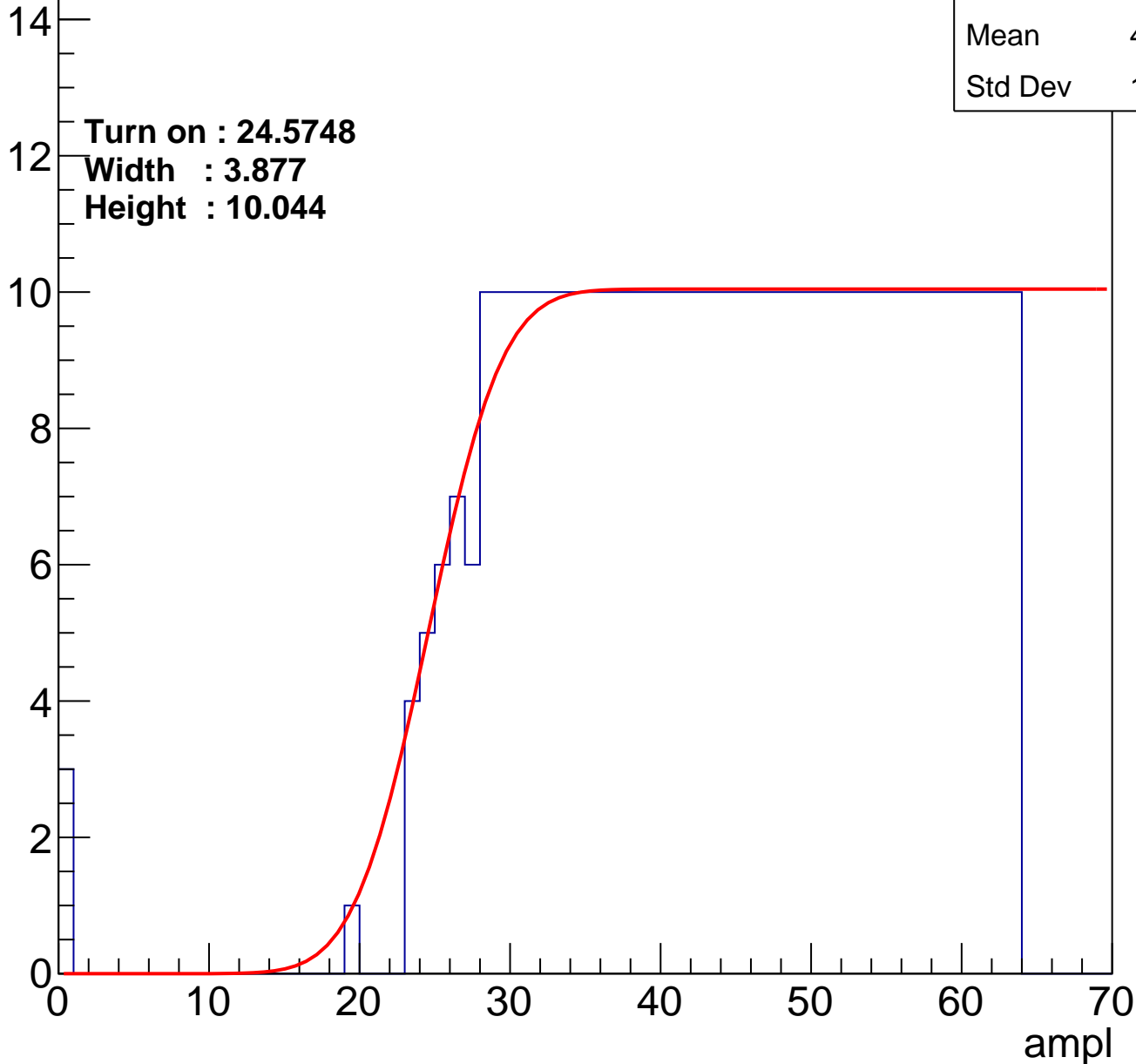
Entries	392
Mean	43.64
Std Dev	11.95

Turn on : 24.5748

Width : 3.877

Height : 10.044

Entry



B1L002S, U23-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.82

Turn on : 27.0110

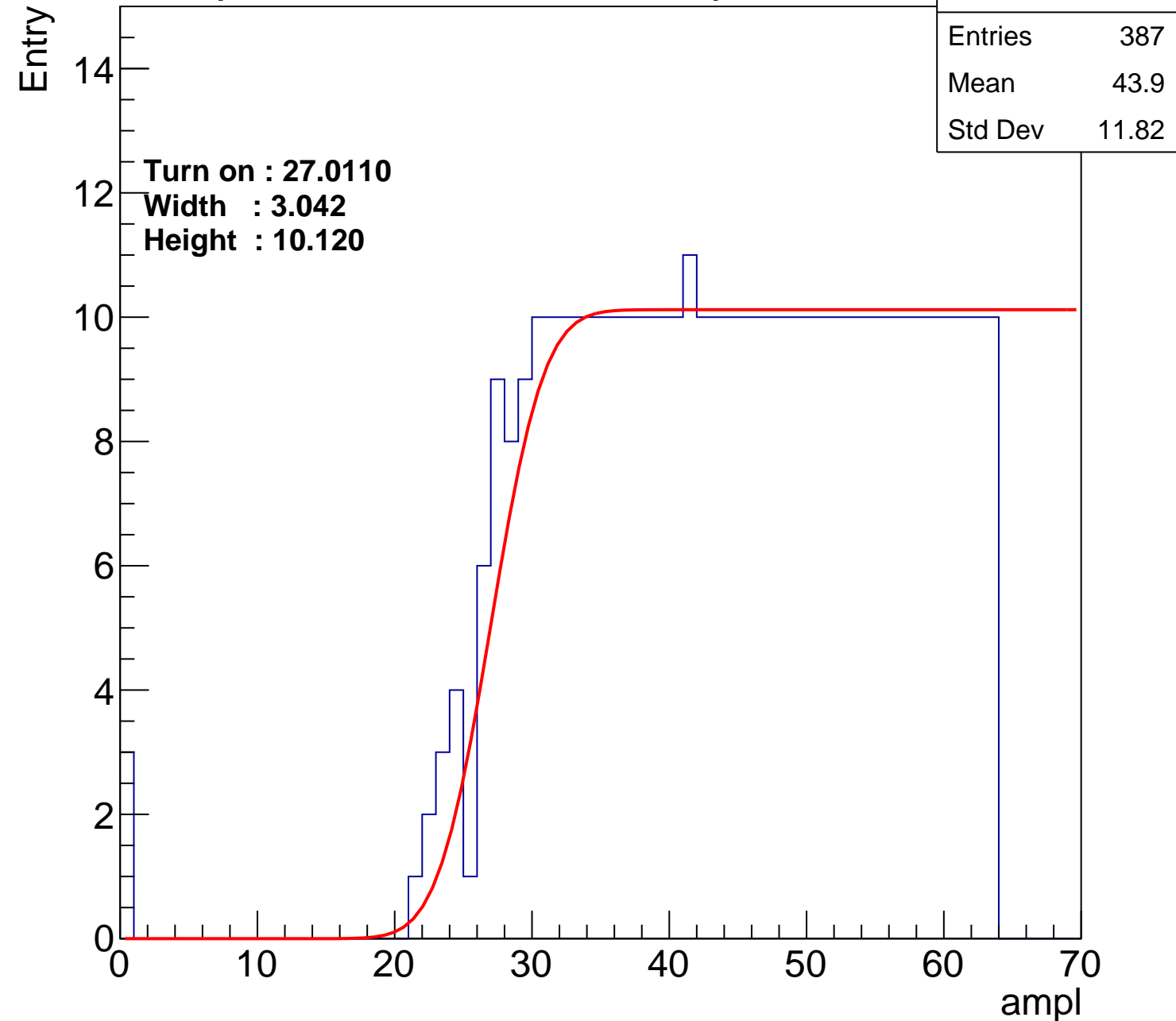
Width : 3.042

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	412
Mean	42.6
Std Dev	12.59

Turn on : 24.1838

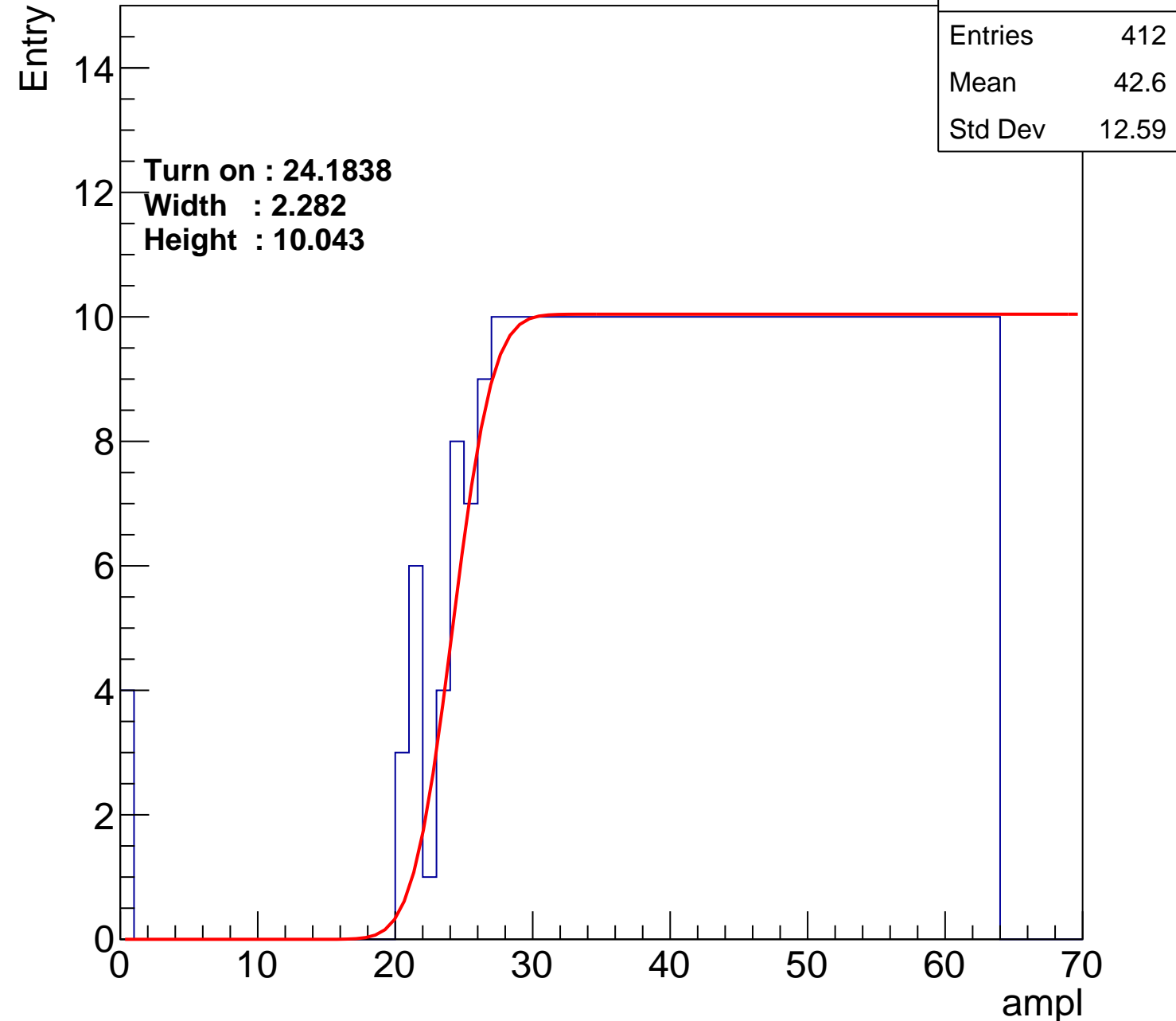
Width : 2.282

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.91
Std Dev	11.7

Turn on : 25.3537

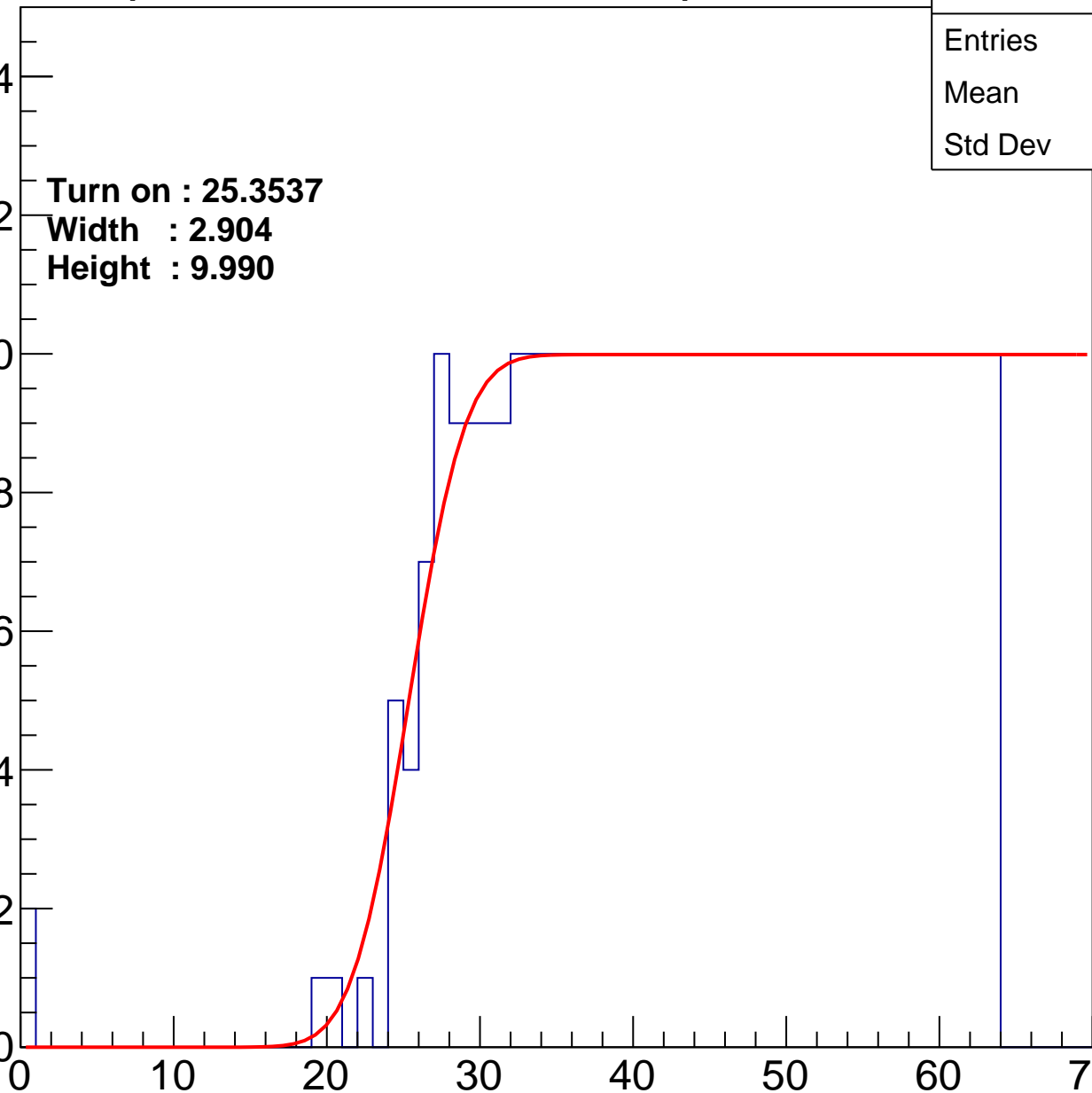
Width : 2.904

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.52
Std Dev	12.03

Turn on : 25.7653

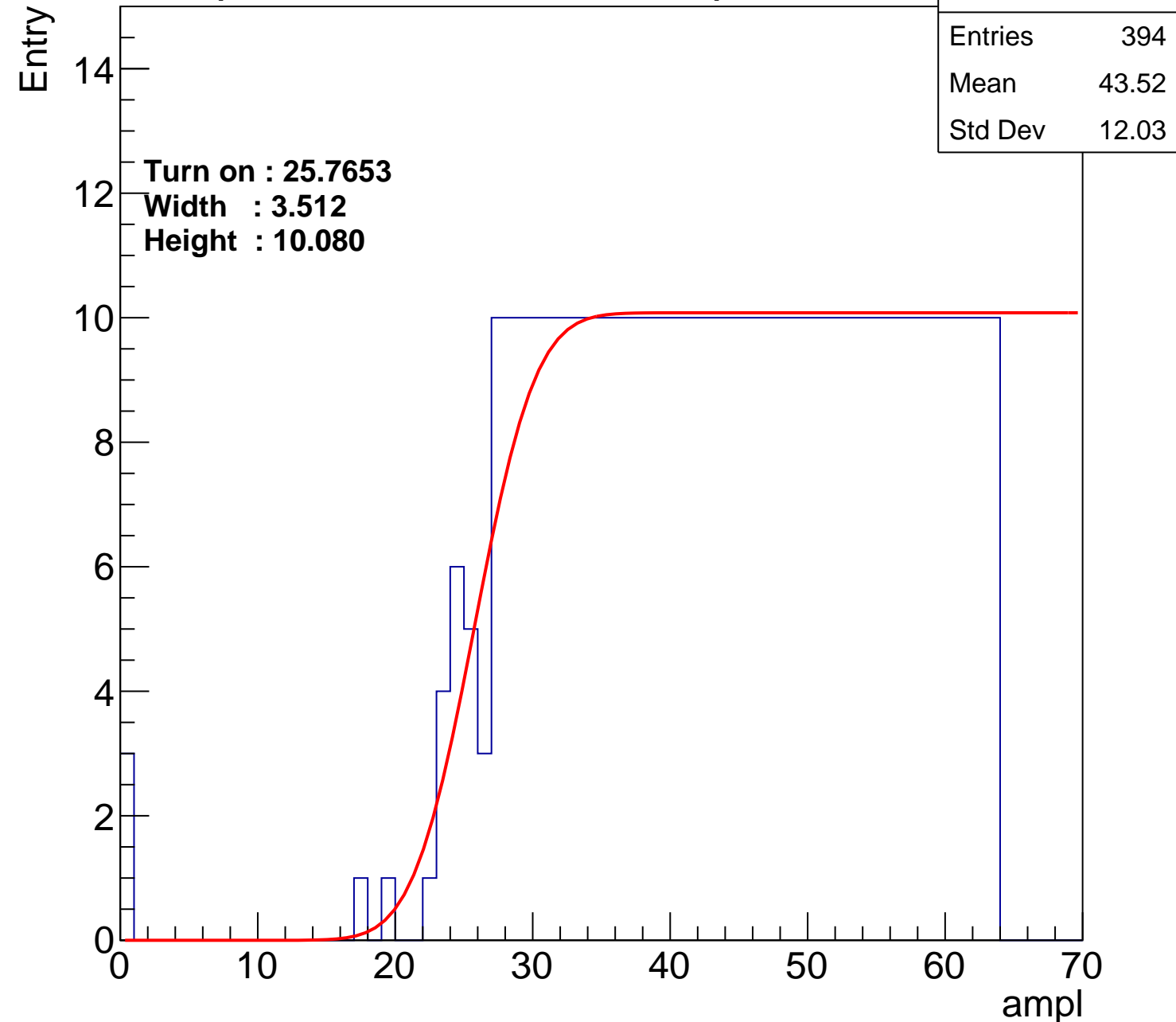
Width : 3.512

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.73
Std Dev	11.9

Turn on : 25.1417

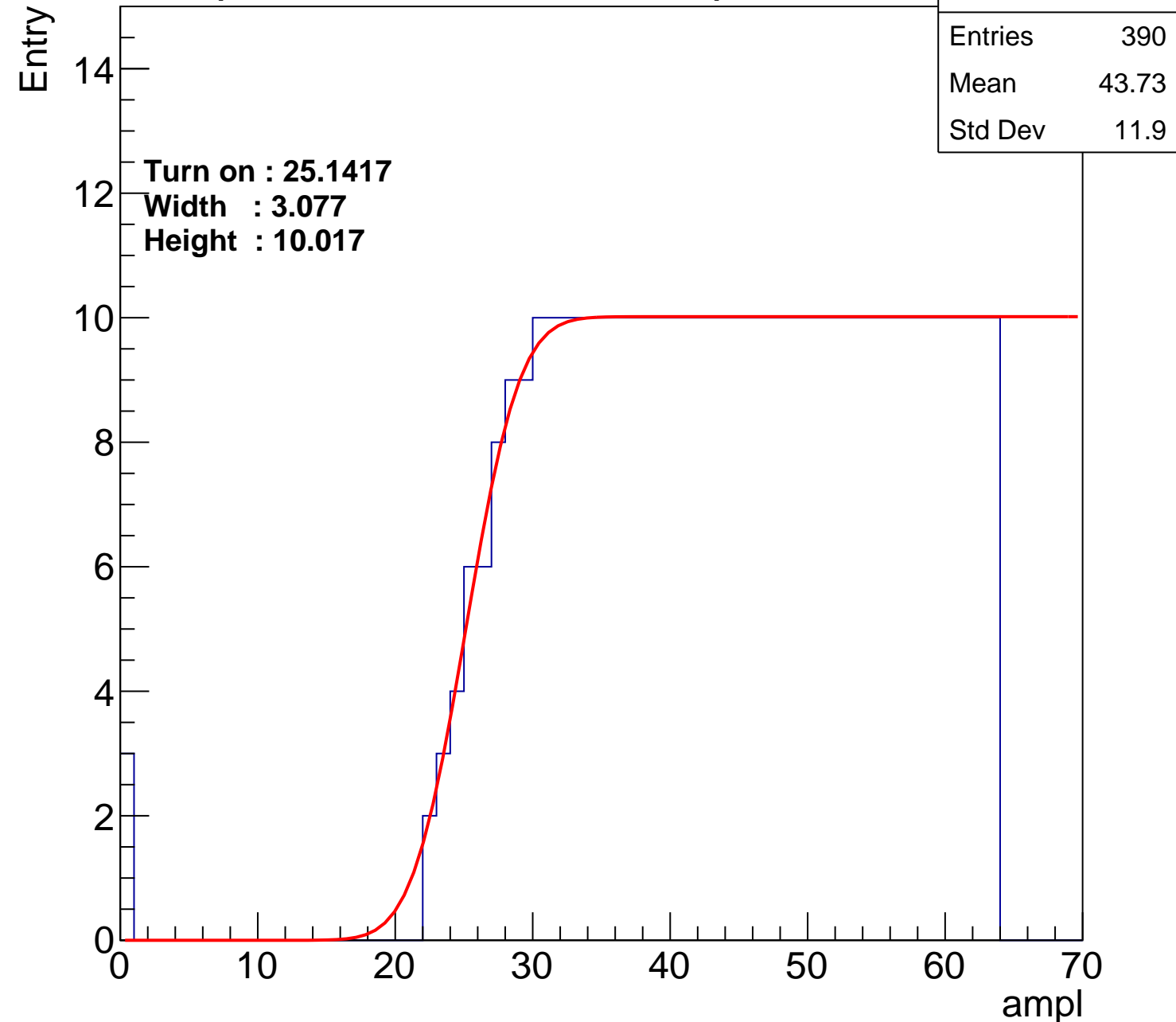
Width : 3.077

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.82
Std Dev	11.7

Turn on : 25.3462

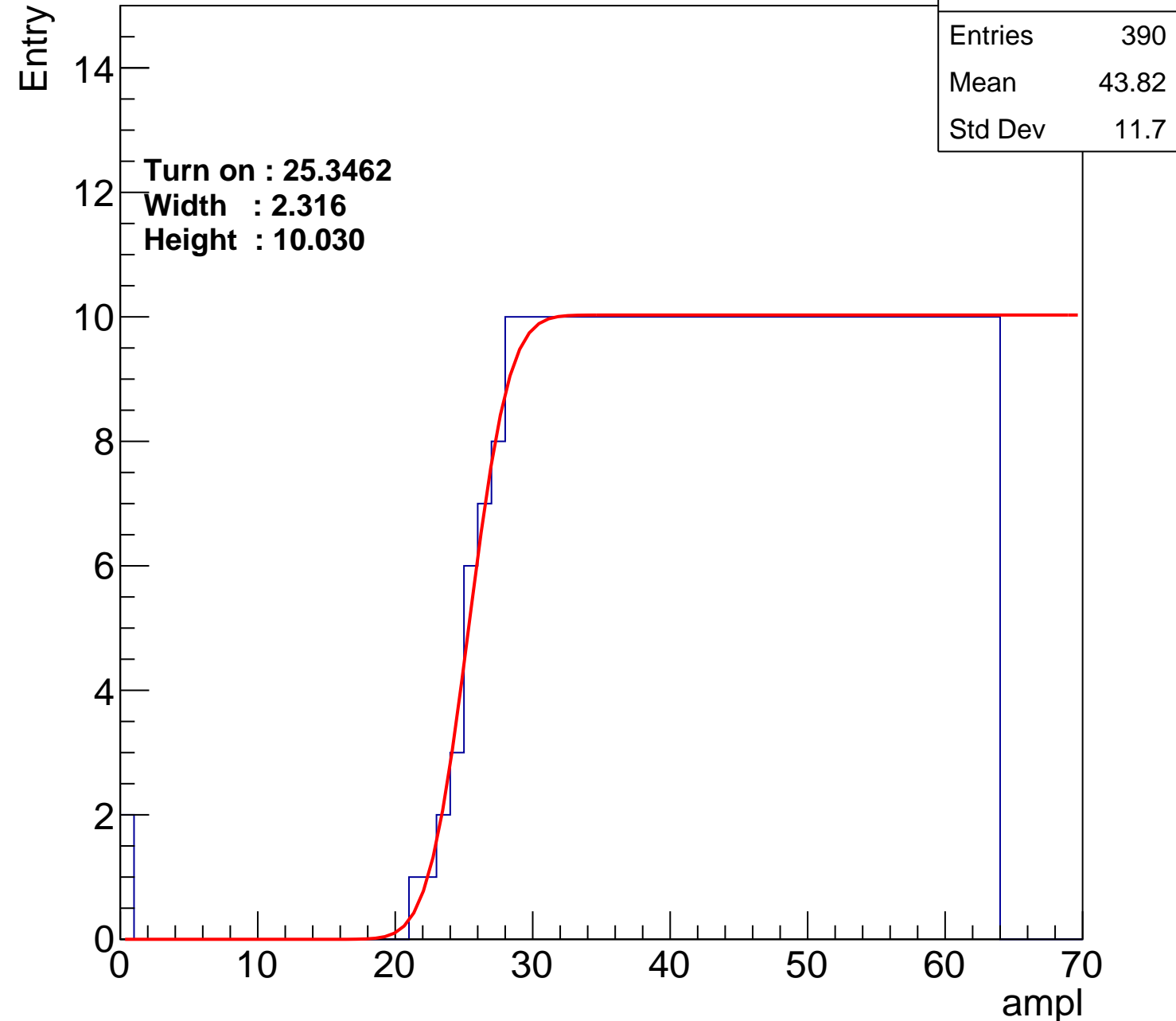
Width : 2.316

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.29
Std Dev	11.76

Turn on : 26.7713

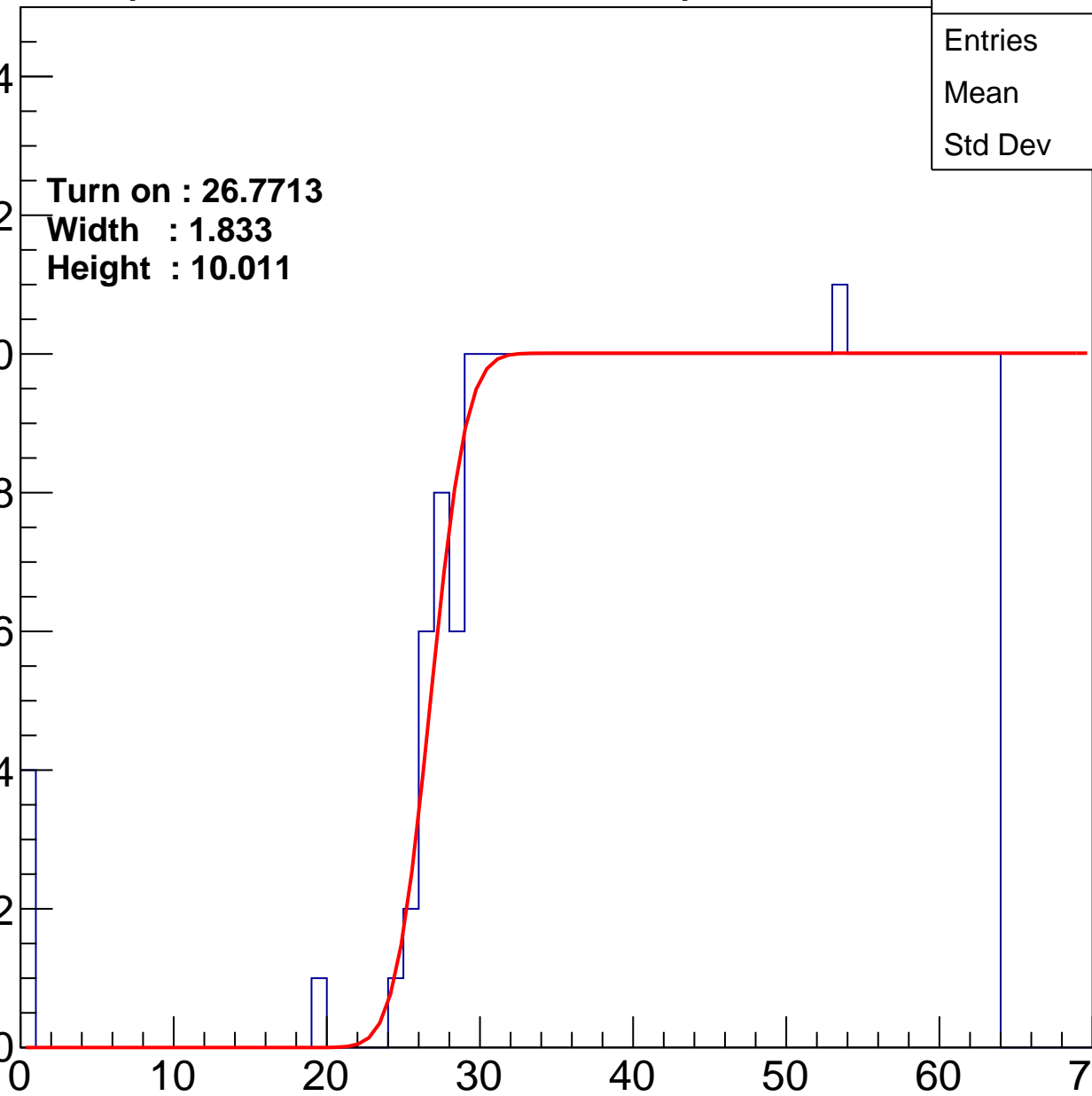
Width : 1.833

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch12

calib_packv5_042523_0143.root, FC#10, port B3

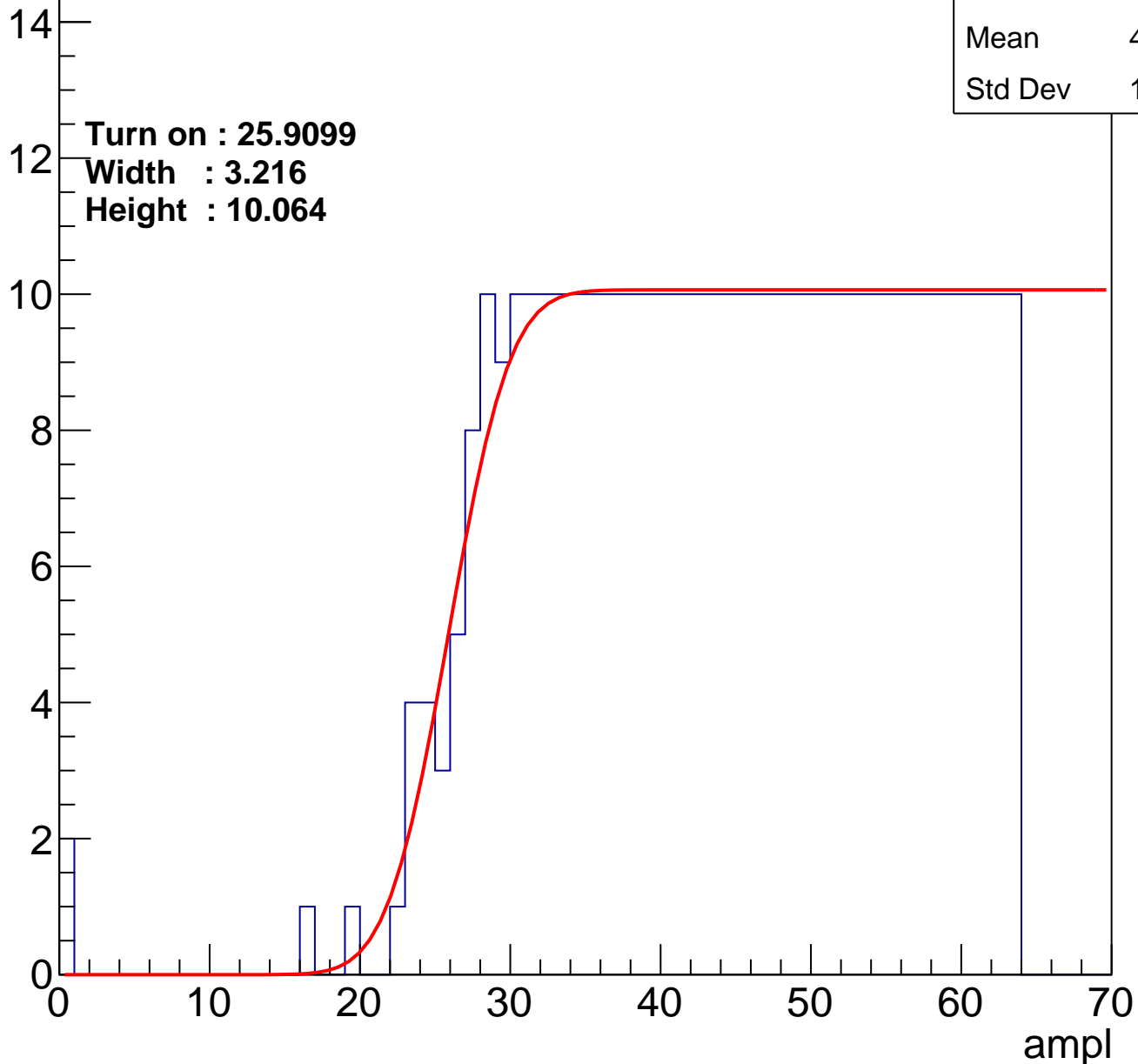
Entries	388
Mean	43.86
Std Dev	11.75

Turn on : 25.9099

Width : 3.216

Height : 10.064

Entry



B1L002S, U23-ch13

calib_packv5_042523_0143.root, FC#10, port B3

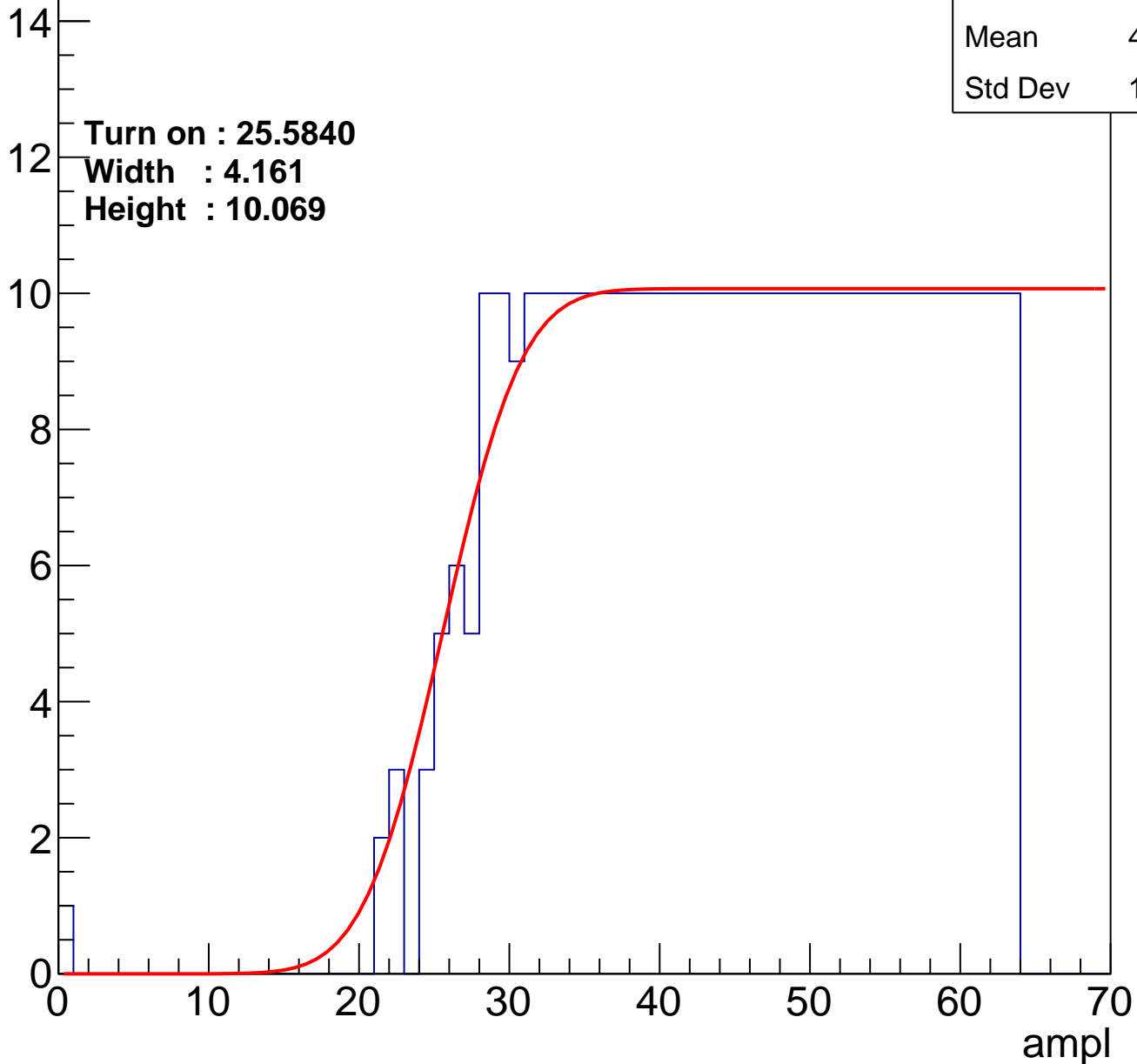
Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 25.5840

Width : 4.161

Height : 10.069

Entry



B1L002S, U23-ch14

calib_packv5_042523_0143.root, FC#10, port B3

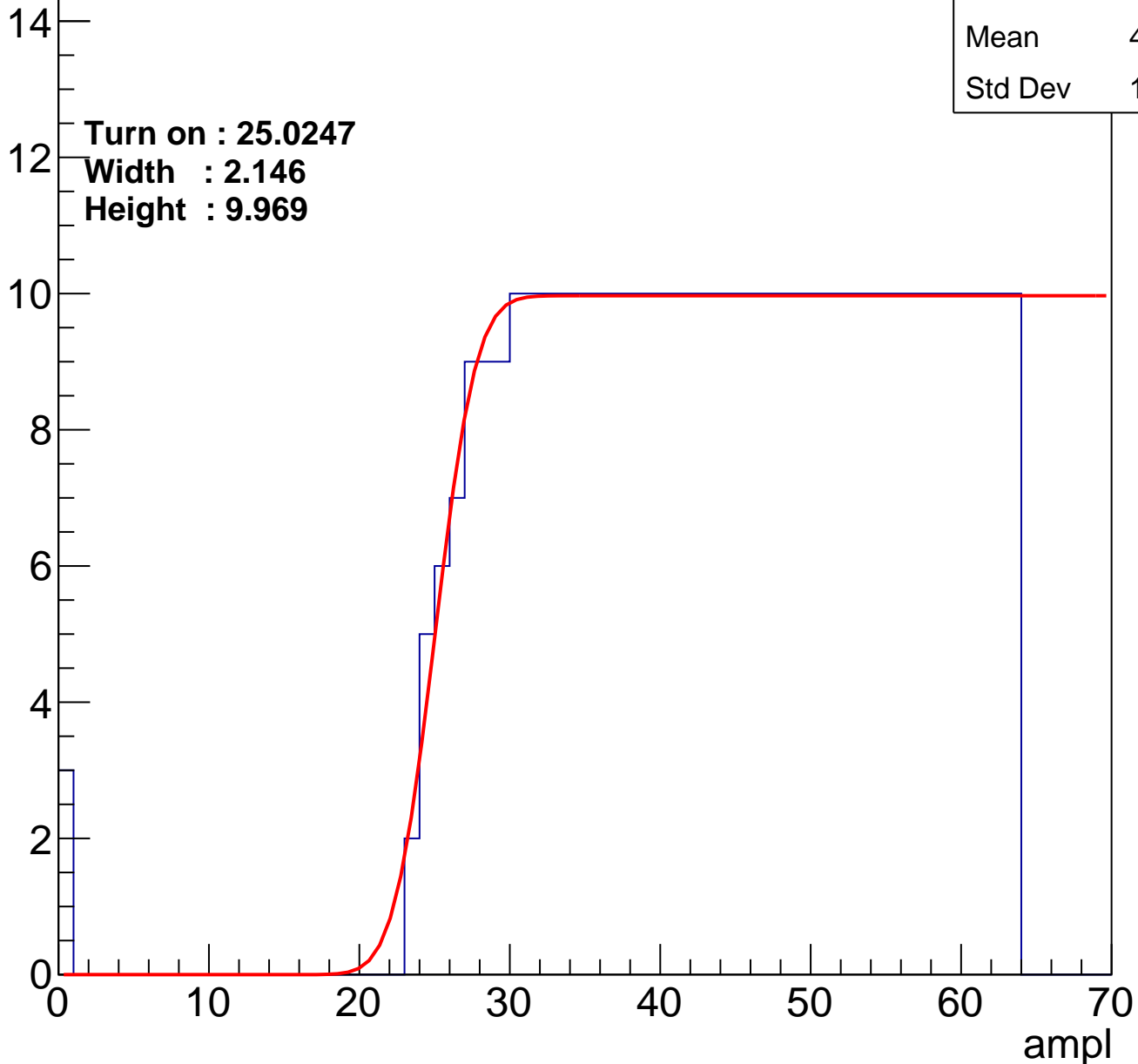
Entries	390
Mean	43.75
Std Dev	11.86

Turn on : 25.0247

Width : 2.146

Height : 9.969

Entry



B1L002S, U23-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.39
Std Dev	11.91

Turn on : 24.6491

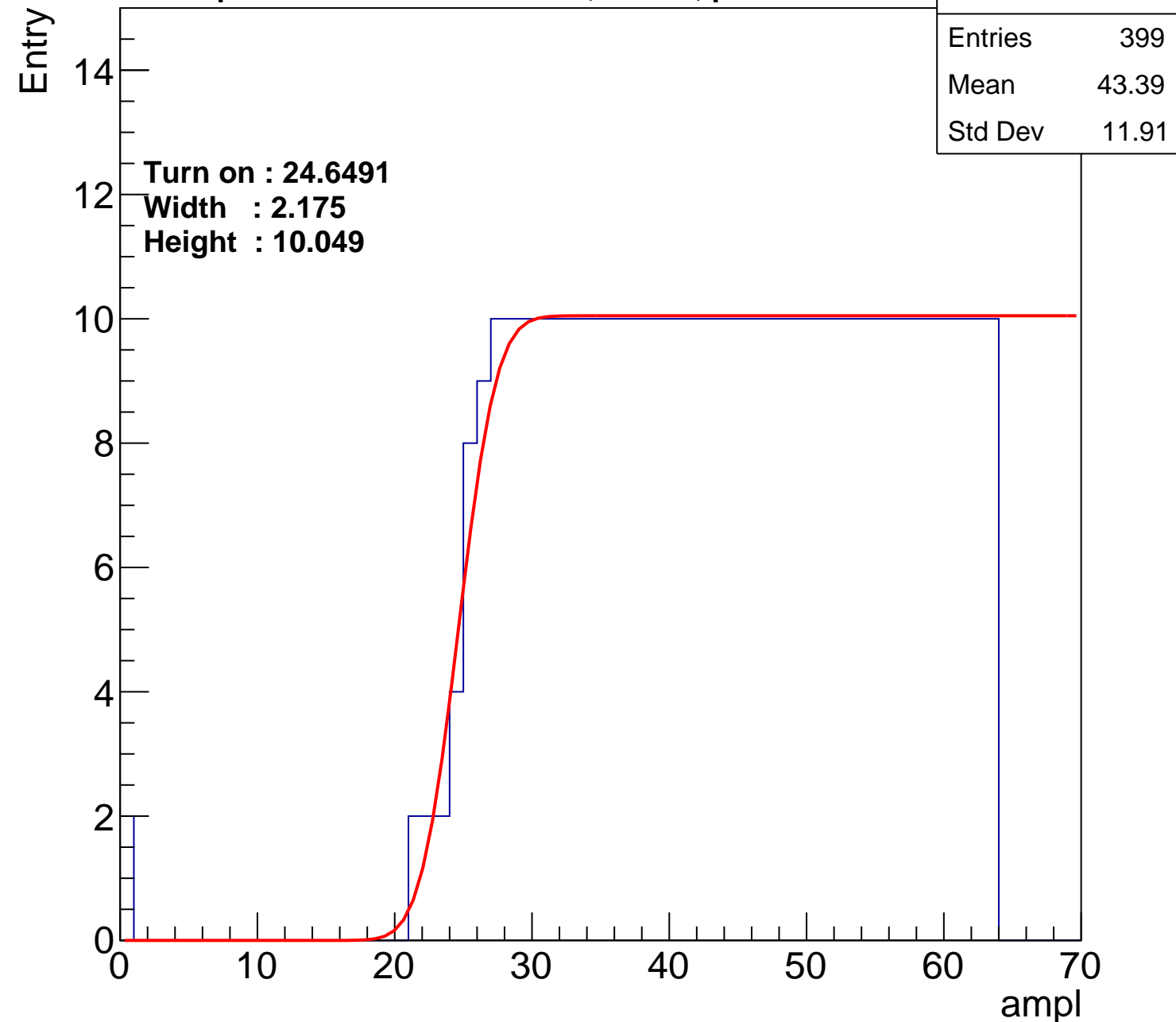
Width : 2.175

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 25.4289

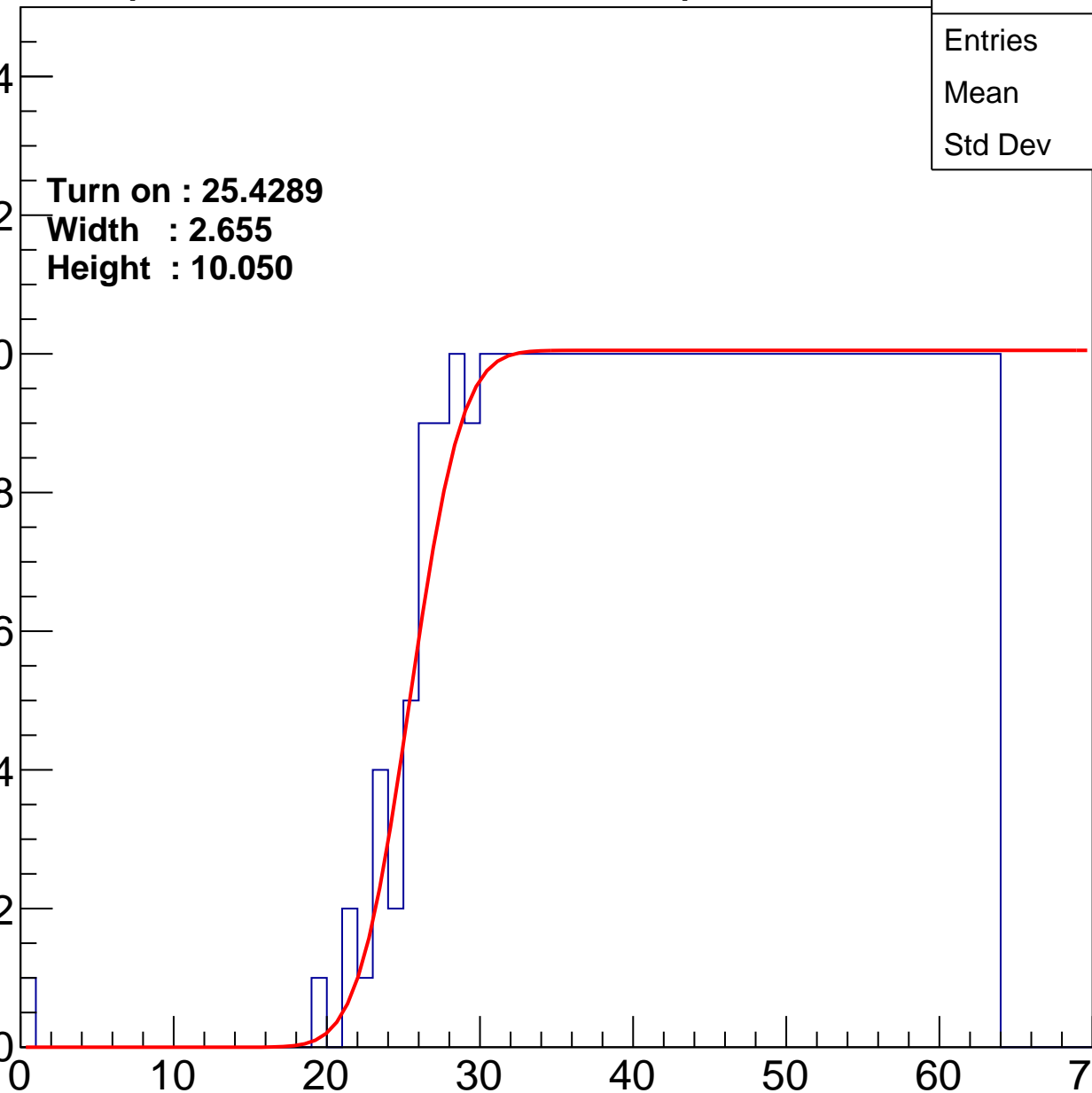
Width : 2.655

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.2
Std Dev	12.28

Turn on : 24.5196

Width : 2.519

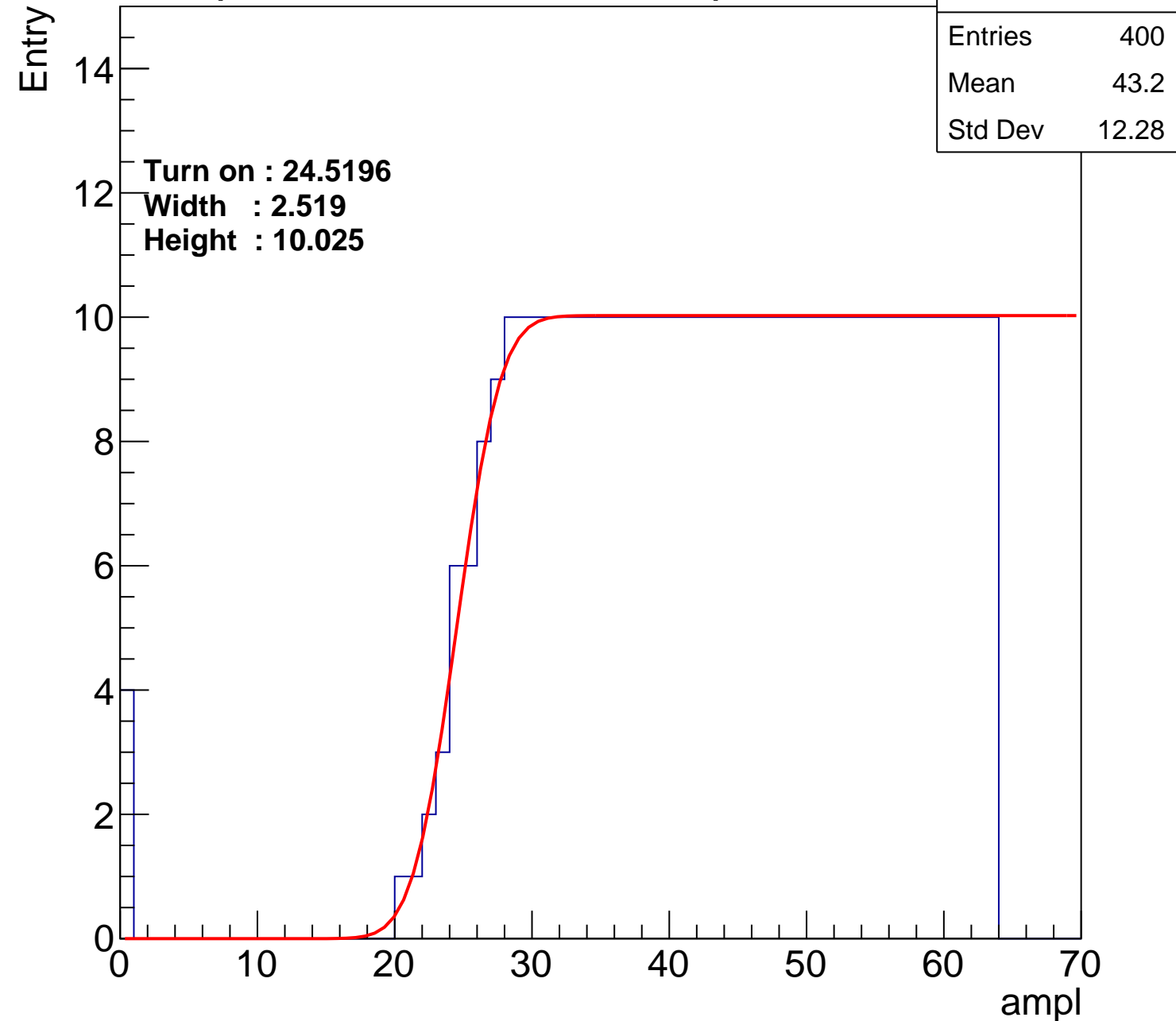
Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U23-ch18

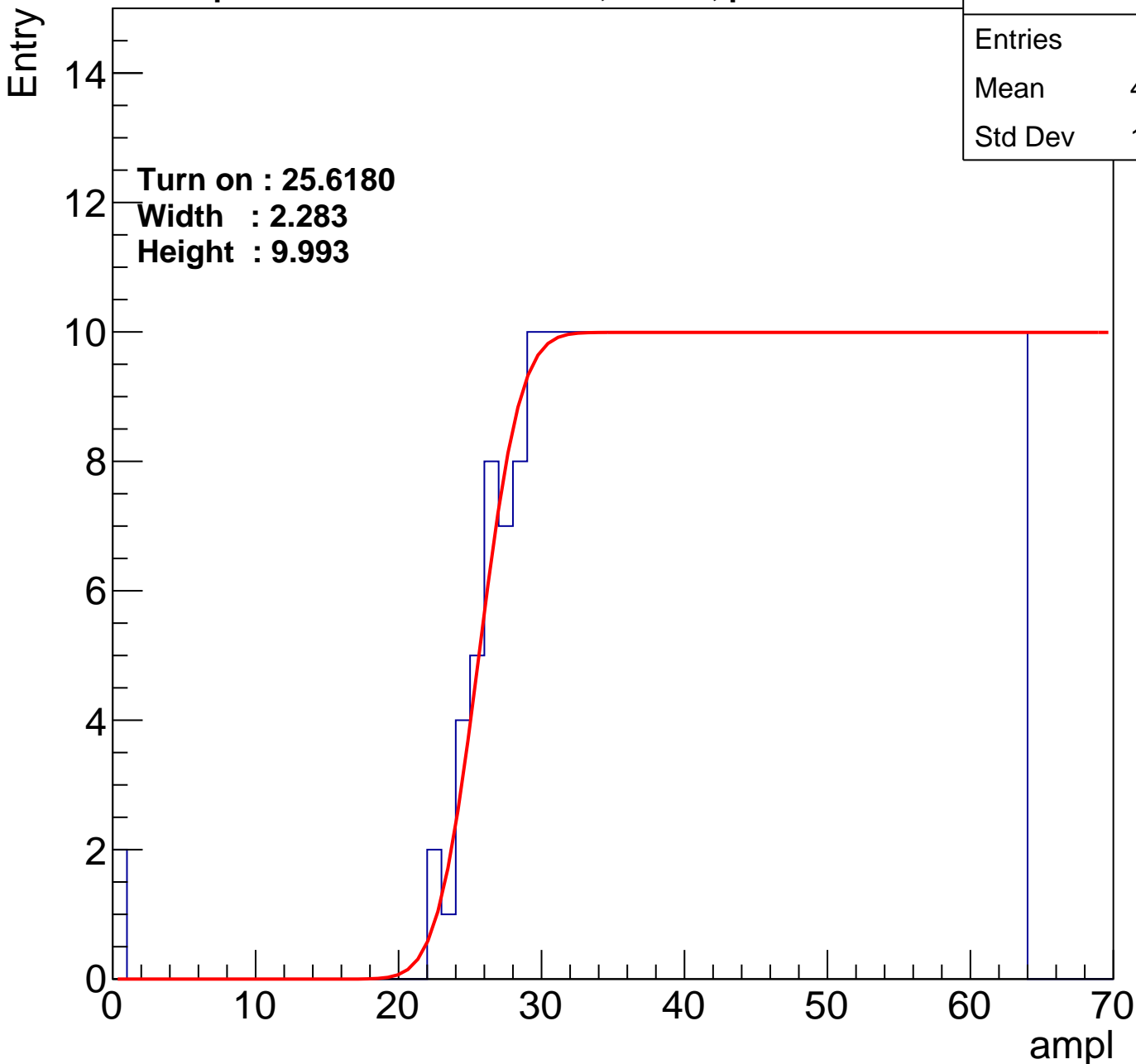
calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.6180

Width : 2.283

Height : 9.993



B1L002S, U23-ch19

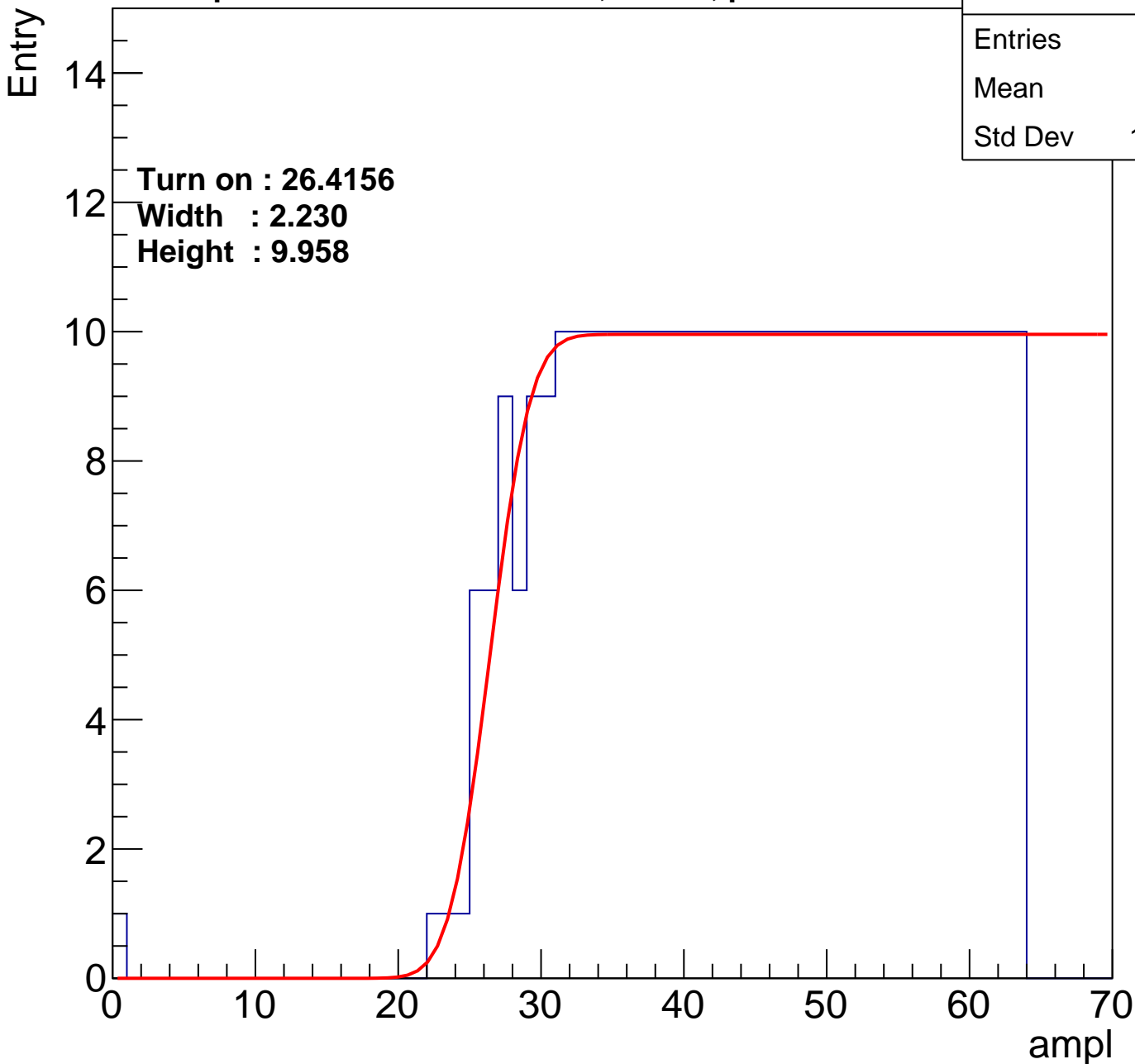
calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.4156

Width : 2.230

Height : 9.958



B1L002S, U23-ch20

calib_packv5_042523_0143.root, FC#10, port B3

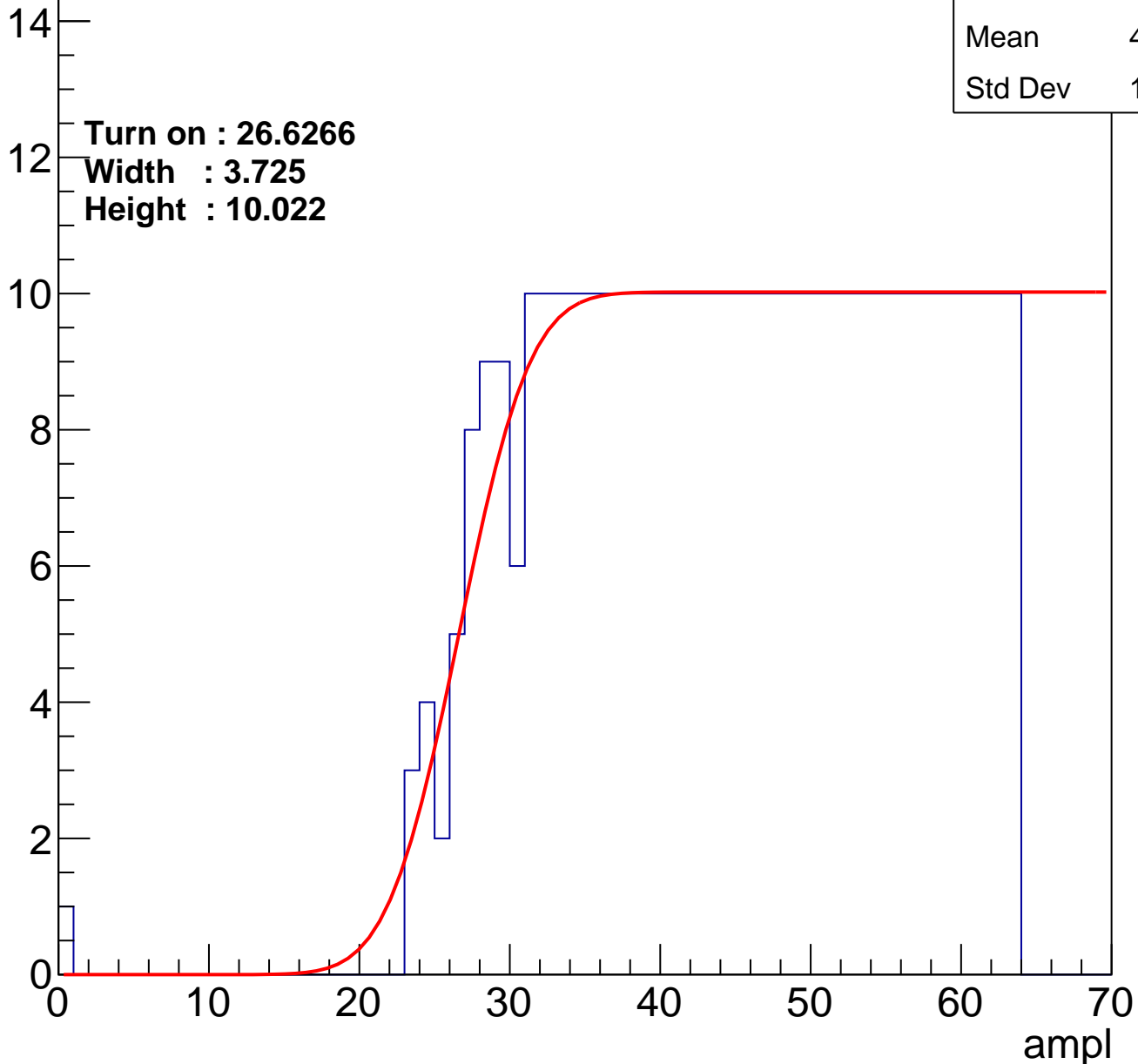
Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.6266

Width : 3.725

Height : 10.022

Entry



B1L002S, U23-ch21

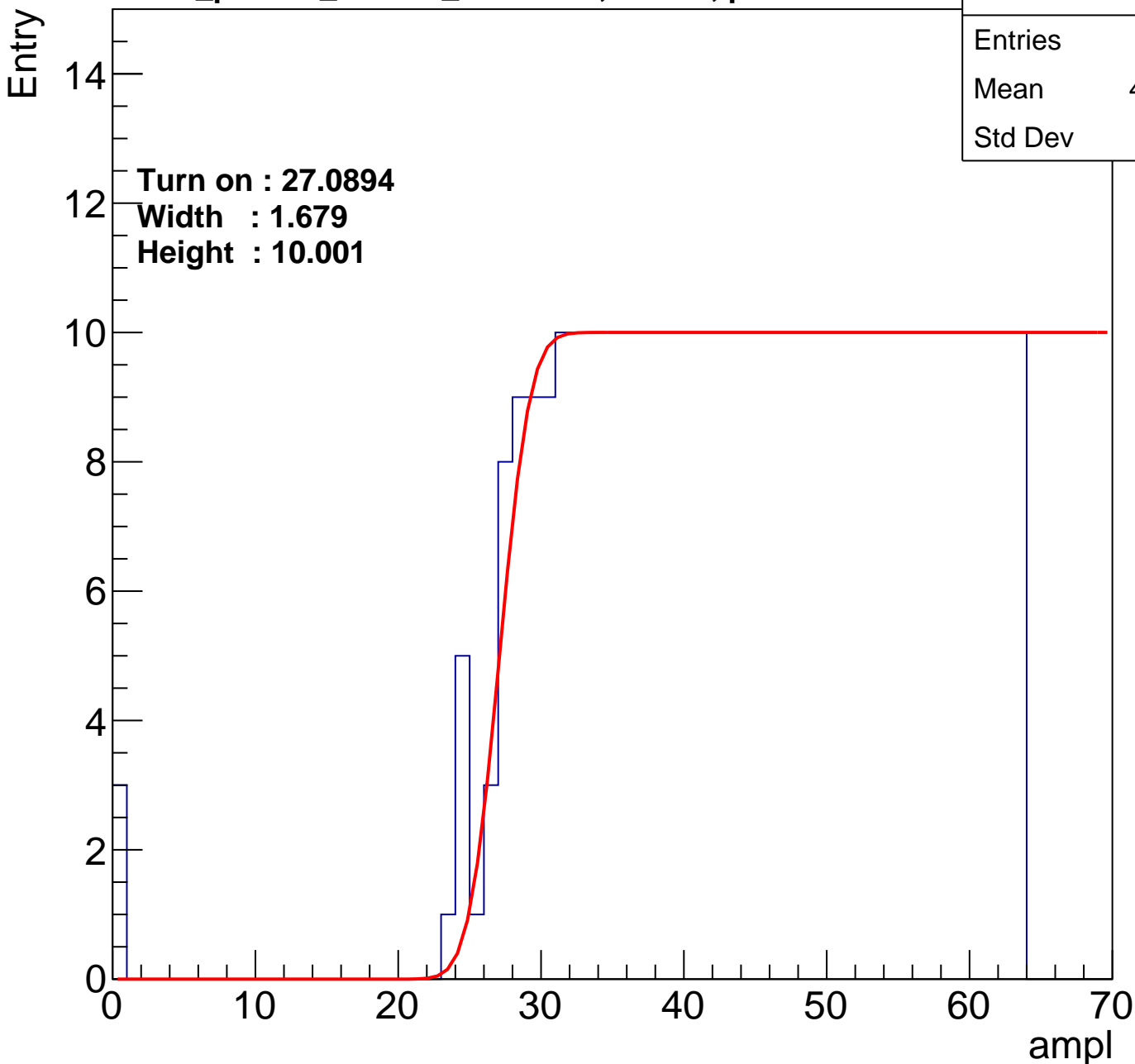
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 27.0894

Width : 1.679

Height : 10.001

Entries	378
Mean	44.33
Std Dev	11.6



B1L002S, U23-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.5348

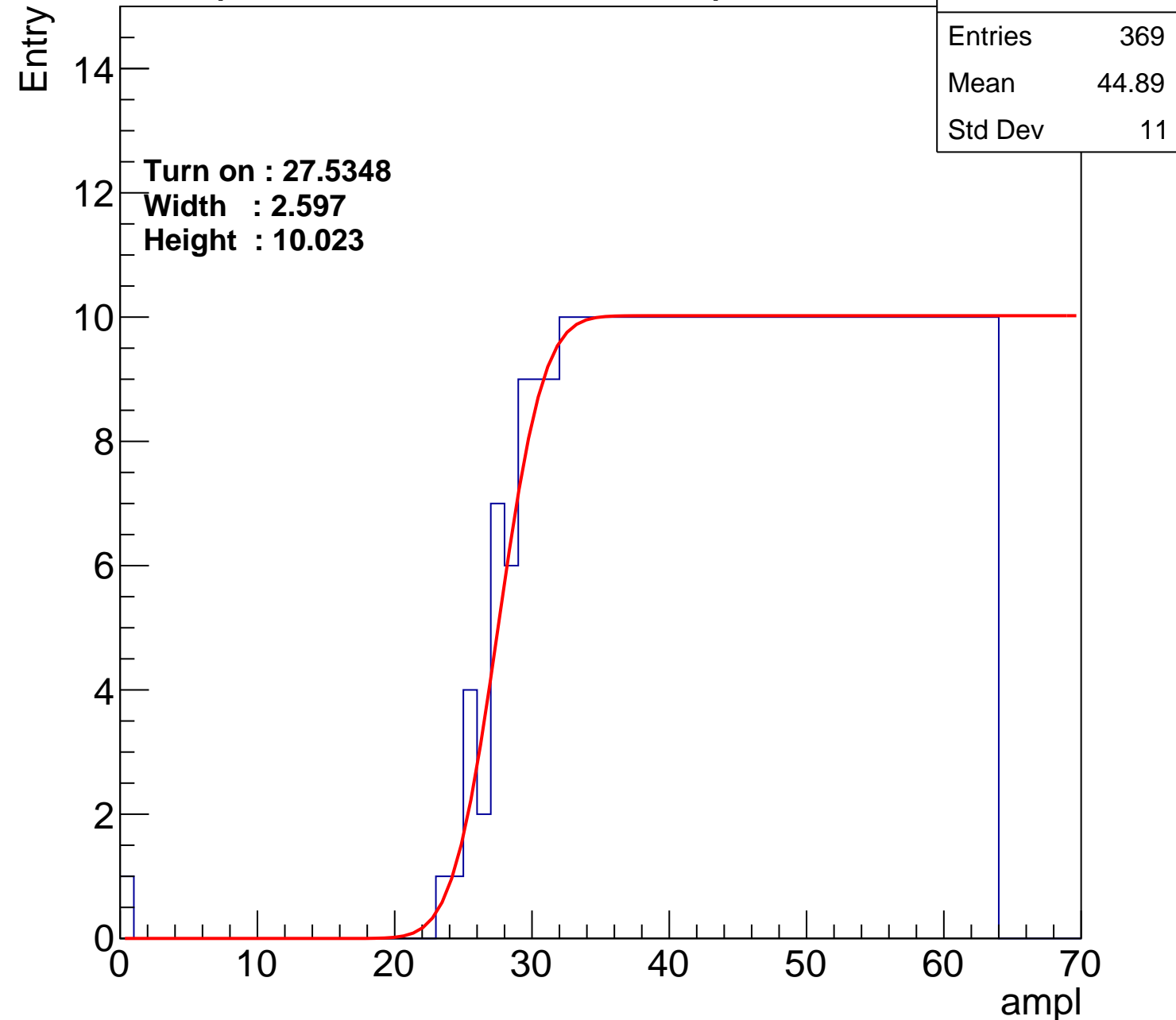
Width : 2.597

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.78
Std Dev	12.42

Turn on : 23.6744

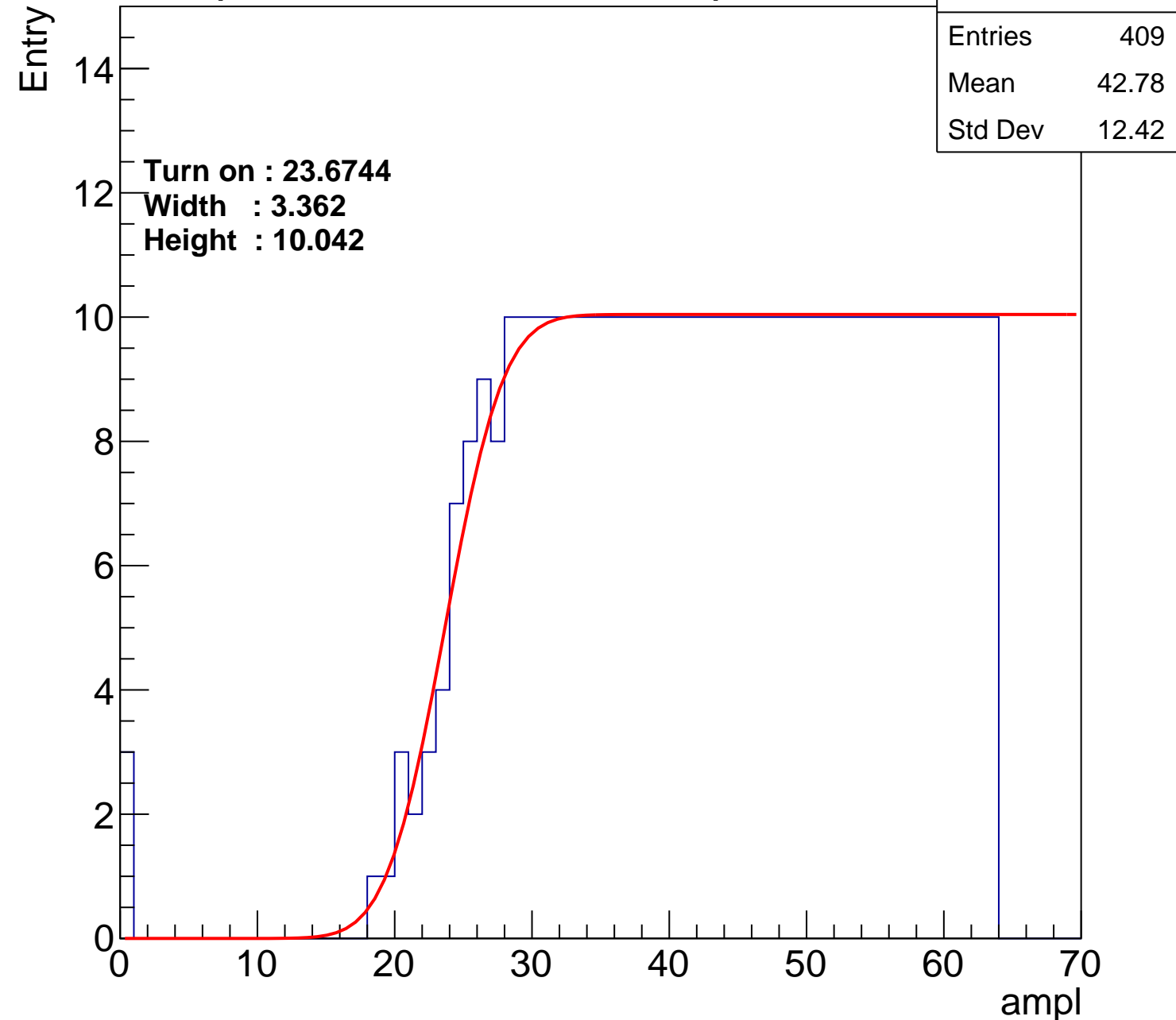
Width : 3.362

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.93
Std Dev	11.63

Turn on : 25.4198

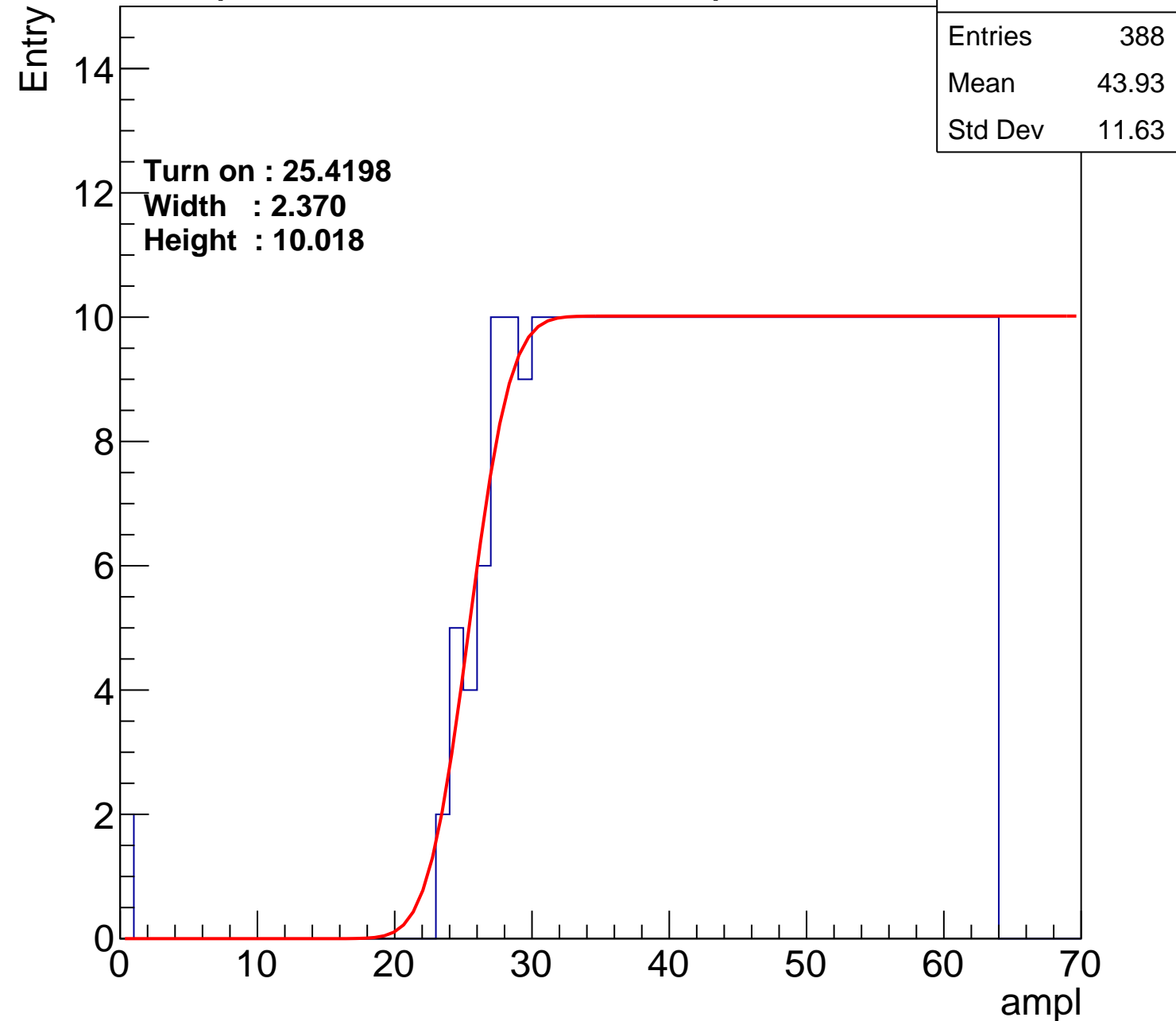
Width : 2.370

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	44.95
Std Dev	11.11

Turn on : 27.9923

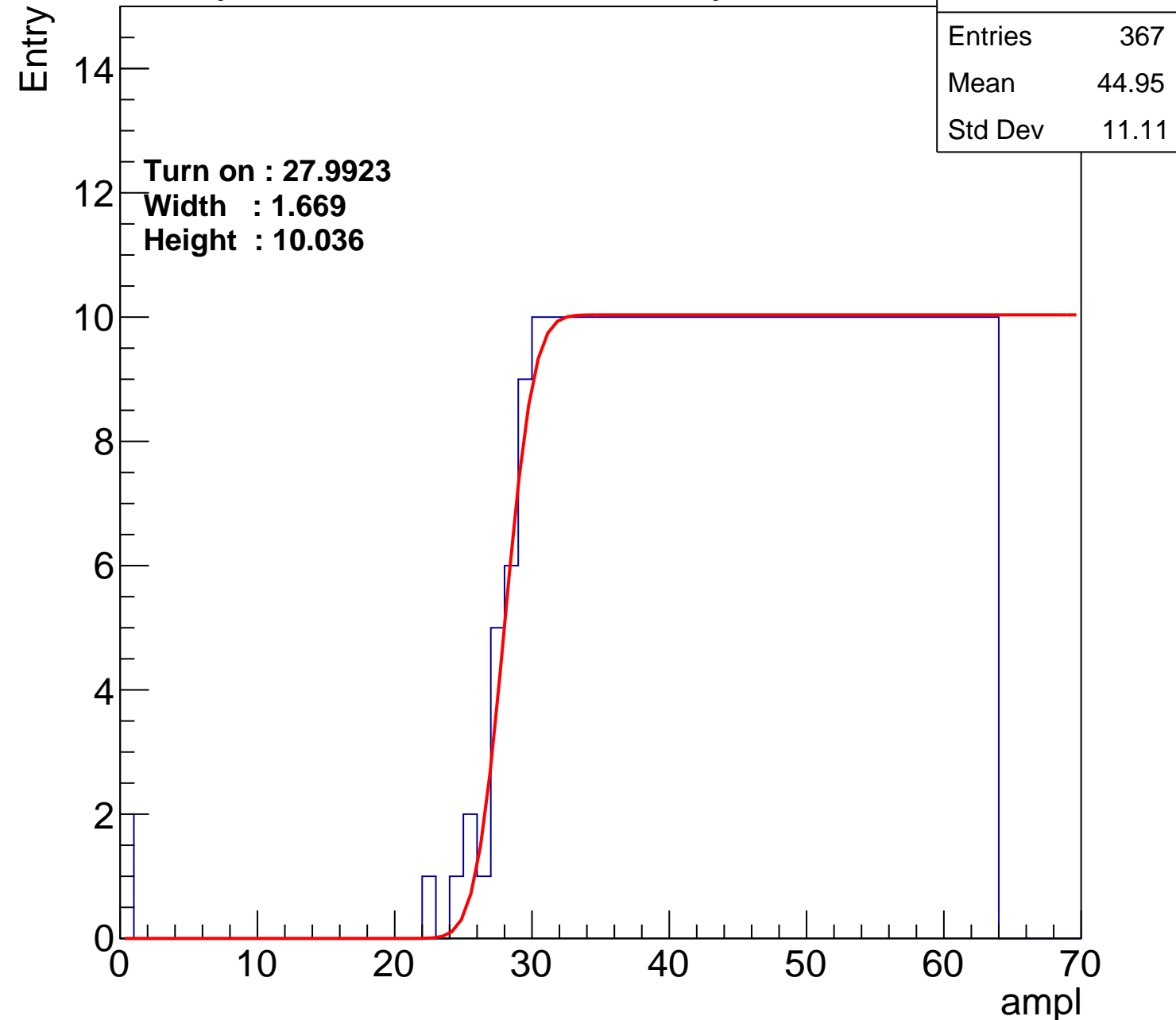
Width : 1.669

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.48
Std Dev	11.75

Turn on : 25.0774

Width : 2.356

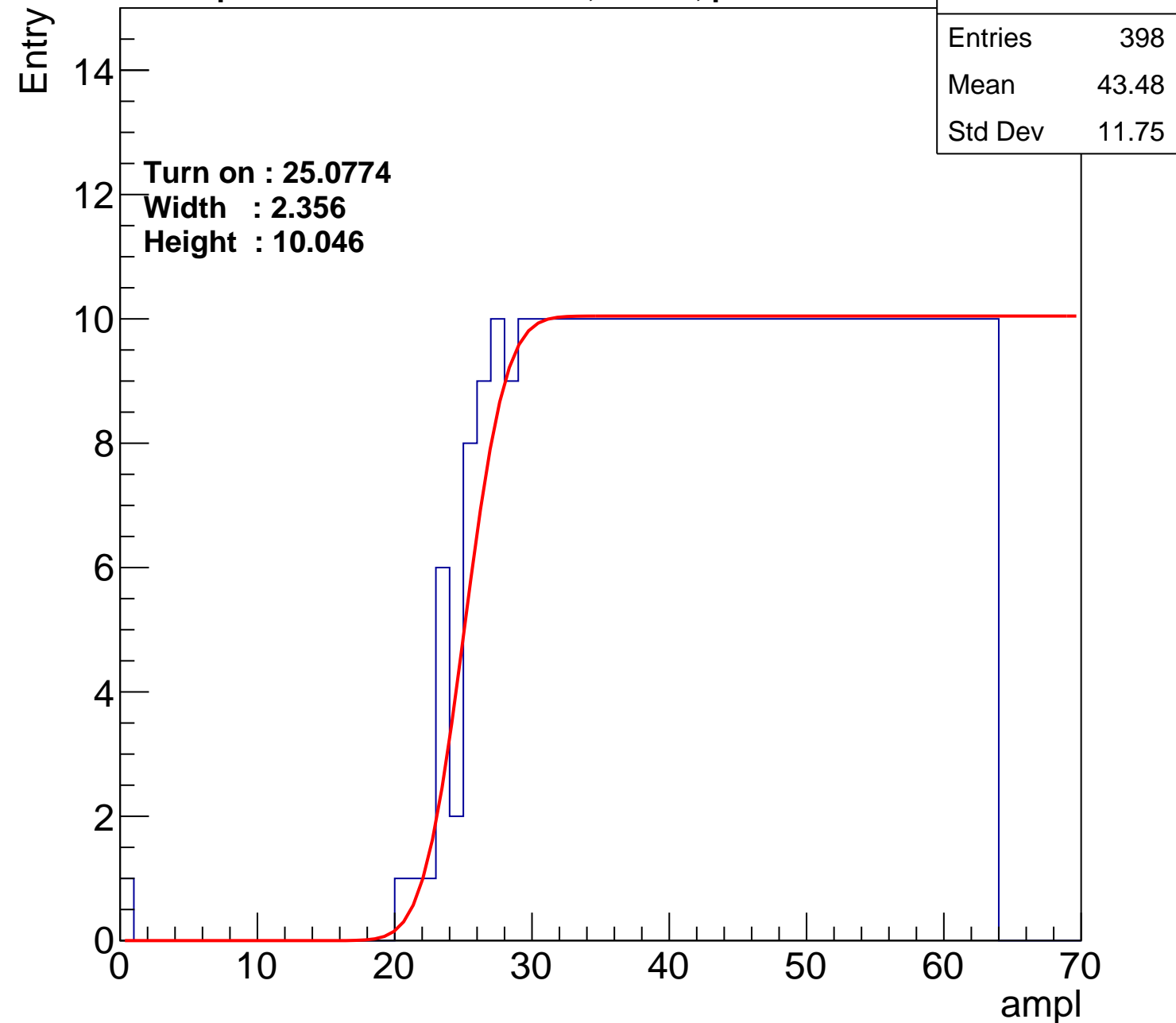
Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U23-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.93
Std Dev	11.51

Turn on : 25.5517

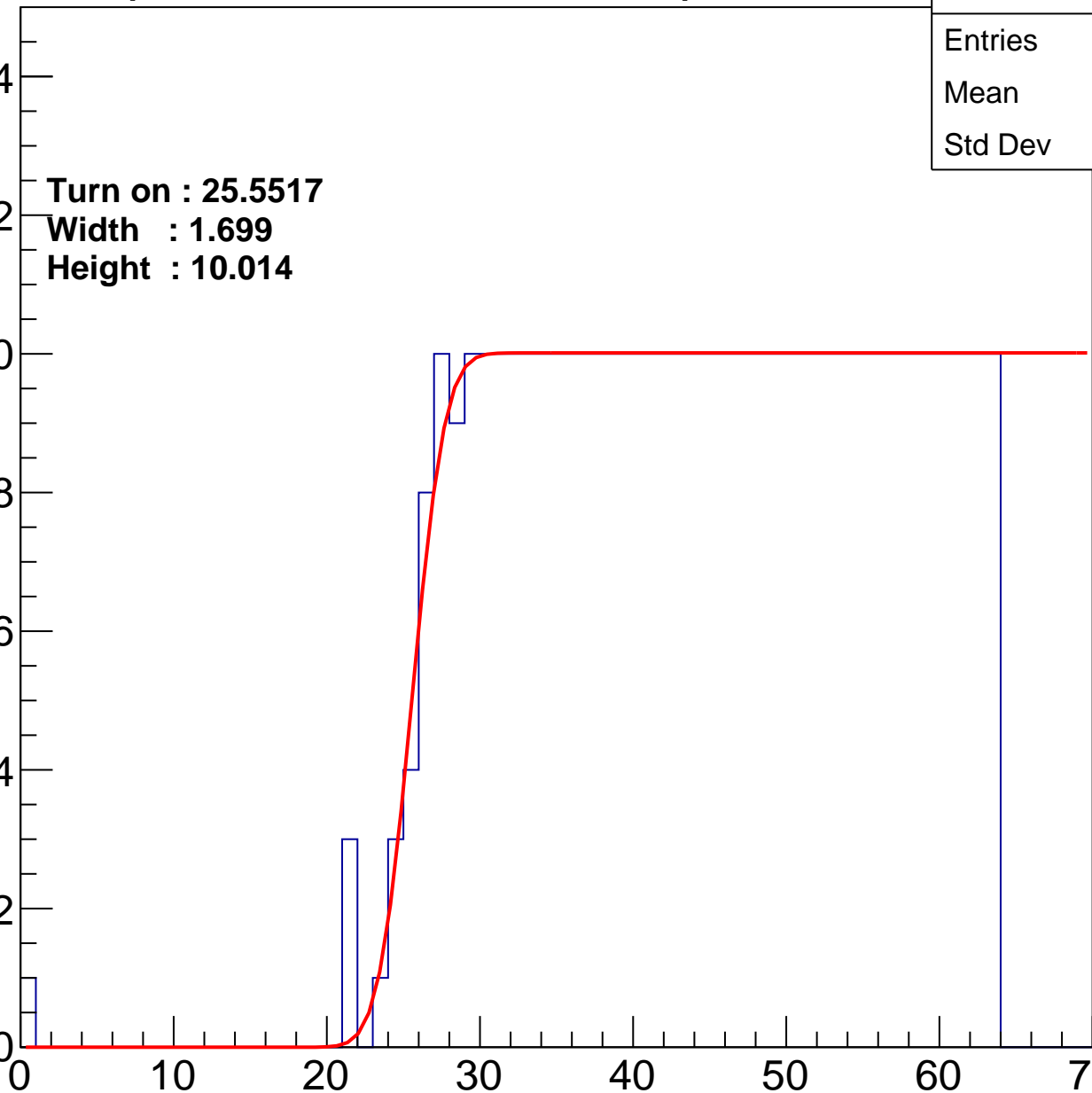
Width : 1.699

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch28

calib_packv5_042523_0143.root, FC#10, port B3

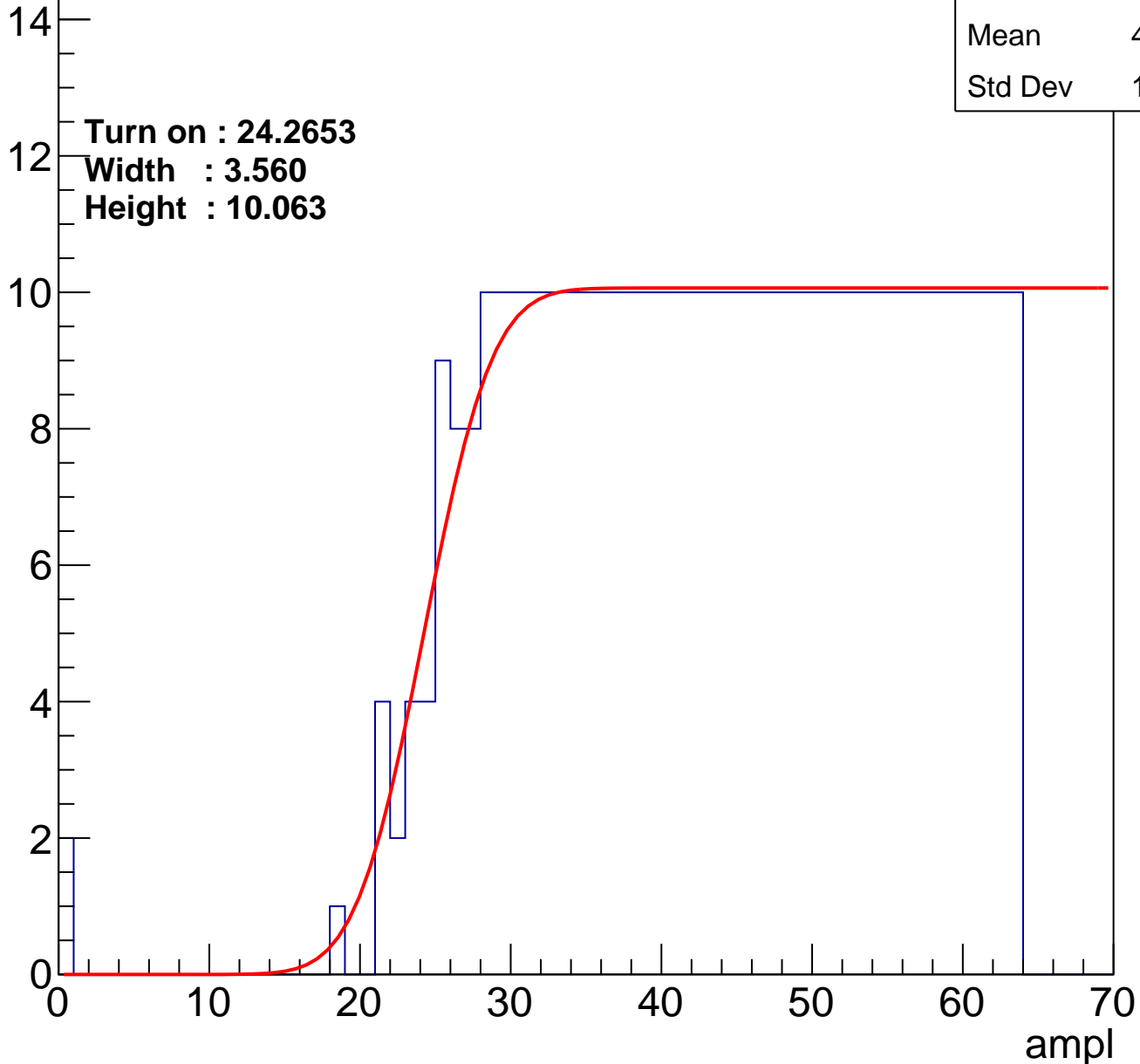
Entries	402
Mean	43.19
Std Dev	12.07

Turn on : 24.2653

Width : 3.560

Height : 10.063

Entry



B1L002S, U23-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.54

Turn on : 25.3199

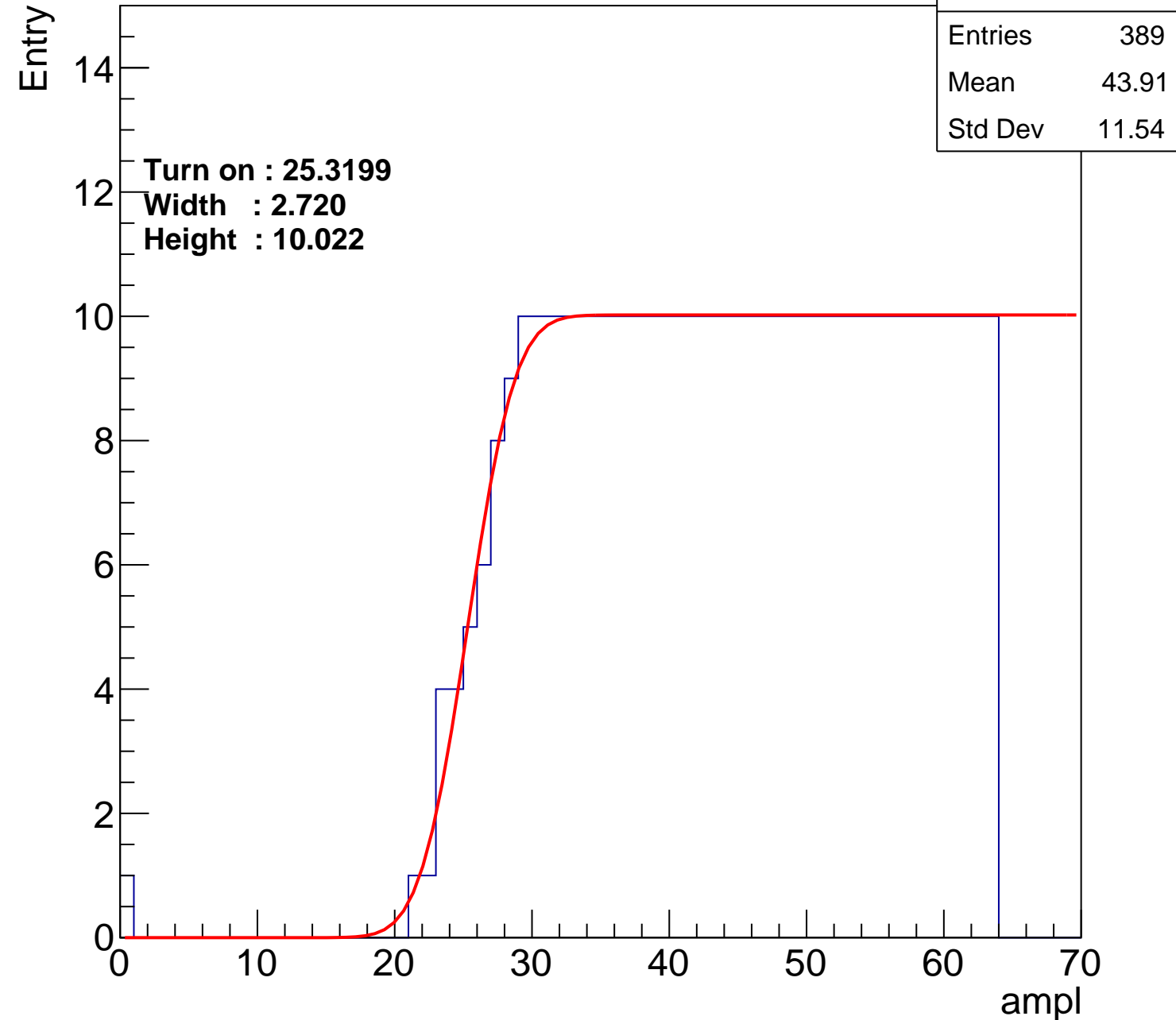
Width : 2.720

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch30

calib_packv5_042523_0143.root, FC#10, port B3

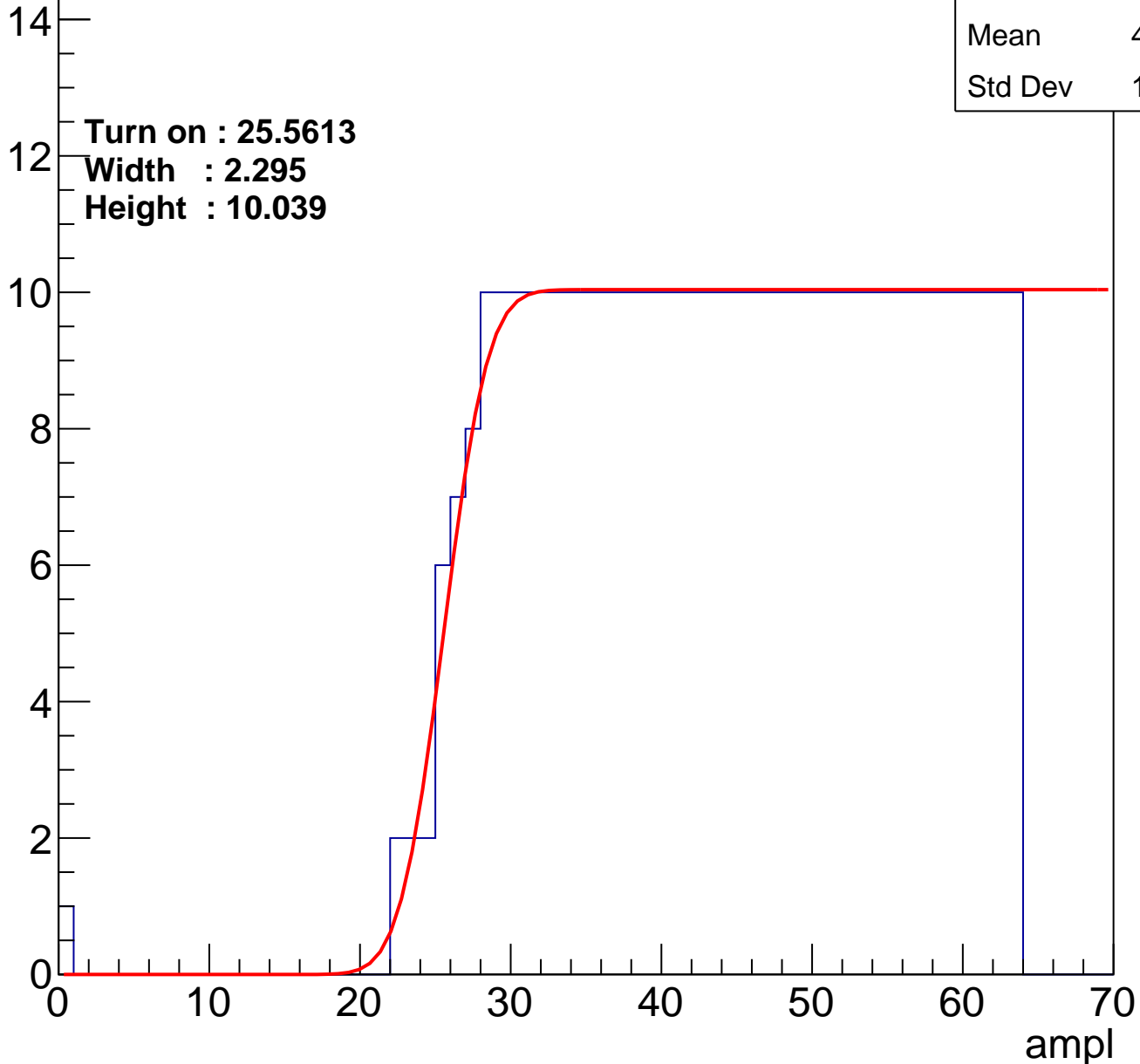
Entries	388
Mean	43.98
Std Dev	11.46

Turn on : 25.5613

Width : 2.295

Height : 10.039

Entry



B1L002S, U23-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	11.99

Turn on : 24.9747

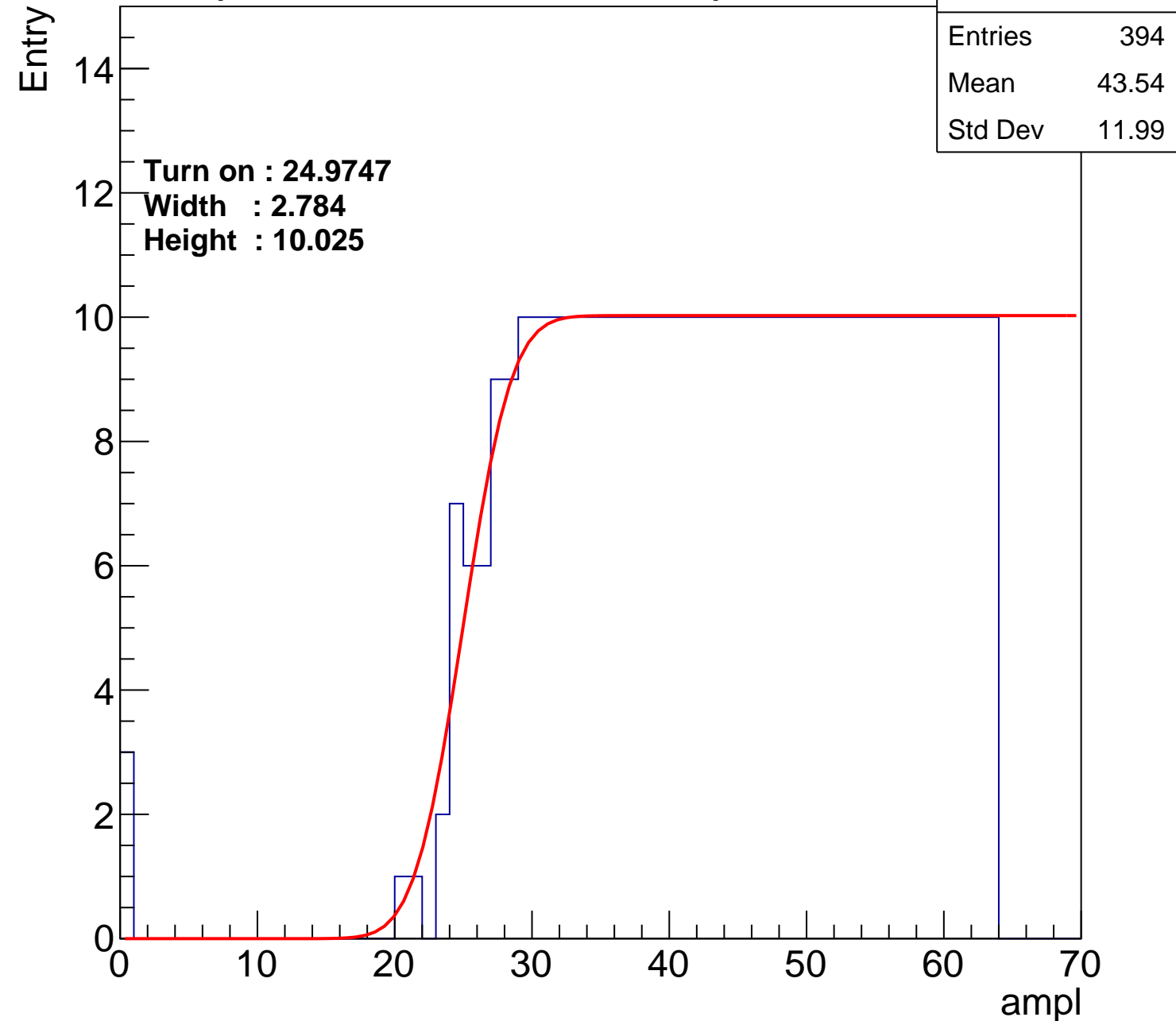
Width : 2.784

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.2506

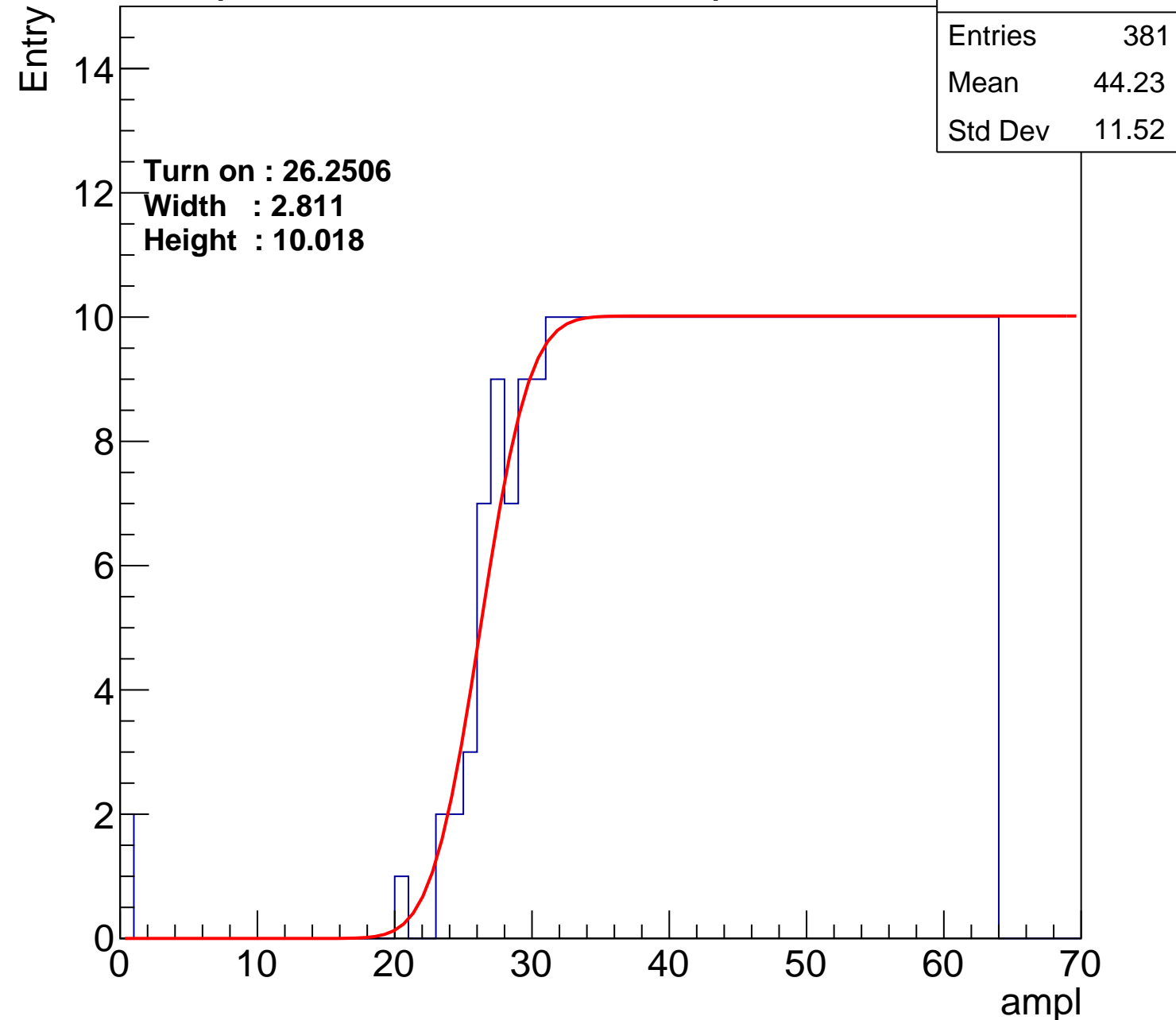
Width : 2.811

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch33

calib_packv5_042523_0143.root, FC#10, port B3

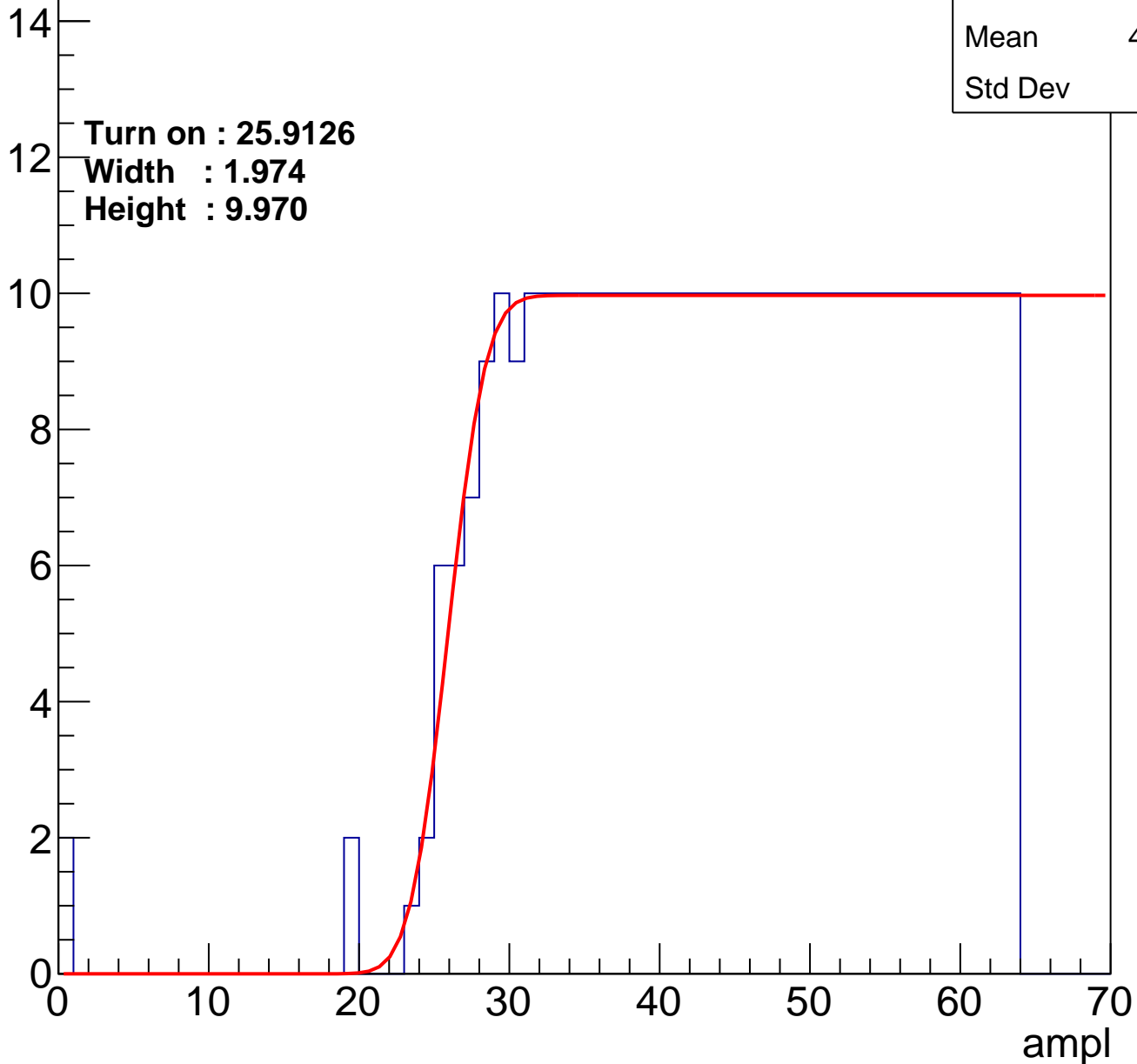
Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 25.9126

Width : 1.974

Height : 9.970

Entry



B1L002S, U23-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 25.2143

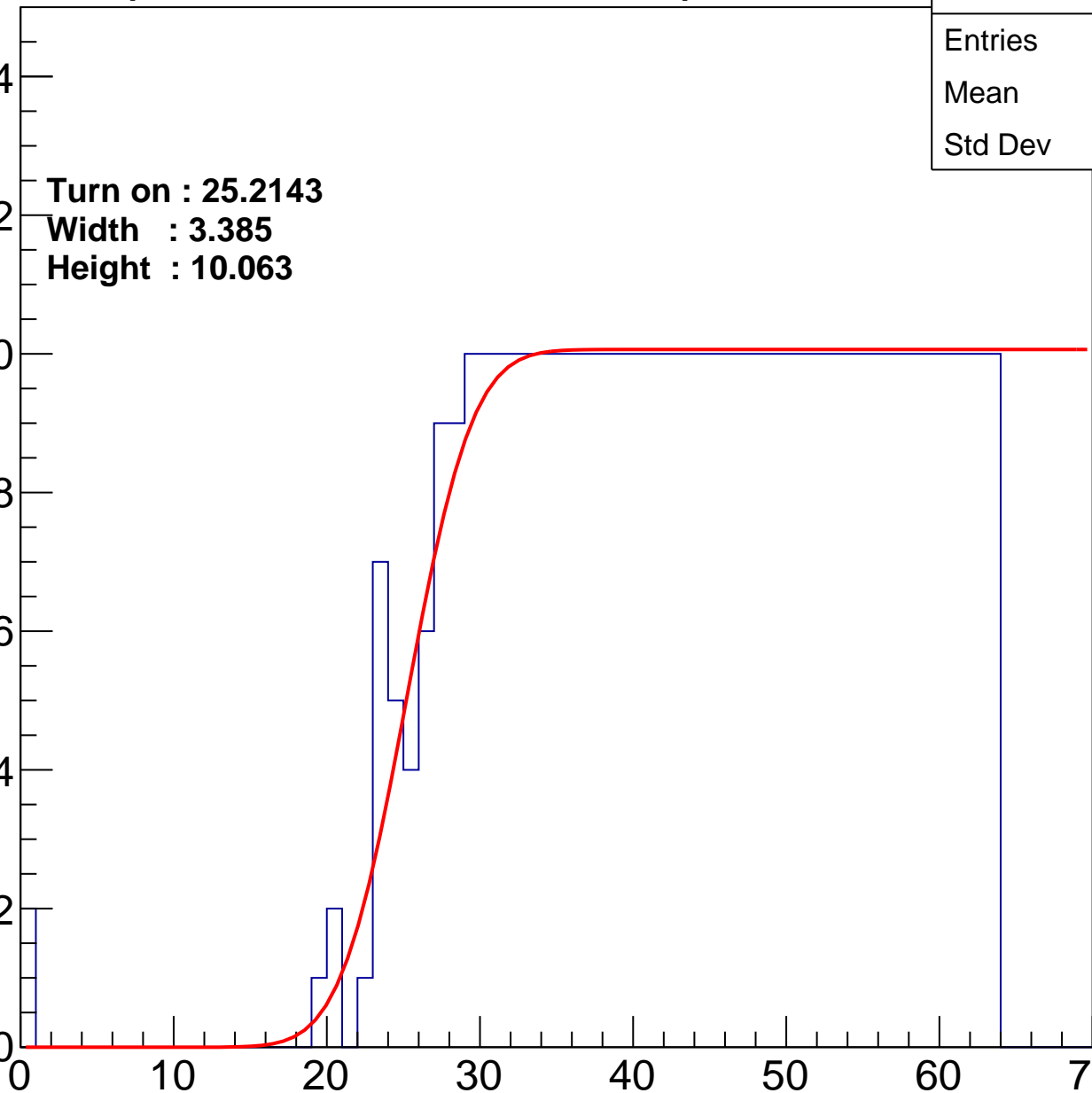
Width : 3.385

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 26.3334

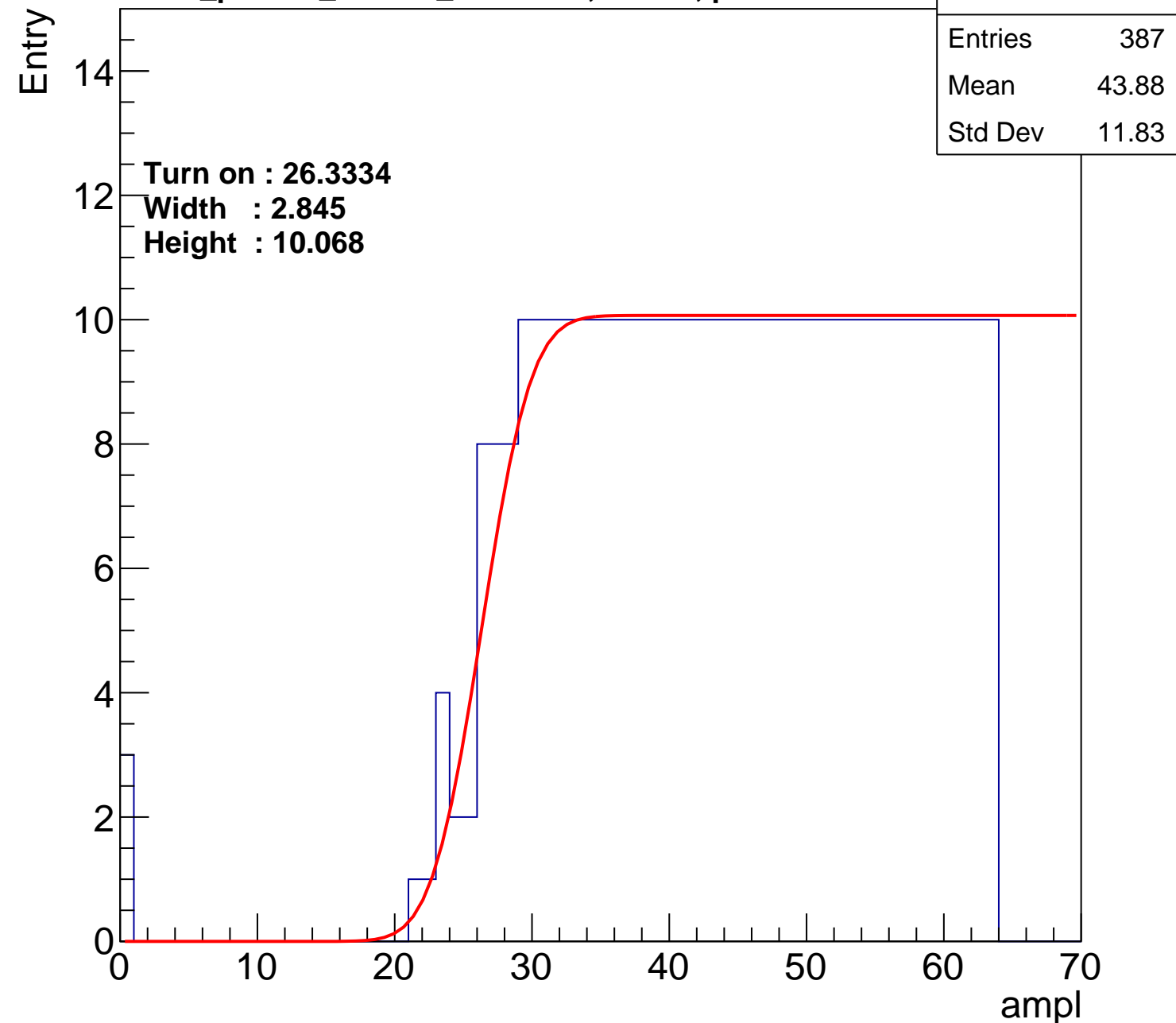
Width : 2.845

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.25
Std Dev	11.92

Turn on : 24.4869

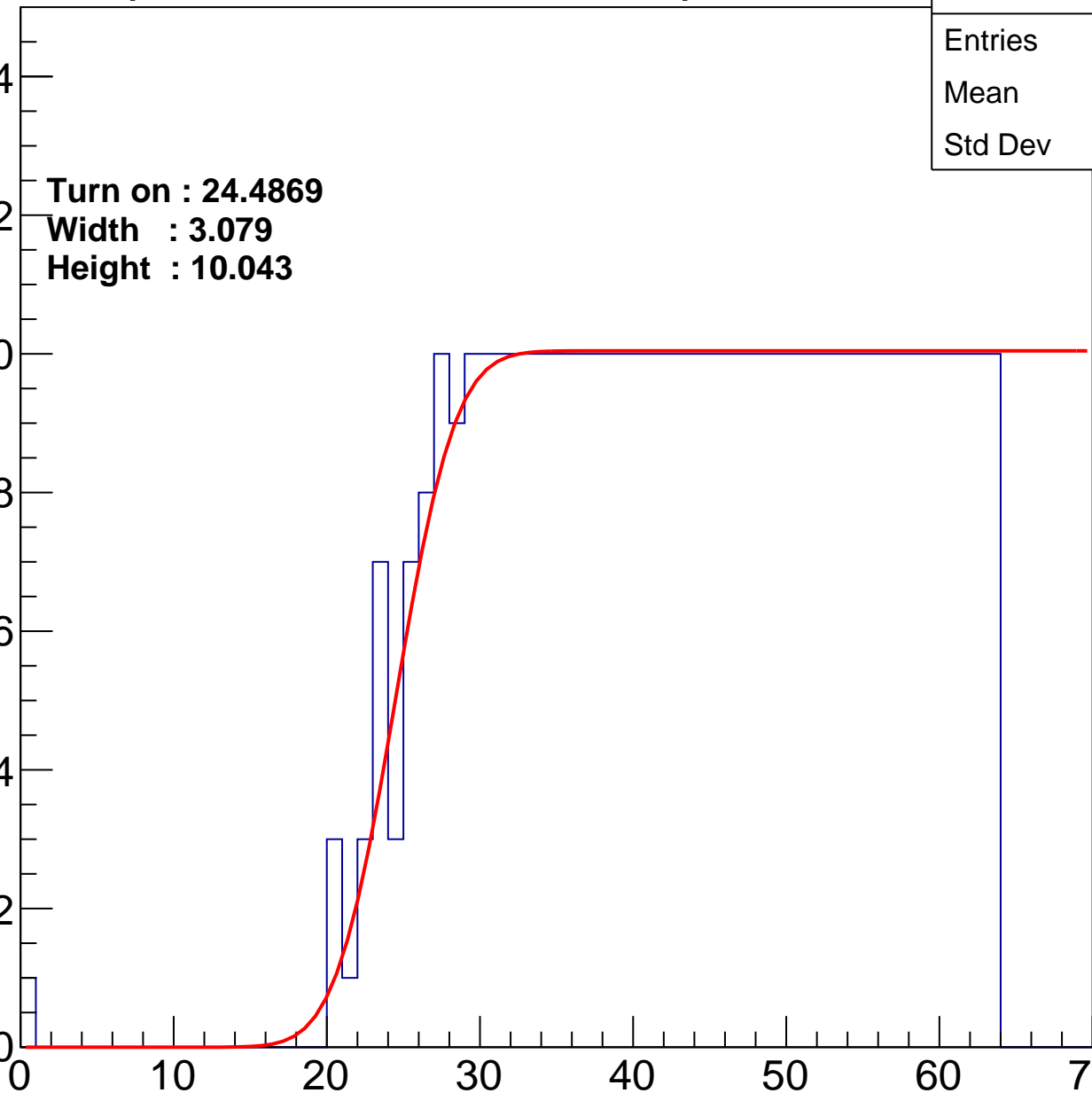
Width : 3.079

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.84

Turn on : 24.5124

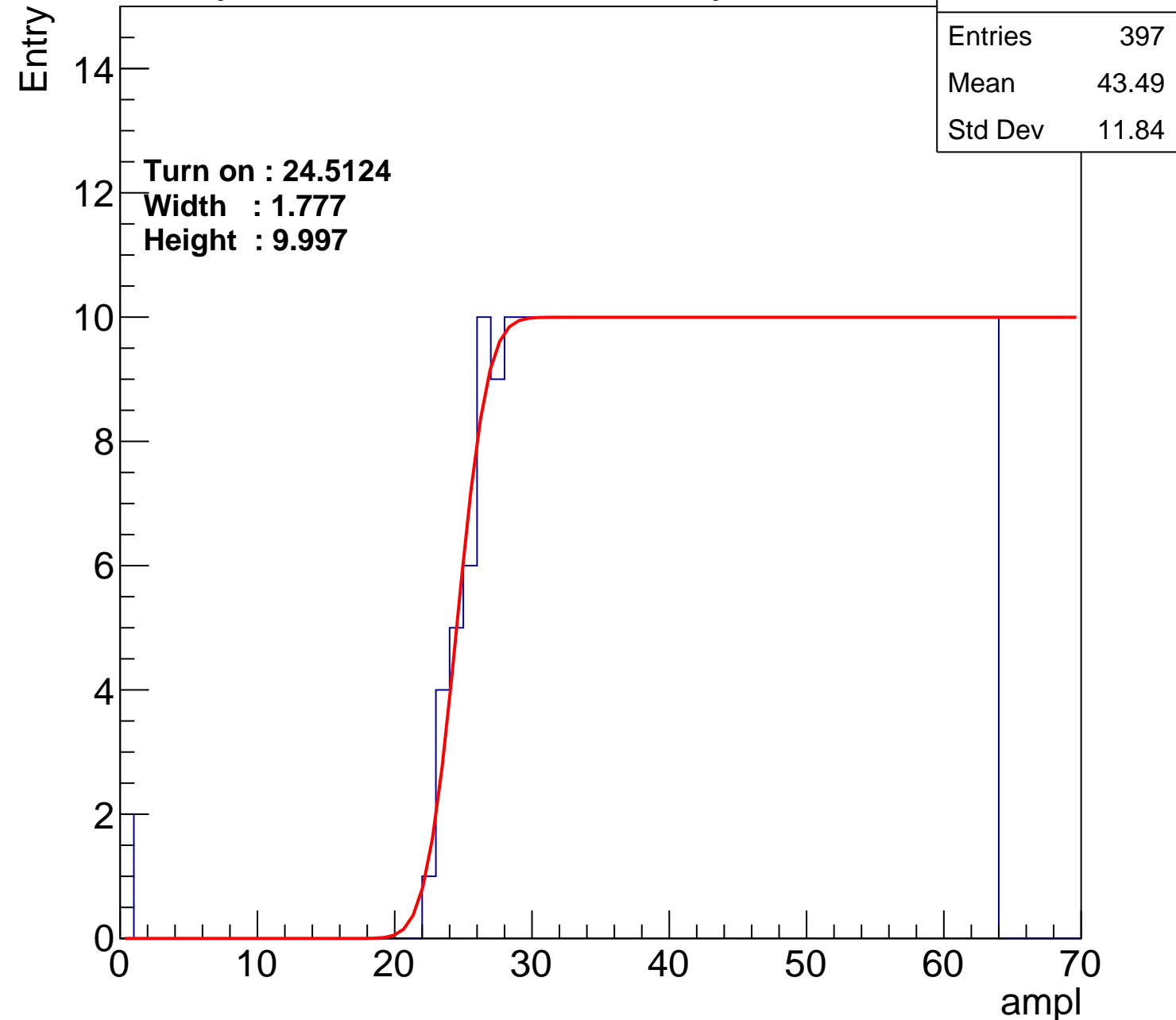
Width : 1.777

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.4640

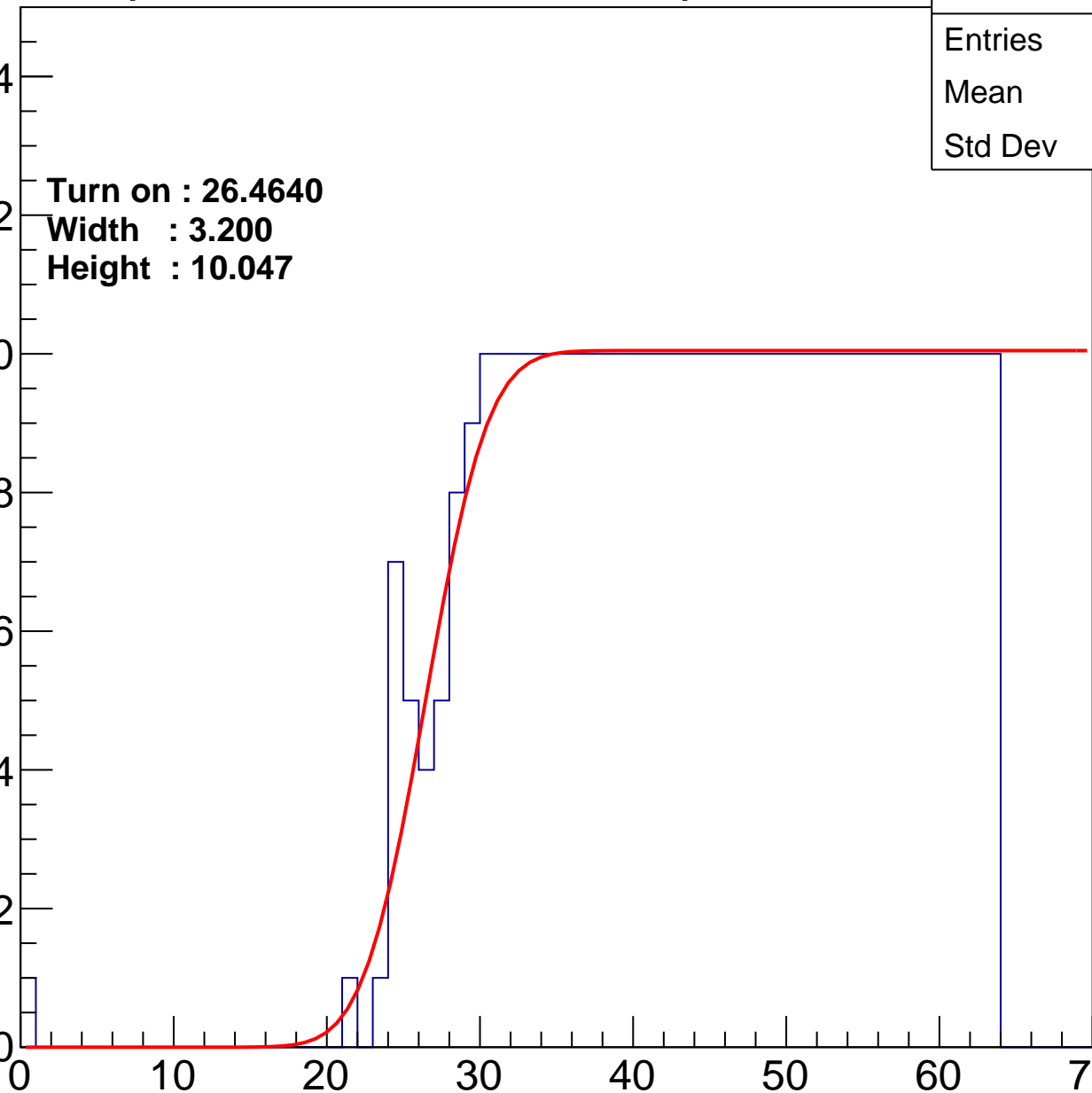
Width : 3.200

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.64
Std Dev	11.81

Turn on : 25.2230

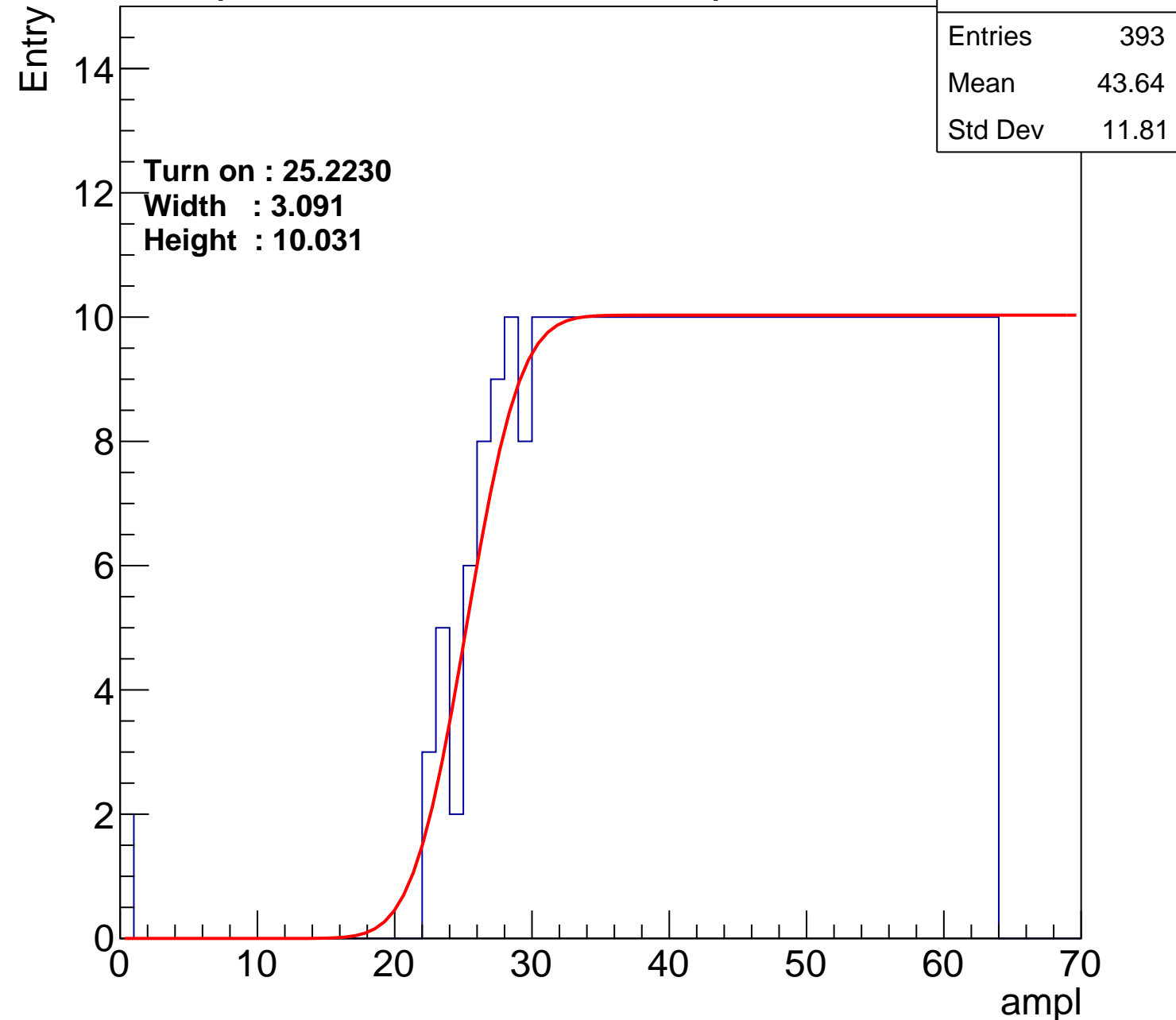
Width : 3.091

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.48
Std Dev	11.19

Turn on : 26.4859

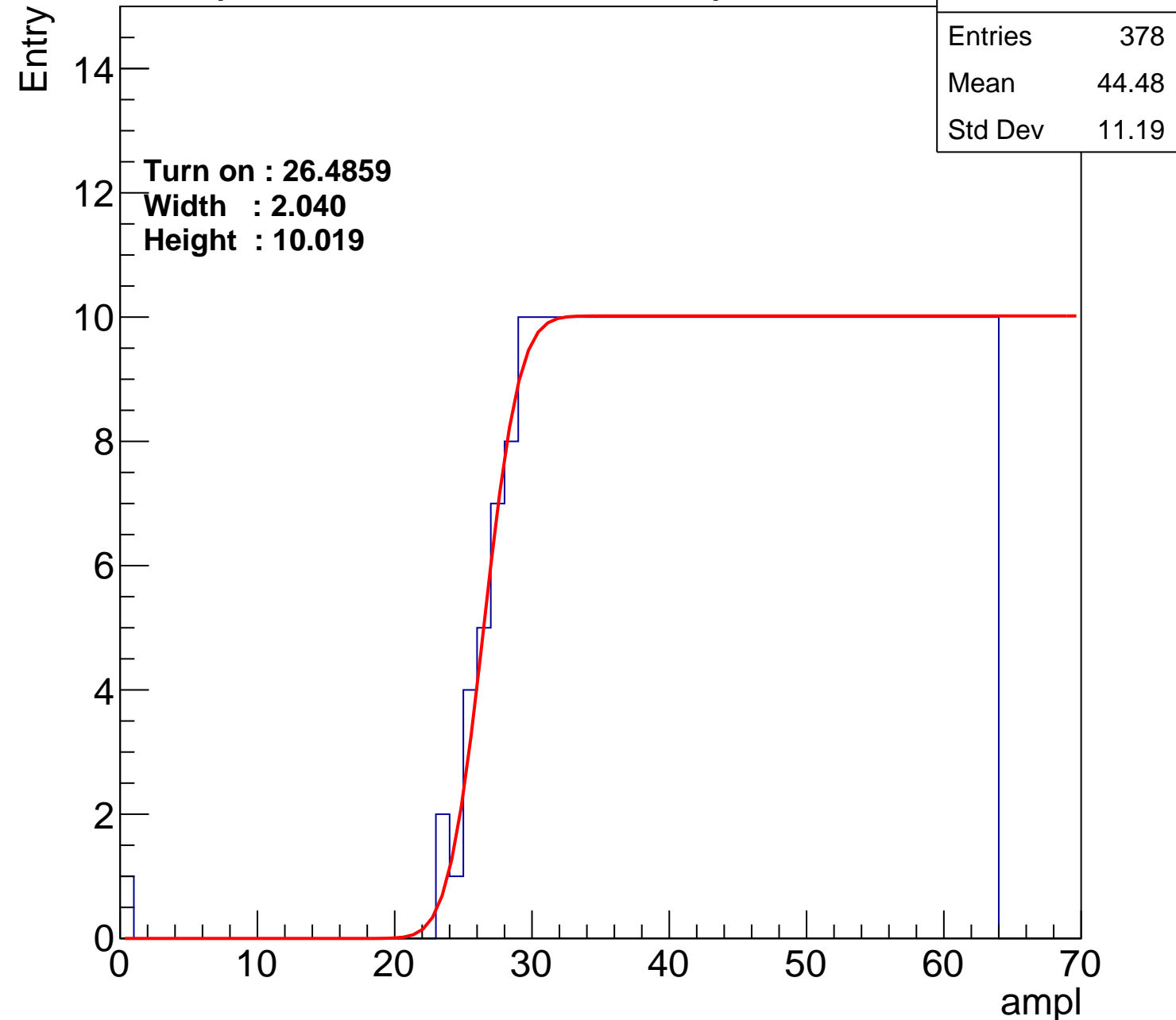
Width : 2.040

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.28
Std Dev	12.25

Turn on : 24.8885

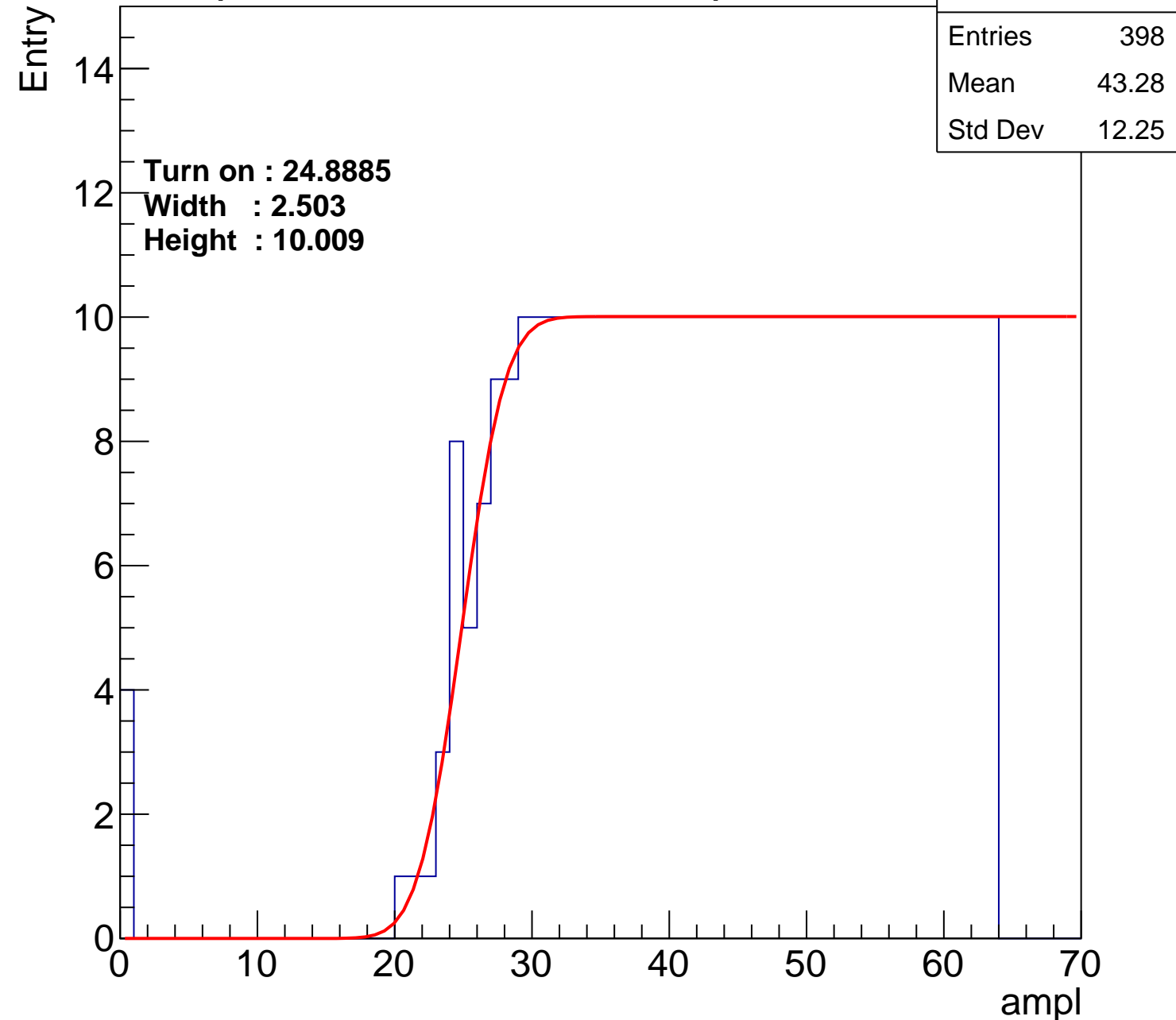
Width : 2.503

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.88
Std Dev	12.36

Turn on : 22.7817

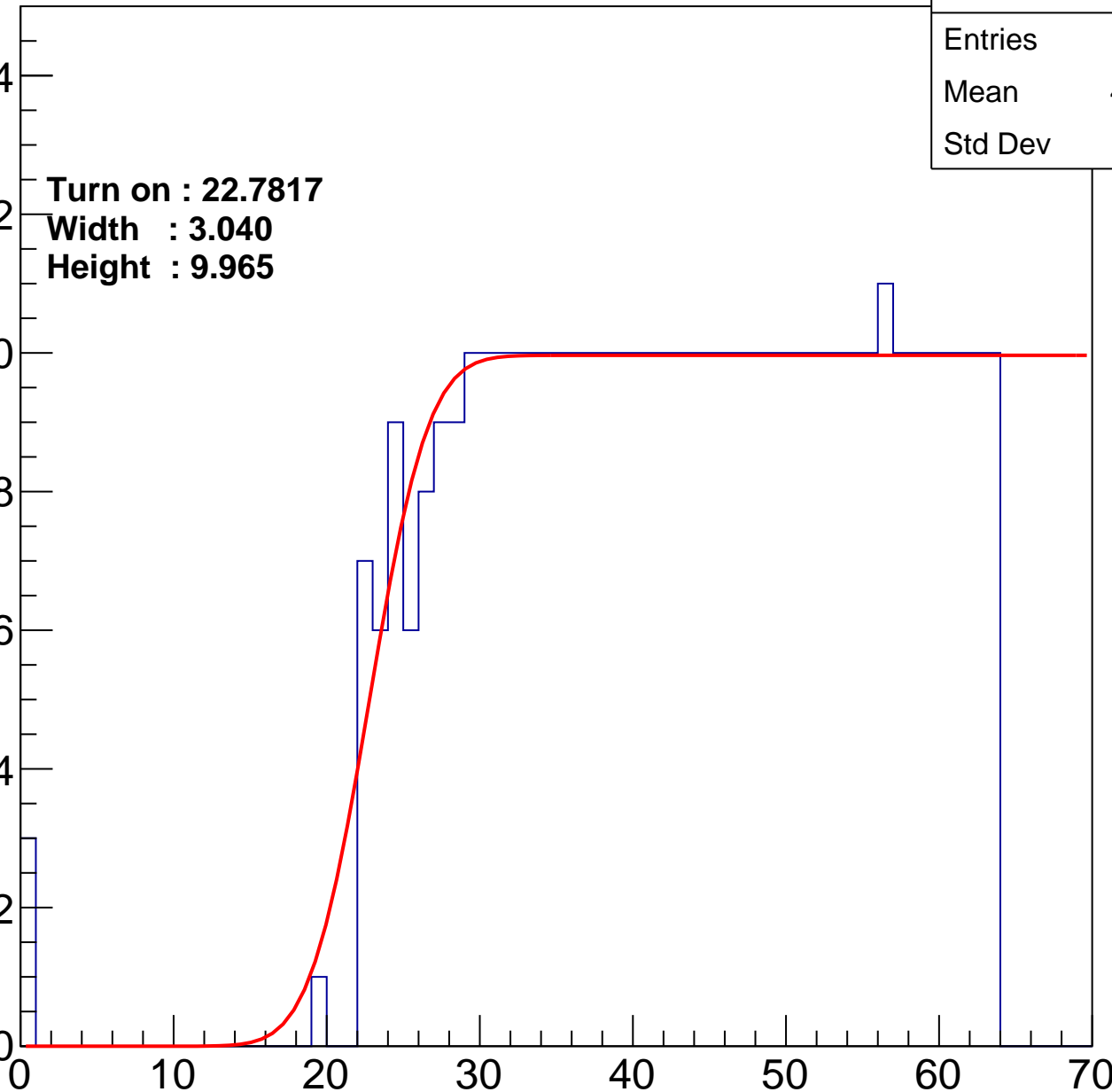
Width : 3.040

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch43

calib_packv5_042523_0143.root, FC#10, port B3

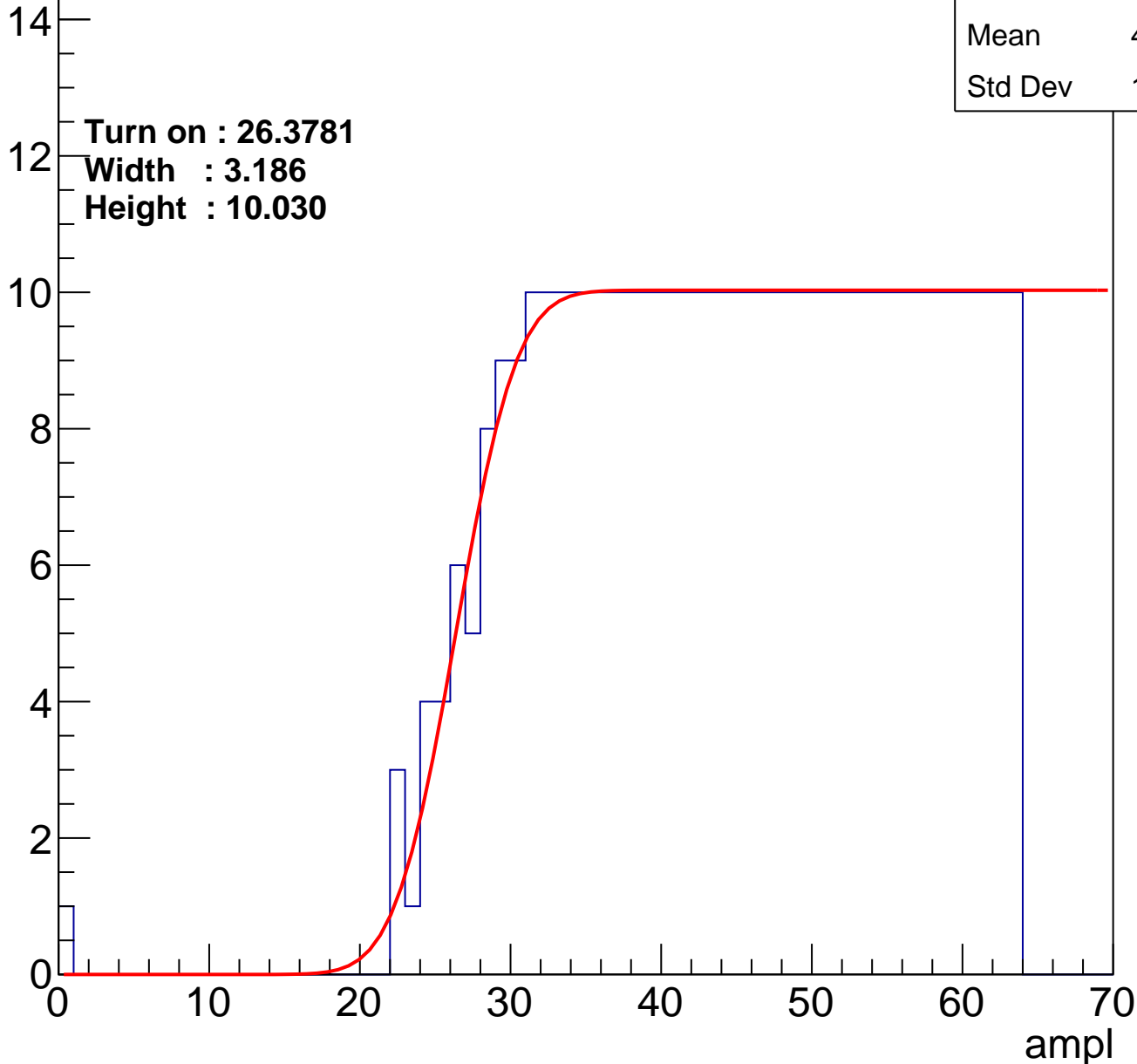
Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.3781

Width : 3.186

Height : 10.030

Entry



B1L002S, U23-ch44

calib_packv5_042523_0143.root, FC#10, port B3

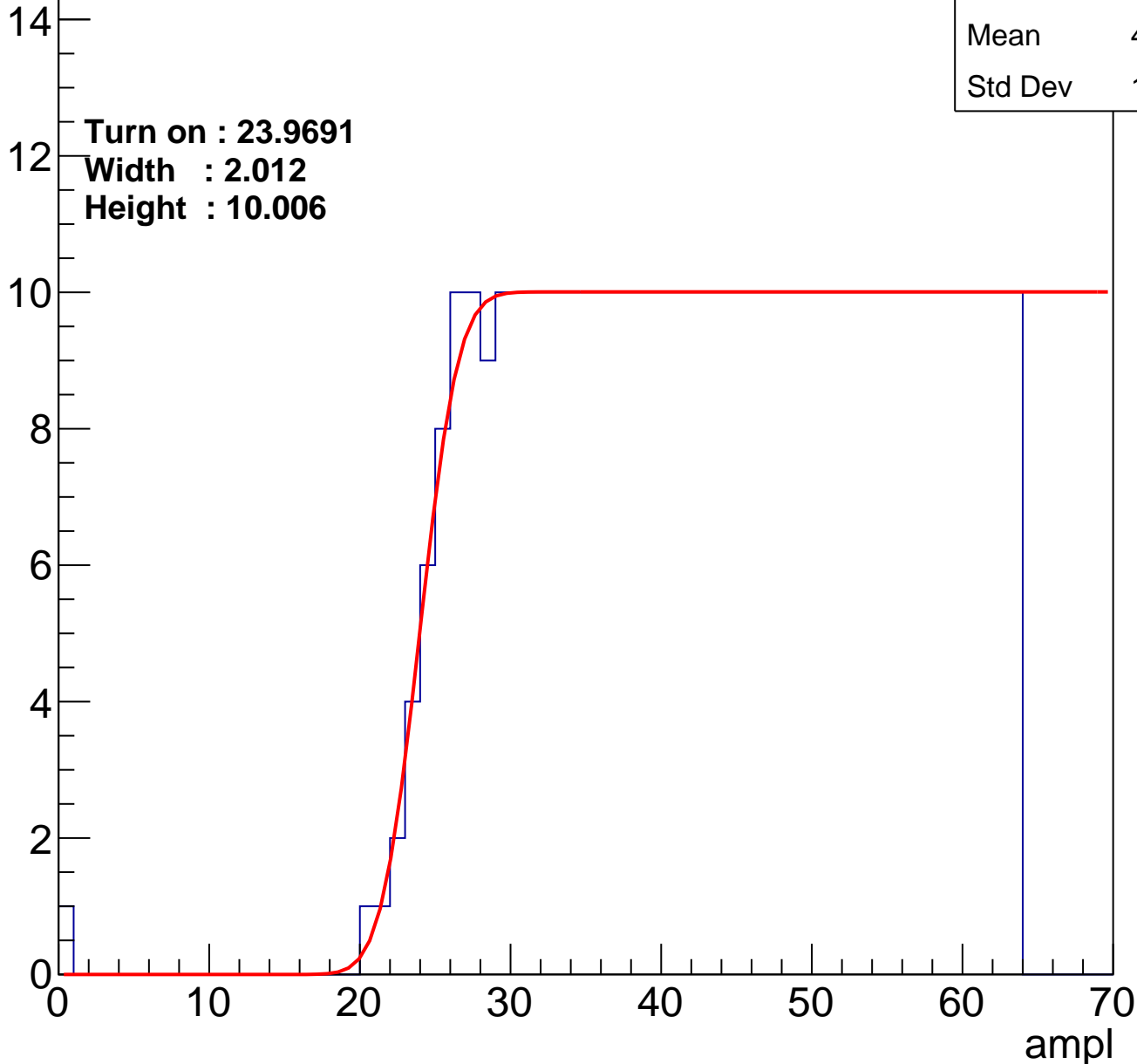
Entries	402
Mean	43.29
Std Dev	11.84

Turn on : 23.9691

Width : 2.012

Height : 10.006

Entry



B1L002S, U23-ch45

calib_packv5_042523_0143.root, FC#10, port B3

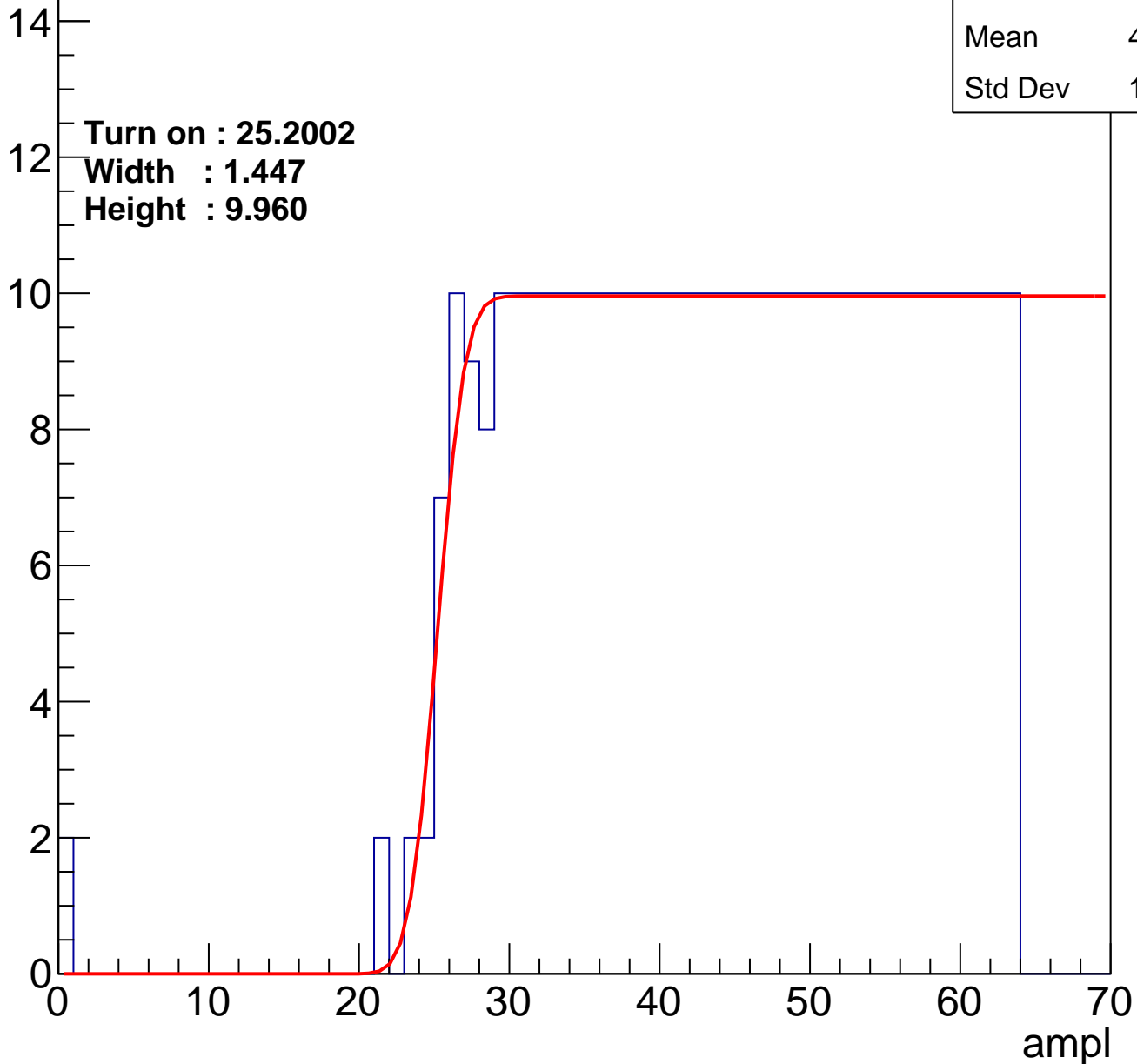
Entries	392
Mean	43.72
Std Dev	11.75

Turn on : 25.2002

Width : 1.447

Height : 9.960

Entry



B1L002S, U23-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.15
Std Dev	12.18

Turn on : 24.3964

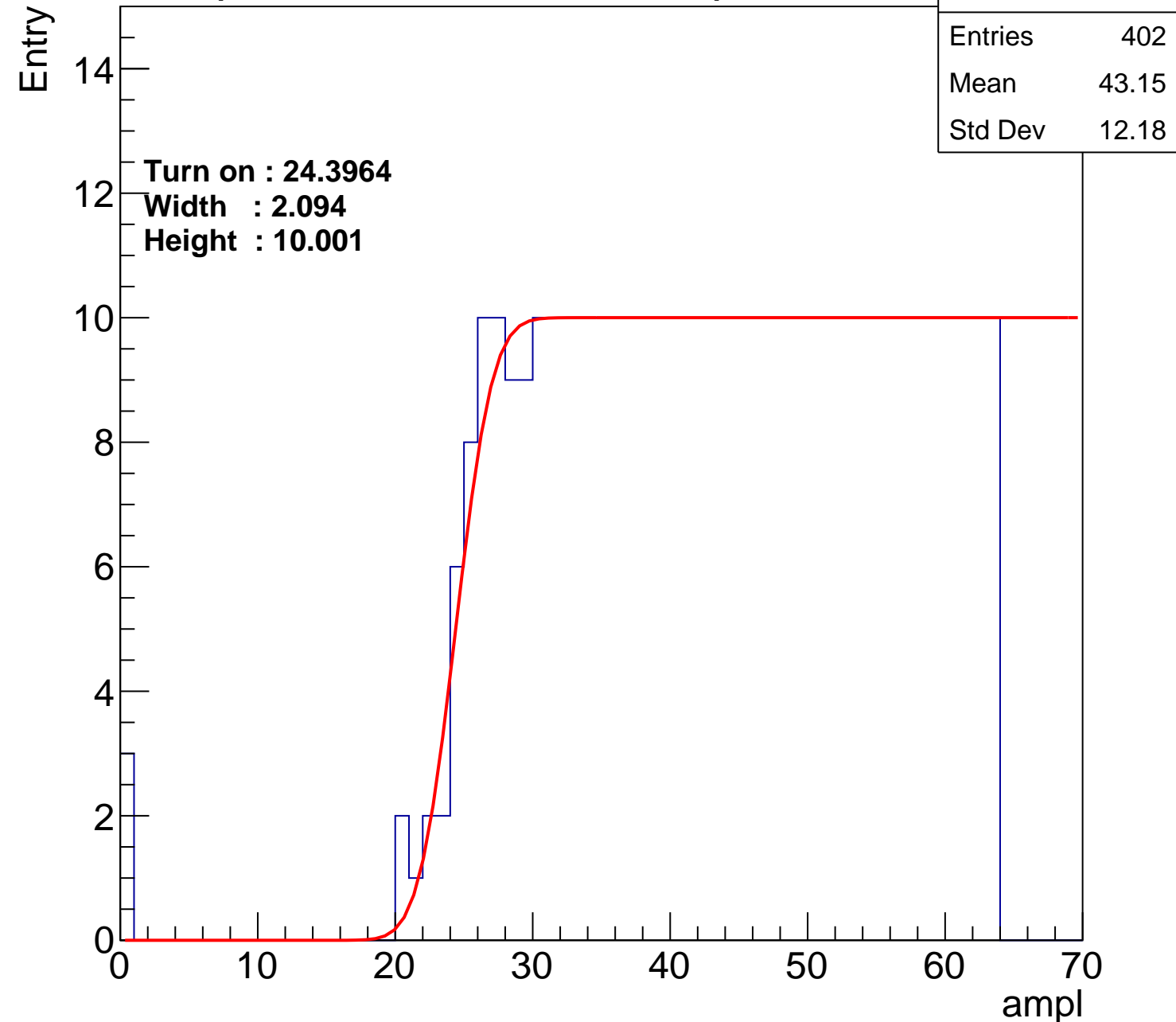
Width : 2.094

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch47

calib_packv5_042523_0143.root, FC#10, port B3

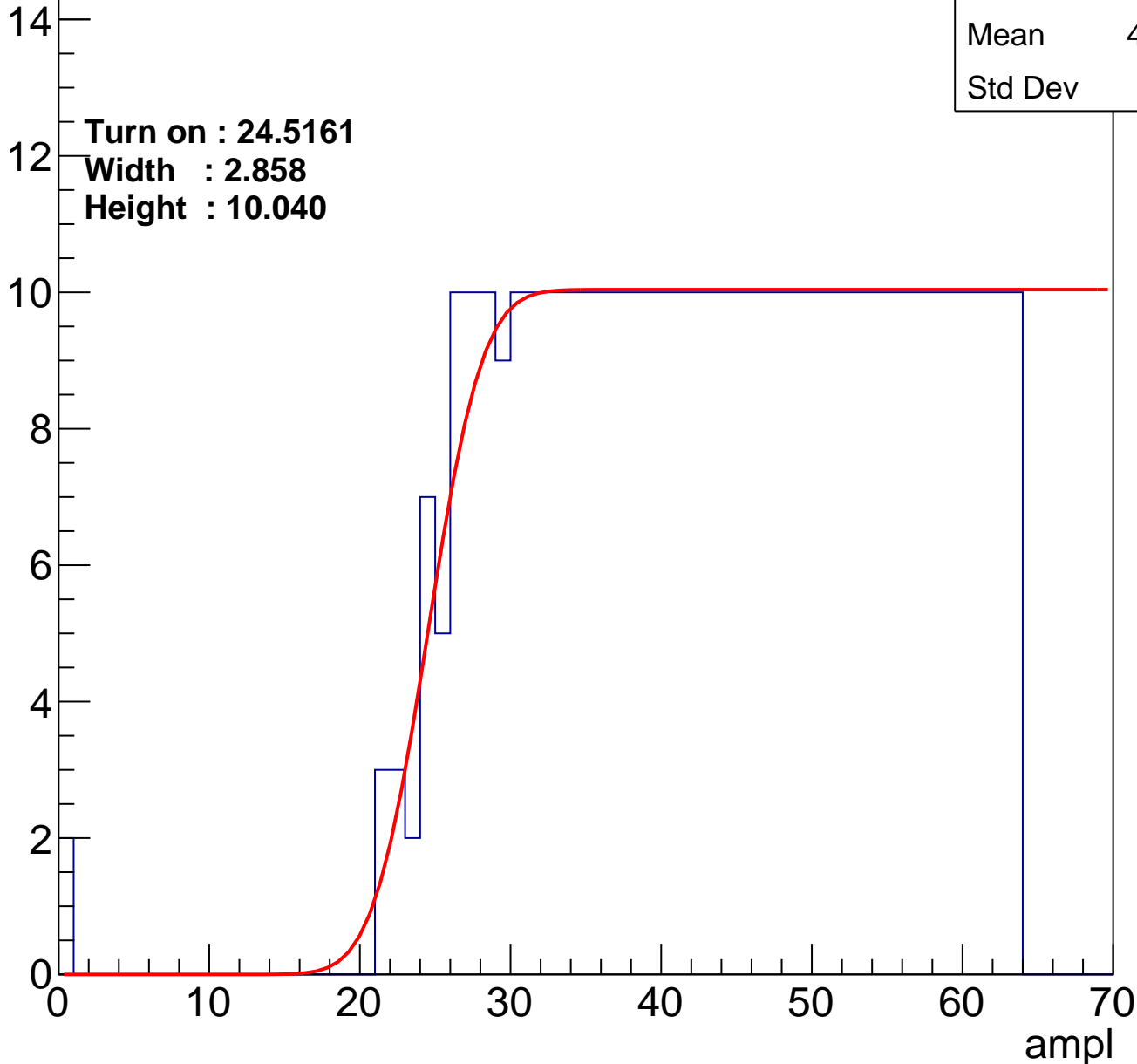
Entries	401
Mean	43.26
Std Dev	12

Turn on : 24.5161

Width : 2.858

Height : 10.040

Entry



B1L002S, U23-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.41
Std Dev	12.05

Turn on : 24.9197

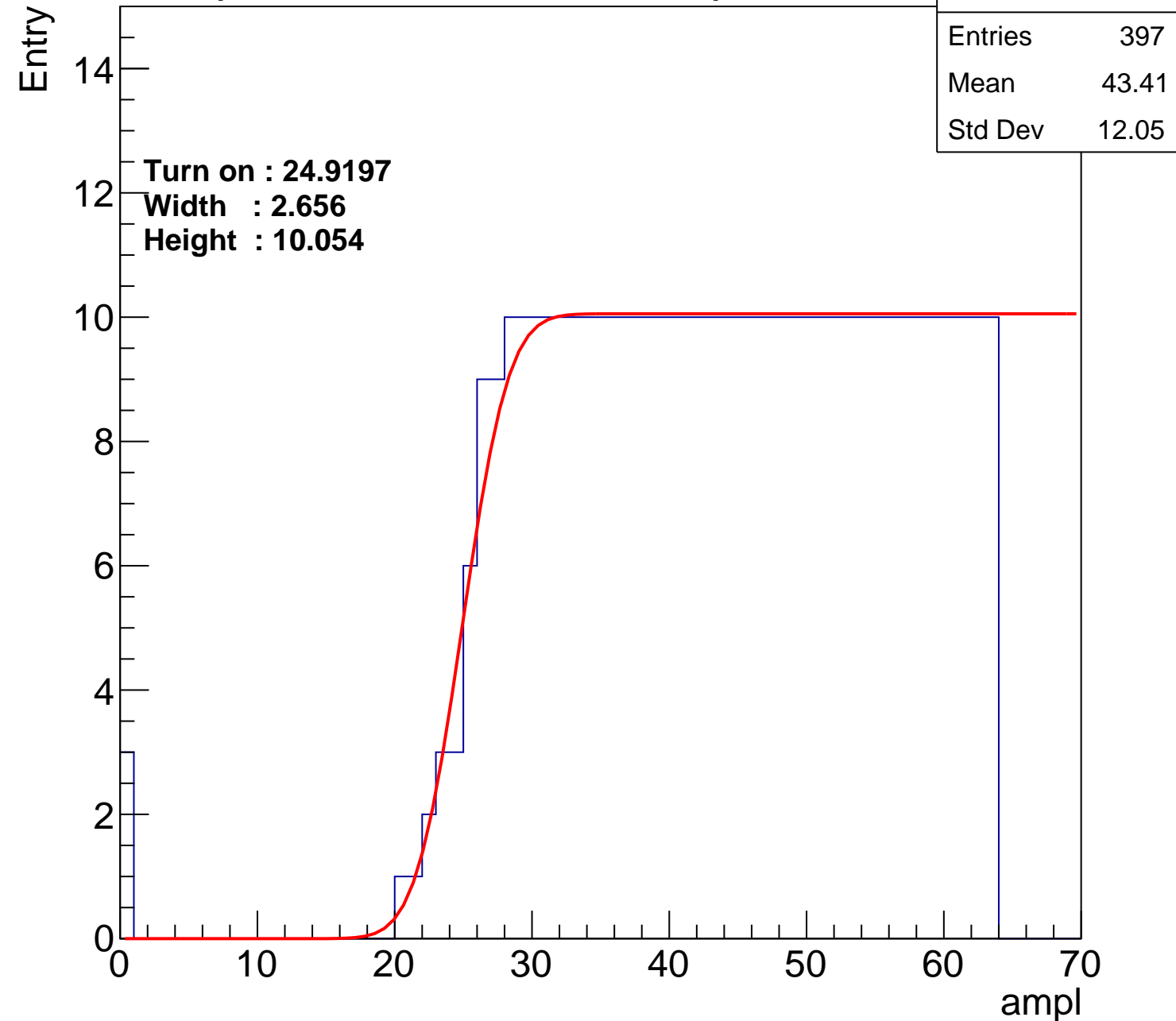
Width : 2.656

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch49

calib_packv5_042523_0143.root, FC#10, port B3

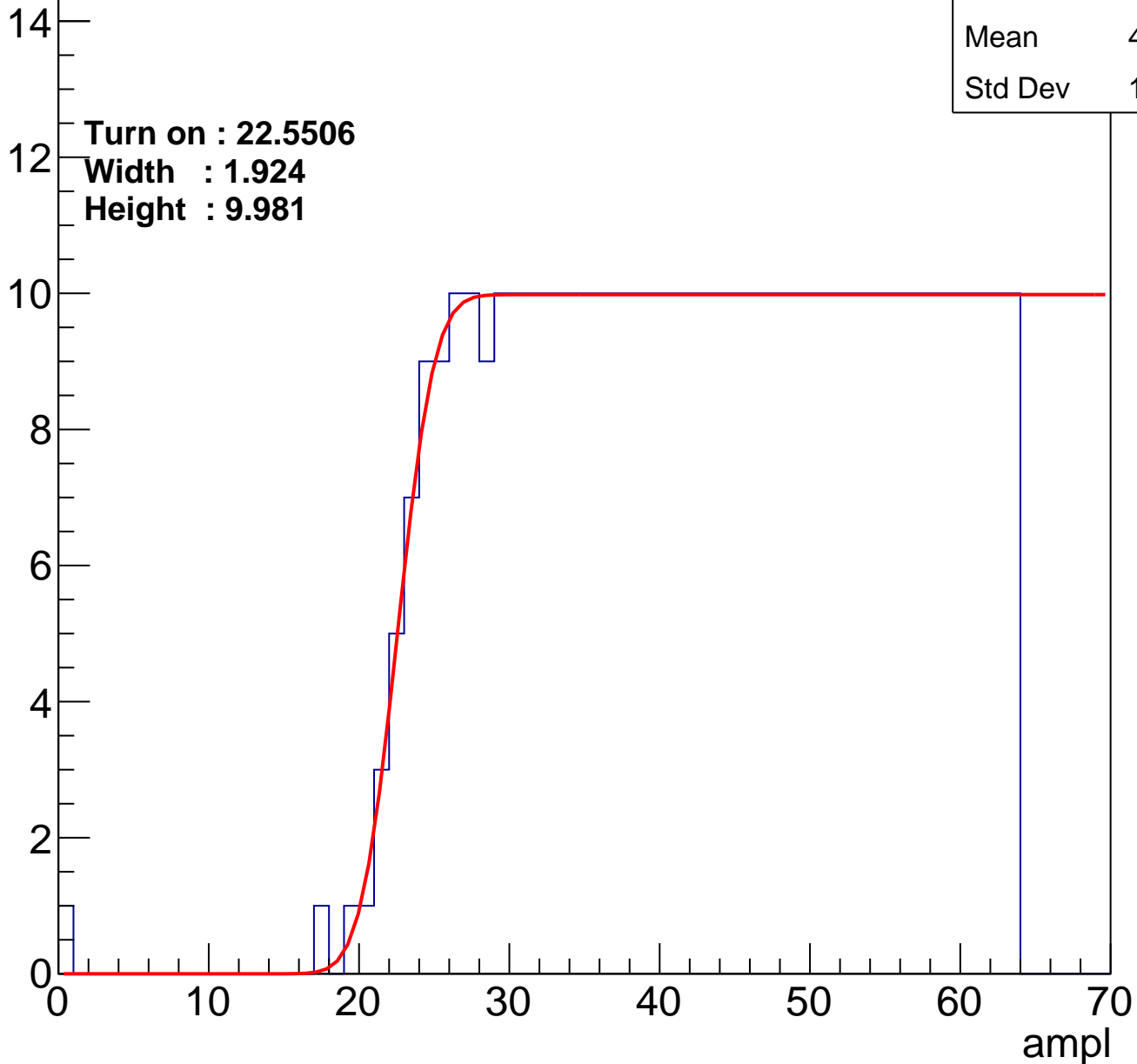
Entries	416
Mean	42.58
Std Dev	12.26

Turn on : 22.5506

Width : 1.924

Height : 9.981

Entry



B1L002S, U23-ch50

calib_packv5_042523_0143.root, FC#10, port B3

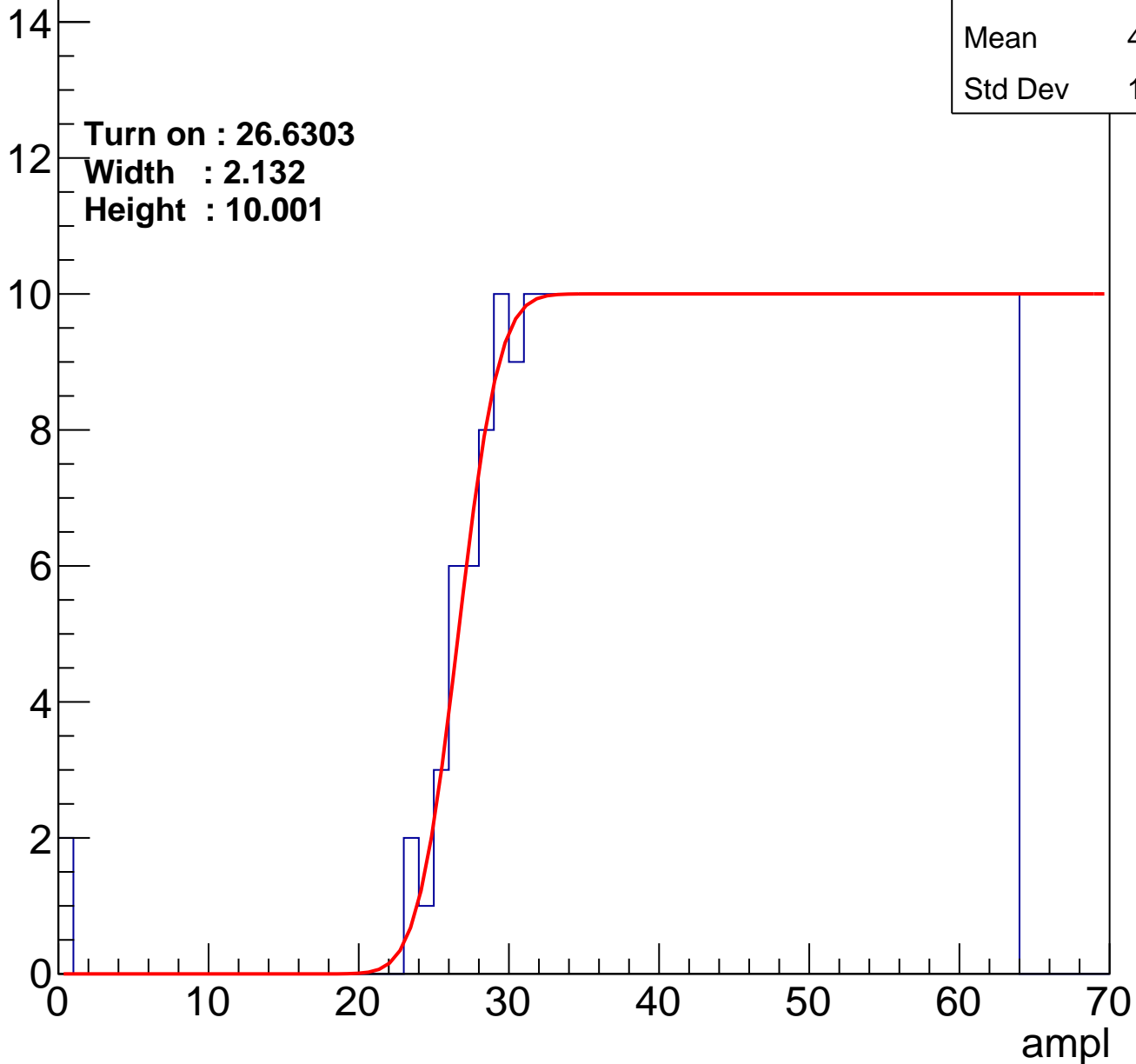
Entries	377
Mean	44.45
Std Dev	11.38

Turn on : 26.6303

Width : 2.132

Height : 10.001

Entry



B1L002S, U23-ch51

calib_packv5_042523_0143.root, FC#10, port B3

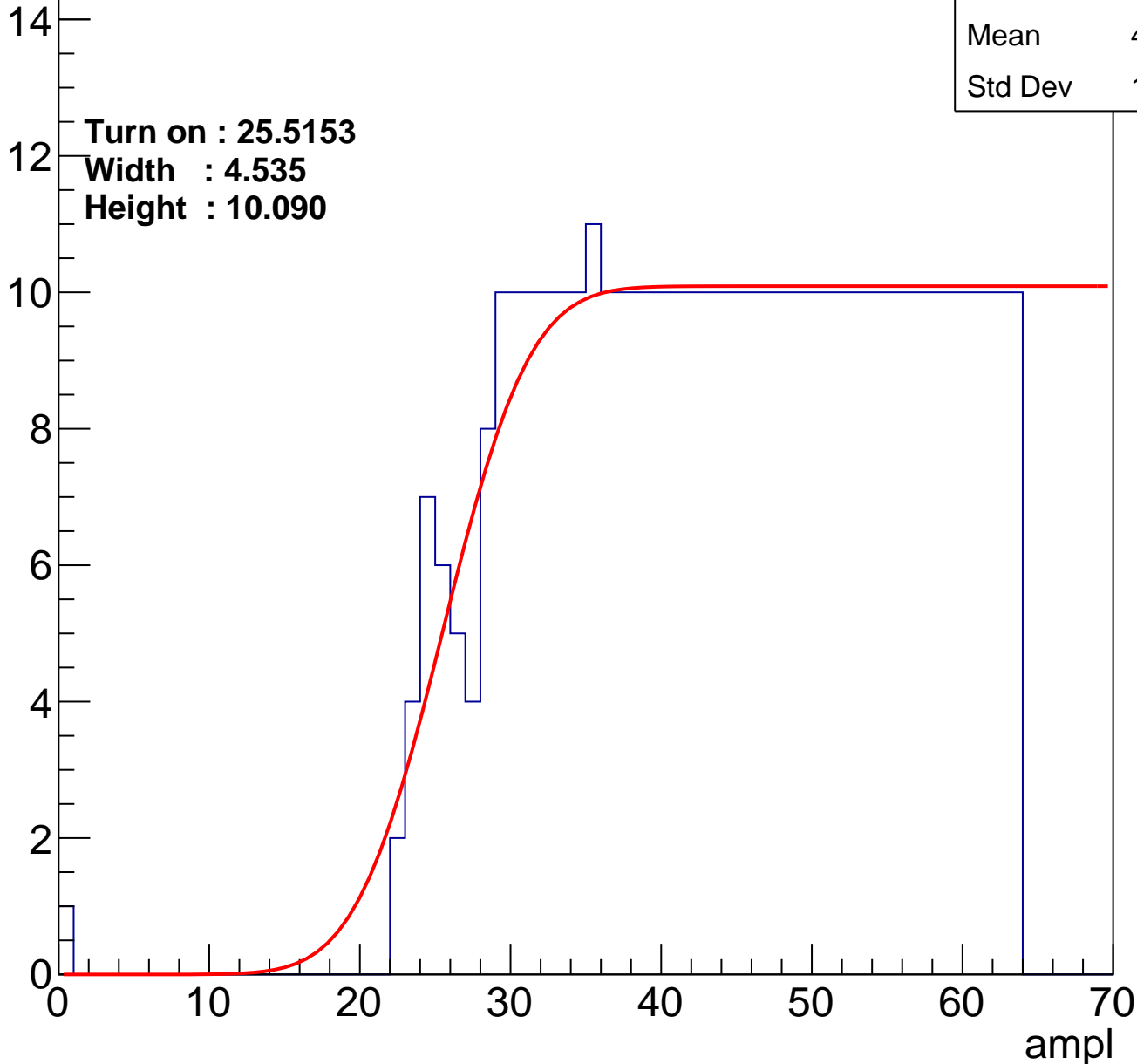
Entries	388
Mean	43.95
Std Dev	11.54

Turn on : 25.5153

Width : 4.535

Height : 10.090

Entry



B1L002S, U23-ch52

calib_packv5_042523_0143.root, FC#10, port B3

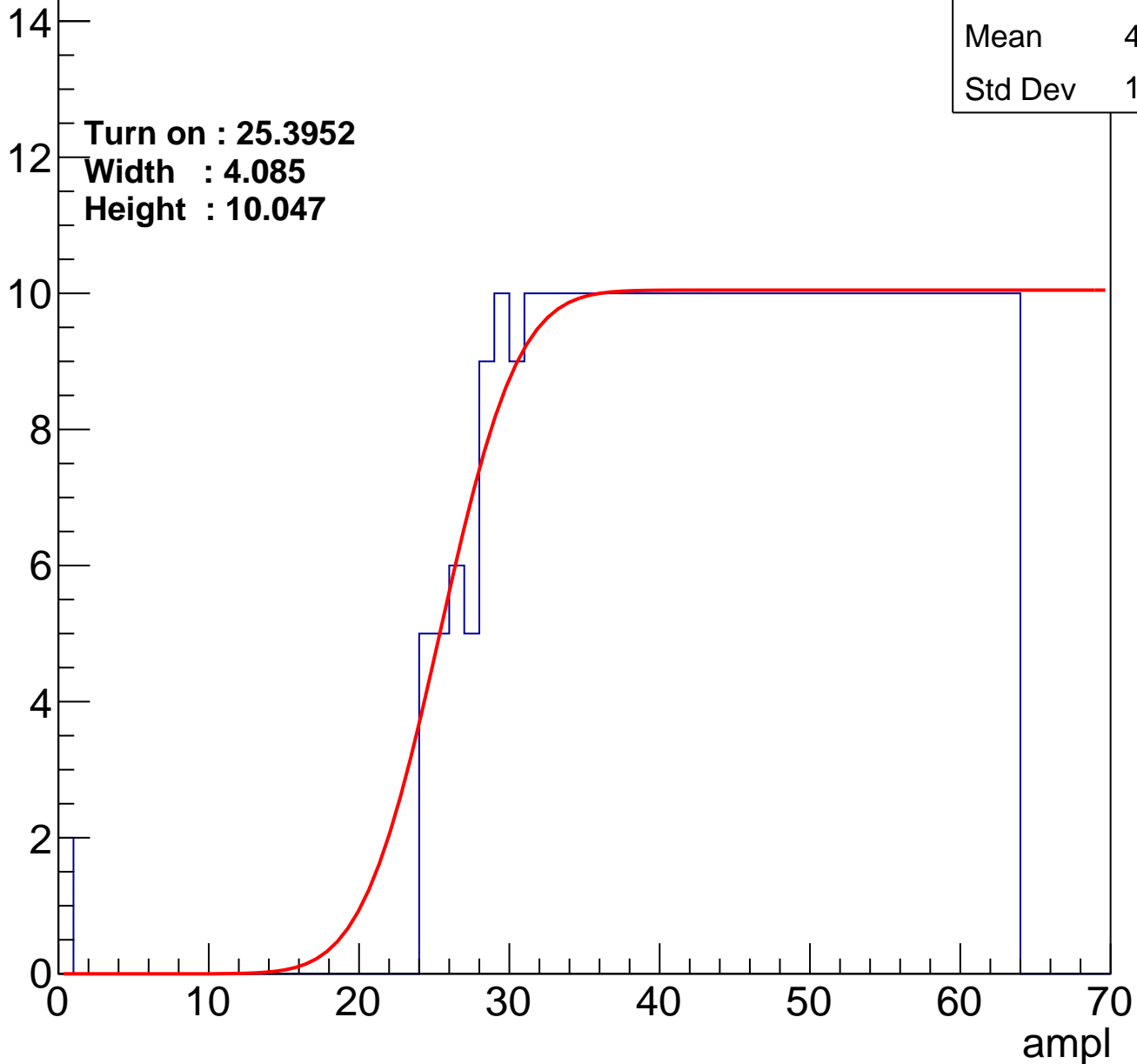
Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 25.3952

Width : 4.085

Height : 10.047

Entry



B1L002S, U23-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.22
Std Dev	12.06

Turn on : 23.8441

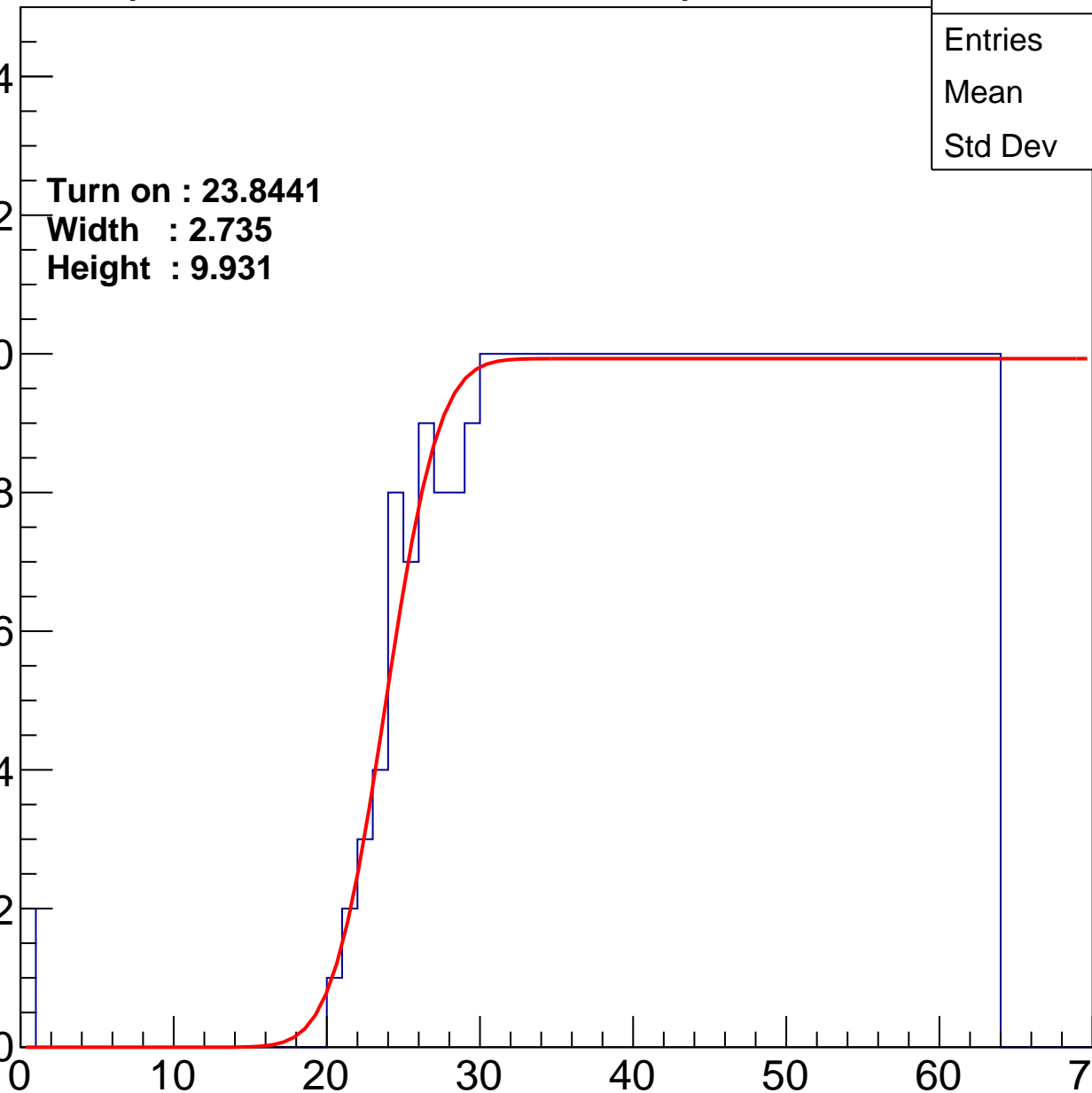
Width : 2.735

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.34
Std Dev	12.23

Turn on : 24.9475

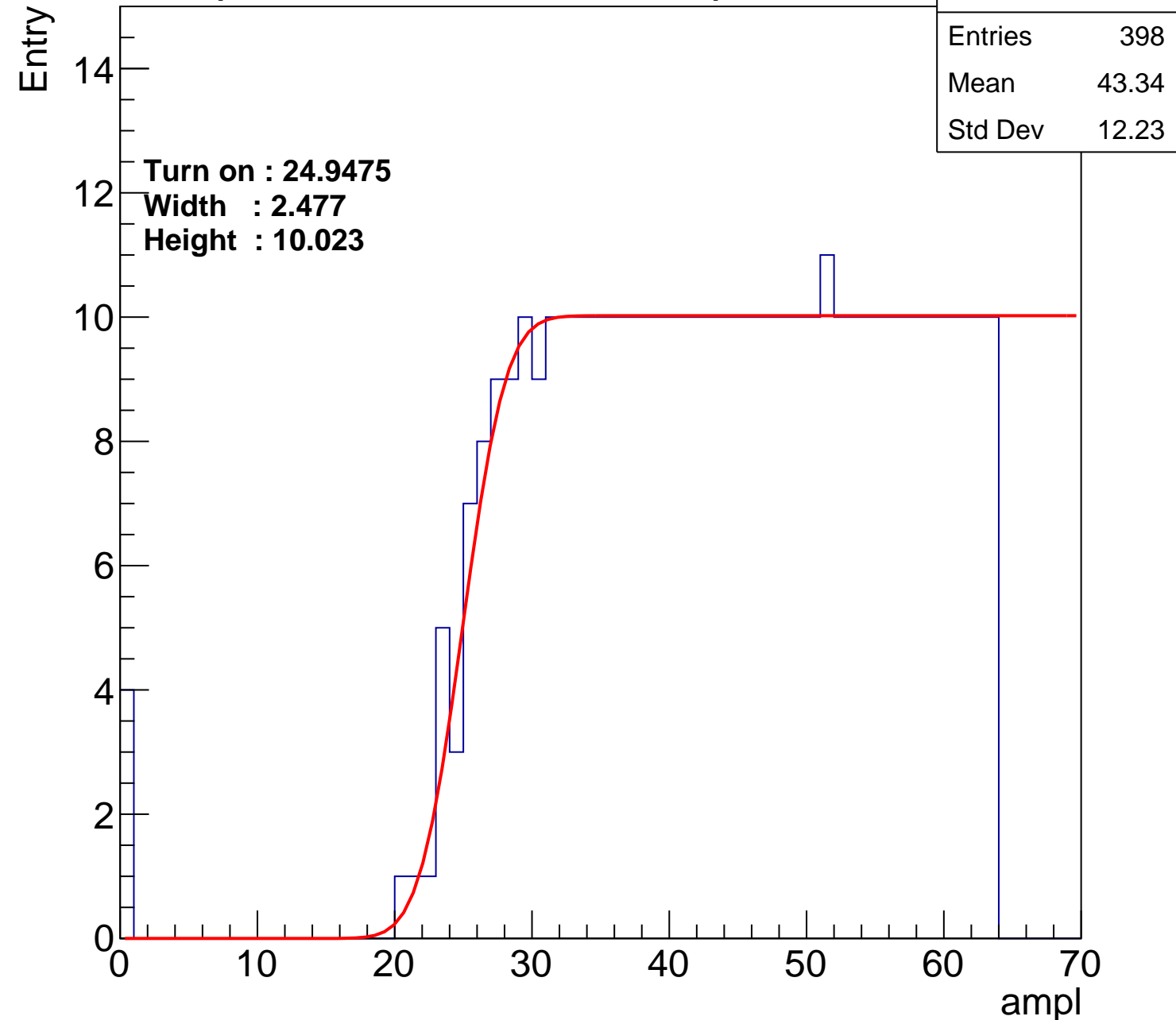
Width : 2.477

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.4186

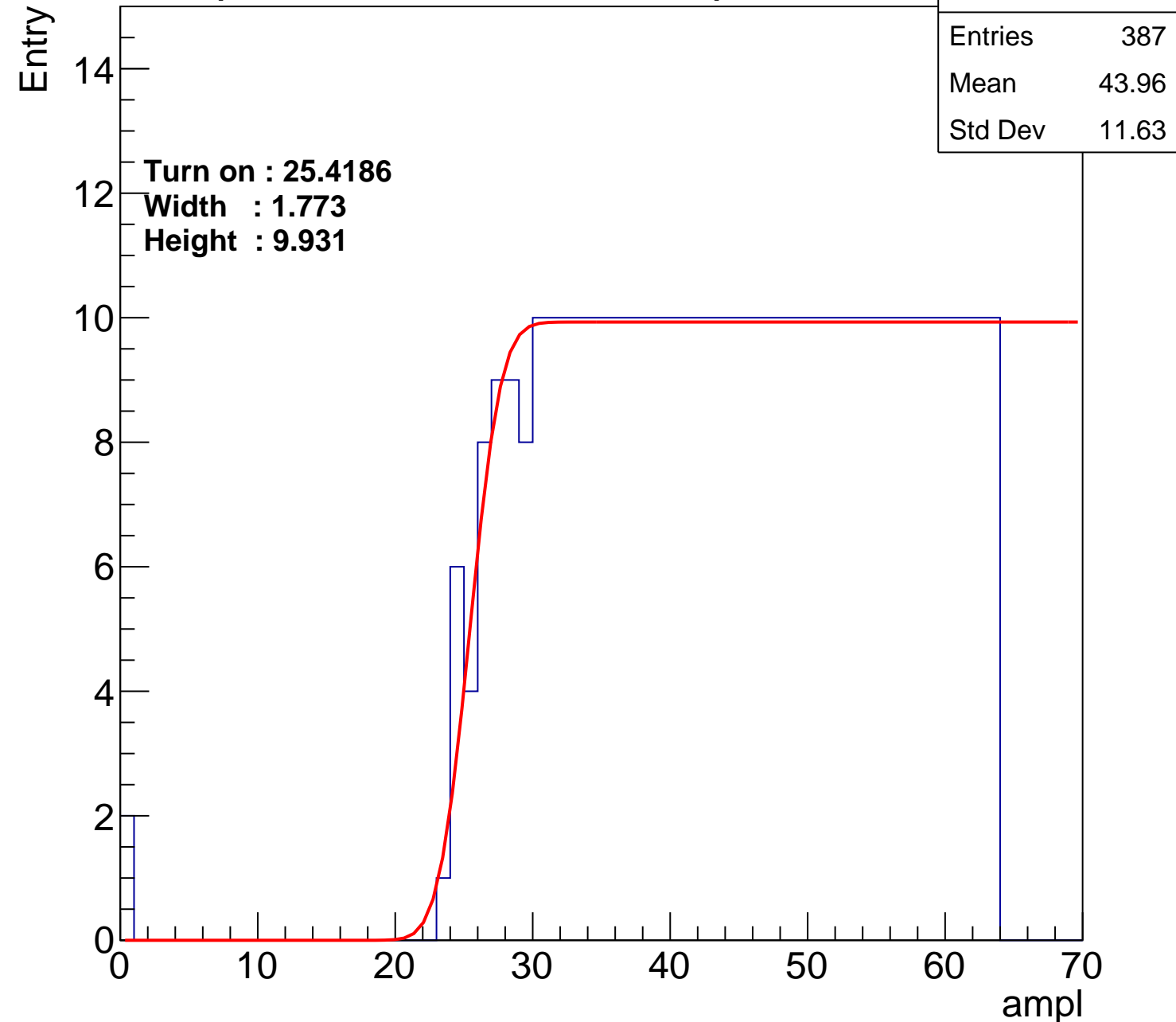
Width : 1.773

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 25.8627

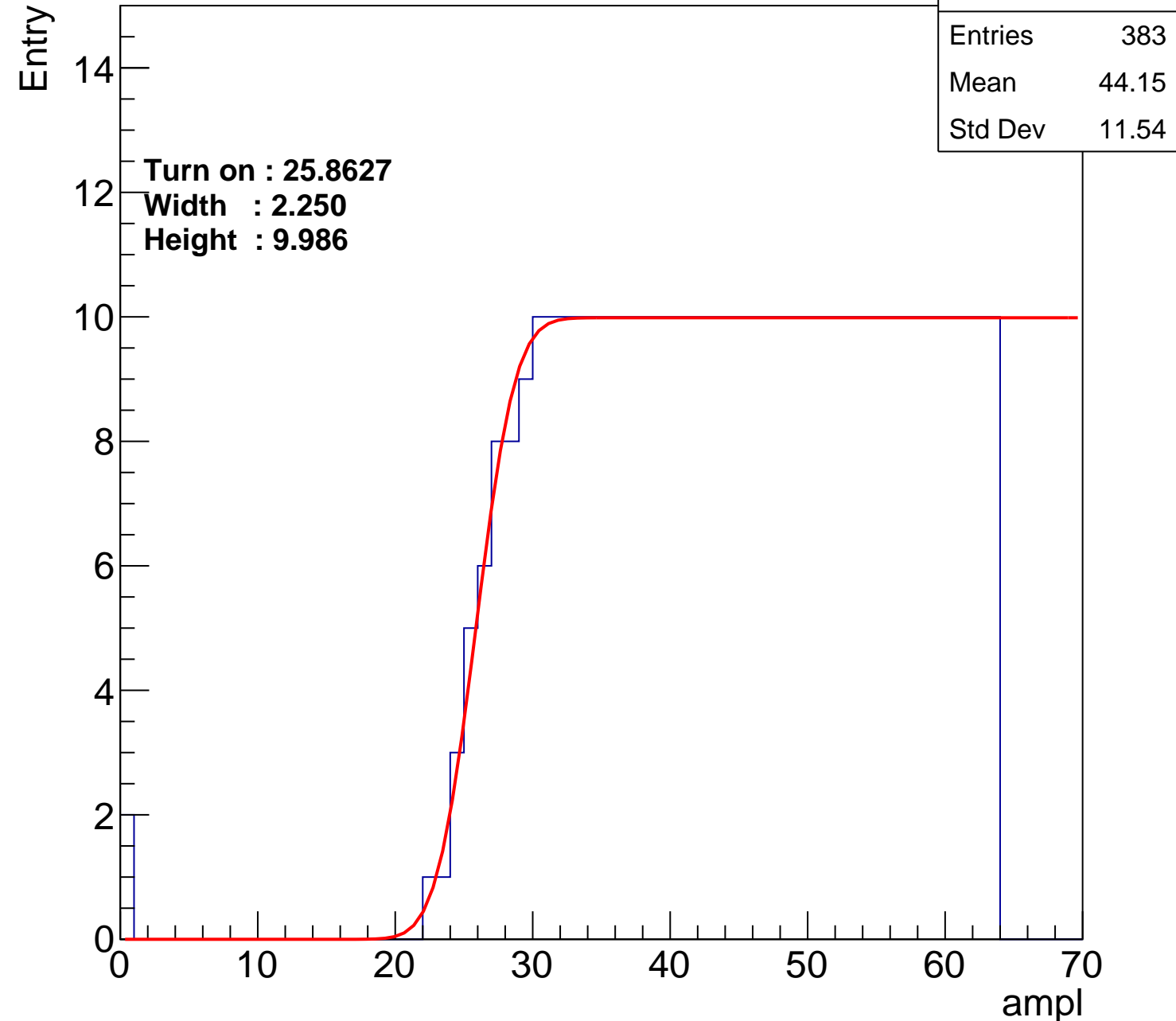
Width : 2.250

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.6301

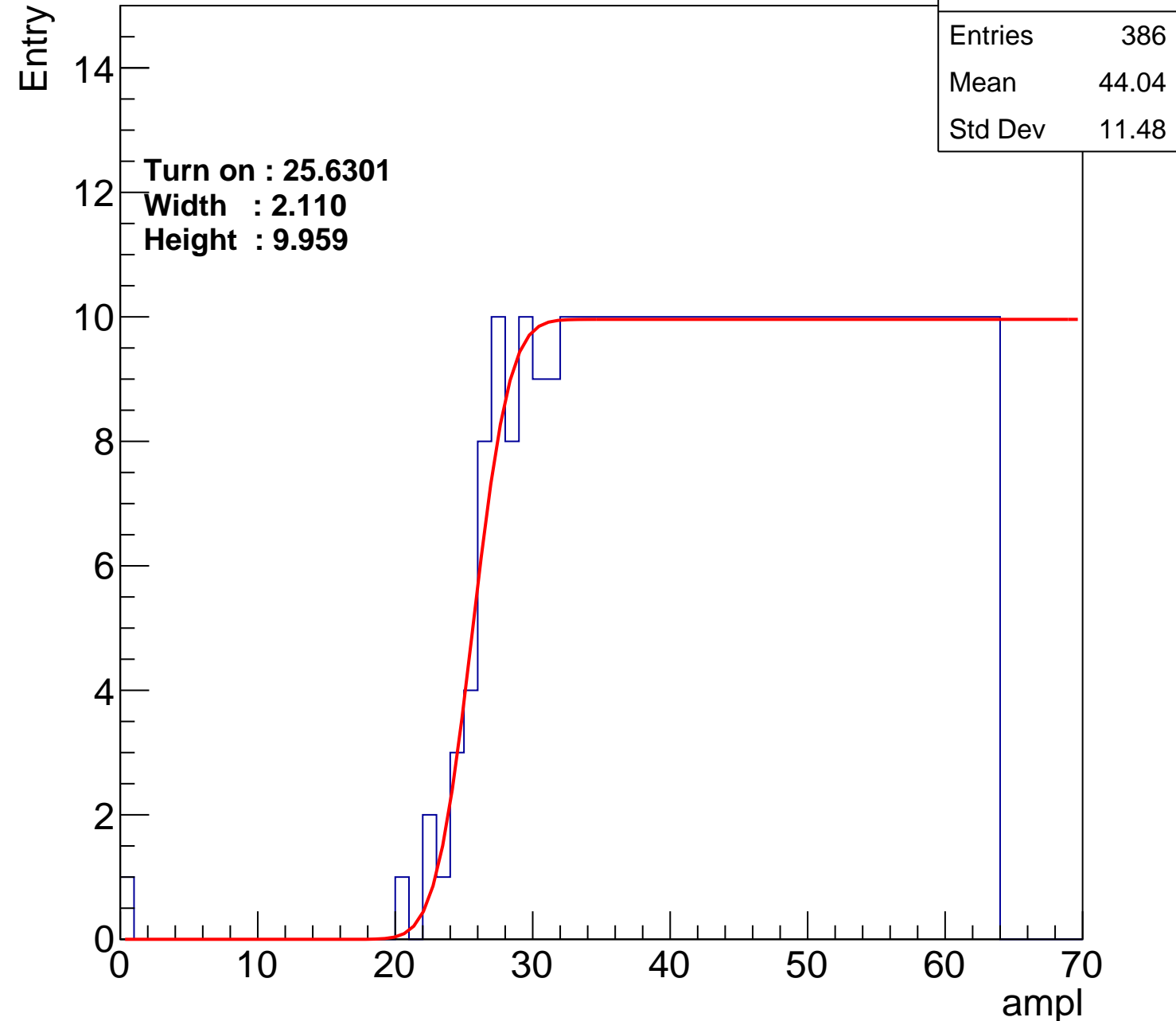
Width : 2.110

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.81
Std Dev	11.83

Turn on : 25.5666

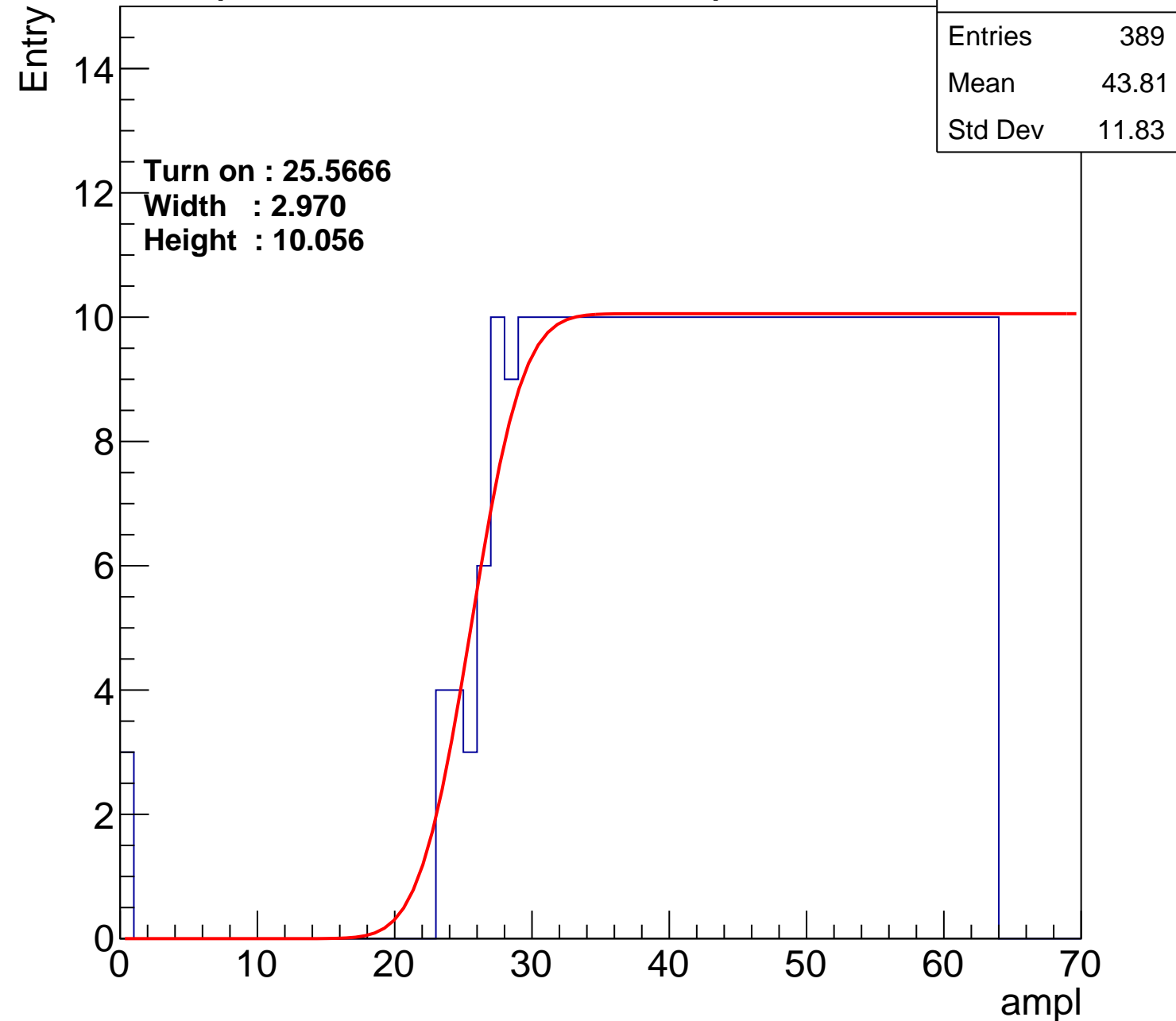
Width : 2.970

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch59

calib_packv5_042523_0143.root, FC#10, port B3

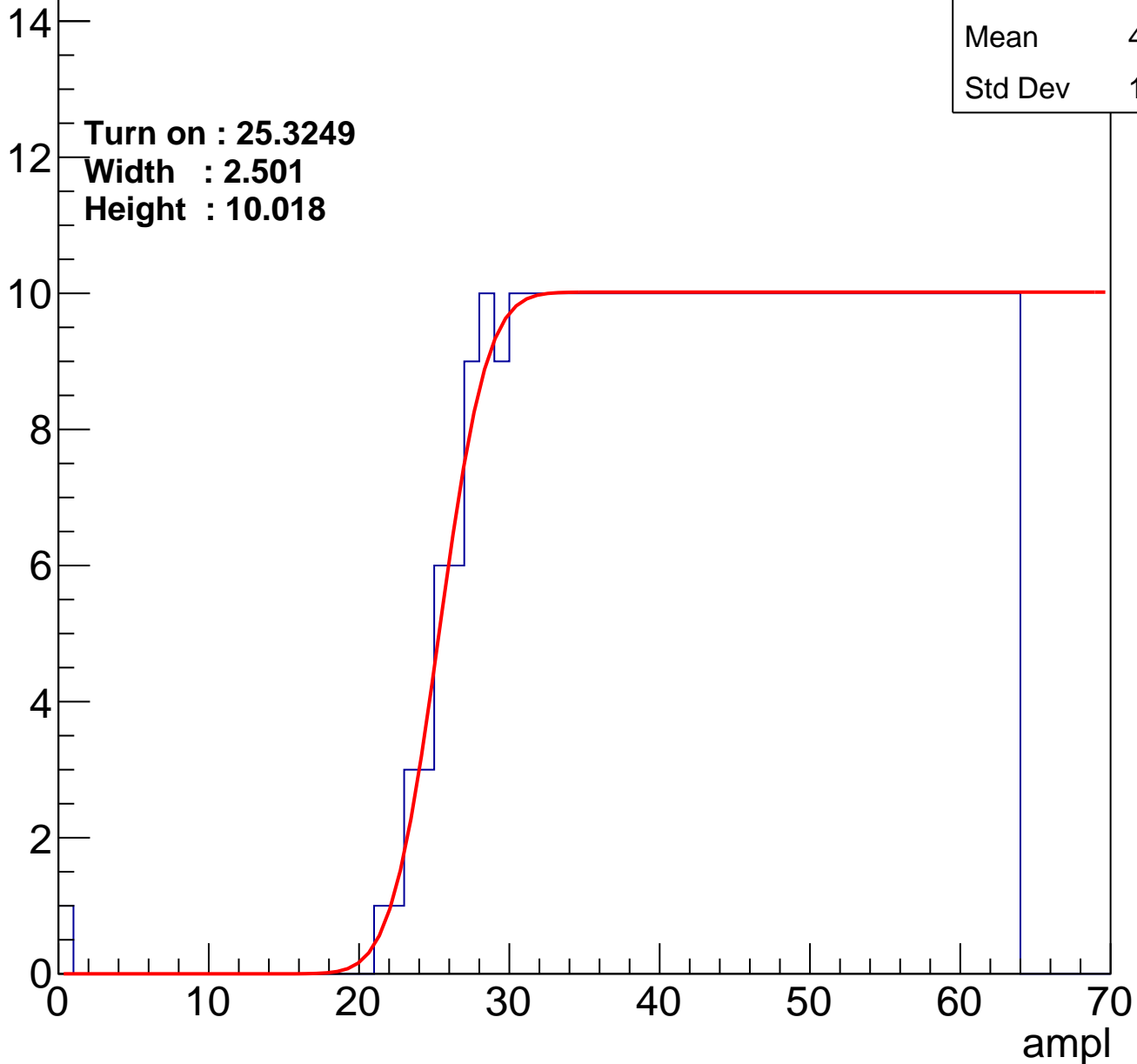
Entries	389
Mean	43.92
Std Dev	11.52

Turn on : 25.3249

Width : 2.501

Height : 10.018

Entry



B1L002S, U23-ch60

calib_packv5_042523_0143.root, FC#10, port B3

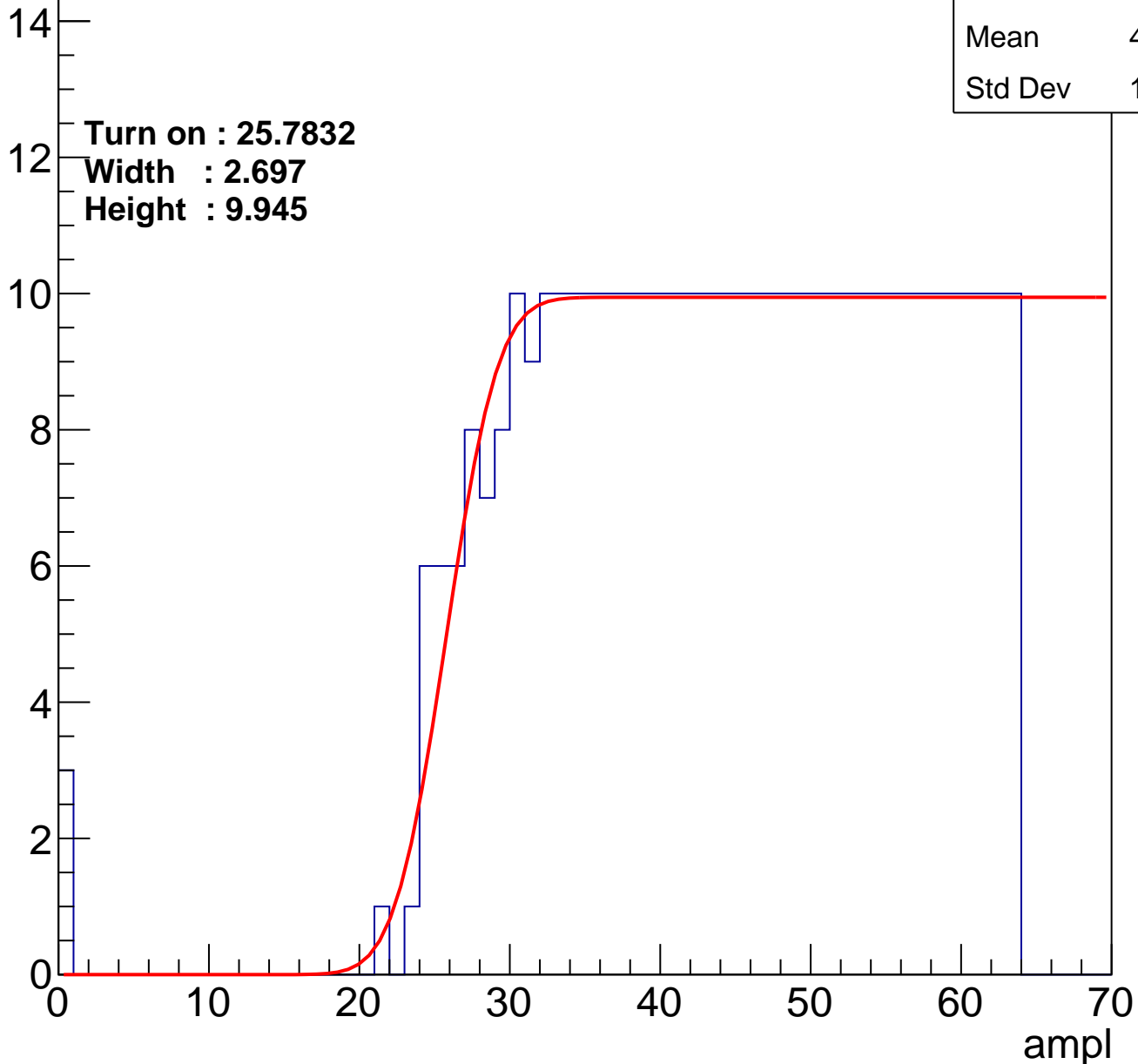
Entries	385
Mean	43.94
Std Dev	11.83

Turn on : 25.7832

Width : 2.697

Height : 9.945

Entry



B1L002S, U23-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.82
Std Dev	11.99

Turn on : 26.2724

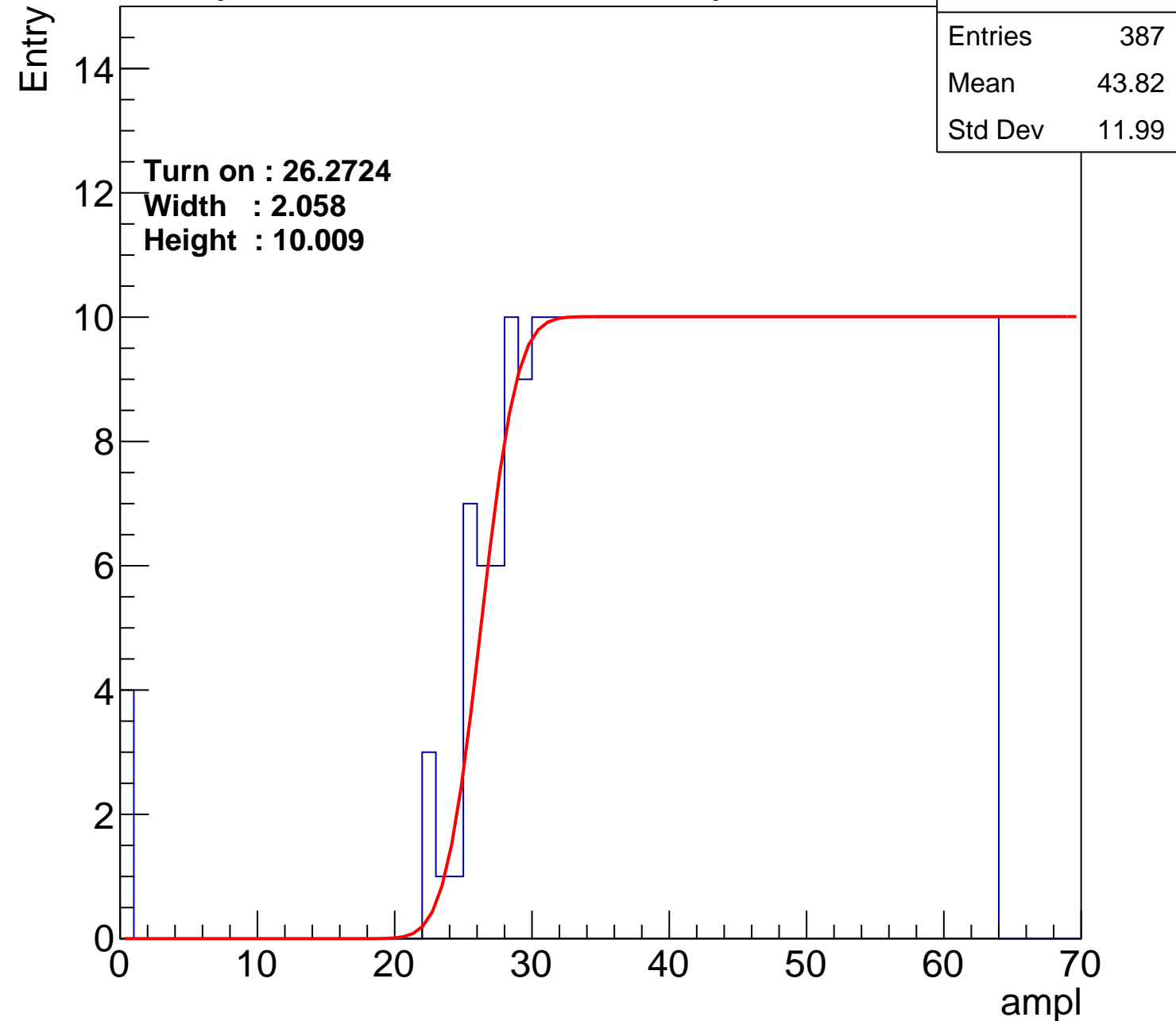
Width : 2.058

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	413
Mean	42.7
Std Dev	12.26

Turn on : 22.8487

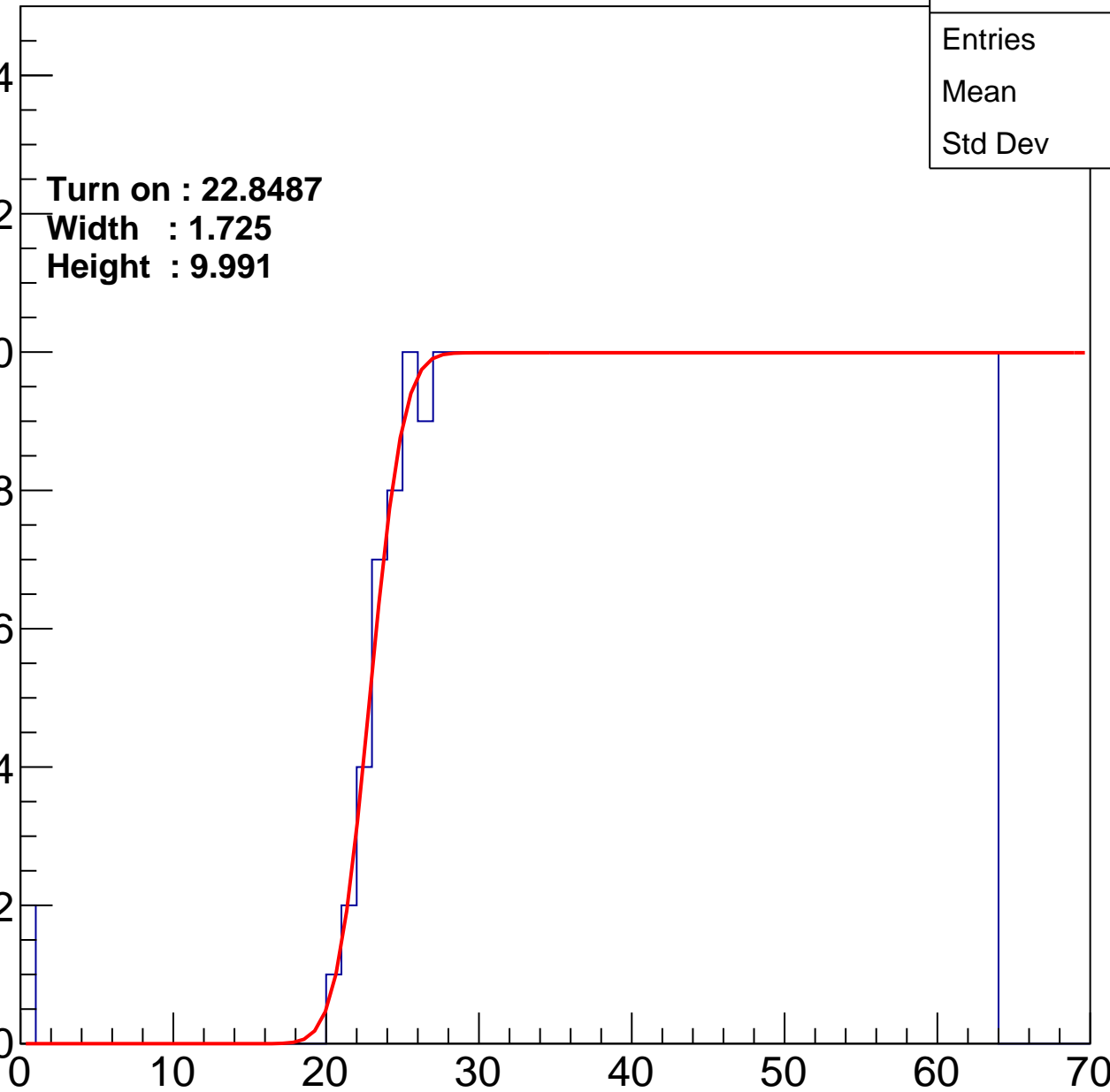
Width : 1.725

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch63

calib_packv5_042523_0143.root, FC#10, port B3

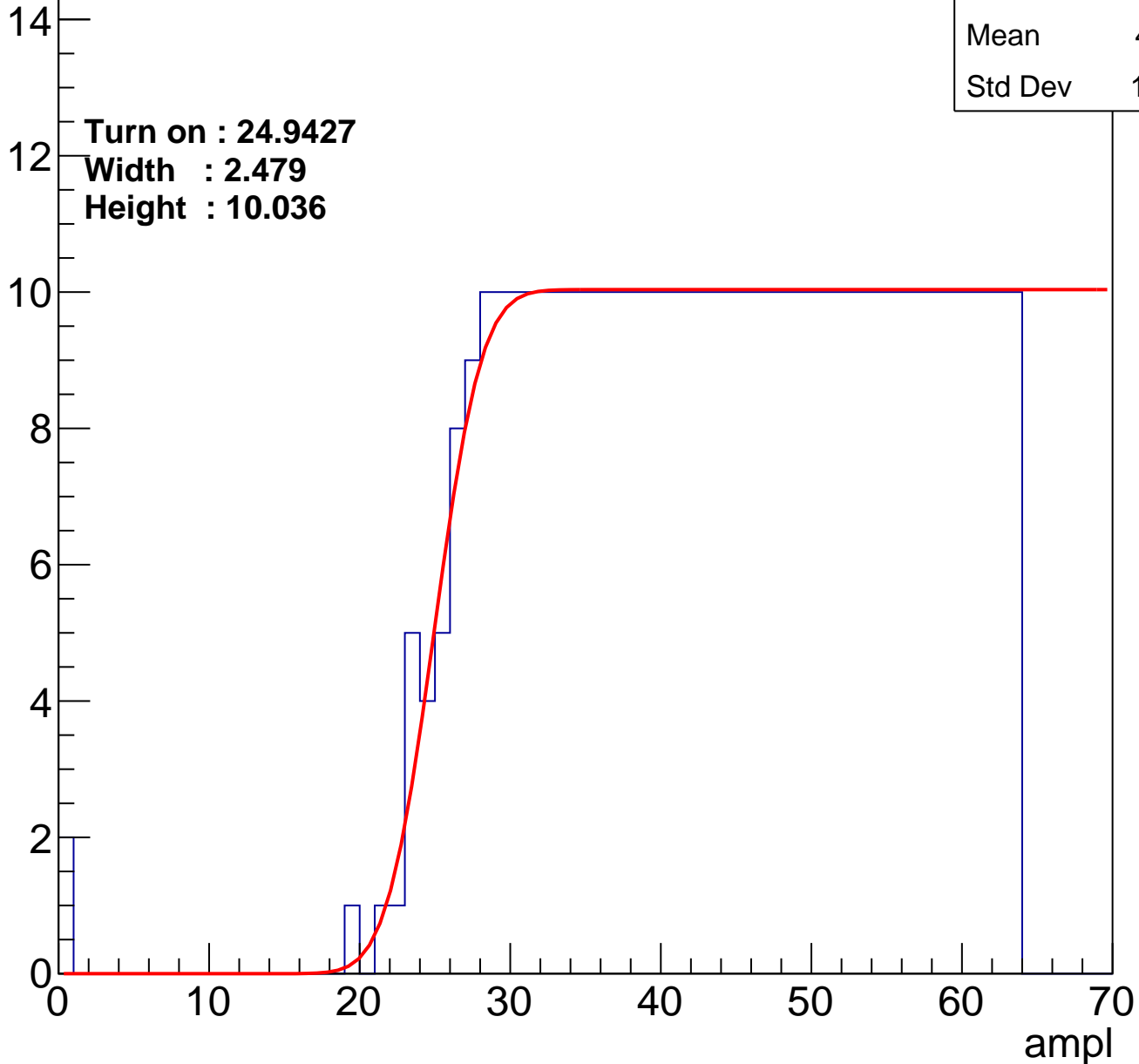
Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.9427

Width : 2.479

Height : 10.036

Entry



B1L002S, U23-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.84

Turn on : 24.5661

Width : 2.014

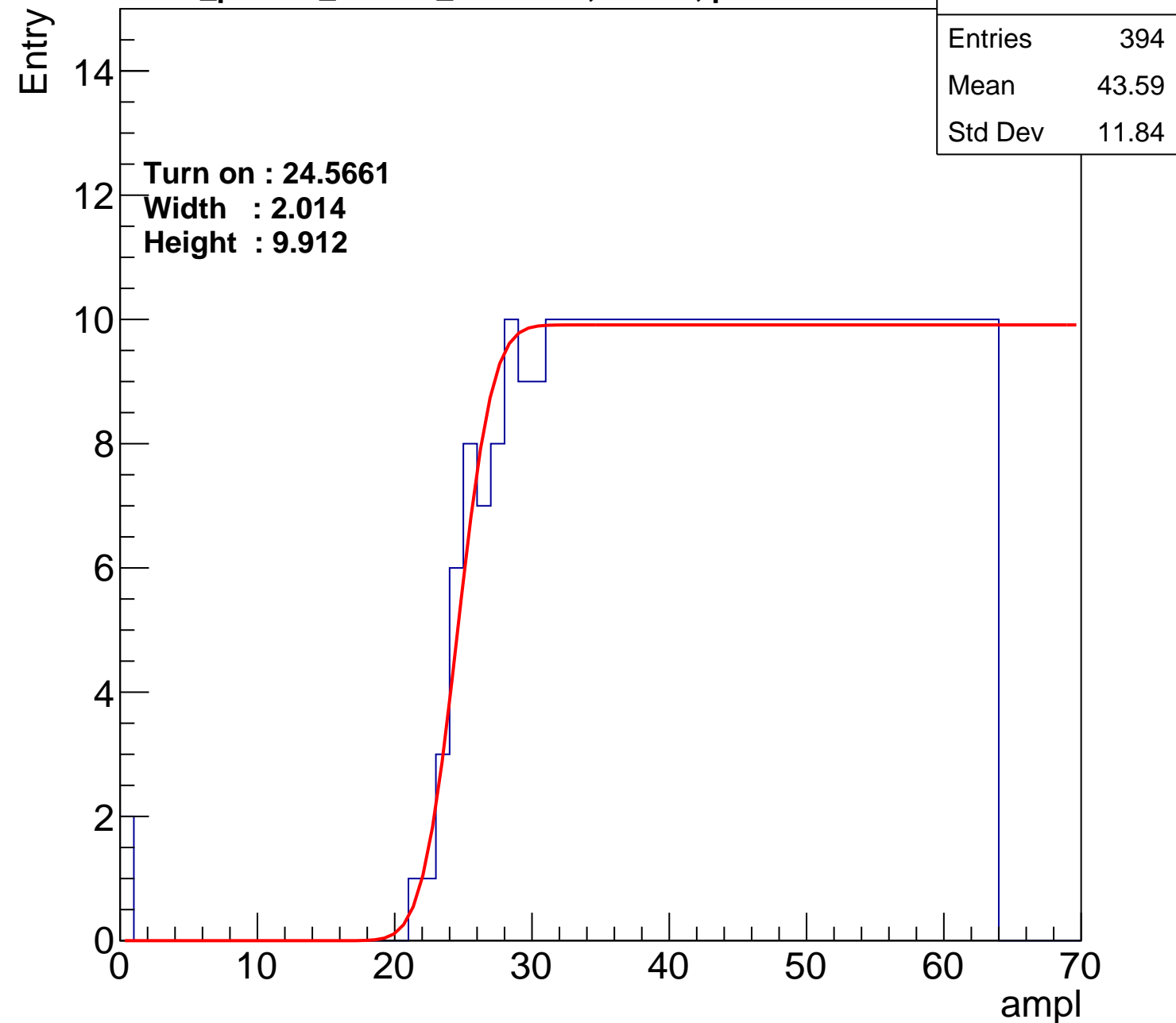
Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U23-ch65

calib_packv5_042523_0143.root, FC#10, port B3

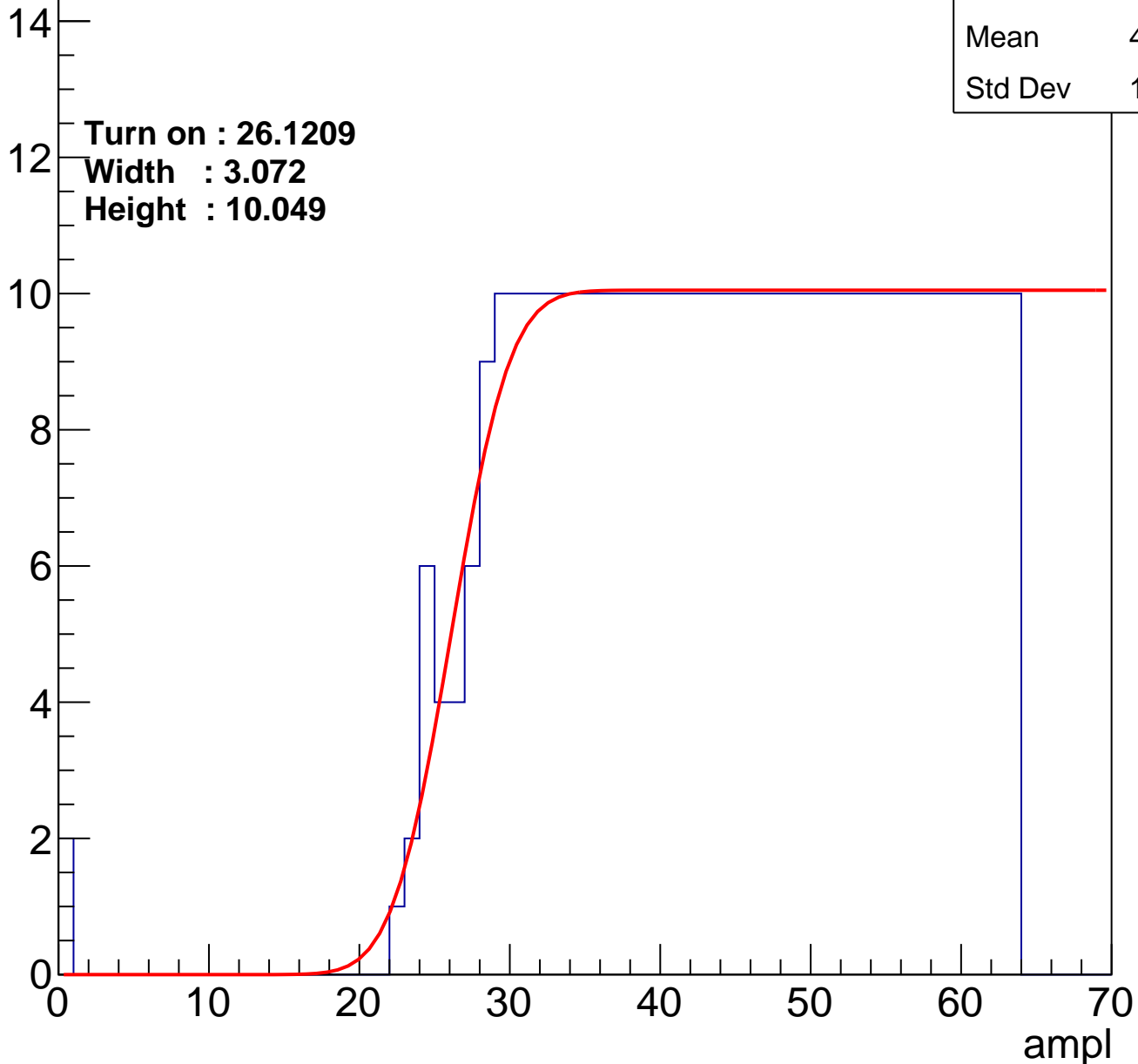
Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.1209

Width : 3.072

Height : 10.049

Entry



B1L002S, U23-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.81
Std Dev	12.48

Turn on : 23.7537

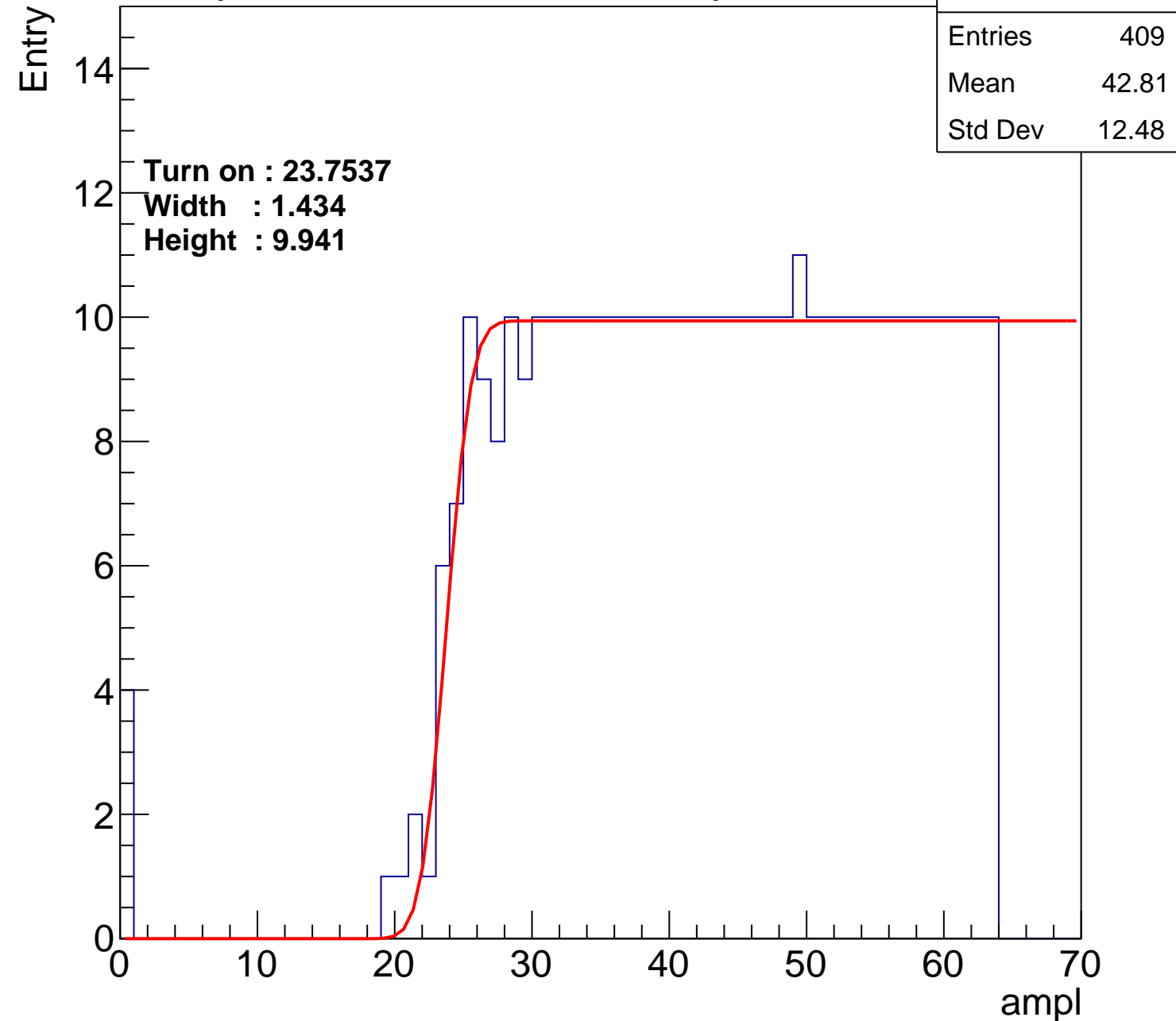
Width : 1.434

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.32
Std Dev	12.03

Turn on : 24.8144

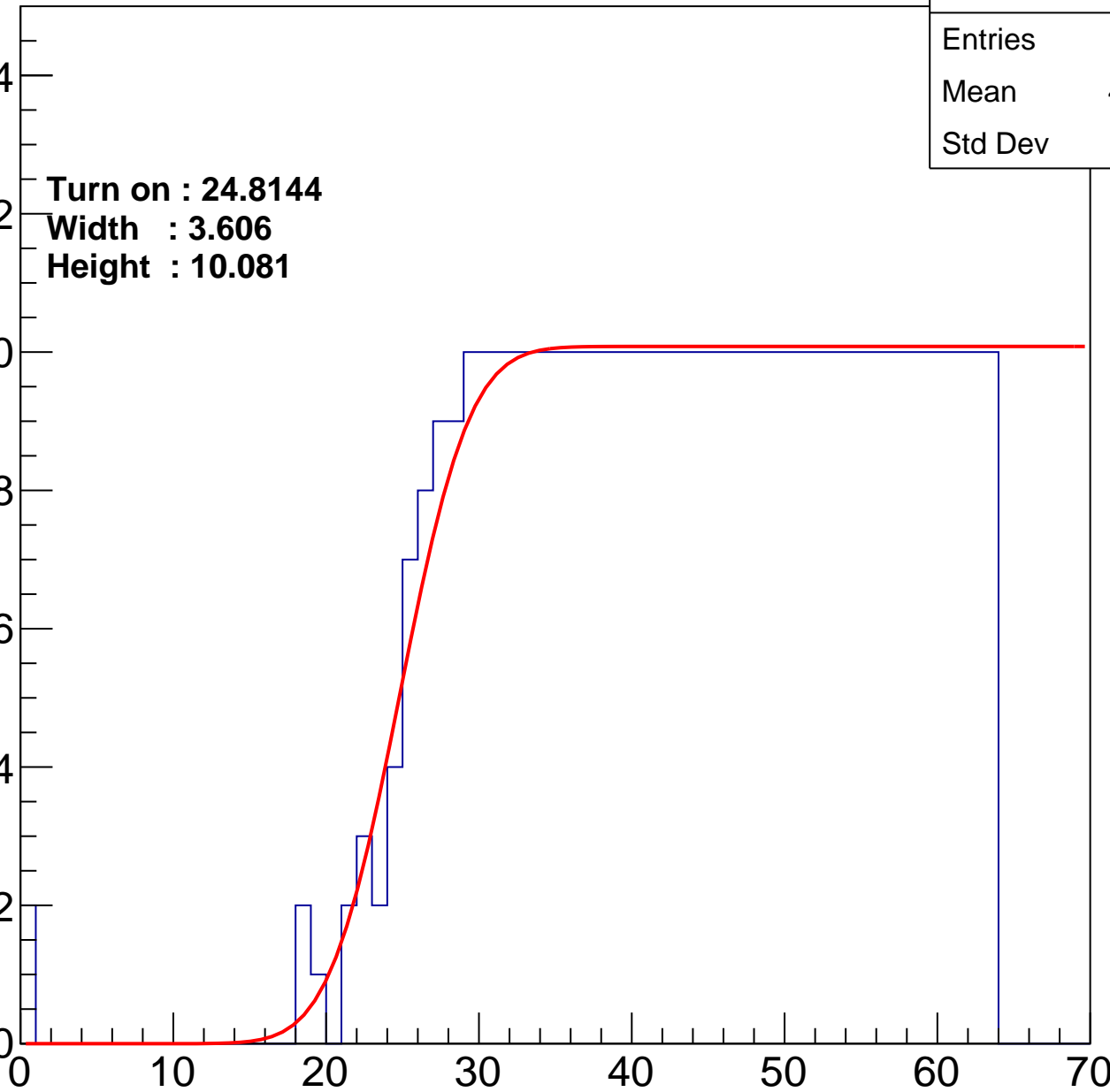
Width : 3.606

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch68

calib_packv5_042523_0143.root, FC#10, port B3

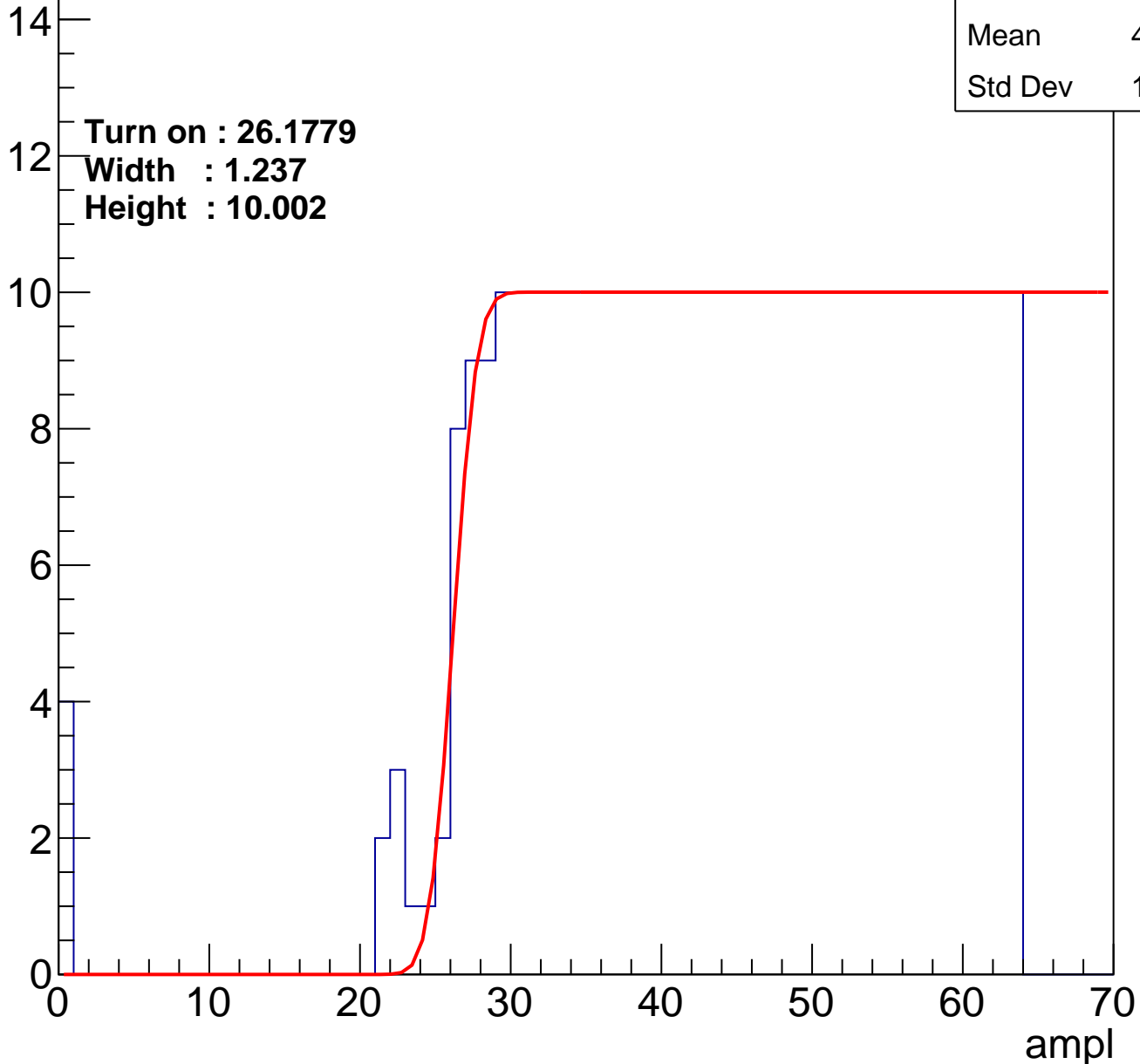
Entries	389
Mean	43.72
Std Dev	12.04

Turn on : 26.1779

Width : 1.237

Height : 10.002

Entry



B1L002S, U23-ch69

calib_packv5_042523_0143.root, FC#10, port B3

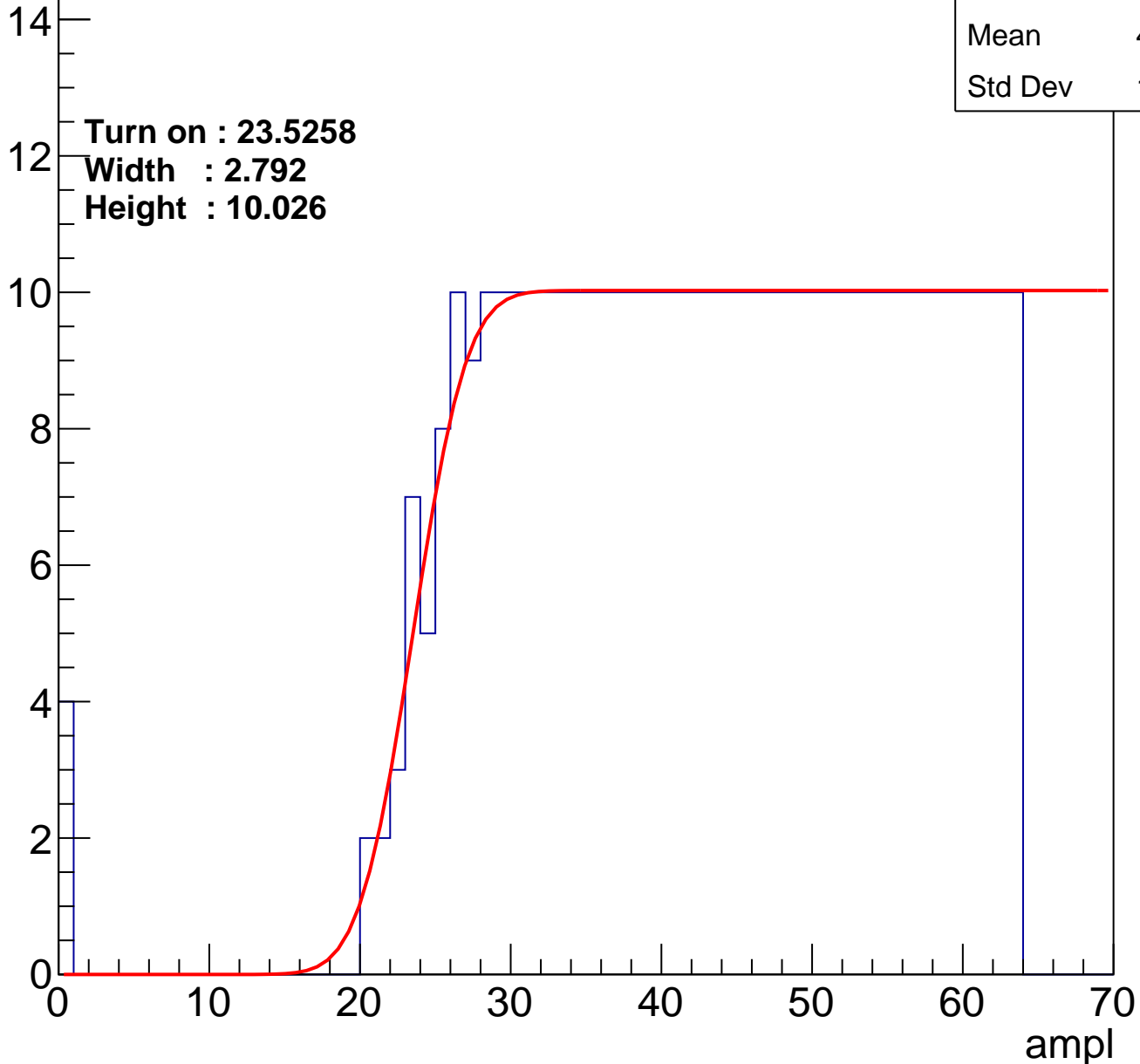
Entries	410
Mean	42.71
Std Dev	12.51

Turn on : 23.5258

Width : 2.792

Height : 10.026

Entry



B1L002S, U23-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.82

Turn on : 25.0950

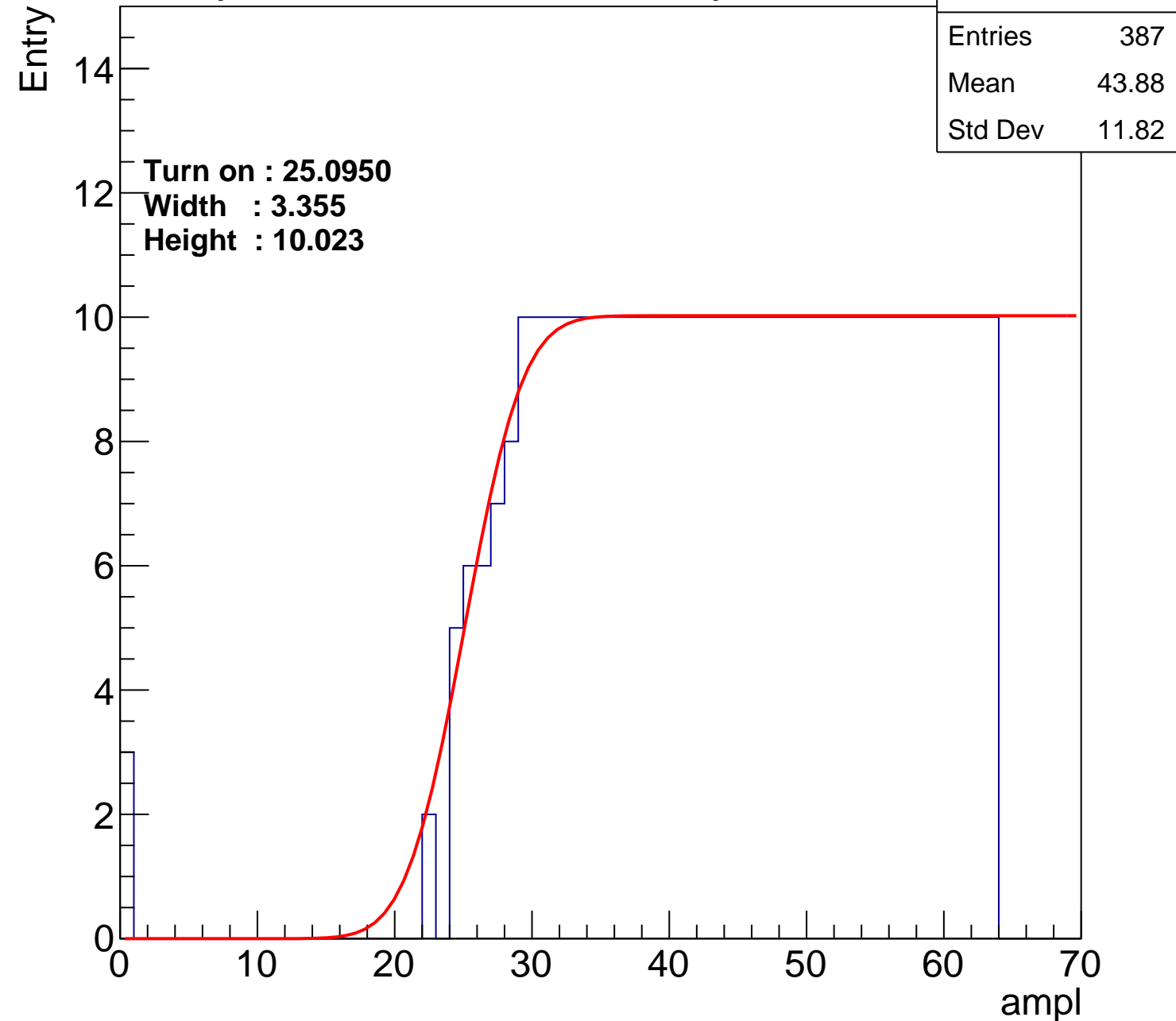
Width : 3.355

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.78

Turn on : 25.4616

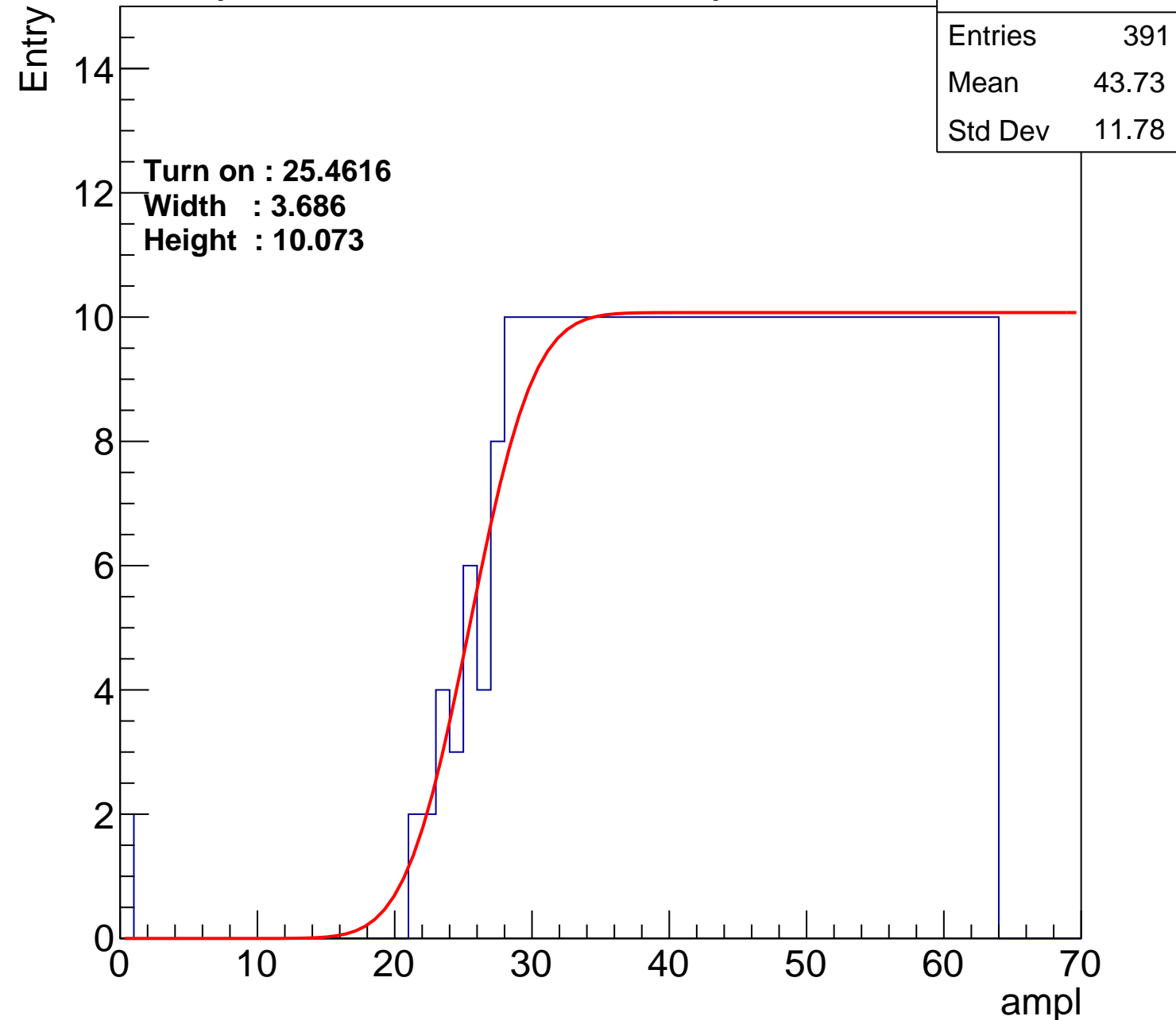
Width : 3.686

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44
Std Dev	11.74

Turn on : 26.6389

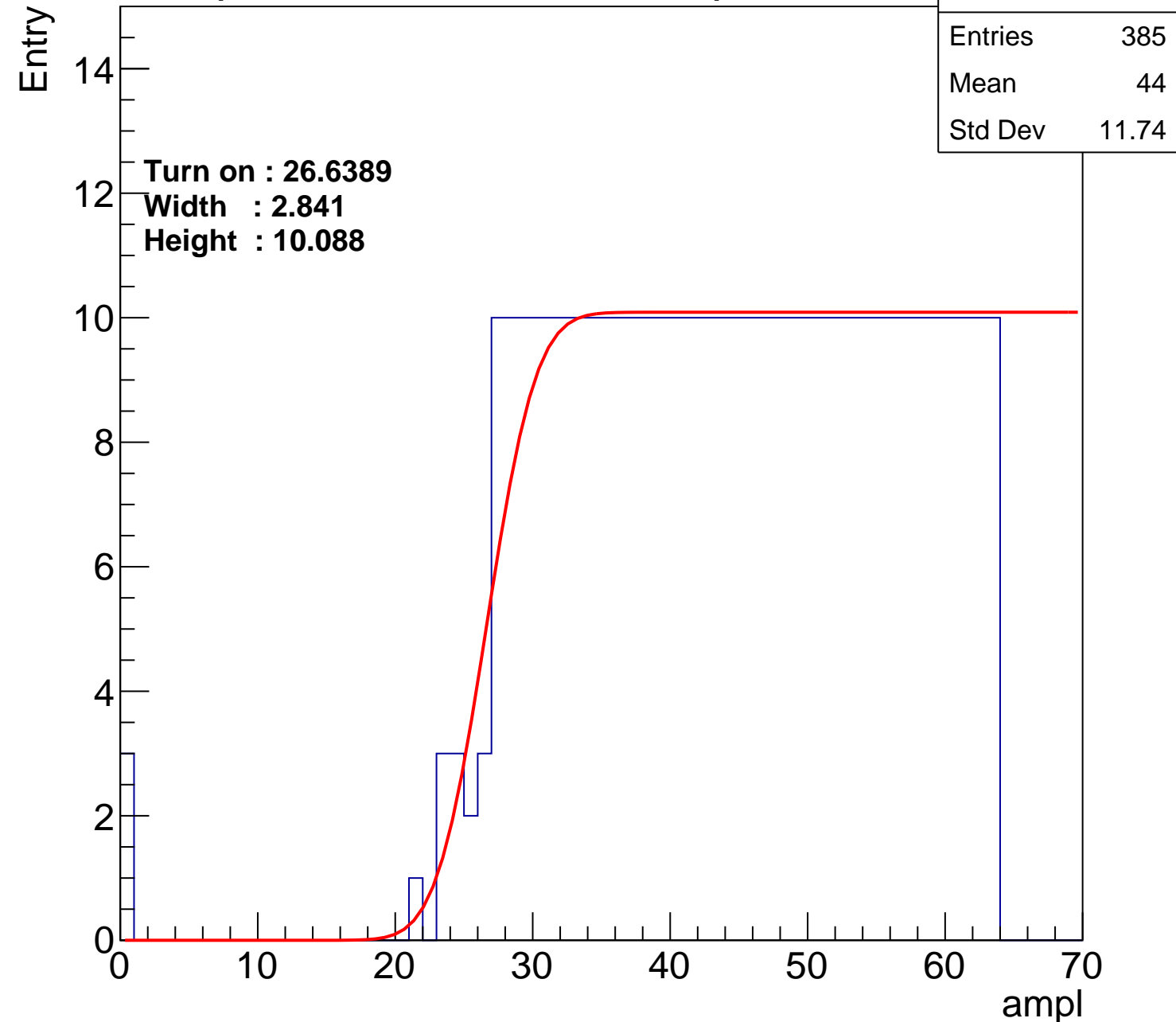
Width : 2.841

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch73

calib_packv5_042523_0143.root, FC#10, port B3

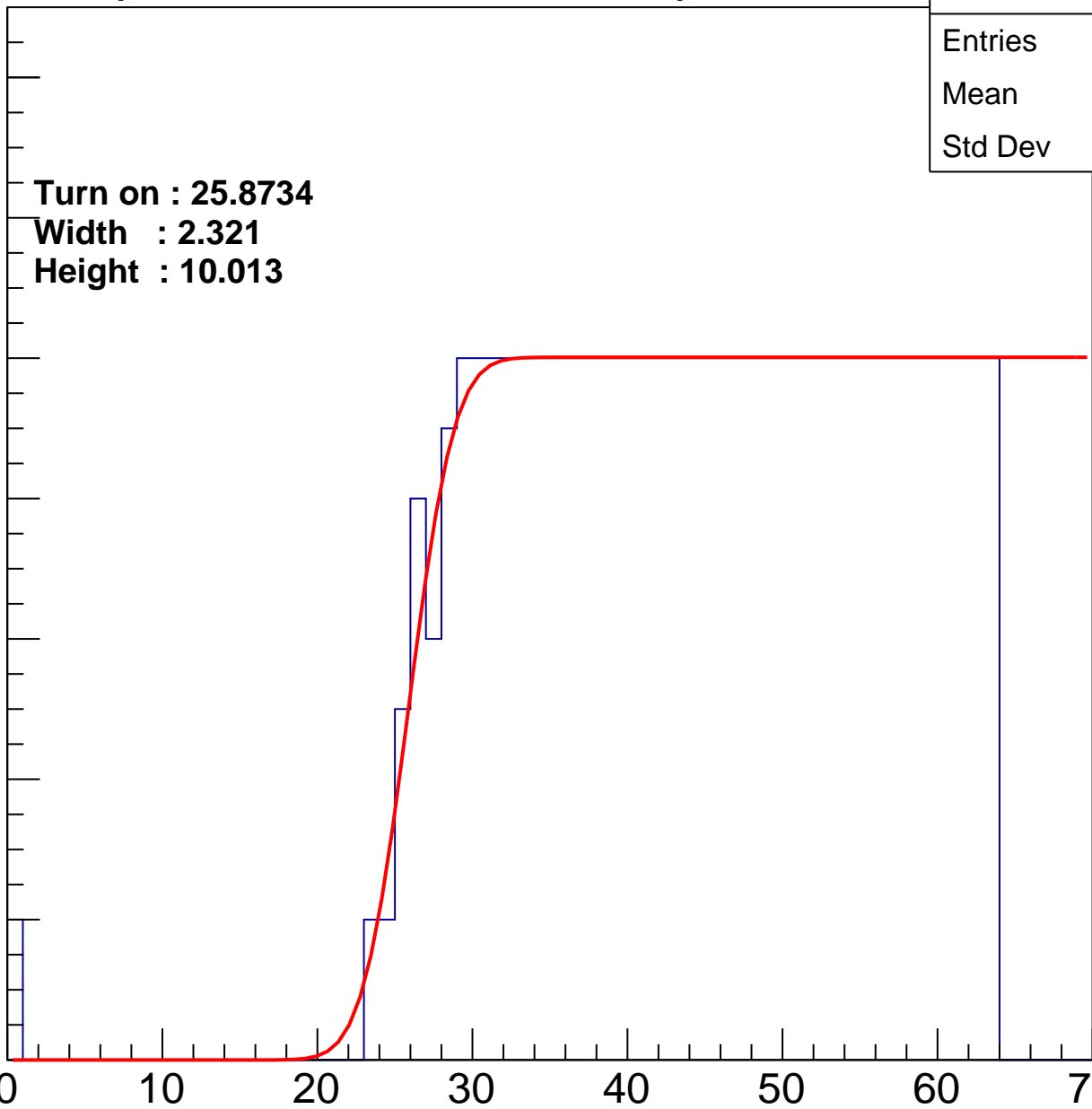
Entry

14
12
10
8
6
4
2
0

Turn on : 25.8734
Width : 2.321
Height : 10.013

Entries	384
Mean	44.12
Std Dev	11.53

ampl



B1L002S, U23-ch74

calib_packv5_042523_0143.root, FC#10, port B3

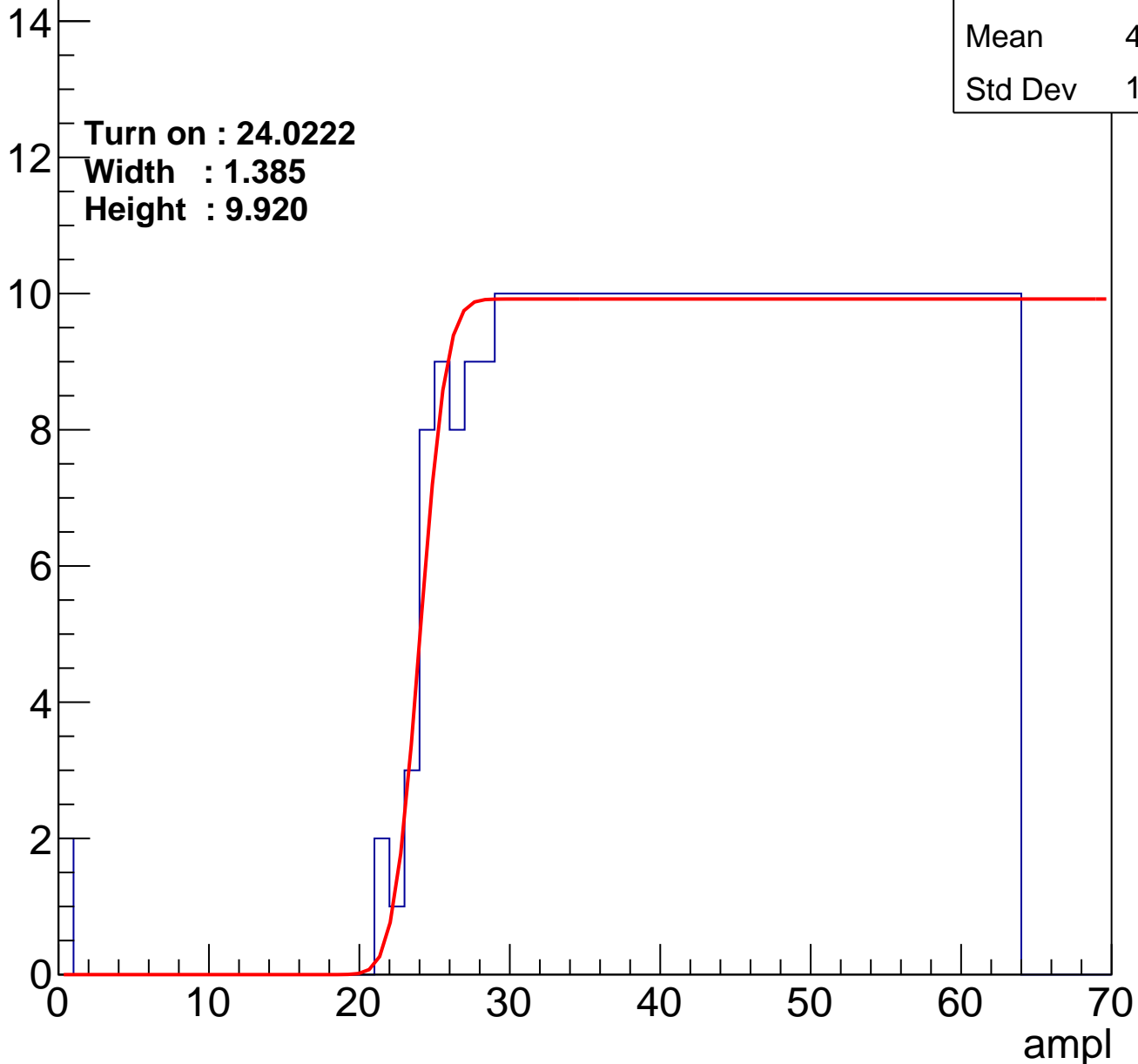
Entries	401
Mean	43.27
Std Dev	11.98

Turn on : 24.0222

Width : 1.385

Height : 9.920

Entry



B1L002S, U23-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.52
Std Dev	11.86

Turn on : 24.8045

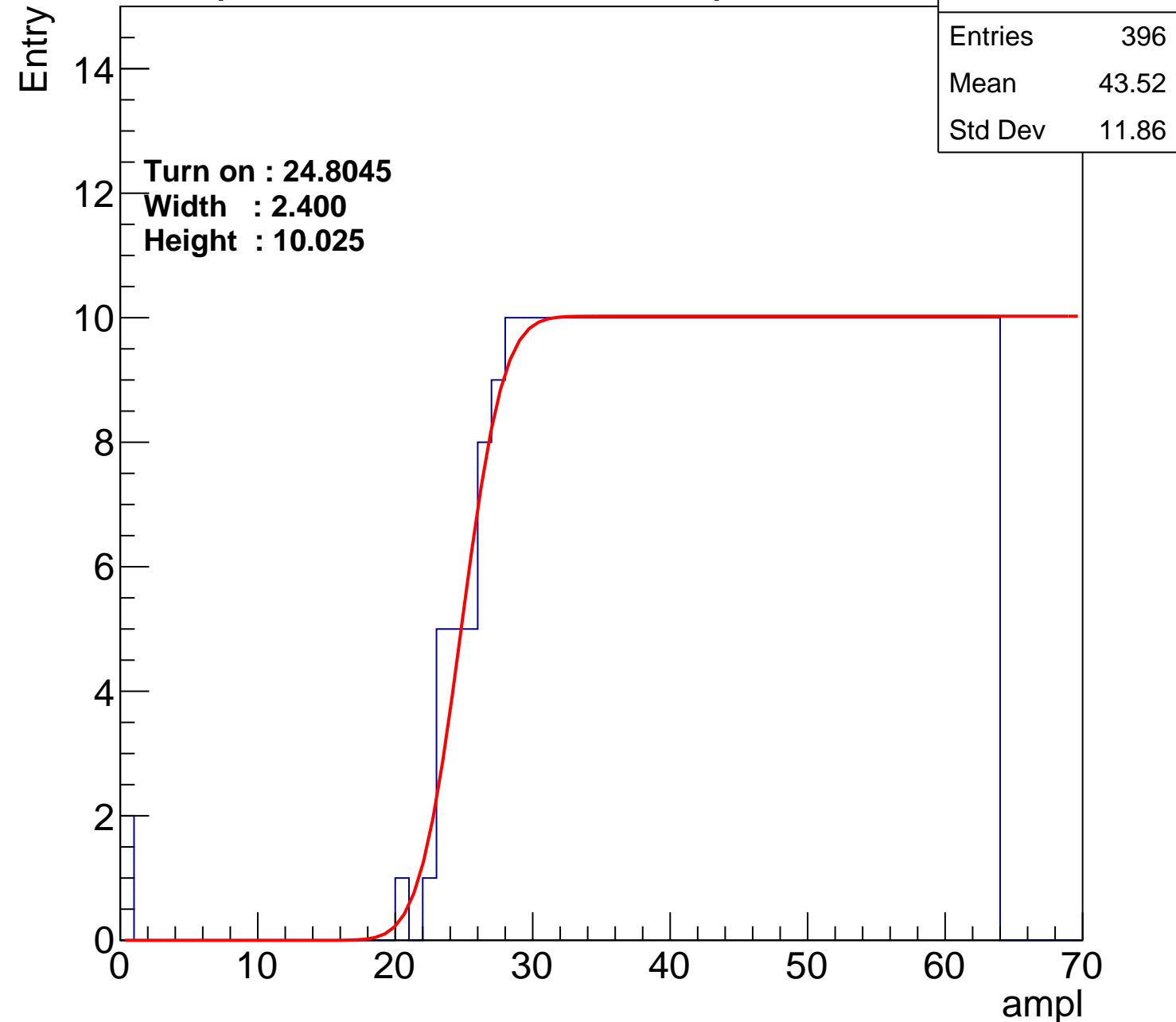
Width : 2.400

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.3
Std Dev	12.05

Turn on : 24.7388

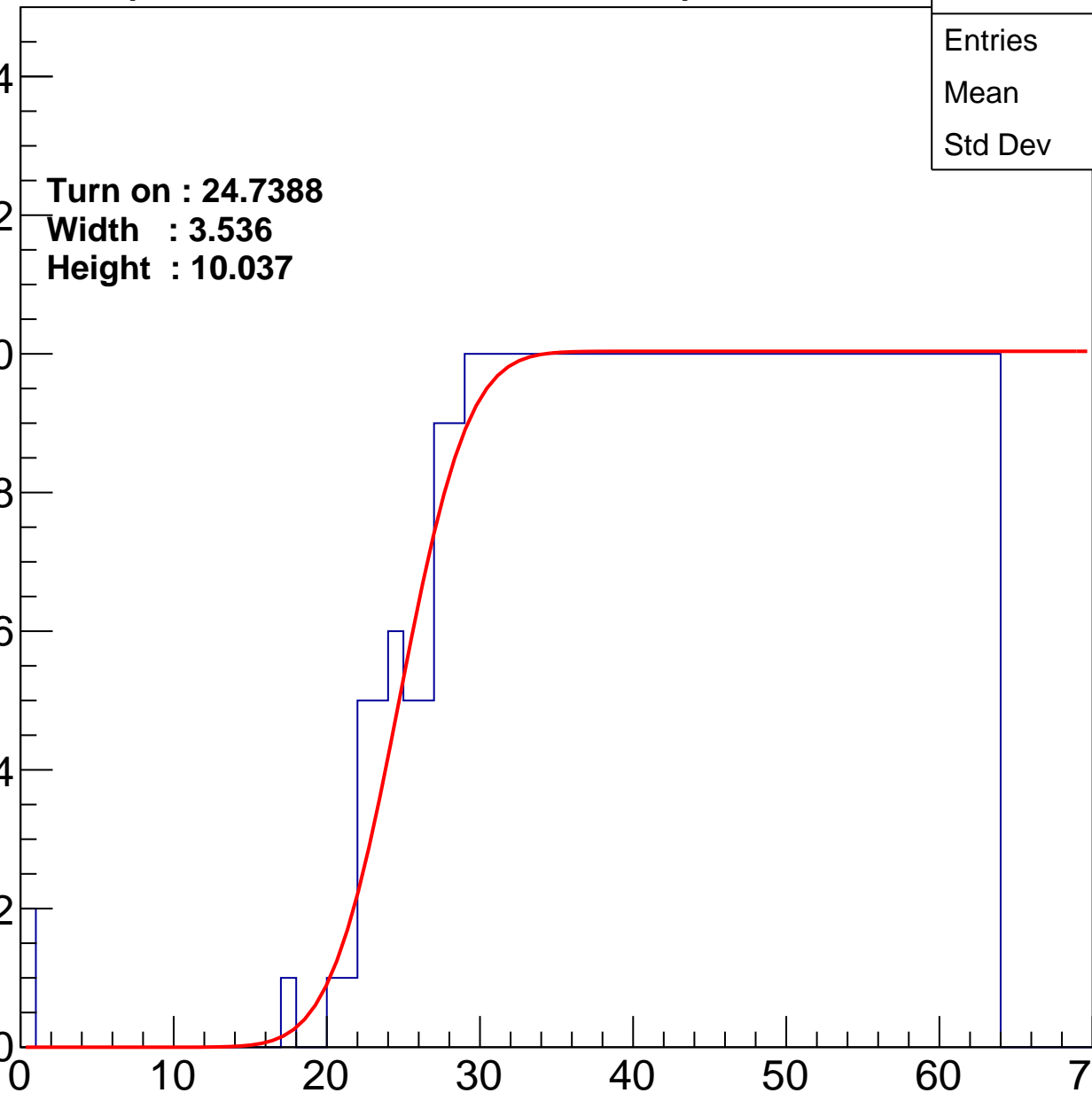
Width : 3.536

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch77

calib_packv5_042523_0143.root, FC#10, port B3

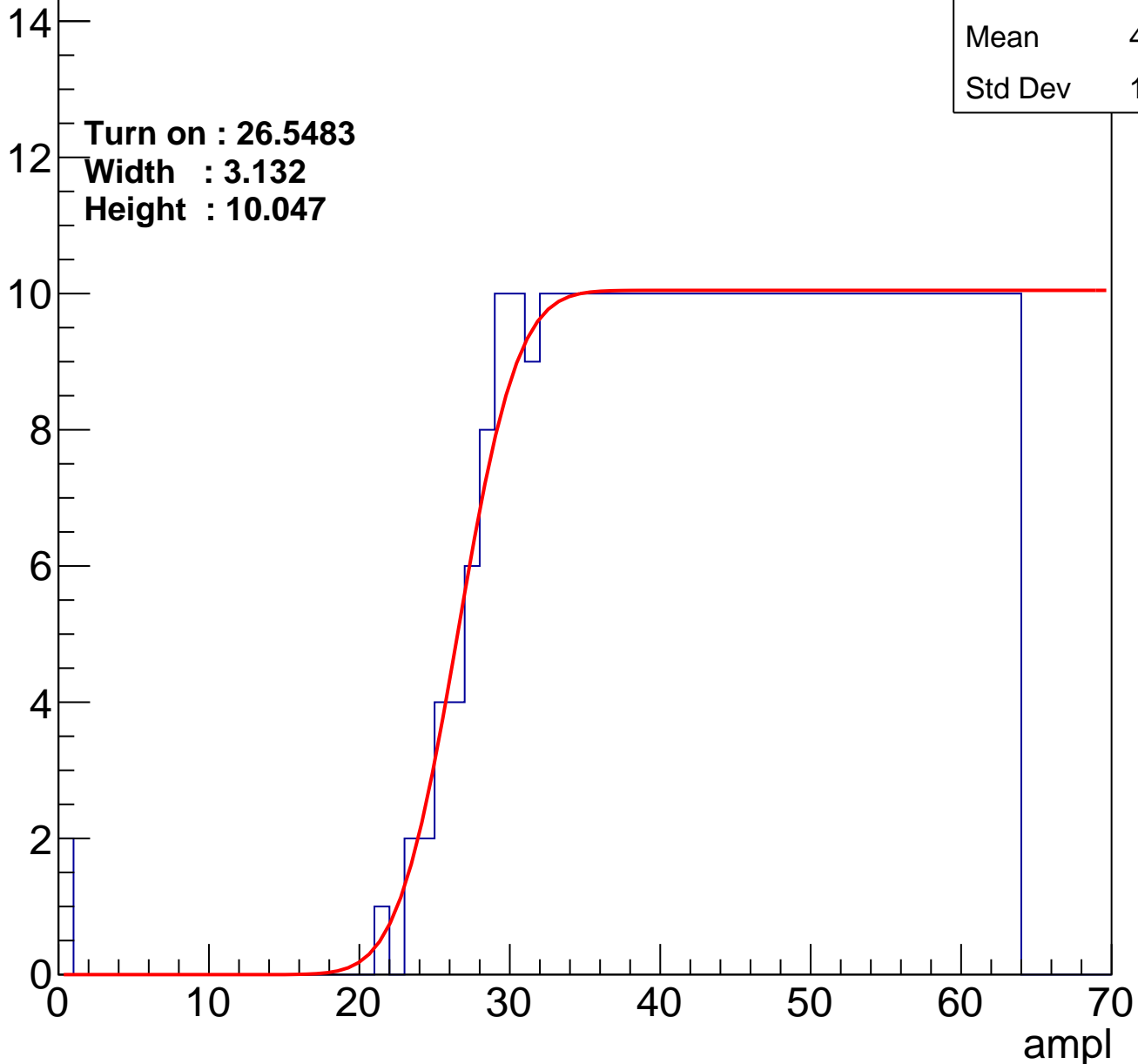
Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.5483

Width : 3.132

Height : 10.047

Entry



B1L002S, U23-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.72
Std Dev	11.91

Turn on : 25.4020

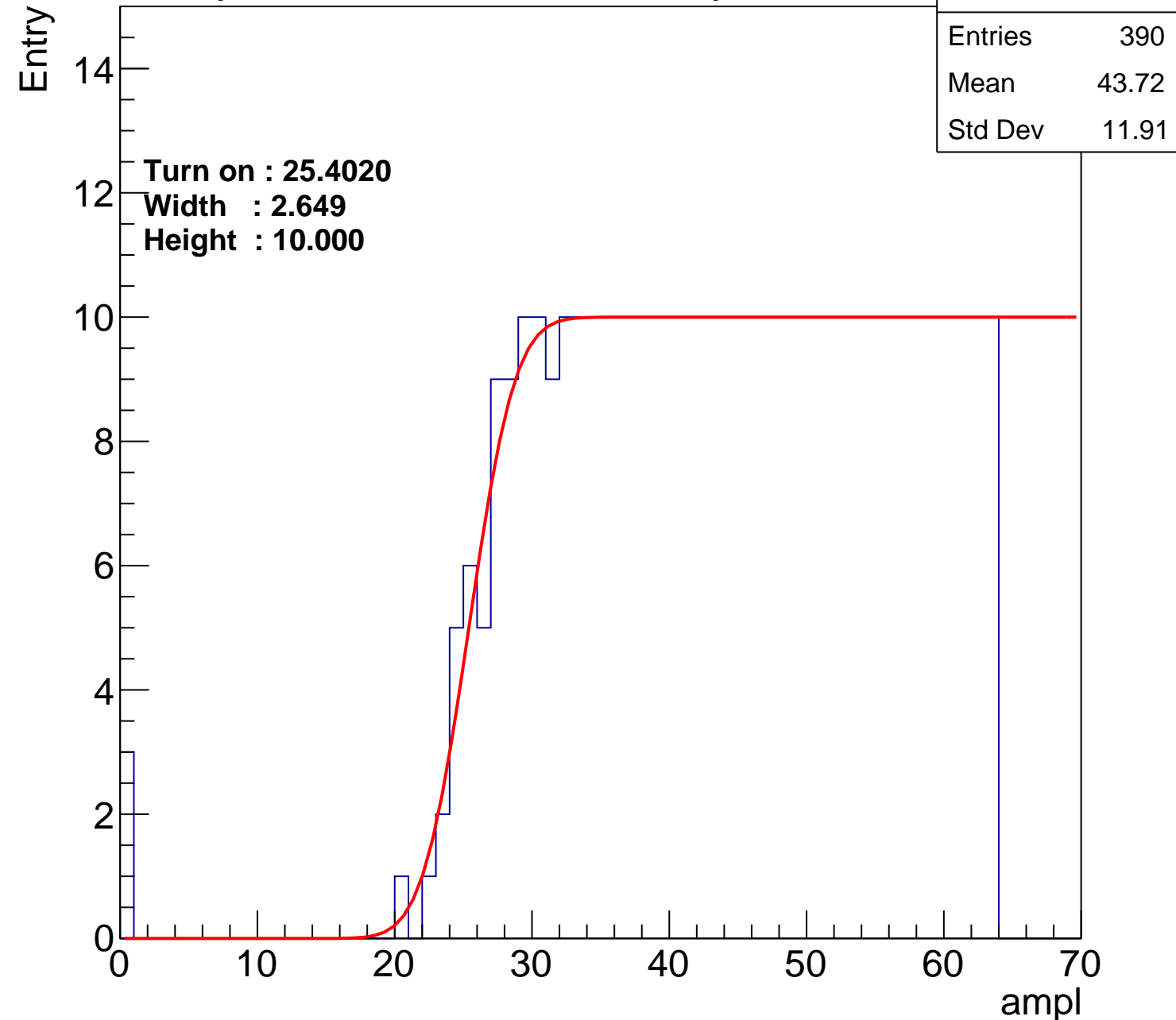
Width : 2.649

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch79

calib_packv5_042523_0143.root, FC#10, port B3

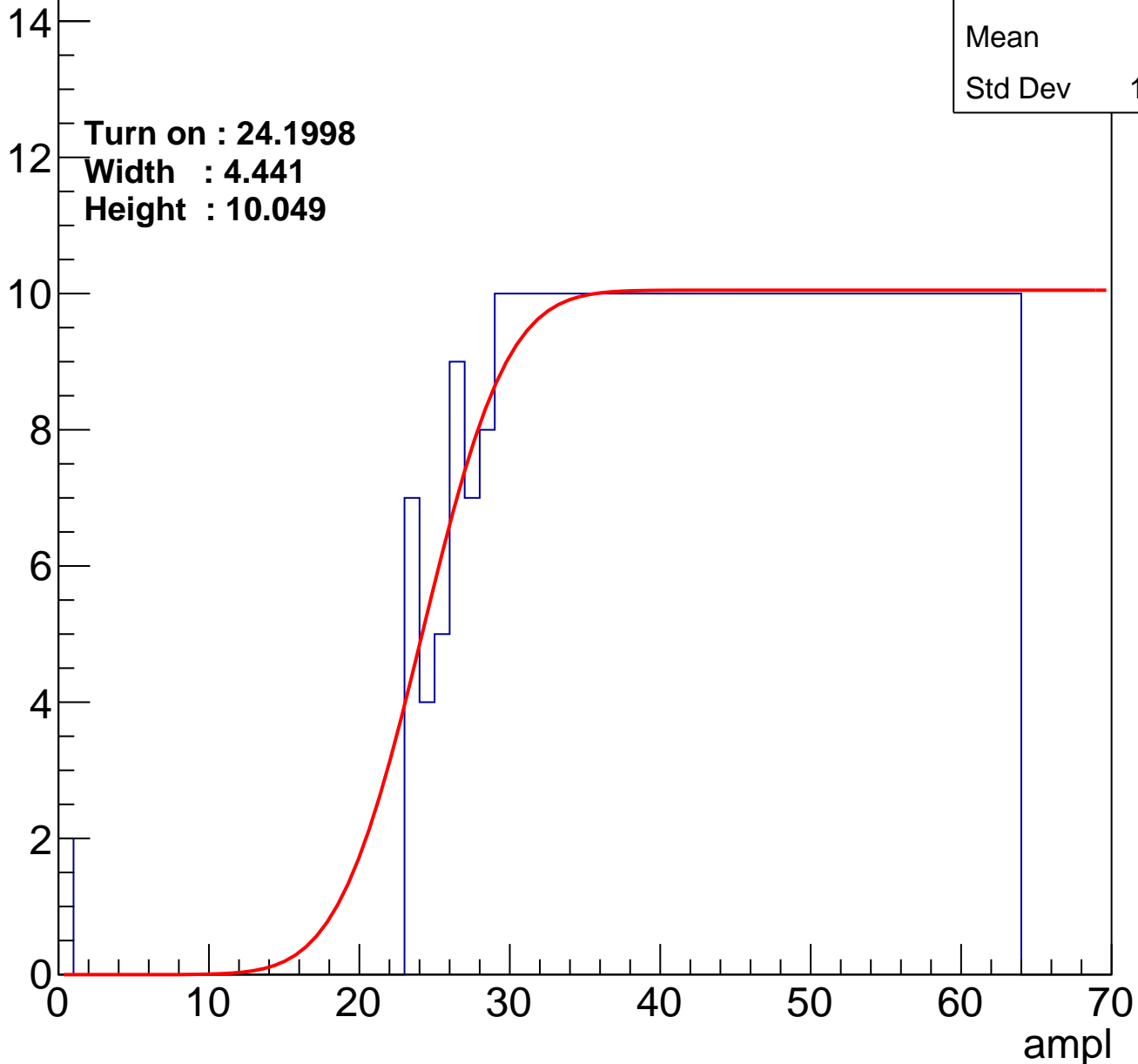
Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 24.1998

Width : 4.441

Height : 10.049

Entry



B1L002S, U23-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.41
Std Dev	11.83

Turn on : 24.9290

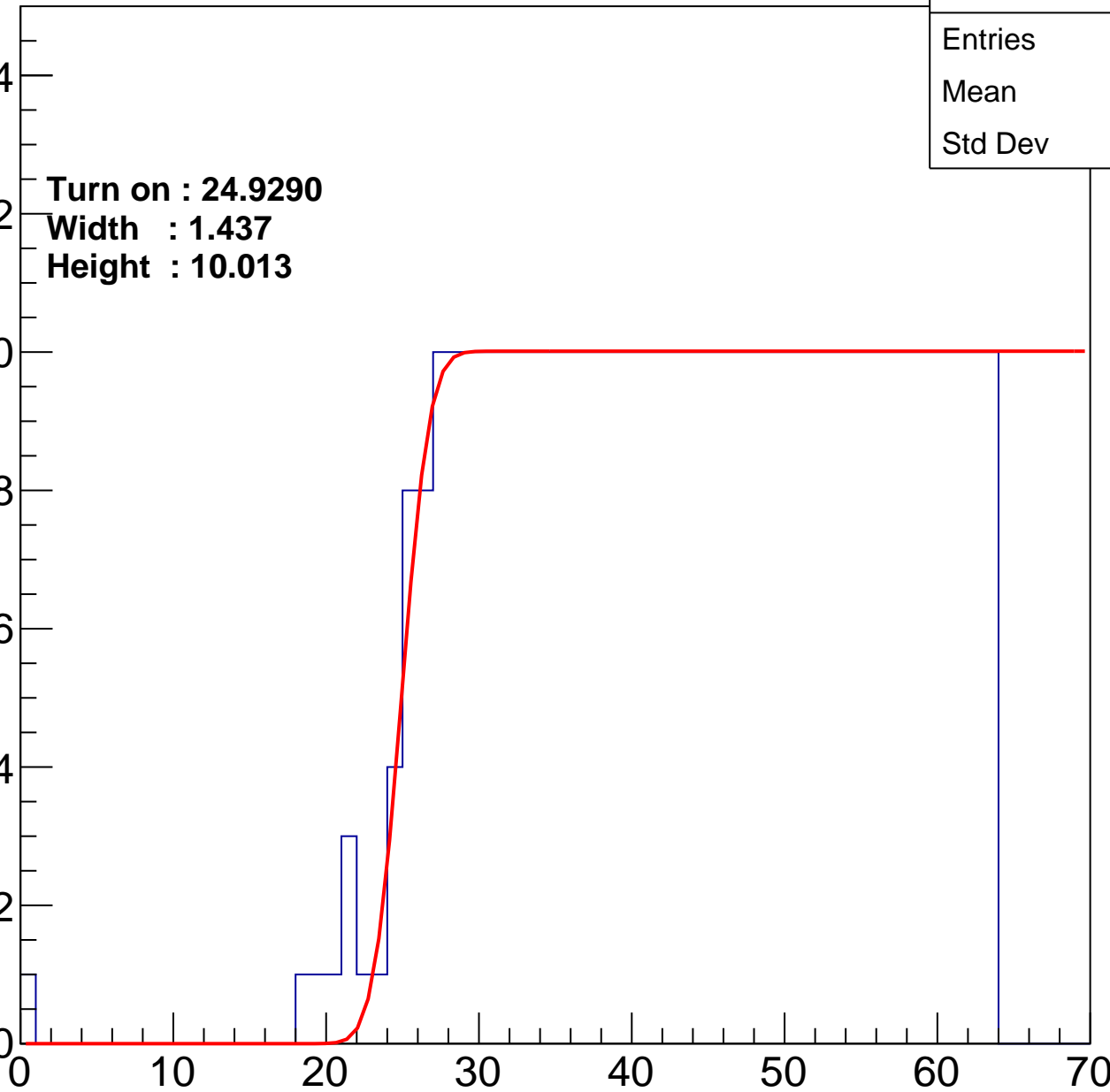
Width : 1.437

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch81

calib_packv5_042523_0143.root, FC#10, port B3

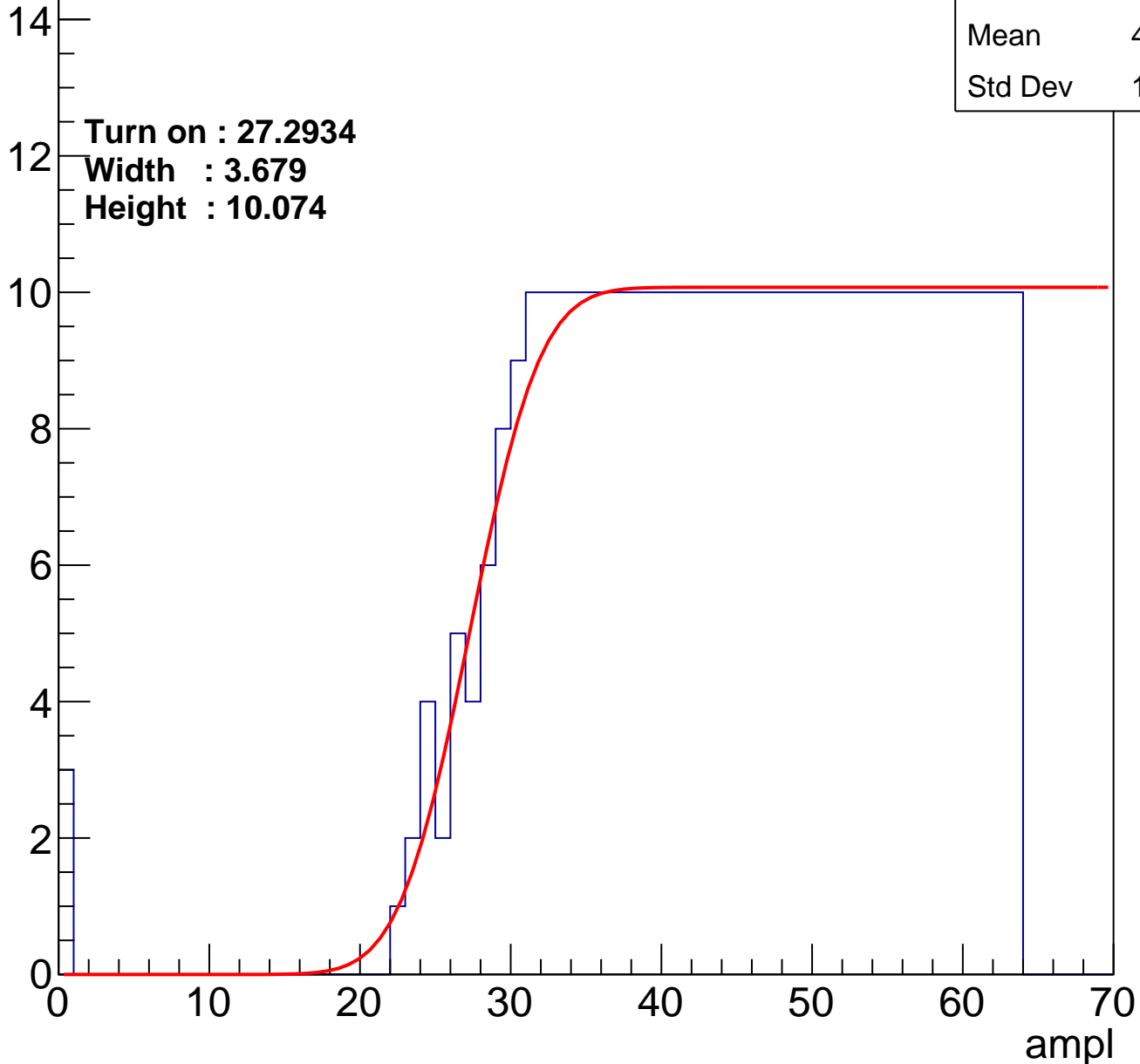
Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 27.2934

Width : 3.679

Height : 10.074

Entry



B1L002S, U23-ch82

calib_packv5_042523_0143.root, FC#10, port B3

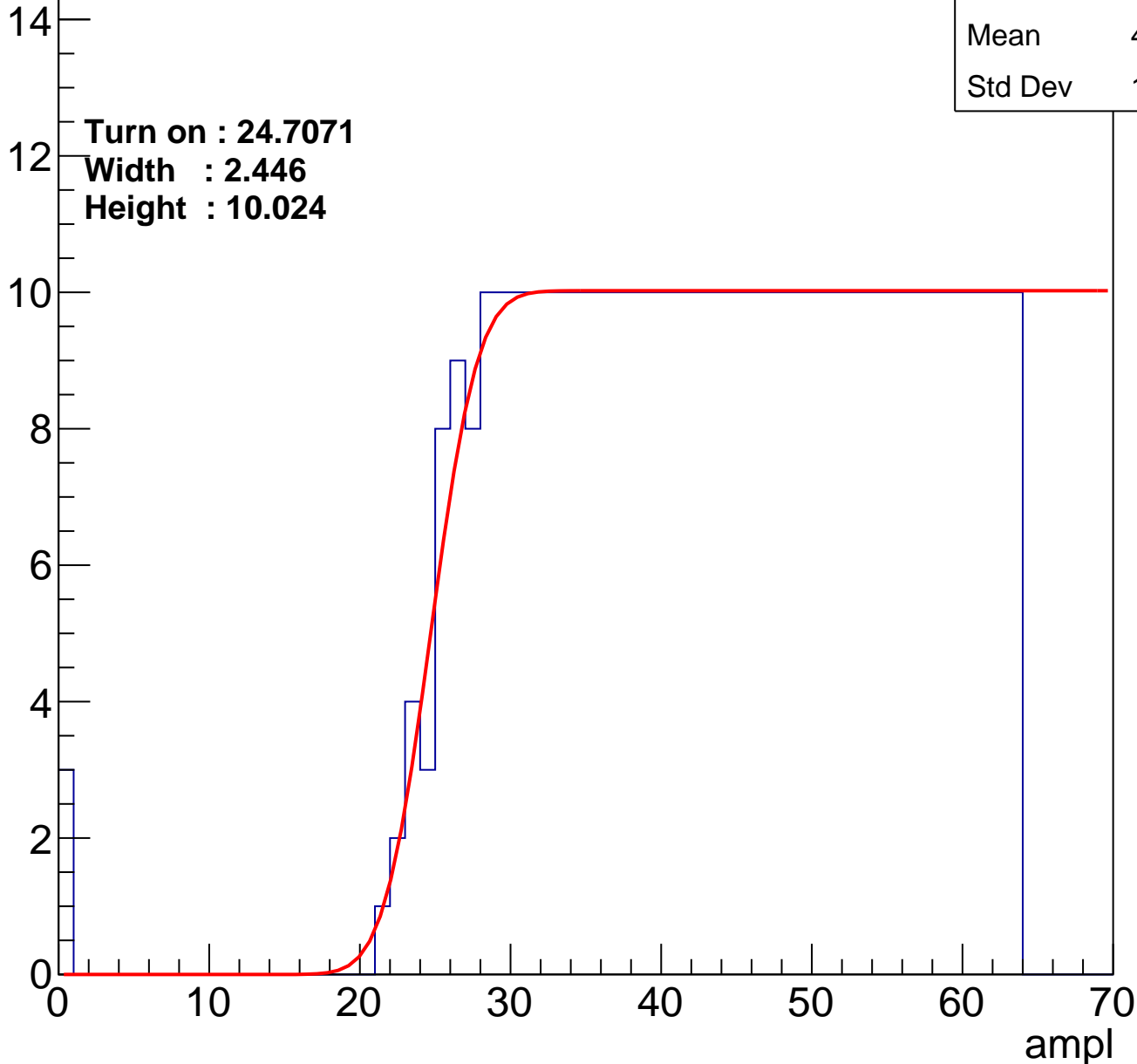
Entries	398
Mean	43.36
Std Dev	12.06

Turn on : 24.7071

Width : 2.446

Height : 10.024

Entry



B1L002S, U23-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.47
Std Dev	12.05

Turn on : 24.7726

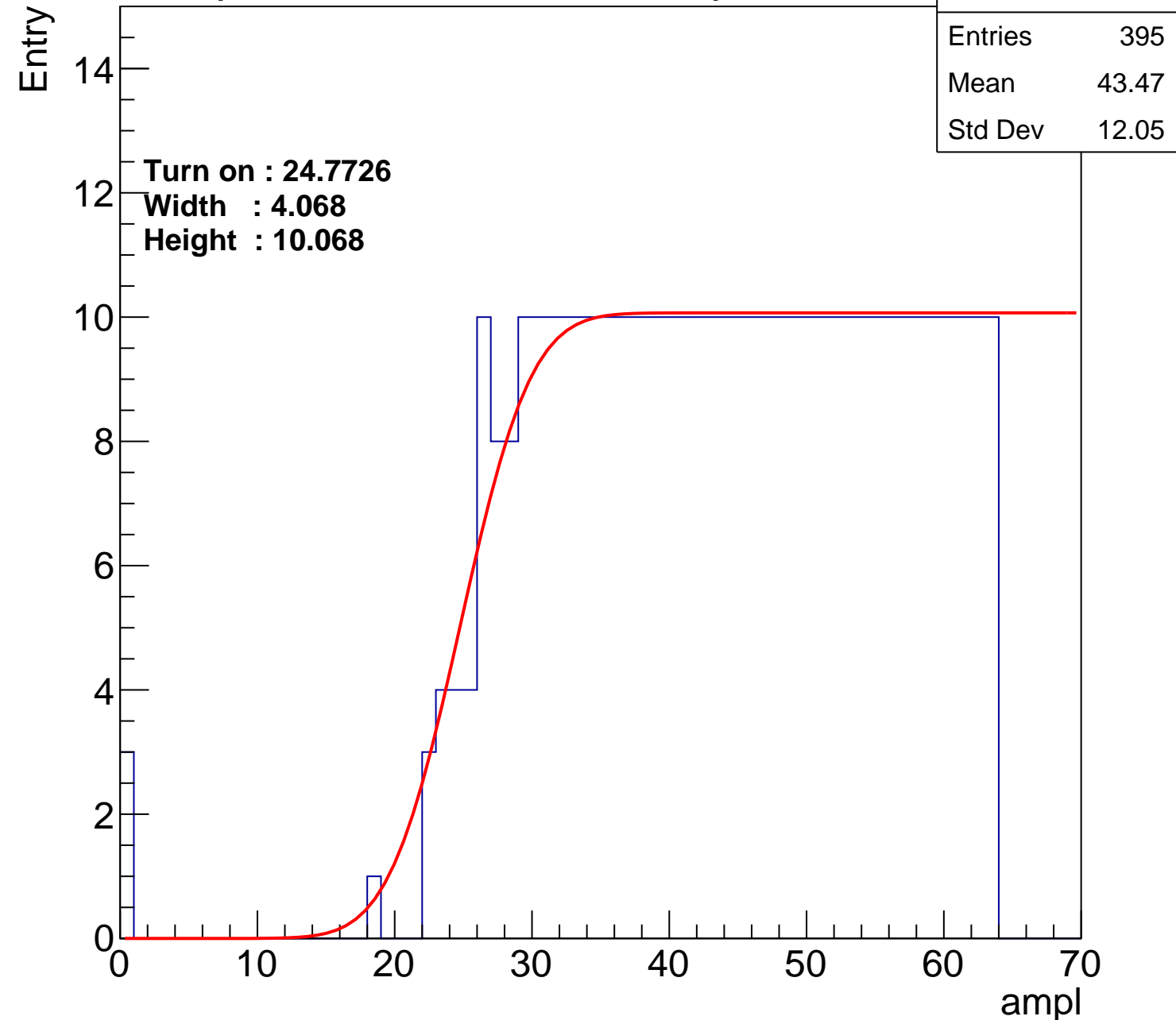
Width : 4.068

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.8975

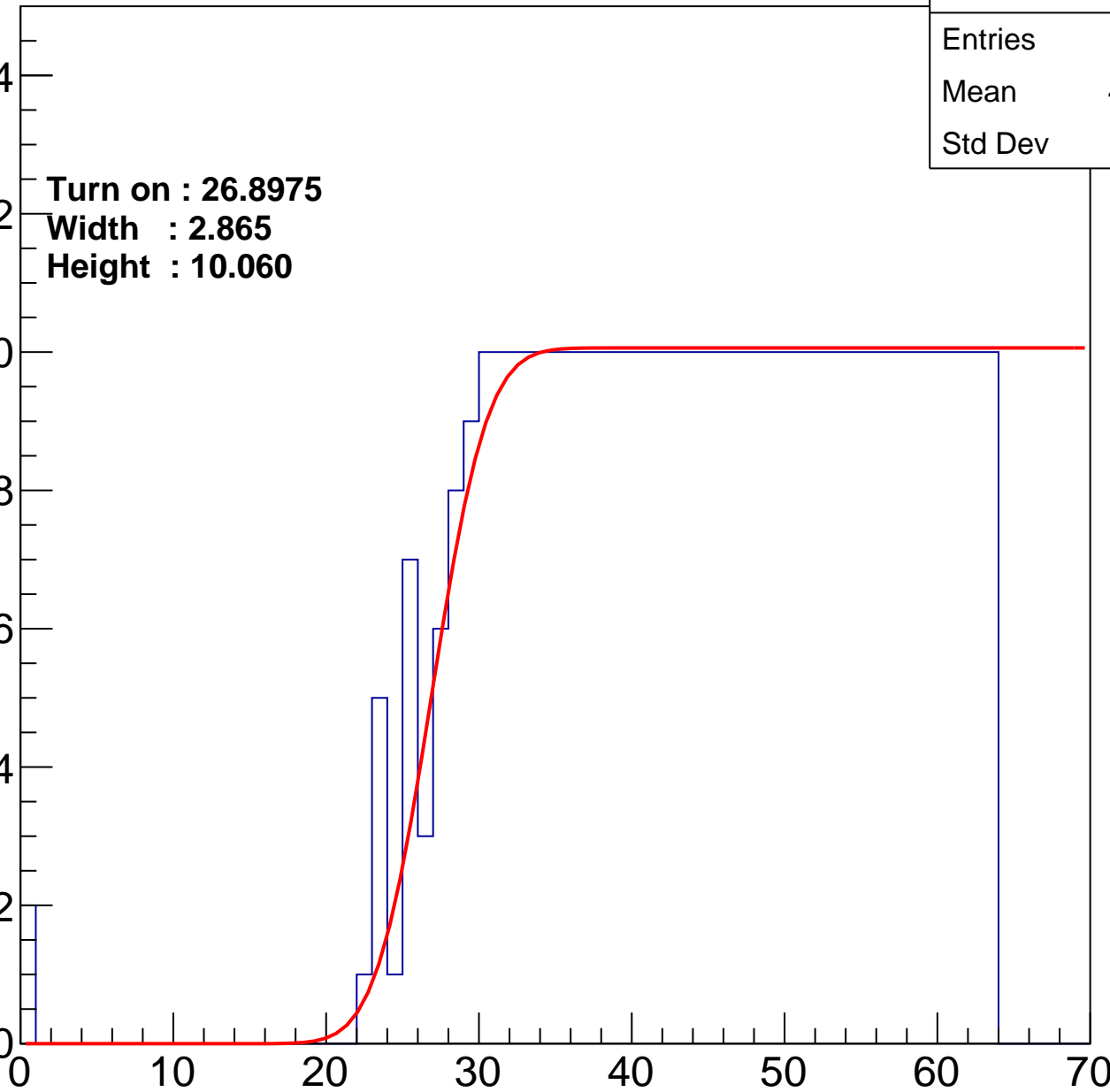
Width : 2.865

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch85

calib_packv5_042523_0143.root, FC#10, port B3

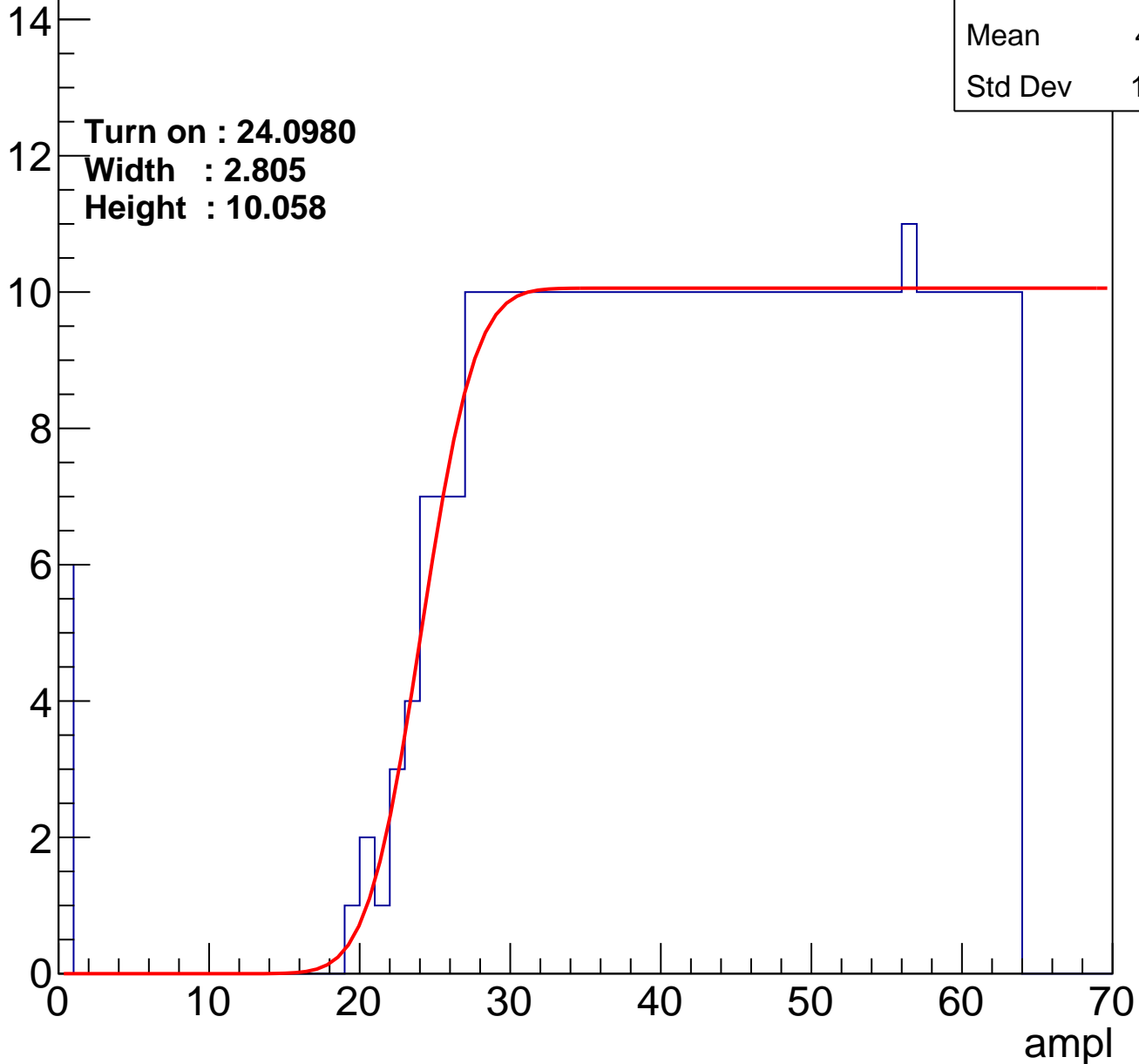
Entries	409
Mean	42.71
Std Dev	12.77

Turn on : 24.0980

Width : 2.805

Height : 10.058

Entry



B1L002S, U23-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 25.4560

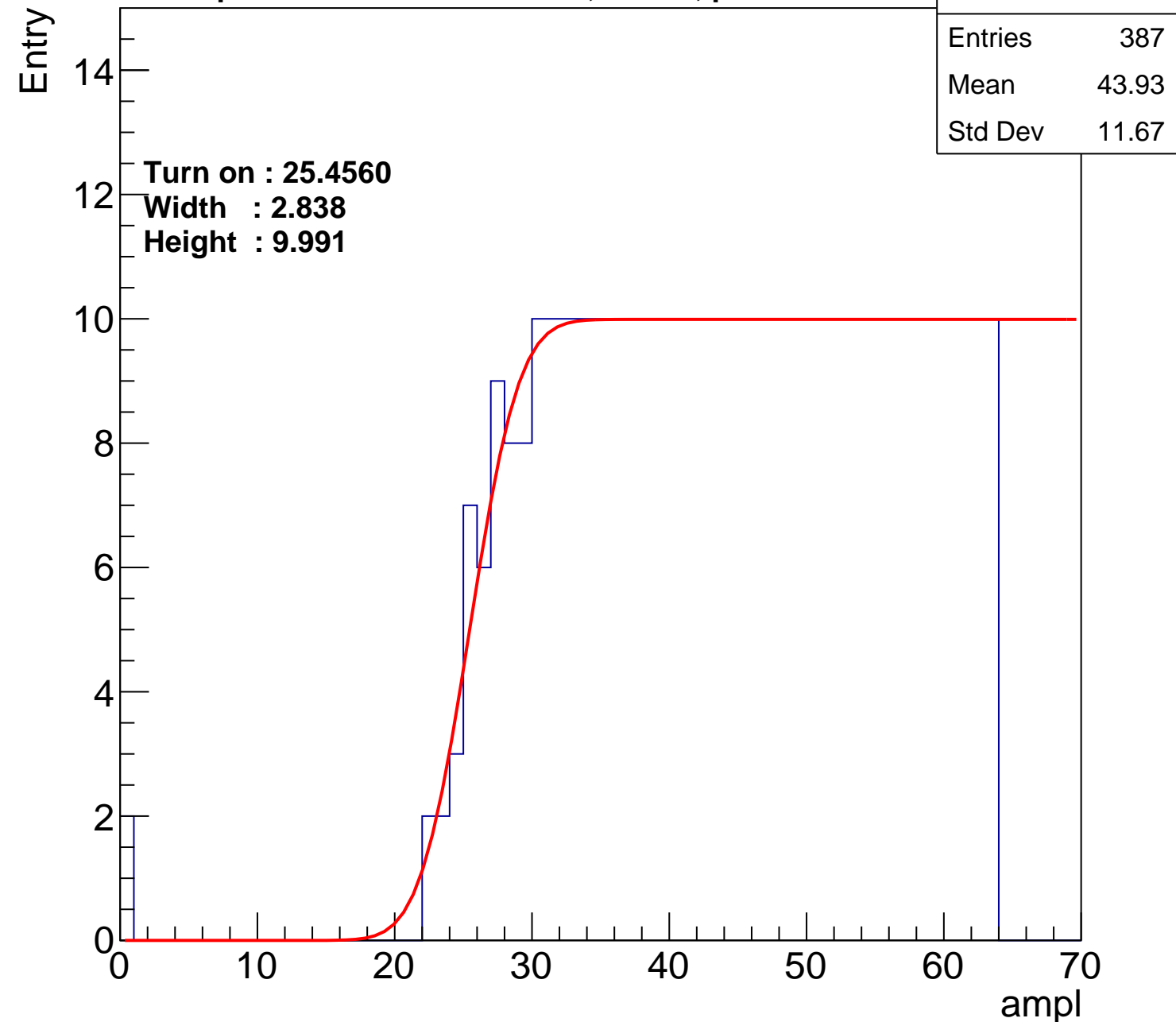
Width : 2.838

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.82
Std Dev	11.22

Turn on : 28.8550

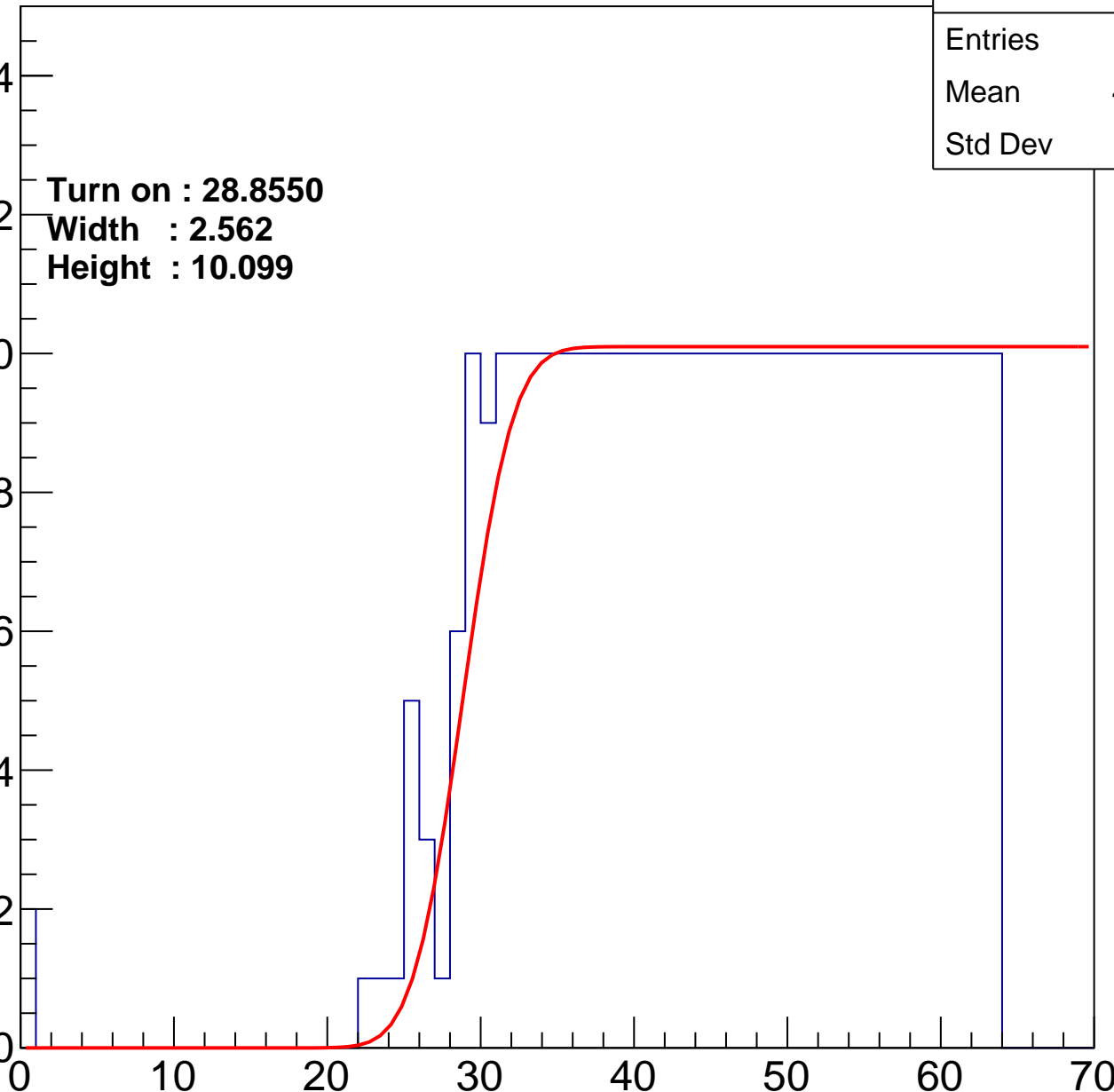
Width : 2.562

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.78

Turn on : 25.2864

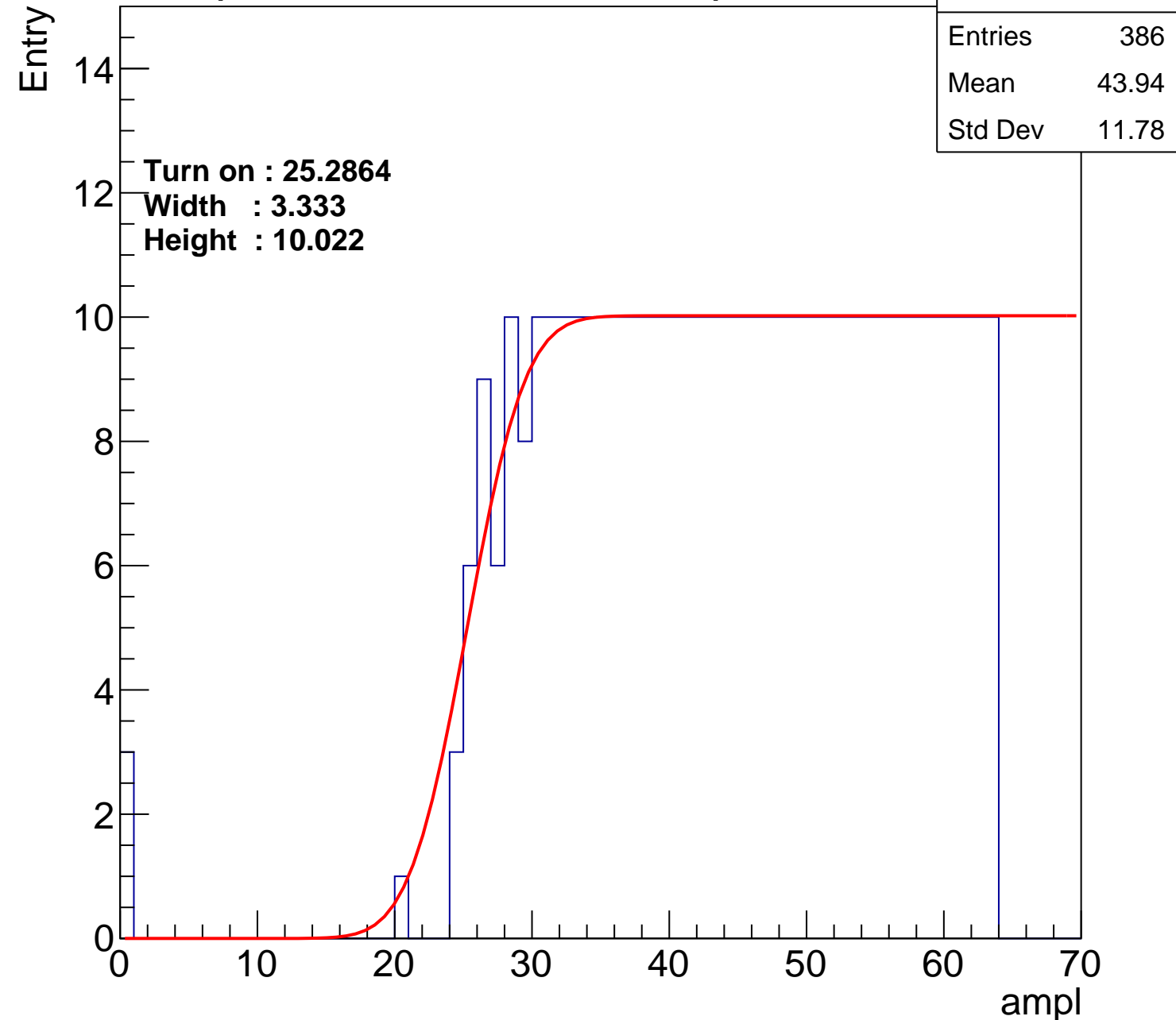
Width : 3.333

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch89

calib_packv5_042523_0143.root, FC#10, port B3

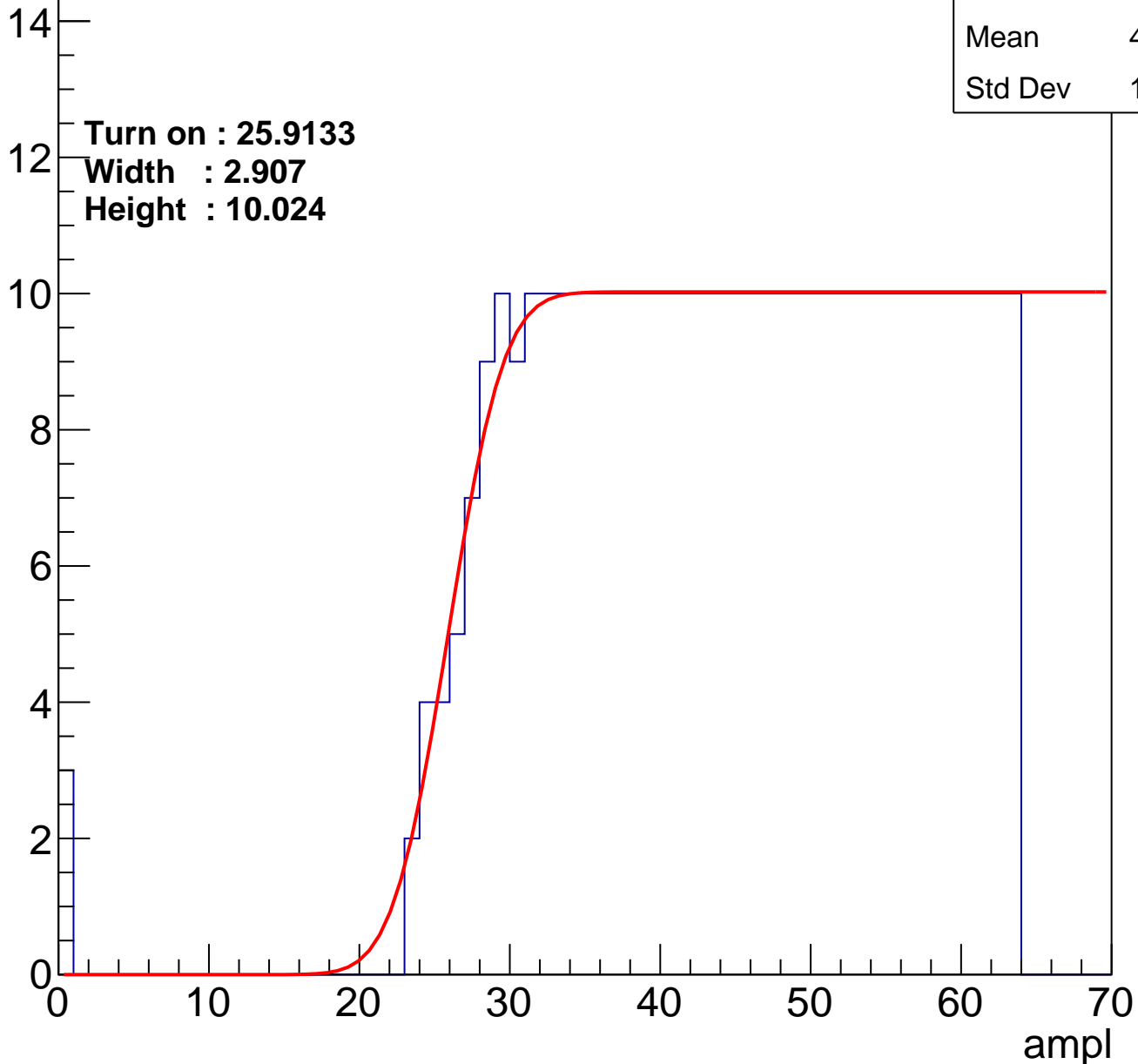
Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 25.9133

Width : 2.907

Height : 10.024

Entry



B1L002S, U23-ch90

calib_packv5_042523_0143.root, FC#10, port B3

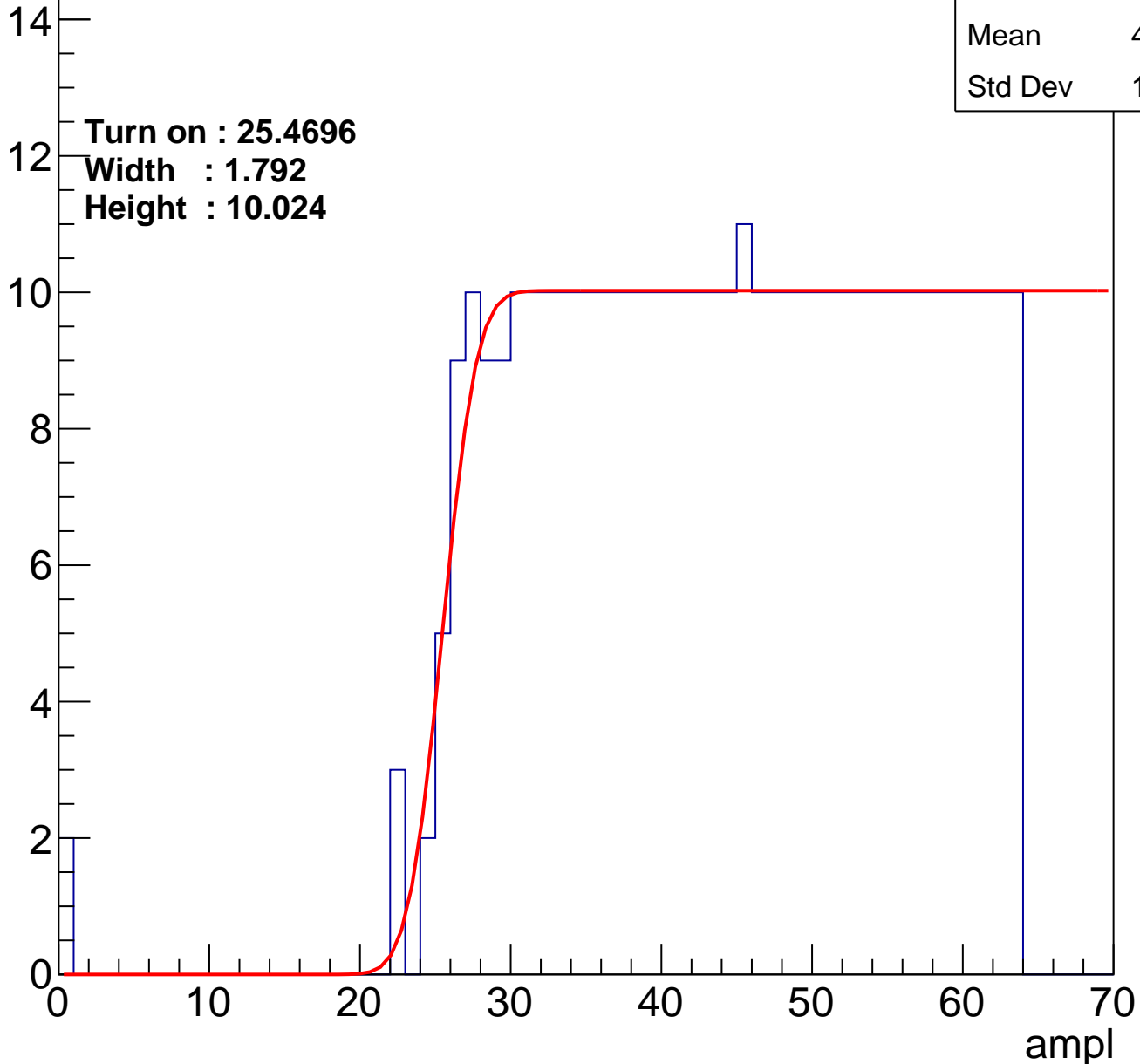
Entries	390
Mean	43.87
Std Dev	11.65

Turn on : 25.4696

Width : 1.792

Height : 10.024

Entry



B1L002S, U23-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.83
Std Dev	11.91

Turn on : 26.0372

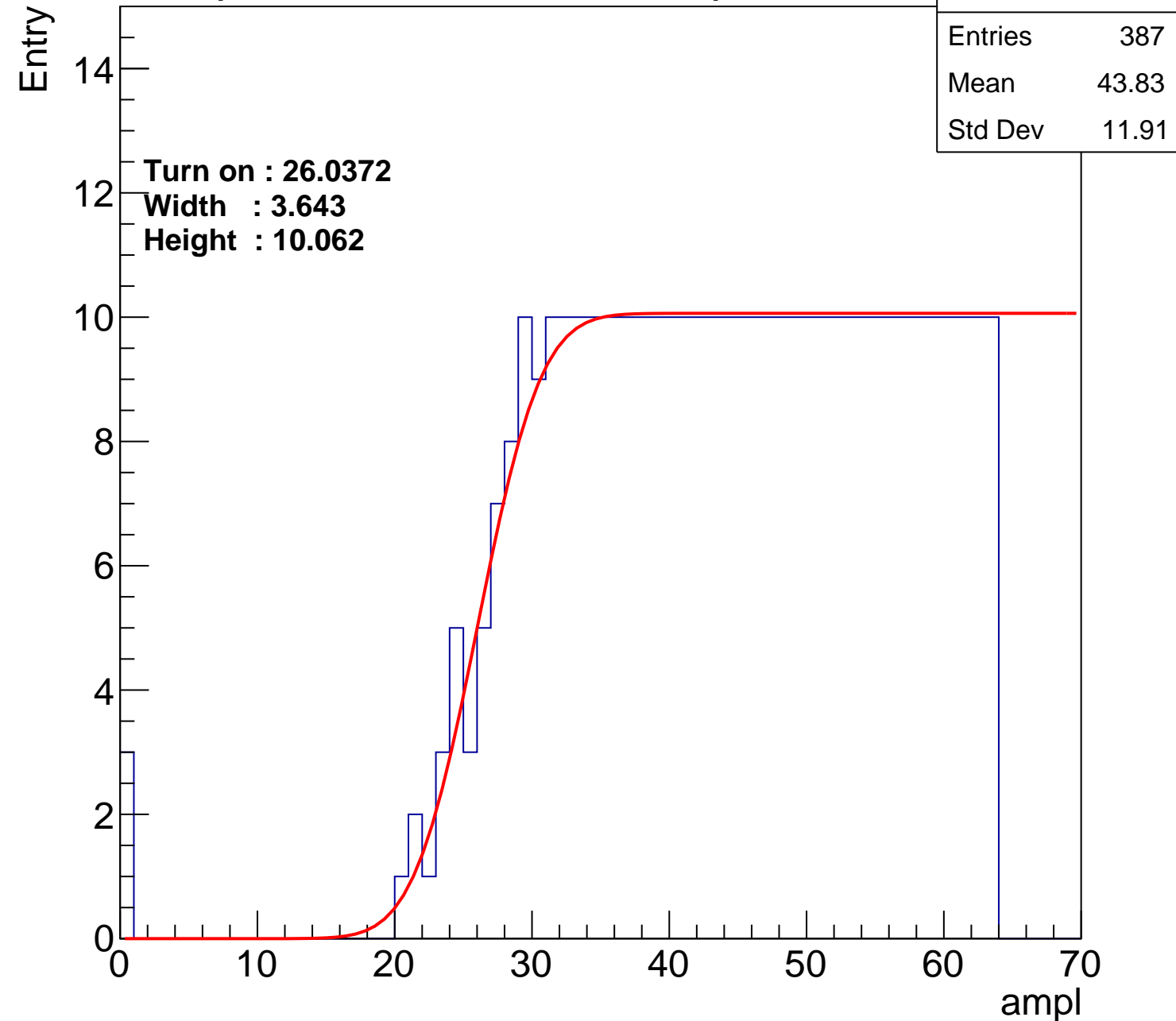
Width : 3.643

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.42
Std Dev	11.96

Turn on : 24.7060

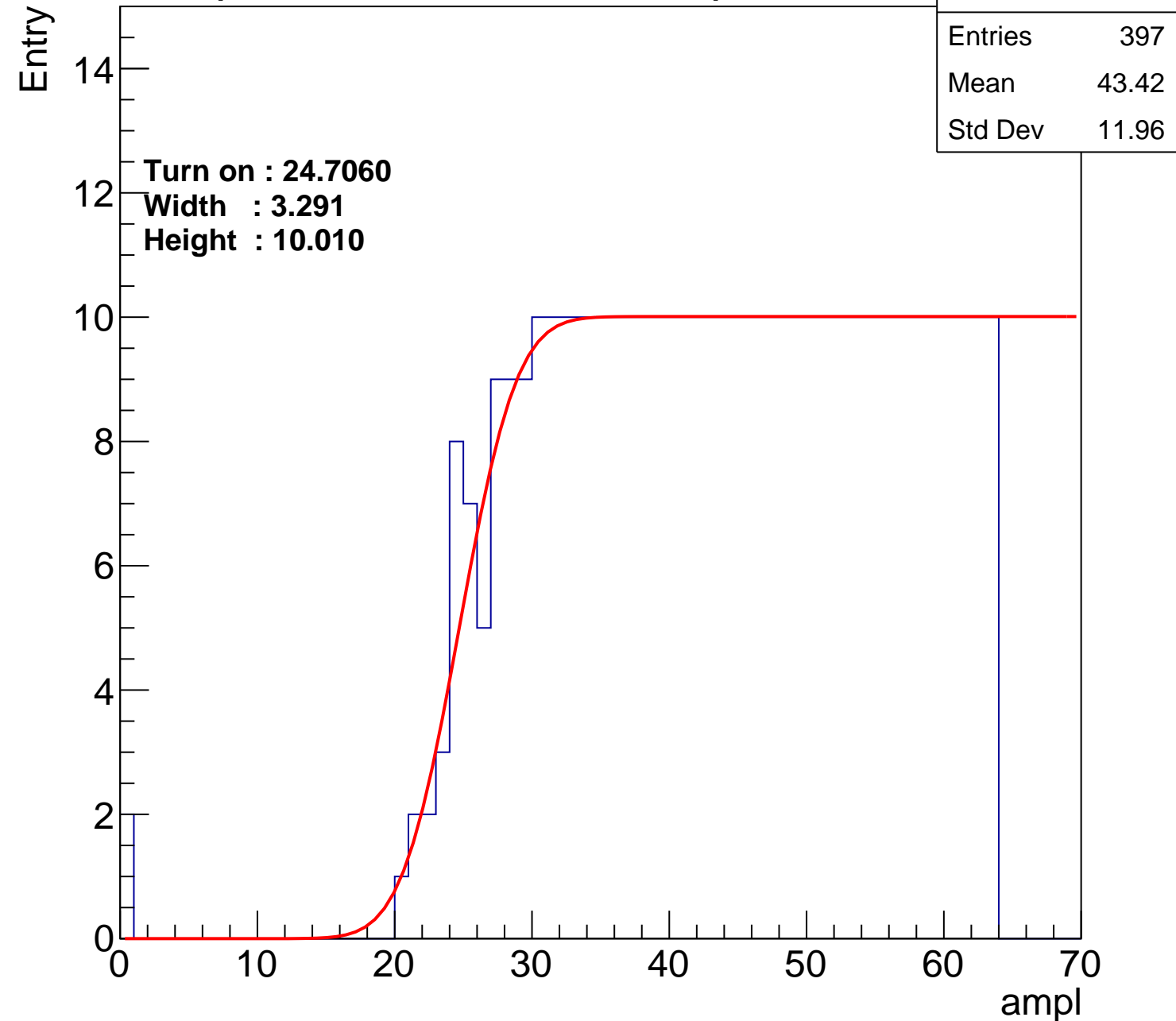
Width : 3.291

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.7758

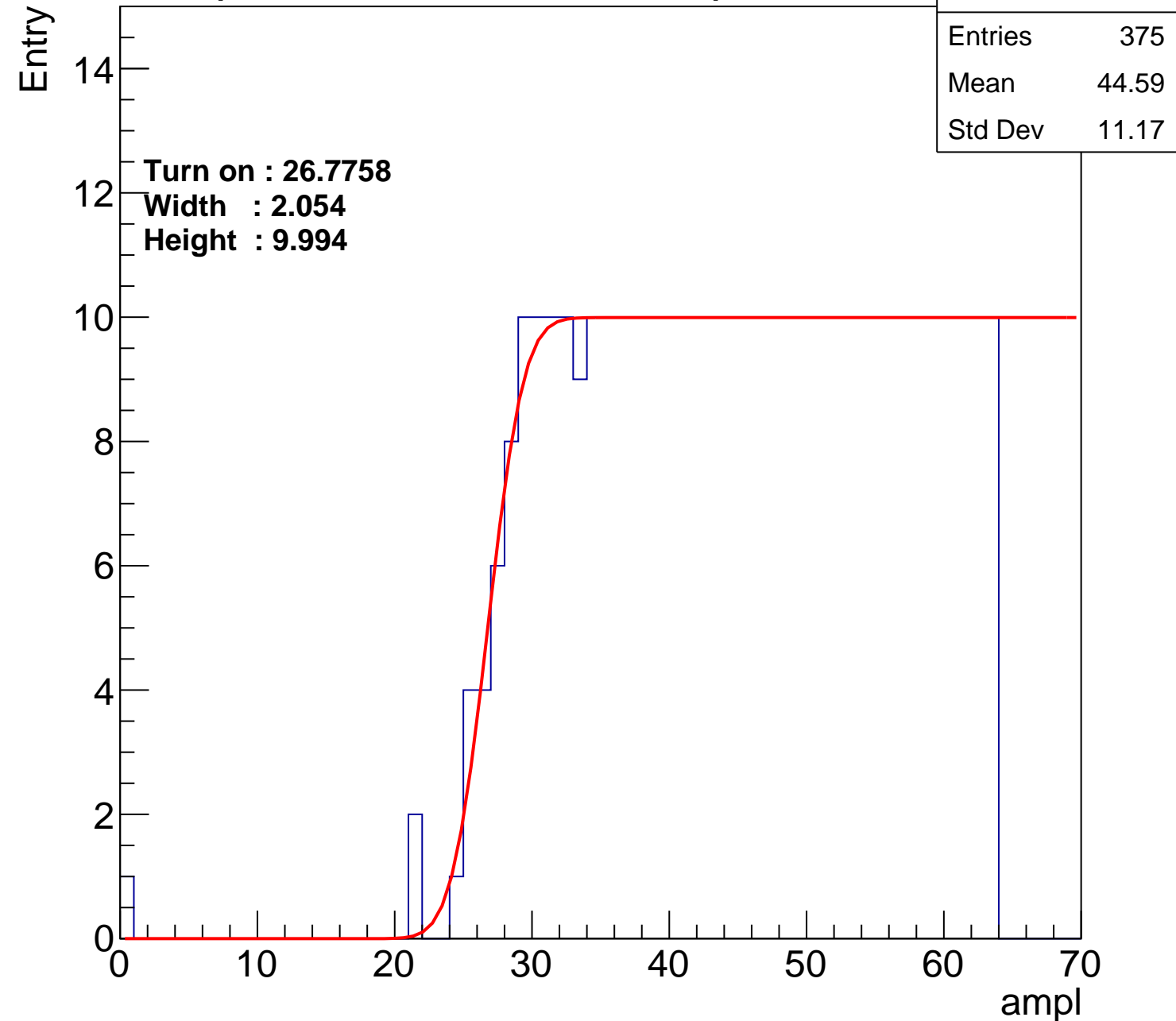
Width : 2.054

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch94

calib_packv5_042523_0143.root, FC#10, port B3

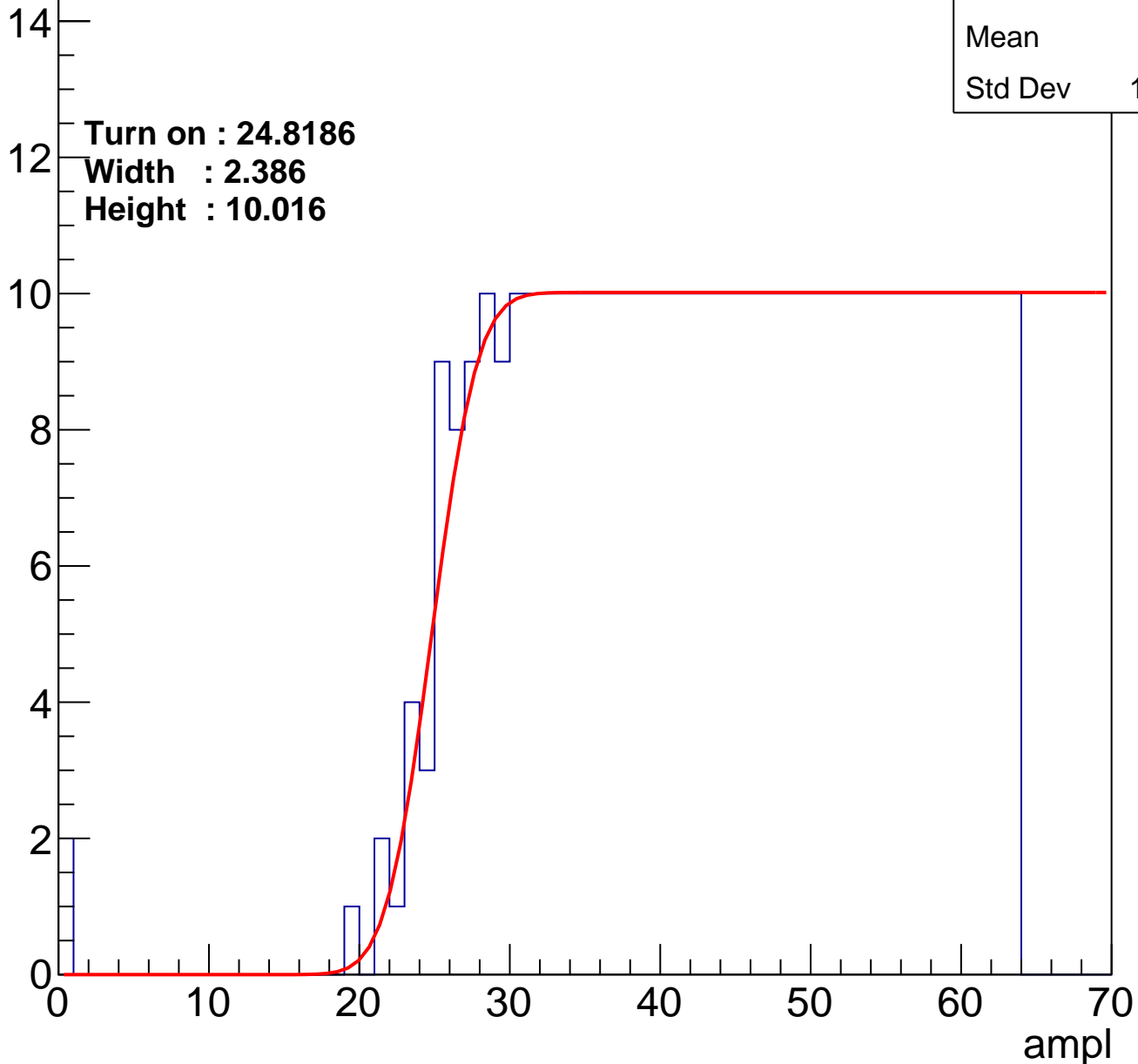
Entries	398
Mean	43.4
Std Dev	11.94

Turn on : 24.8186

Width : 2.386

Height : 10.016

Entry



B1L002S, U23-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.63
Std Dev	12.02

Turn on : 26.2698

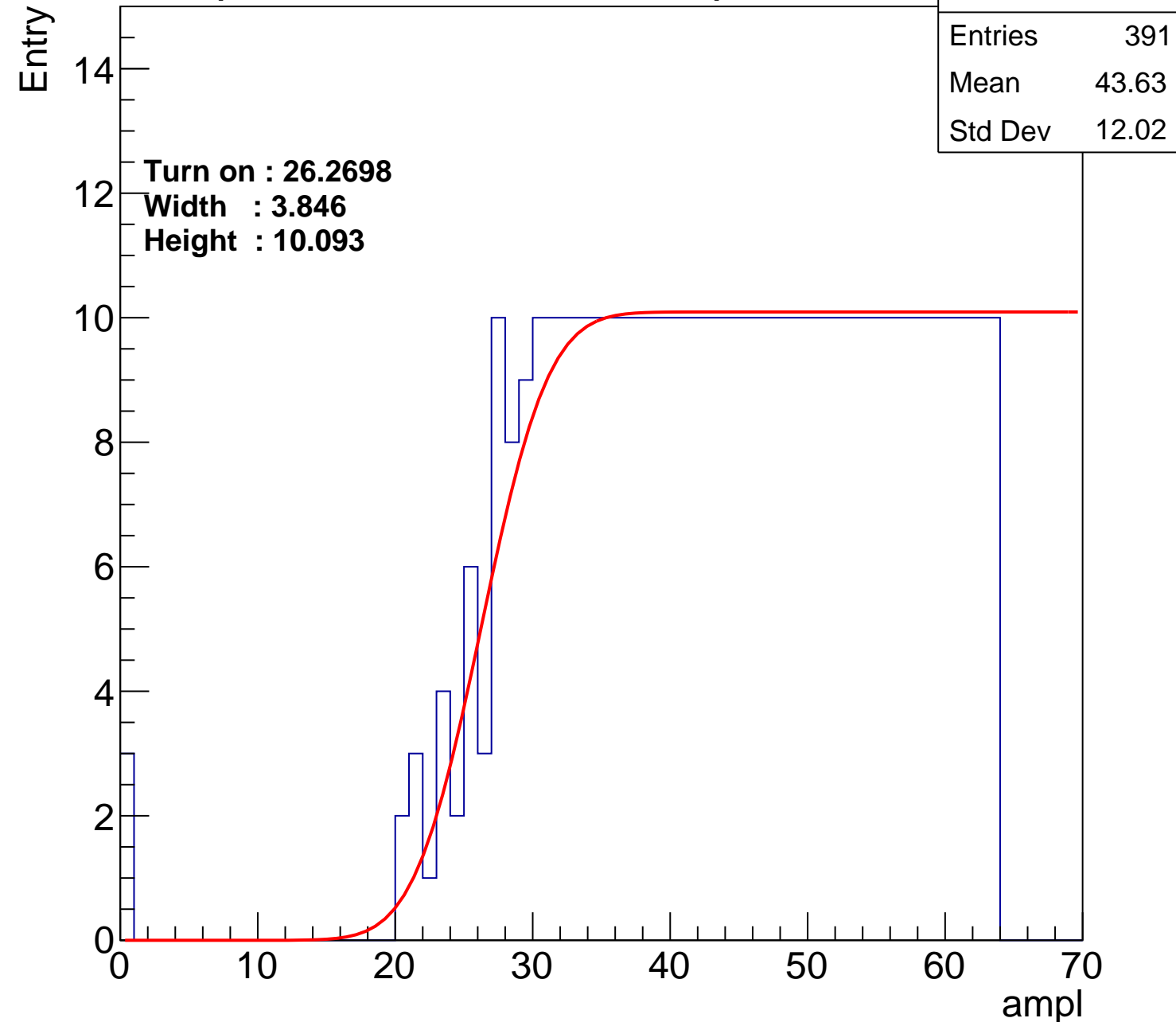
Width : 3.846

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.58
Std Dev	12.21

Turn on : 25.7821

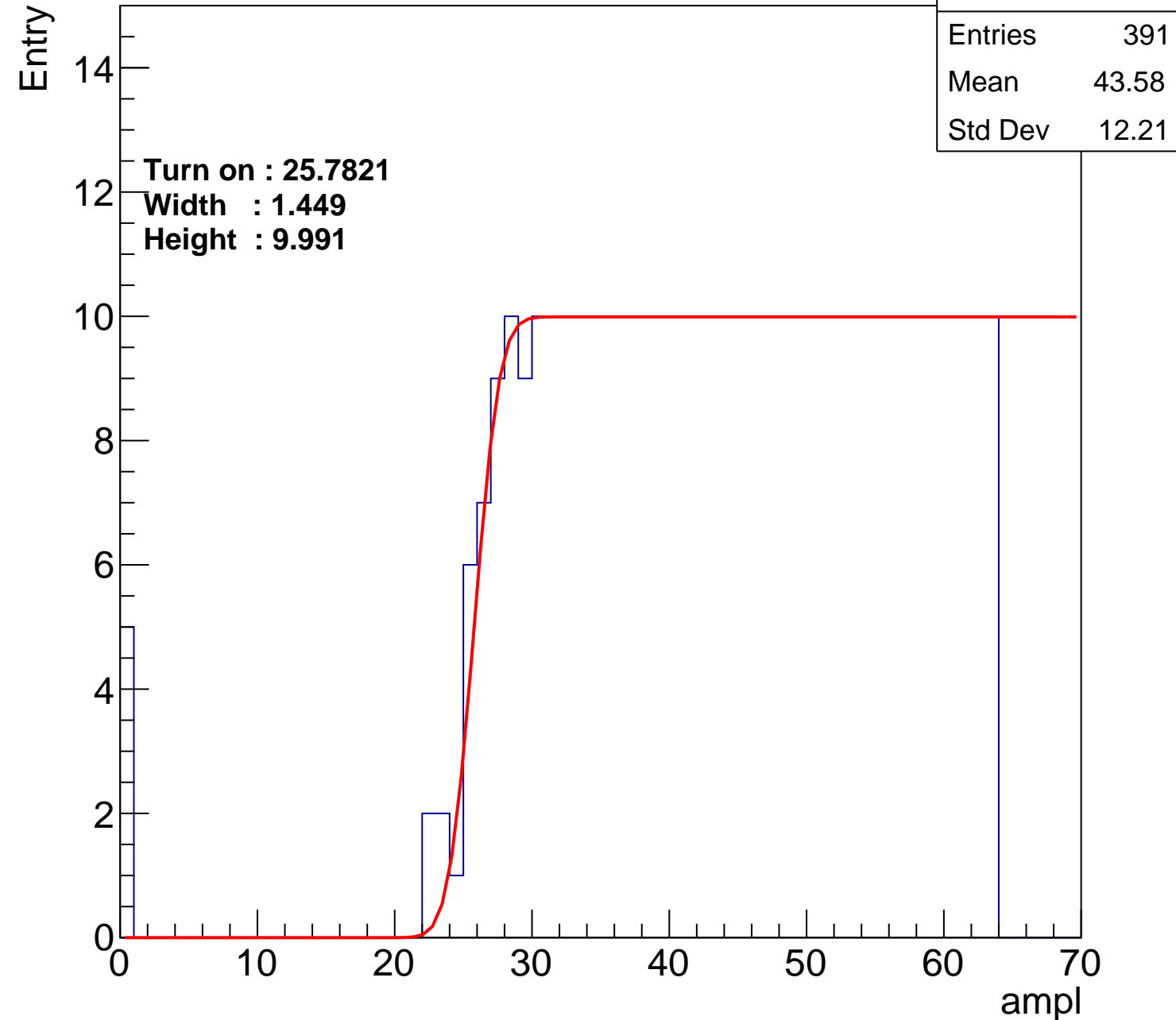
Width : 1.449

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.48
Std Dev	12.04

Turn on : 25.1884

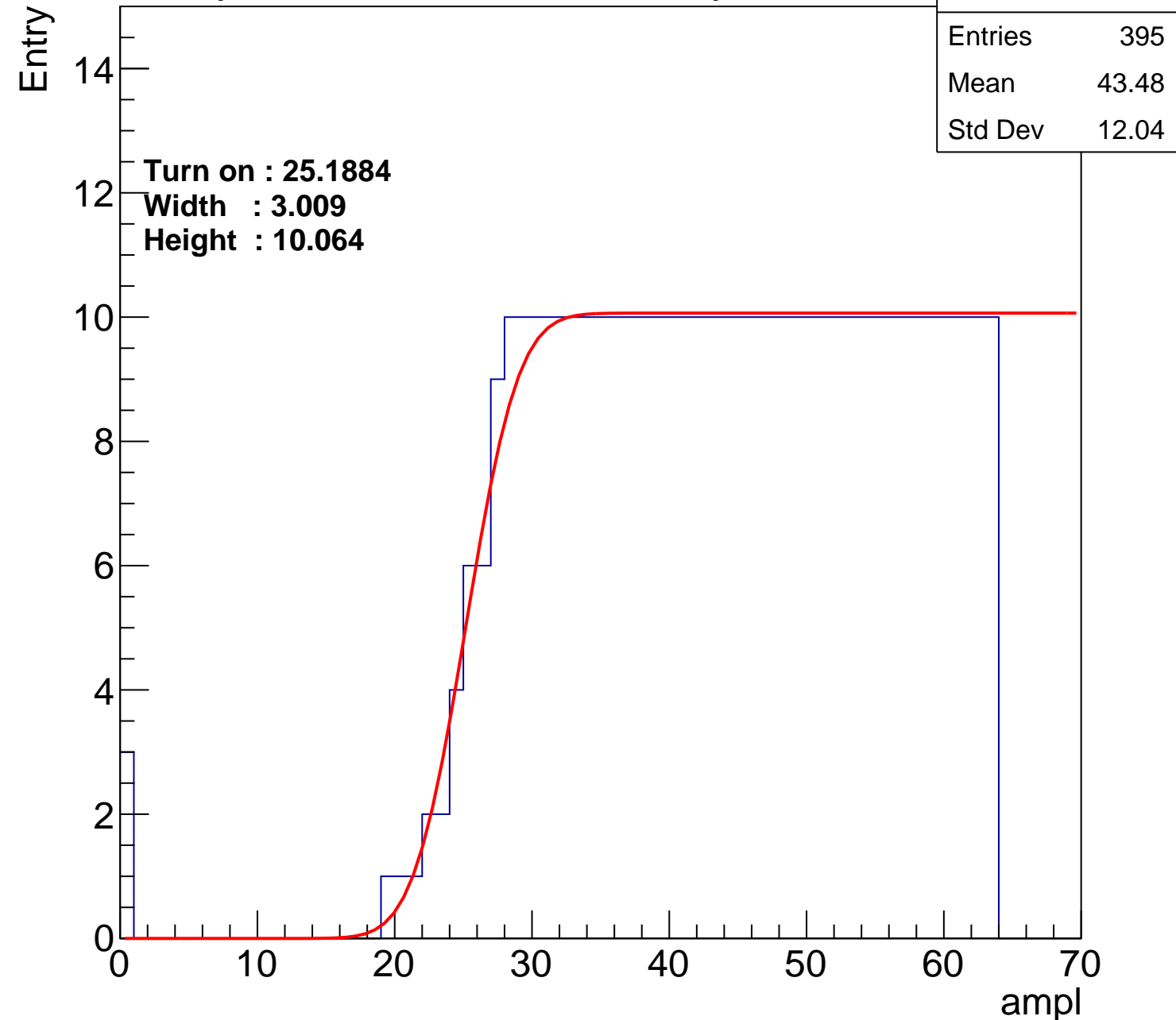
Width : 3.009

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	43.01
Std Dev	12.03

Turn on : 23.6739

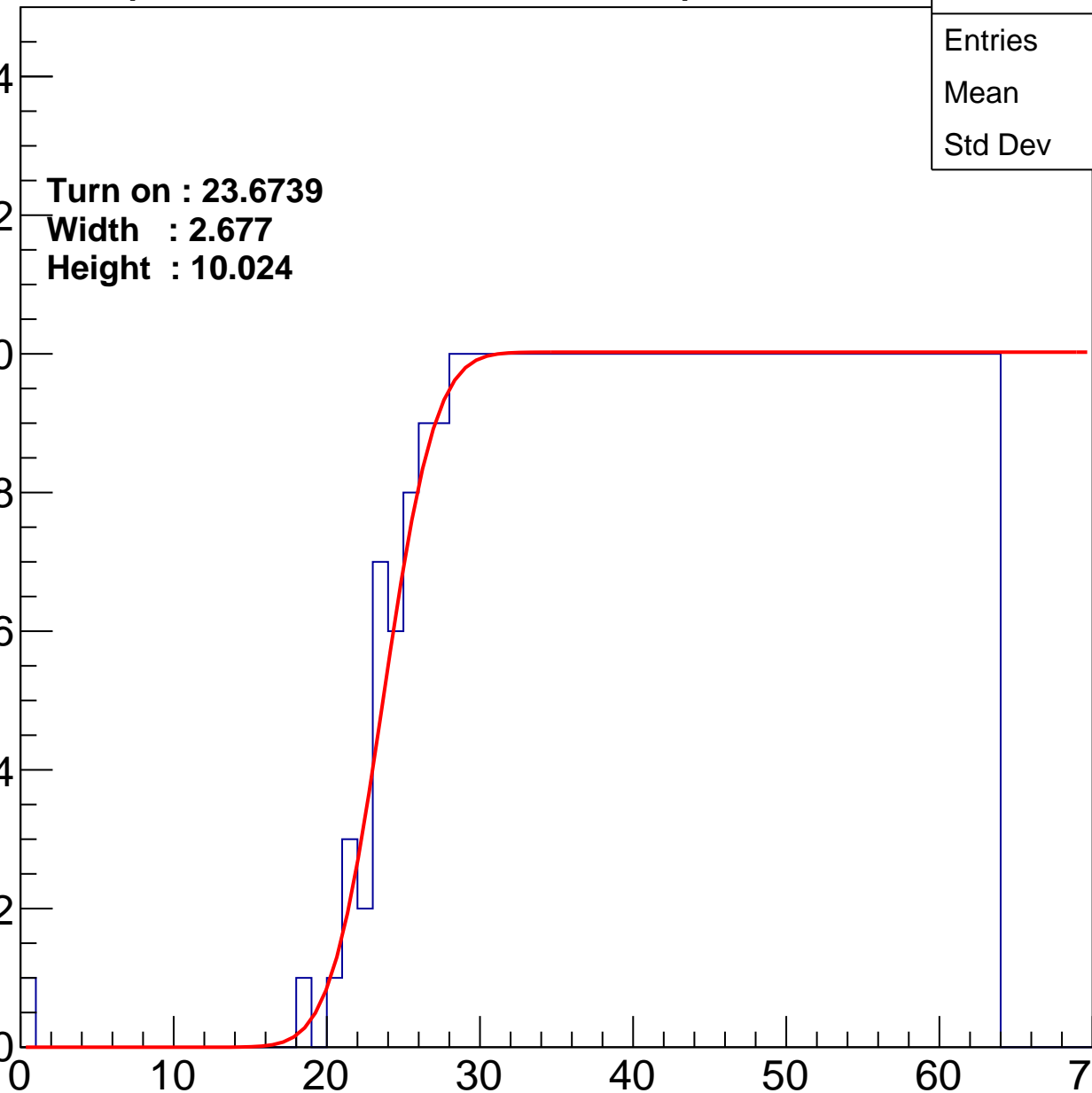
Width : 2.677

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 26.2542

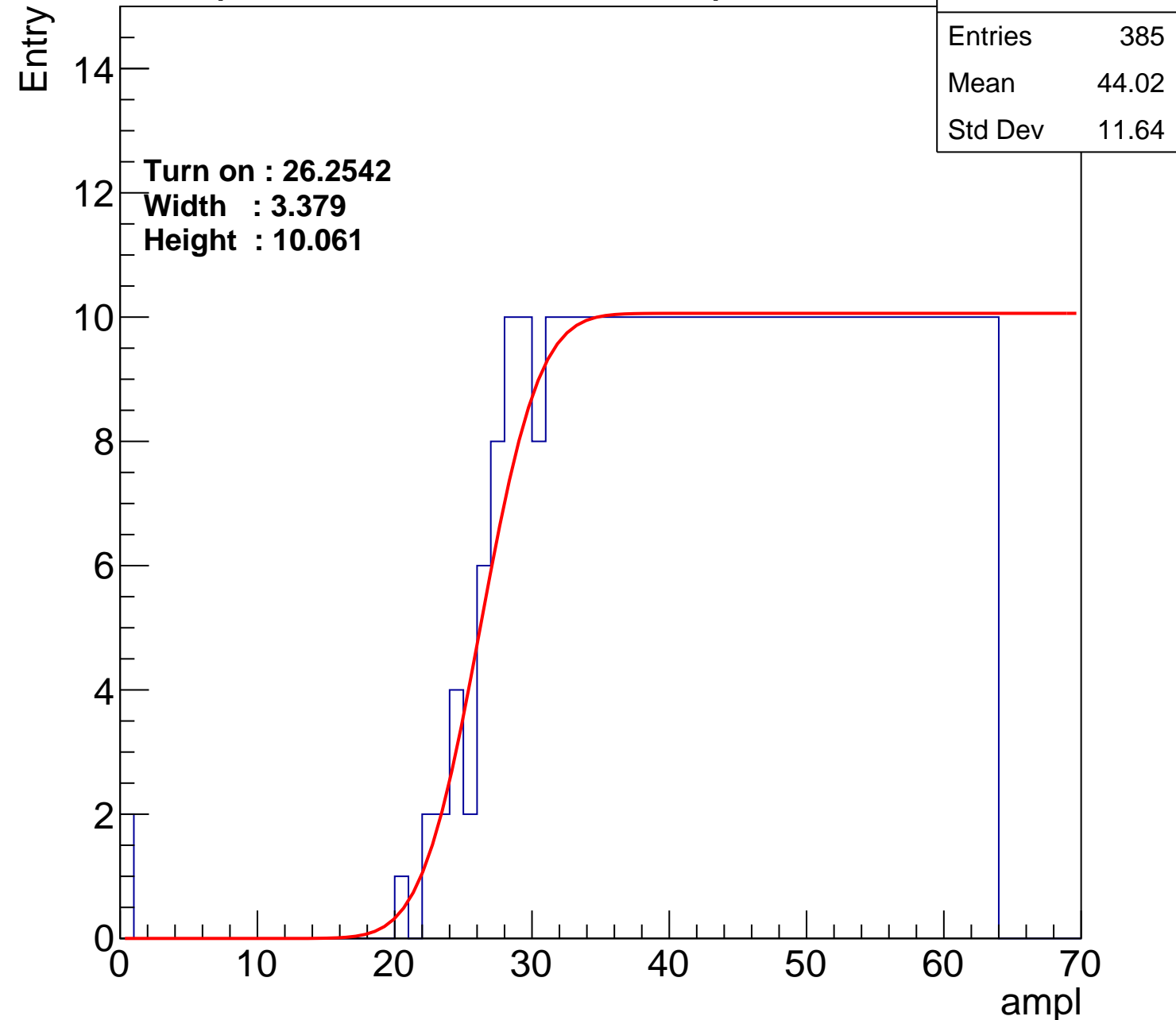
Width : 3.379

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch100

calib_packv5_042523_0143.root, FC#10, port B3

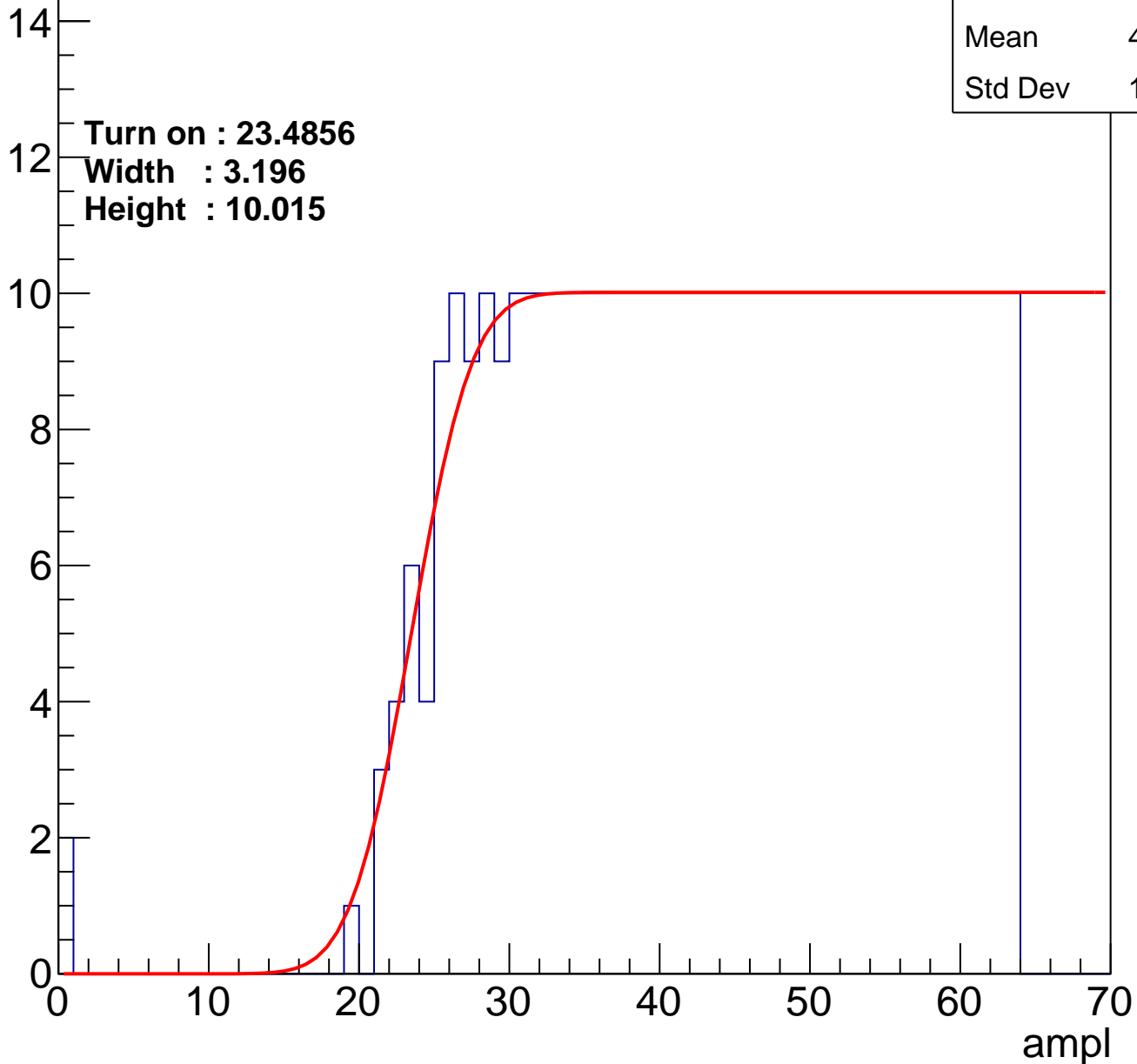
Entries	407
Mean	42.96
Std Dev	12.18

Turn on : 23.4856

Width : 3.196

Height : 10.015

Entry



B1L002S, U23-ch101

calib_packv5_042523_0143.root, FC#10, port B3

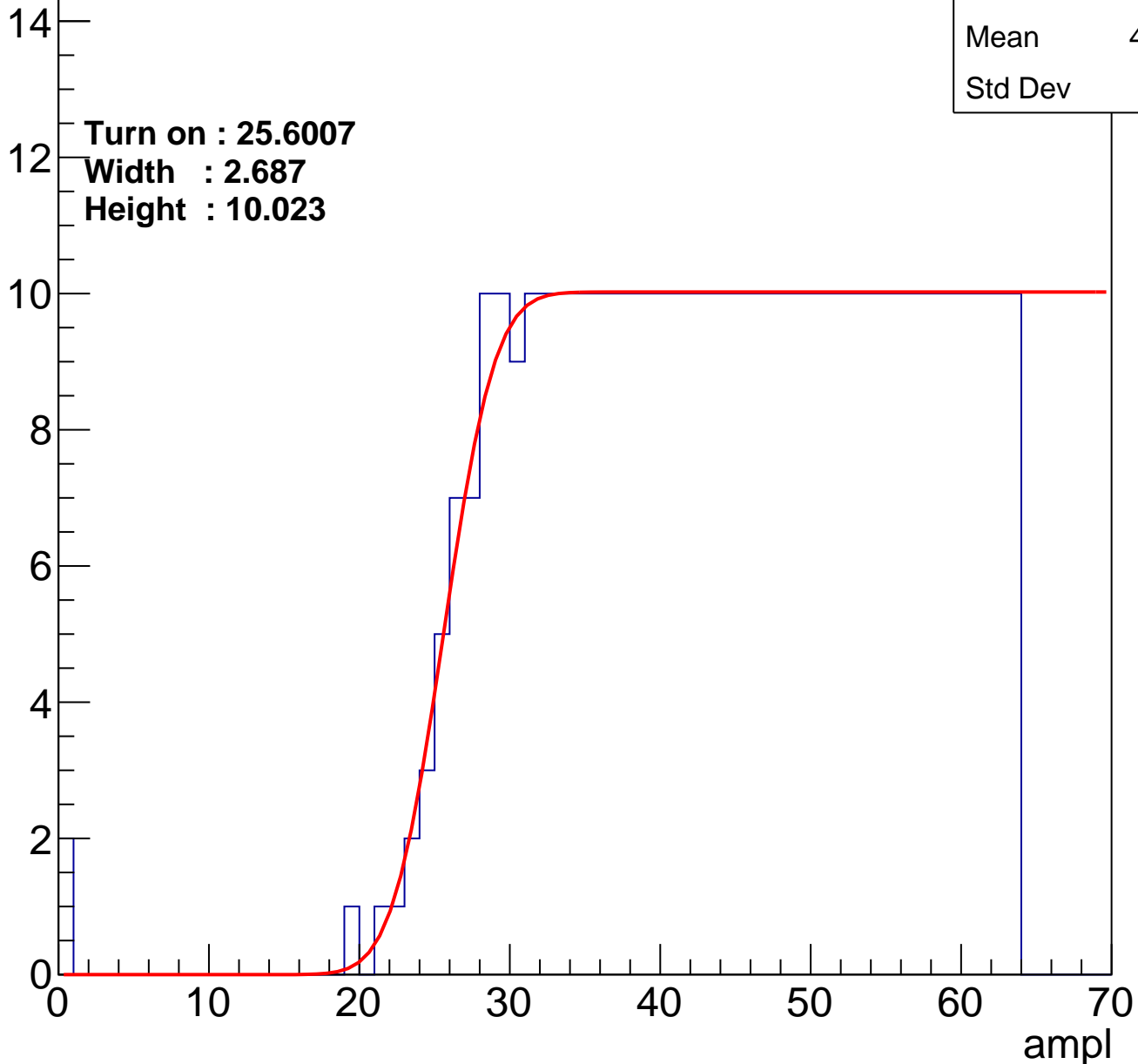
Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.6007

Width : 2.687

Height : 10.023

Entry



B1L002S, U23-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.97
Std Dev	12.27

Turn on : 24.0062

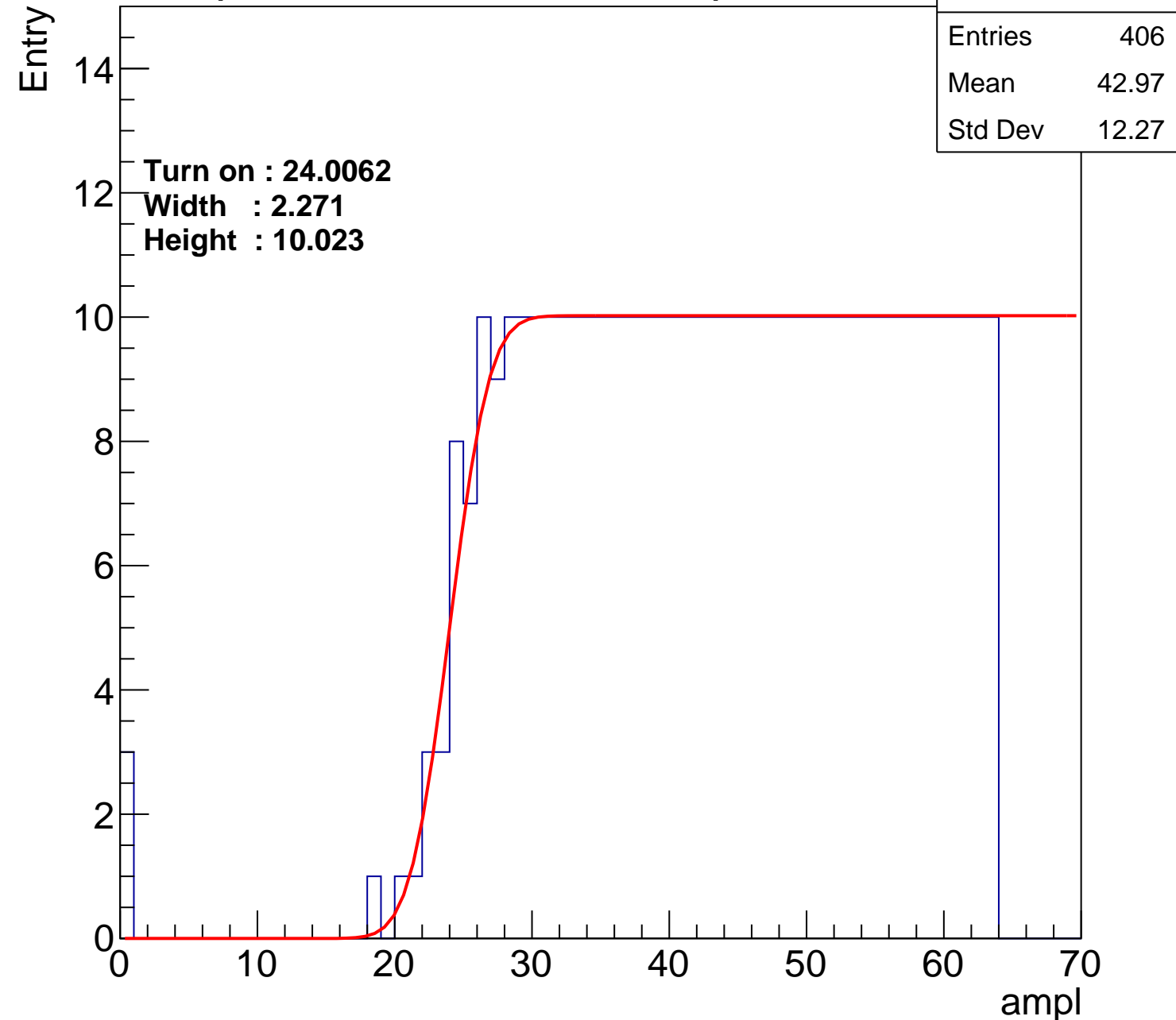
Width : 2.271

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.82
Std Dev	11.05

Turn on : 26.8544

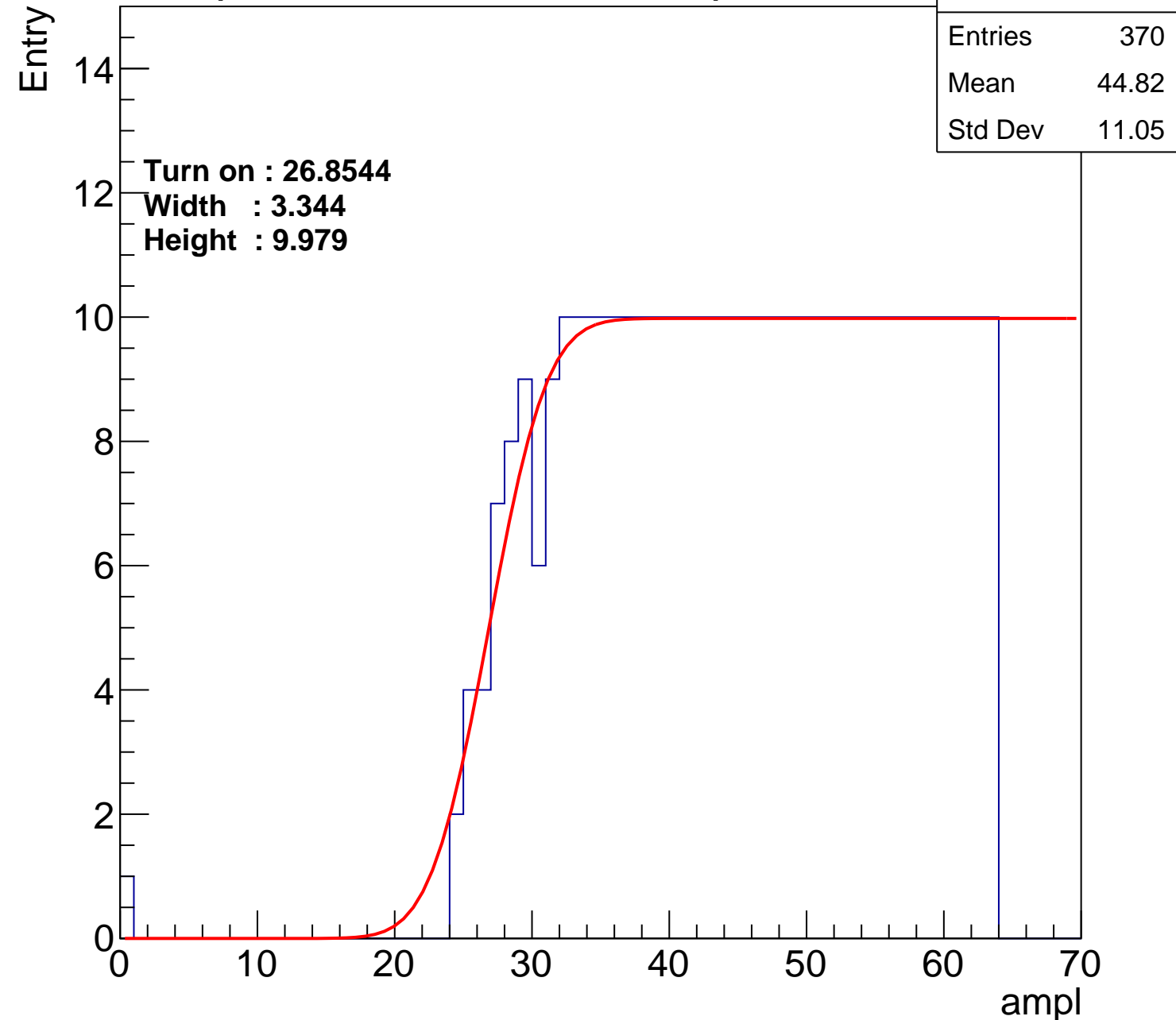
Width : 3.344

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.61
Std Dev	11.33

Turn on : 26.8590

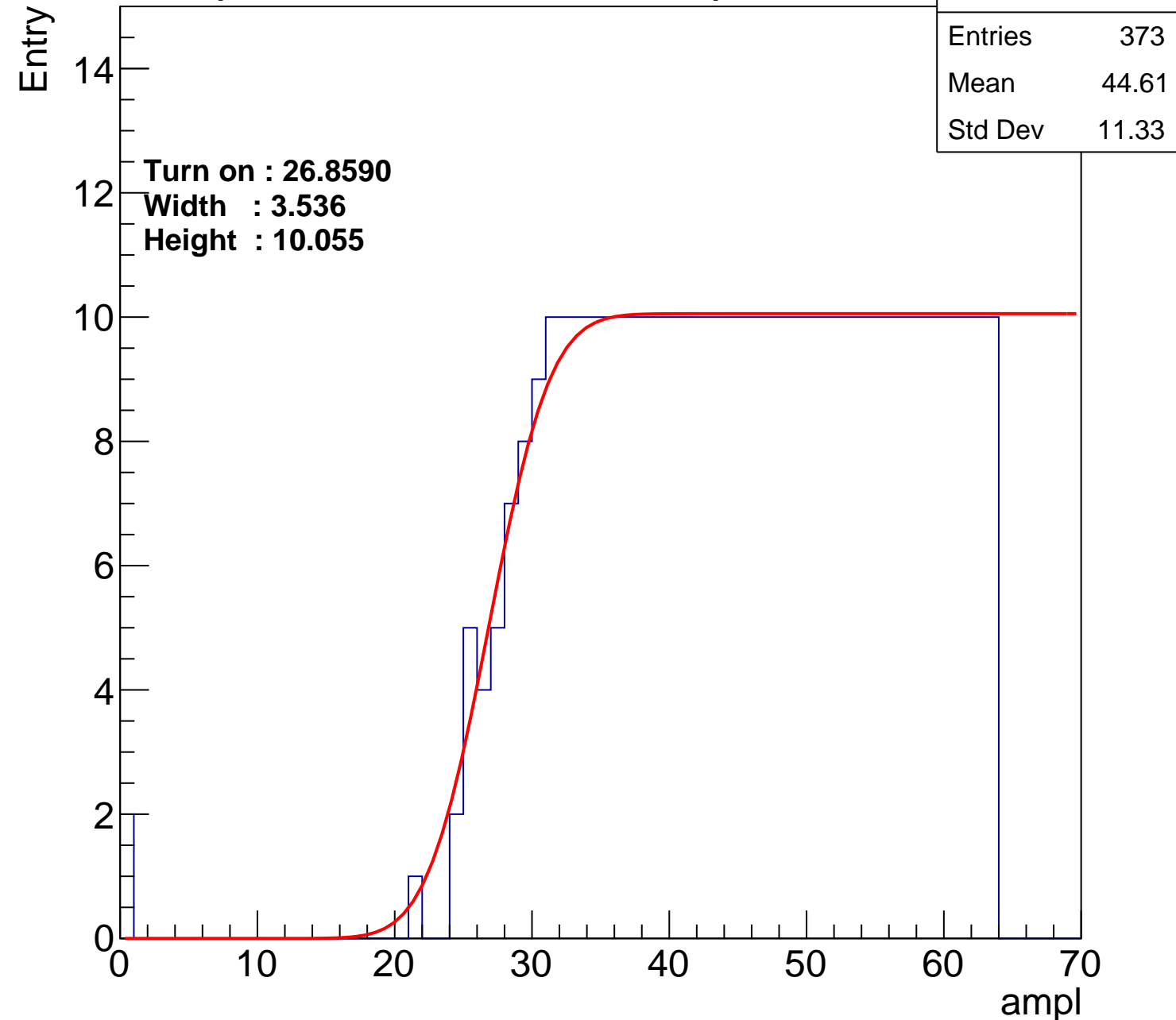
Width : 3.536

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.09
Std Dev	11.76

Turn on : 25.9719

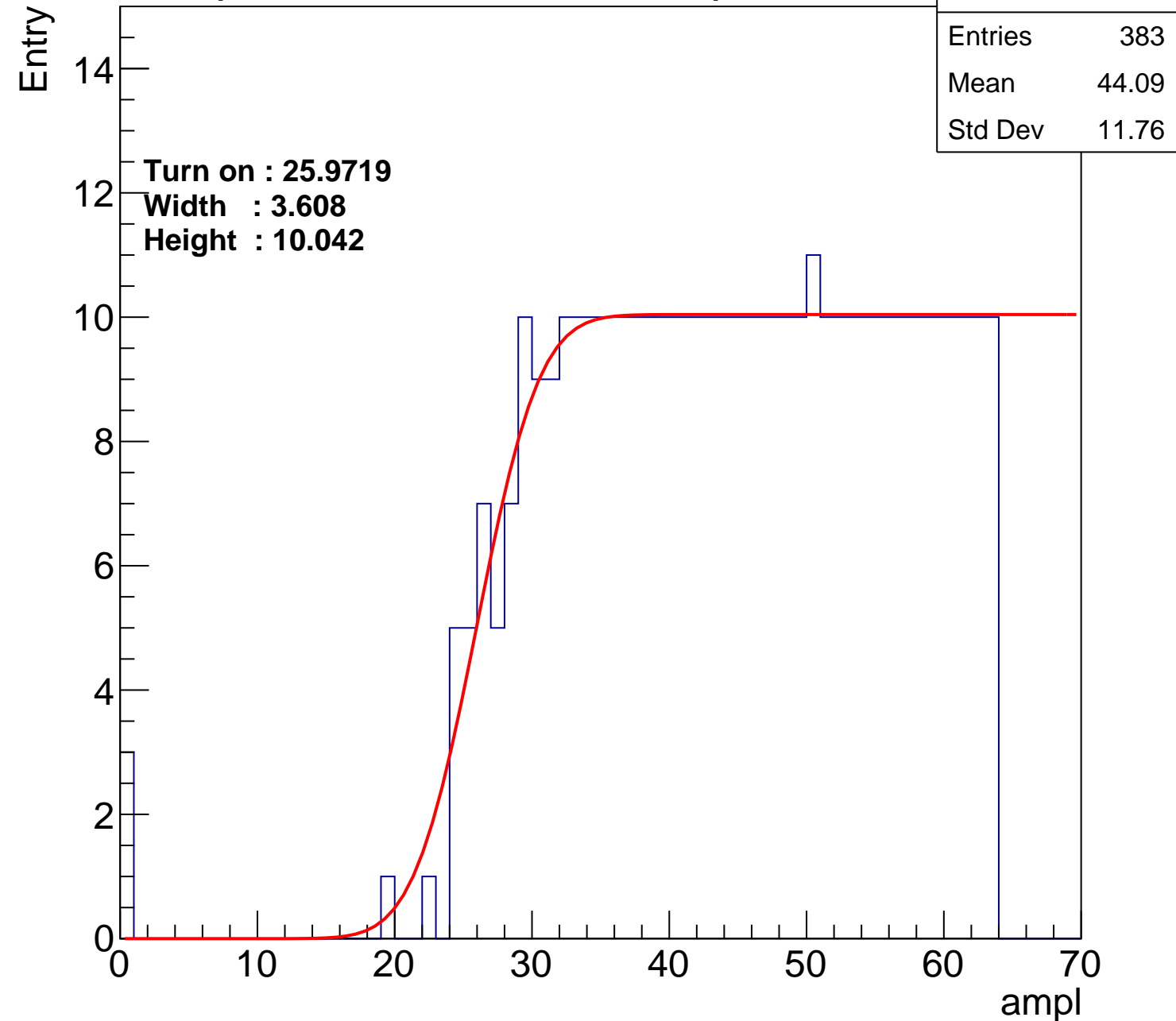
Width : 3.608

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.35
Std Dev	11.85

Turn on : 24.6383

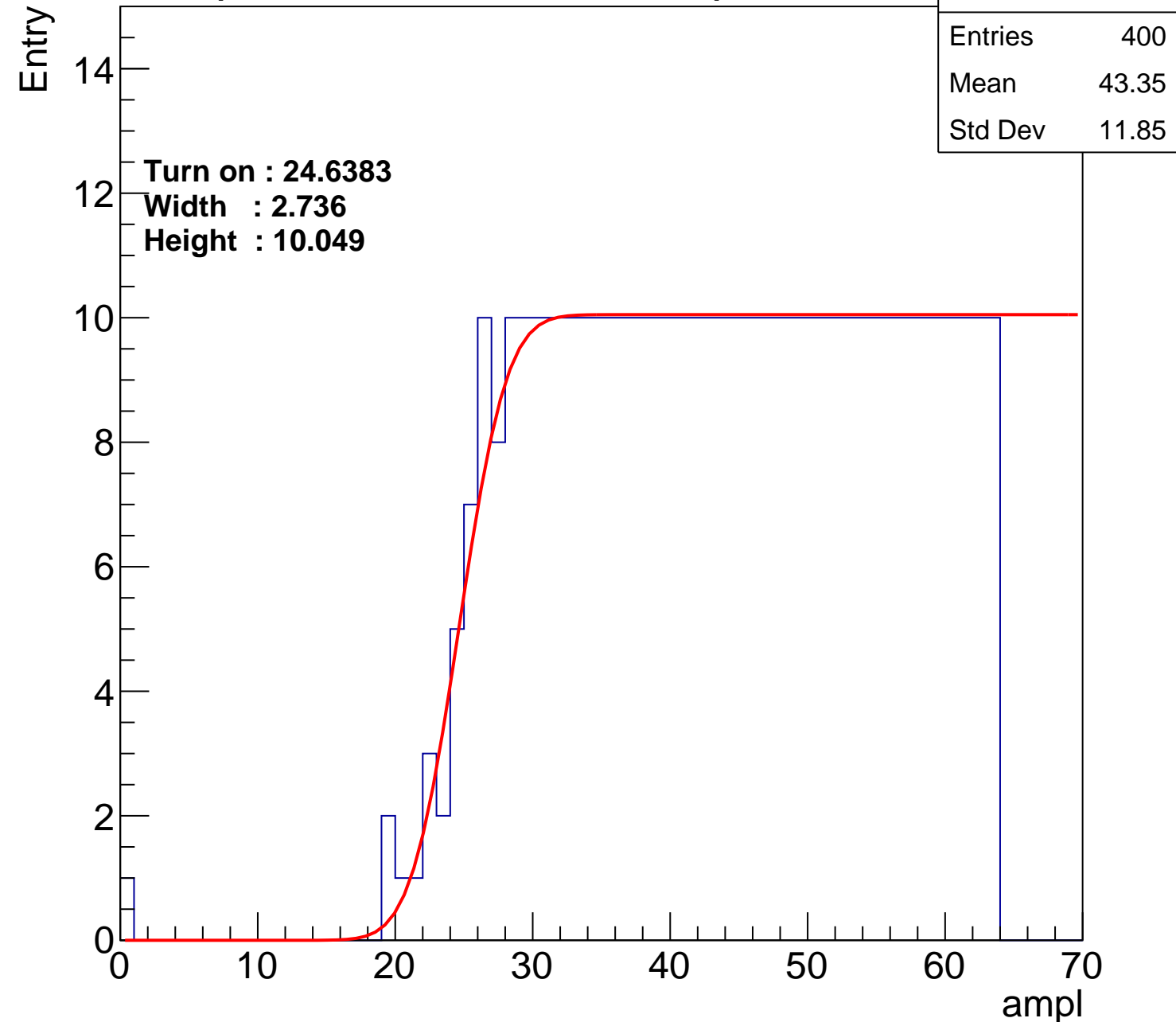
Width : 2.736

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.78

Turn on : 25.6818

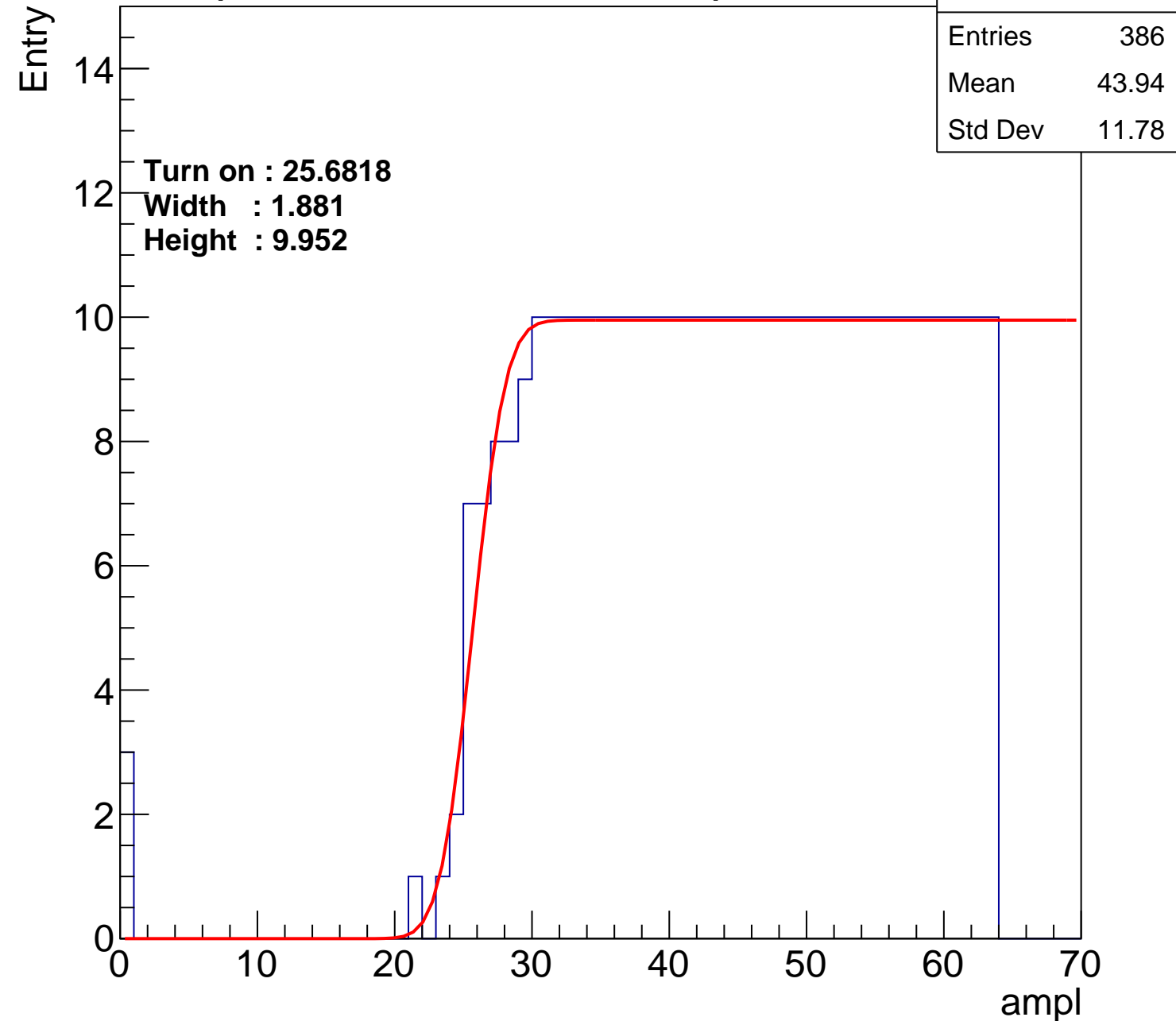
Width : 1.881

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.55
Std Dev	11.98

Turn on : 23.7642

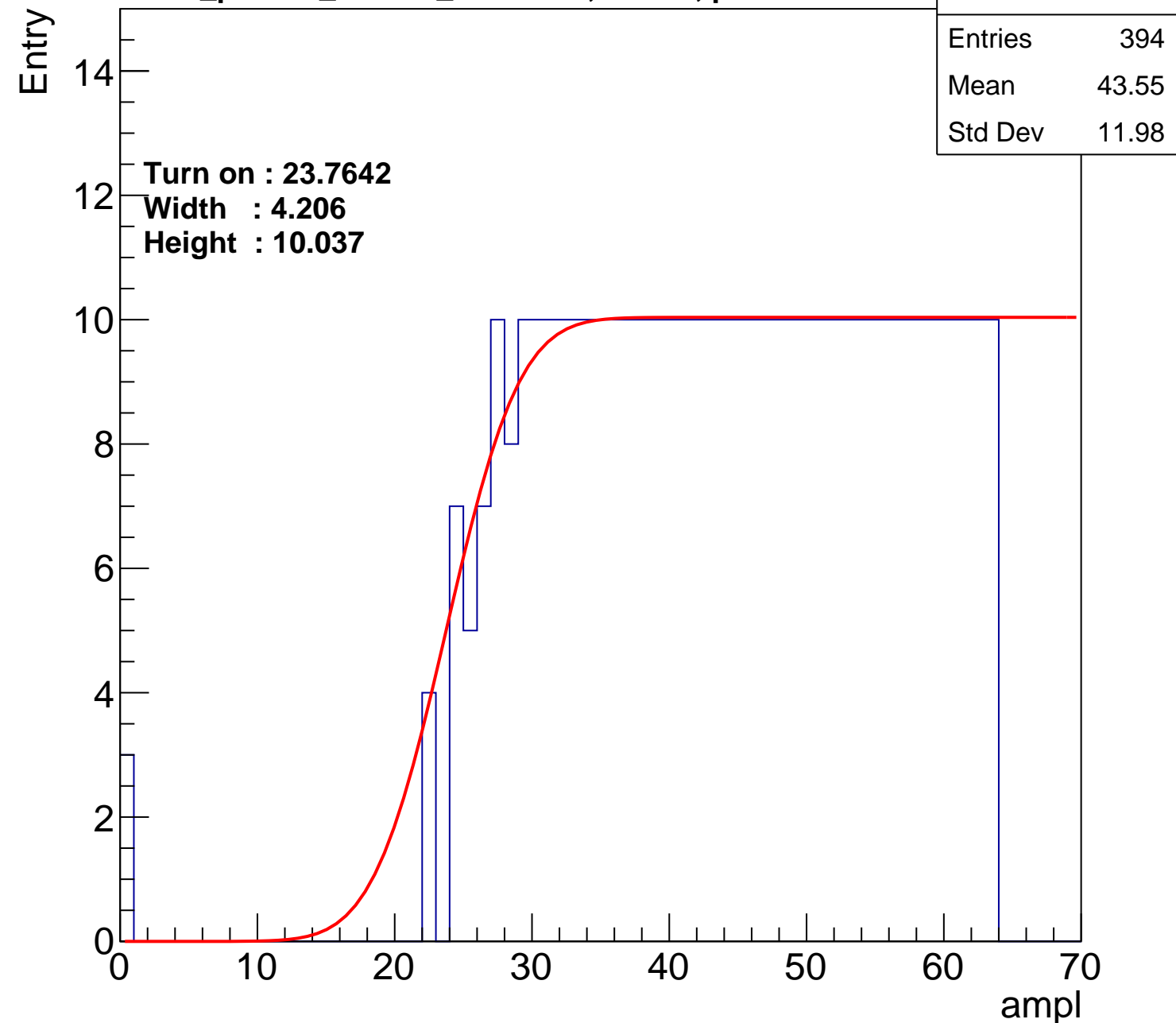
Width : 4.206

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.67

Turn on : 24.8887

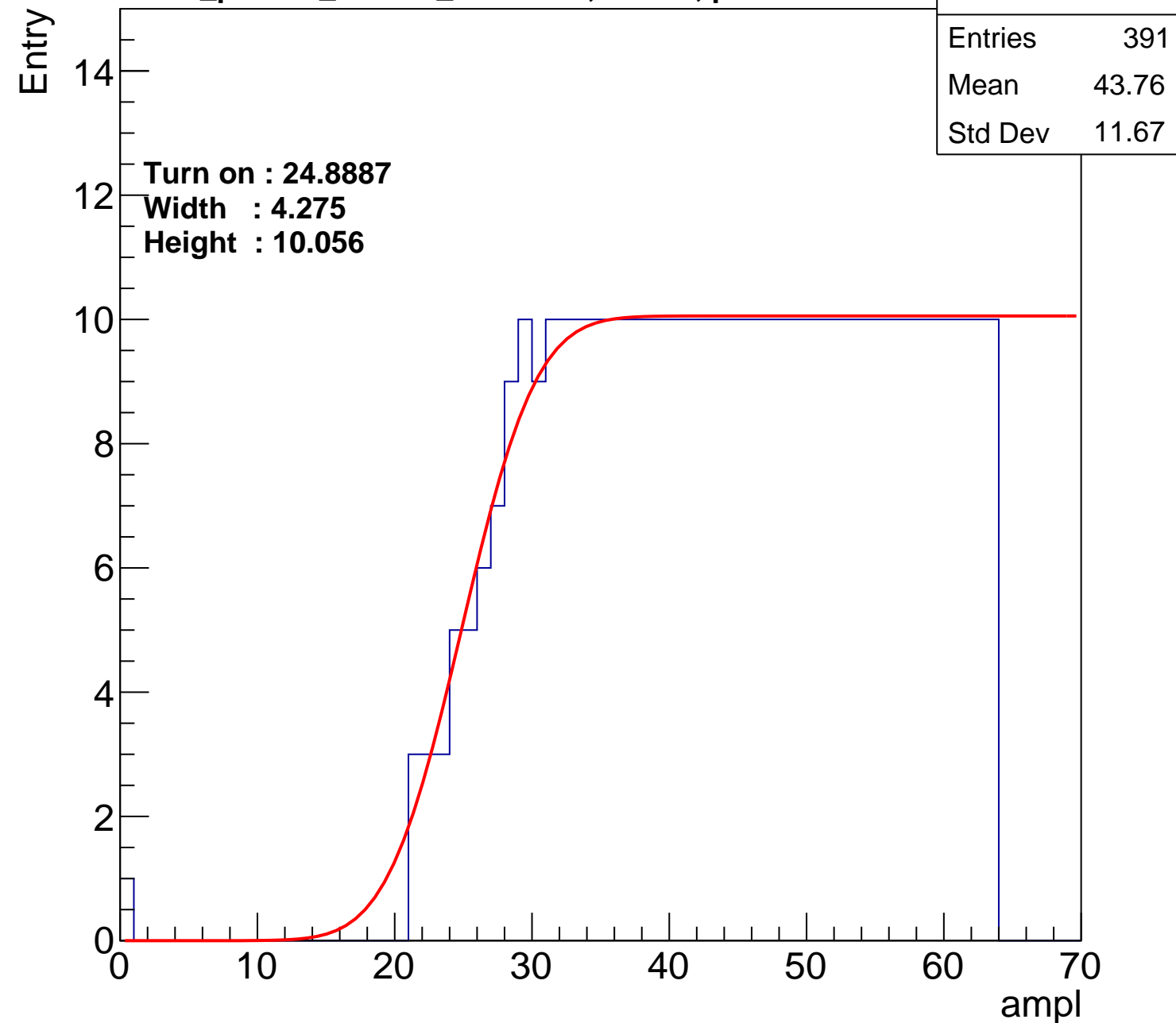
Width : 4.275

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.22
Std Dev	12.02

Turn on : 24.2611

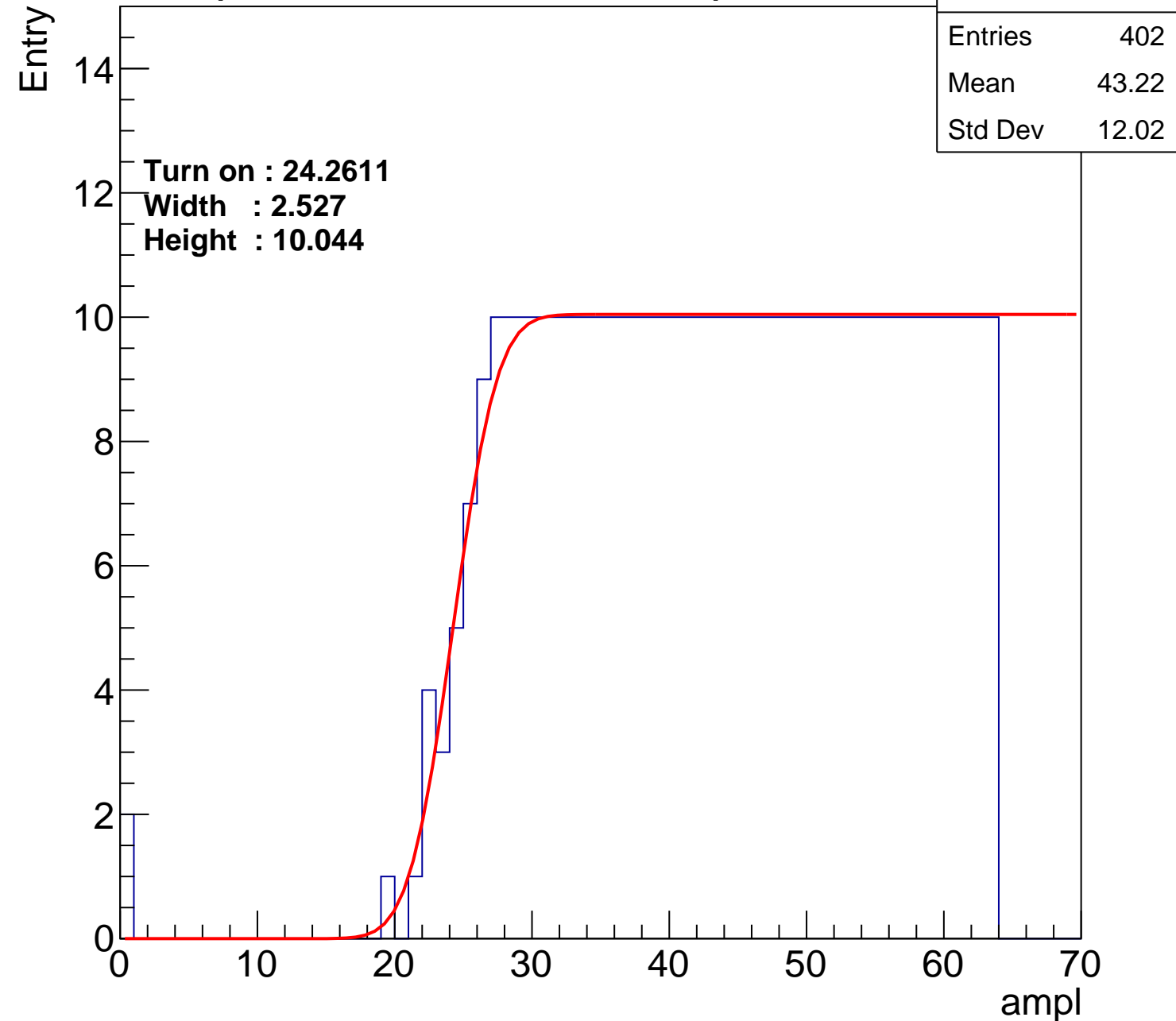
Width : 2.527

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.37
Std Dev	11.76

Turn on : 27.0017

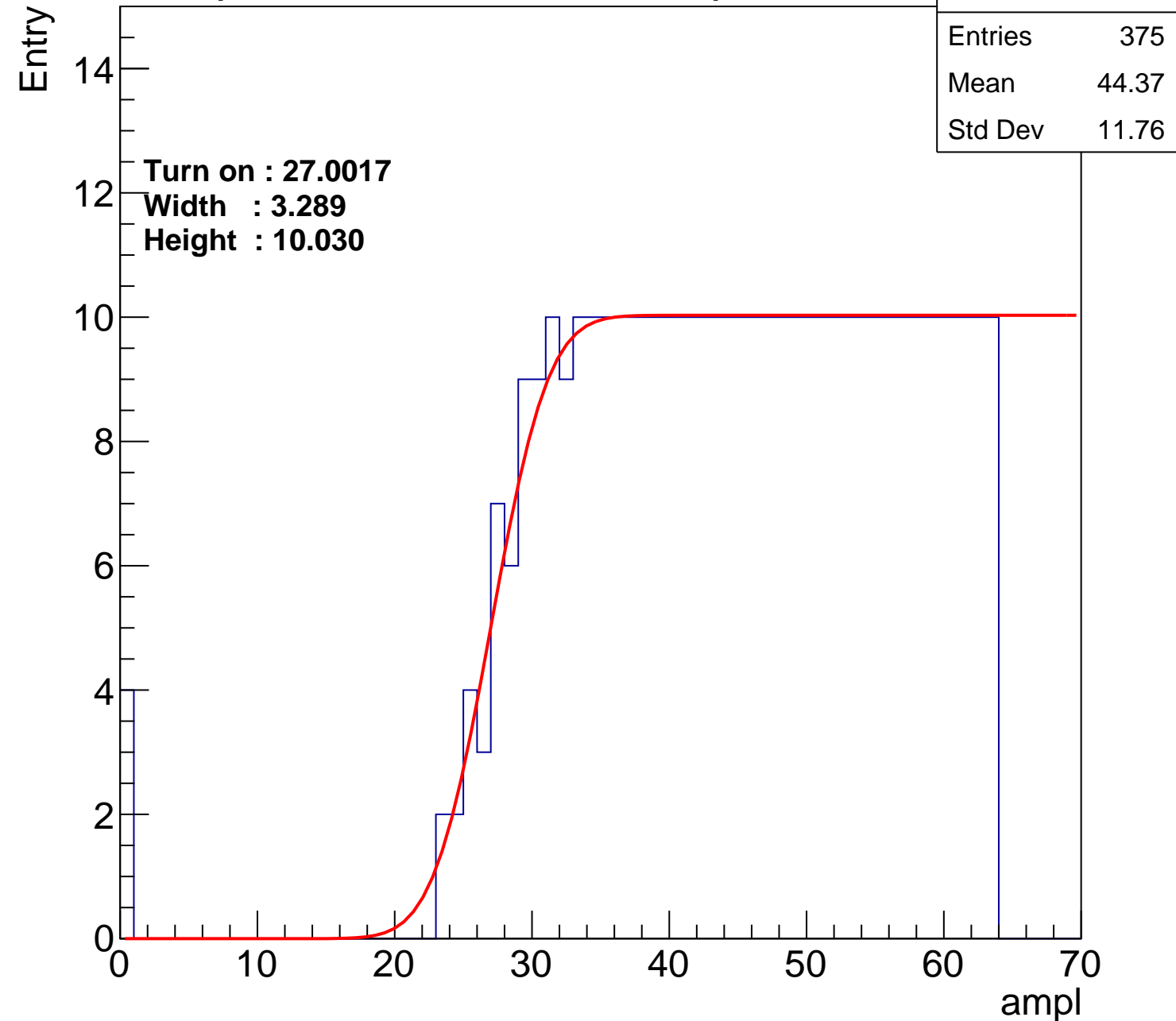
Width : 3.289

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.99
Std Dev	12.24

Turn on : 24.1284

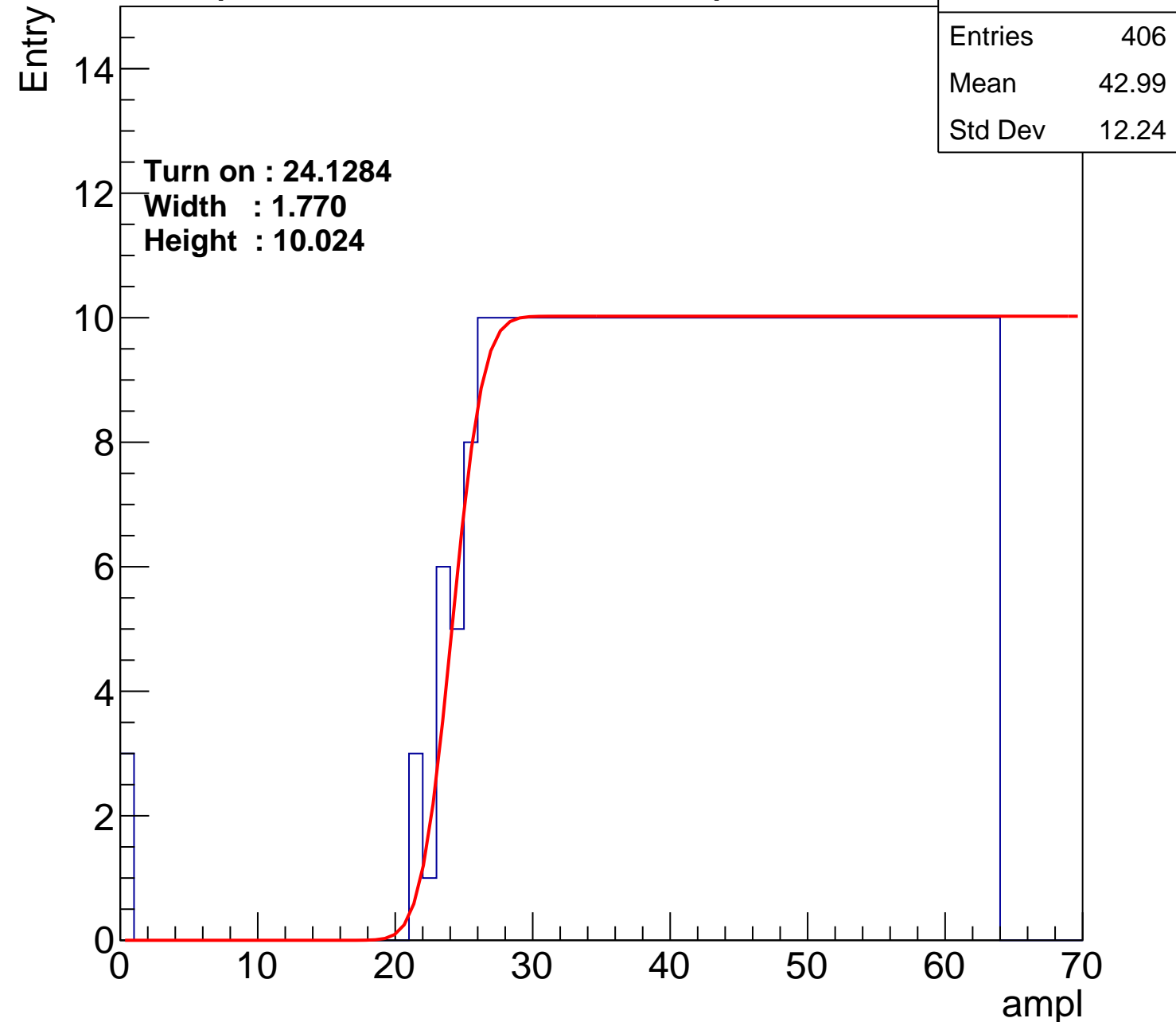
Width : 1.770

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.83
Std Dev	11.85

Turn on : 25.3722

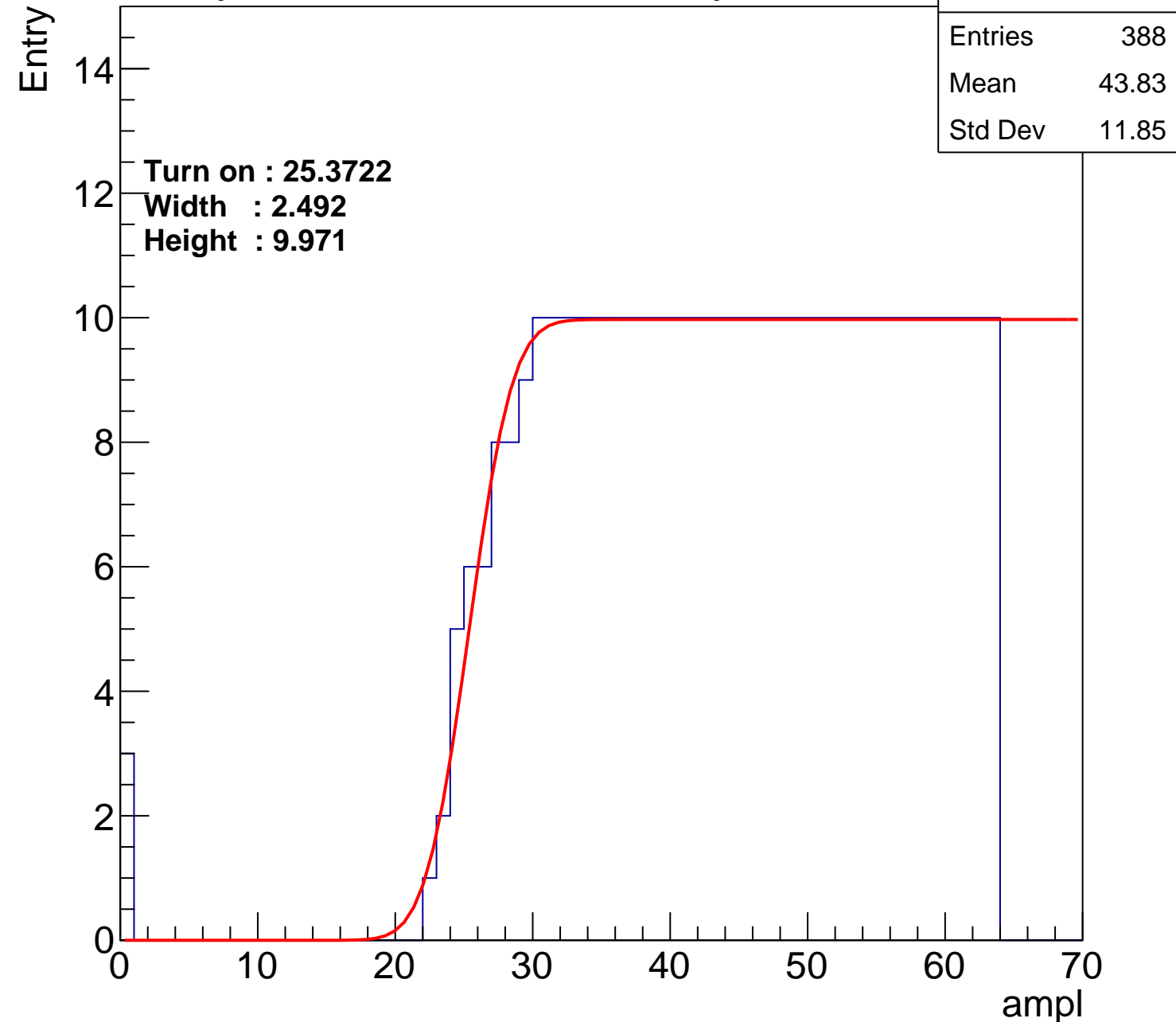
Width : 2.492

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.85

Turn on : 25.6557

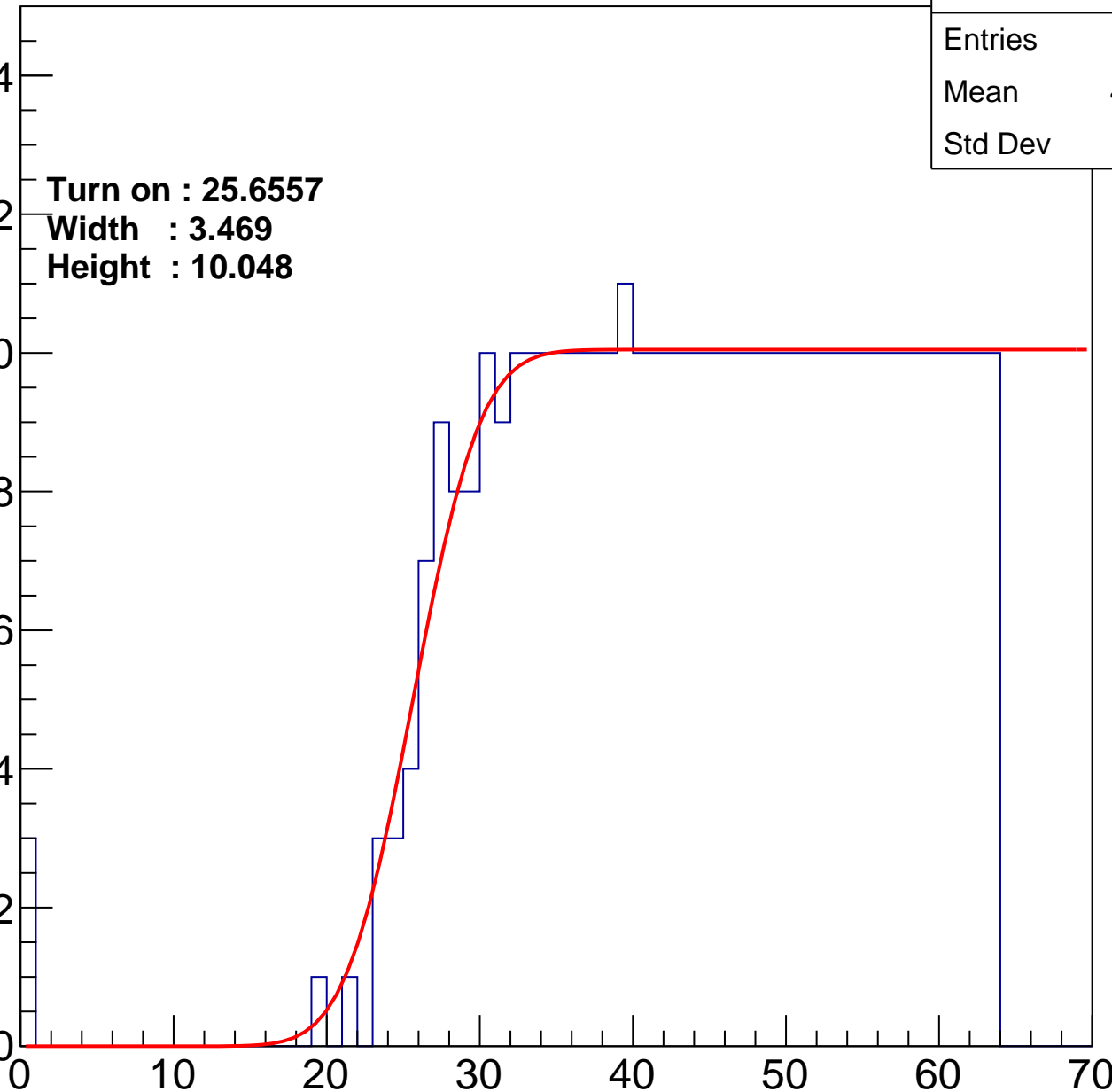
Width : 3.469

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.34
Std Dev	11.98

Turn on : 24.5190

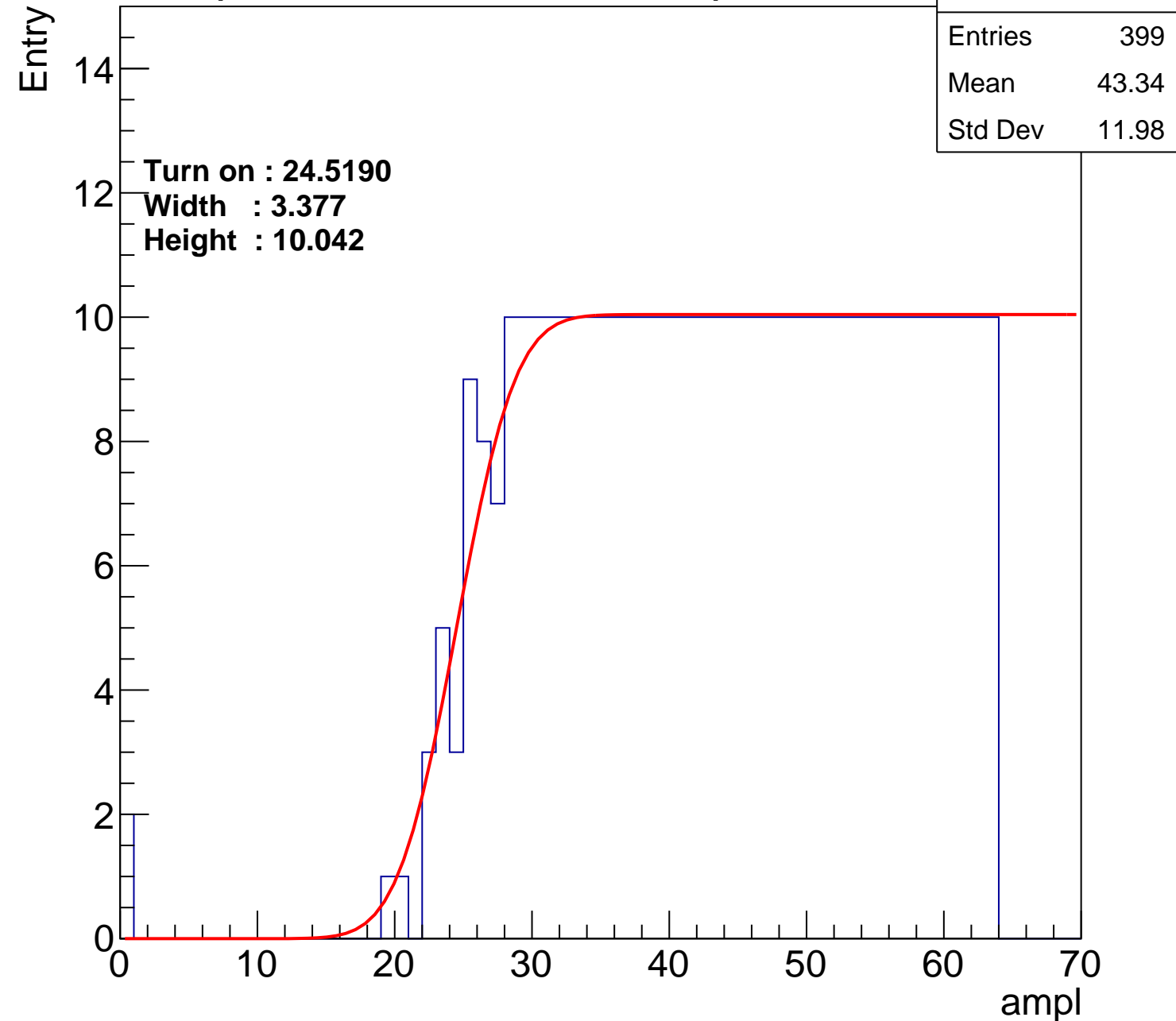
Width : 3.377

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.8290

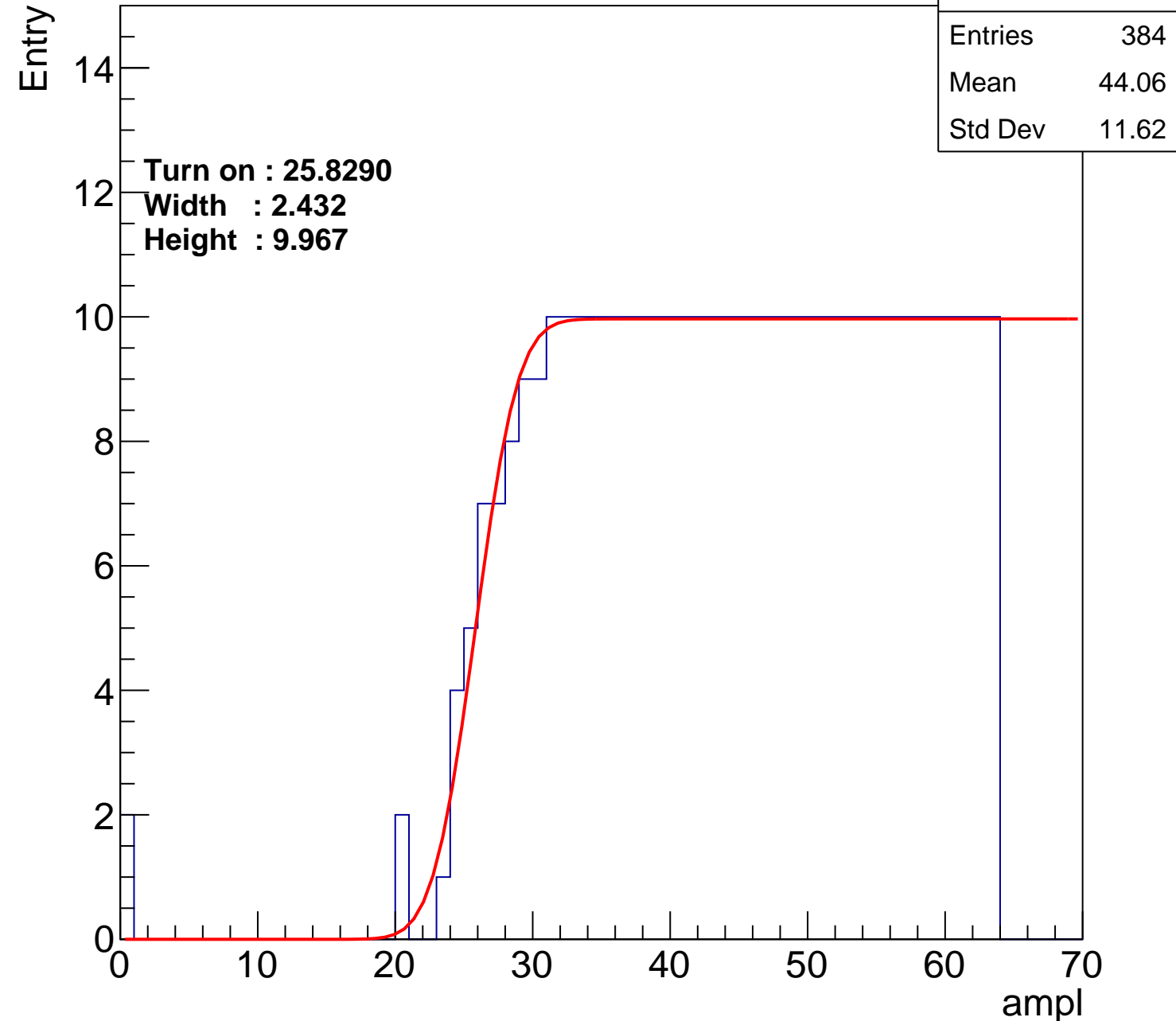
Width : 2.432

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.66
Std Dev	12.25

Turn on : 26.4156

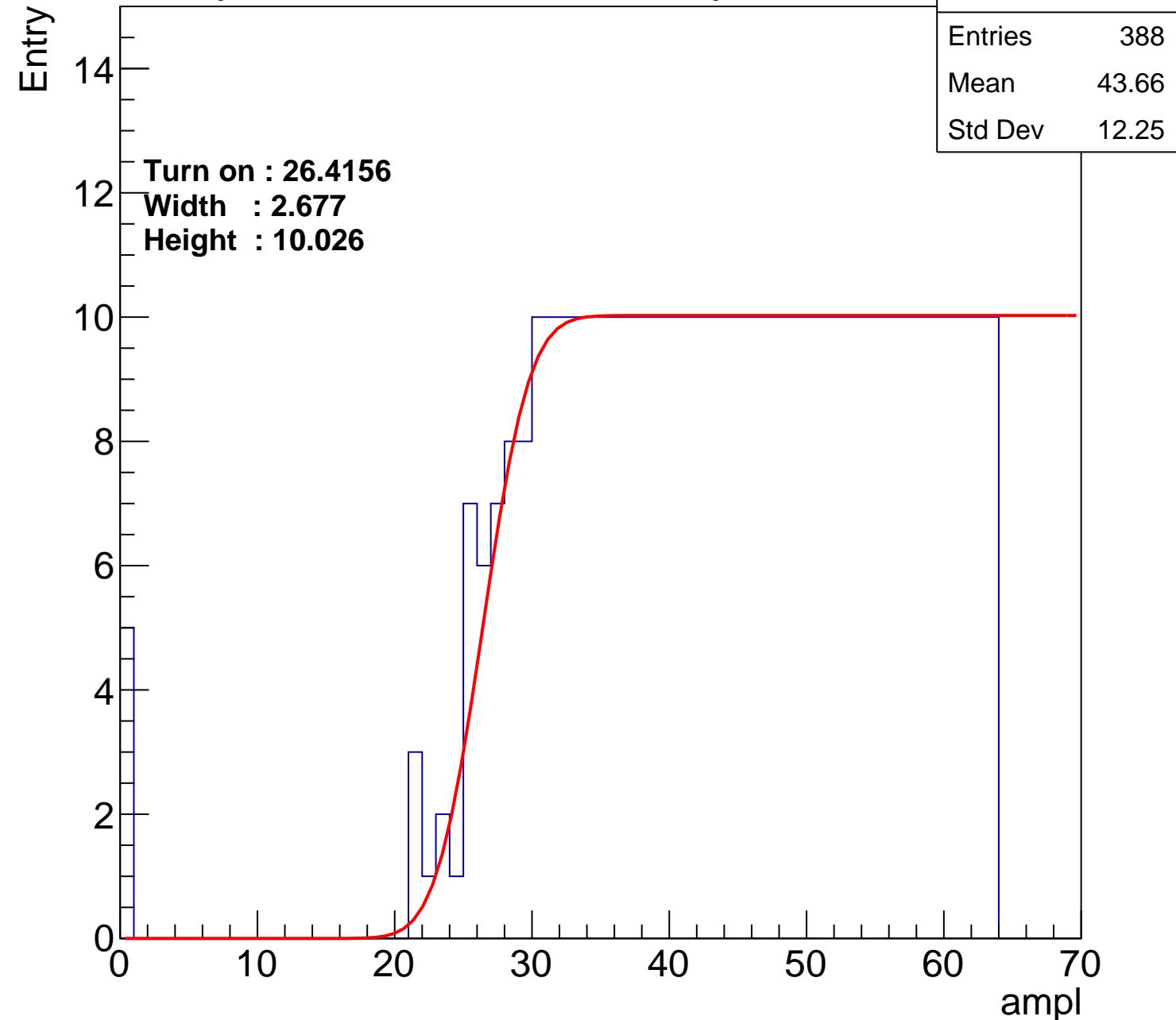
Width : 2.677

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.31
Std Dev	12.03

Turn on : 24.6463

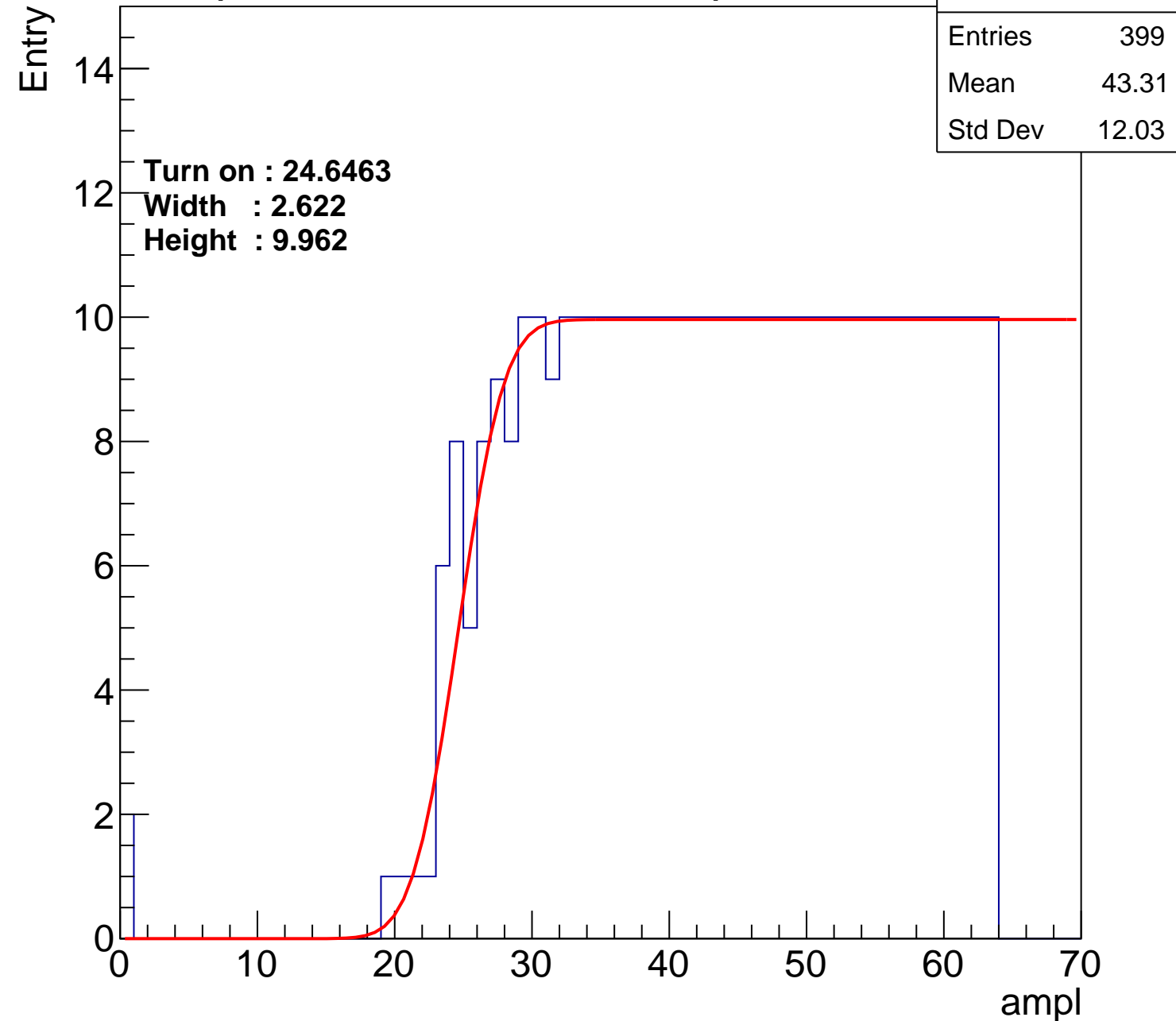
Width : 2.622

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.2033

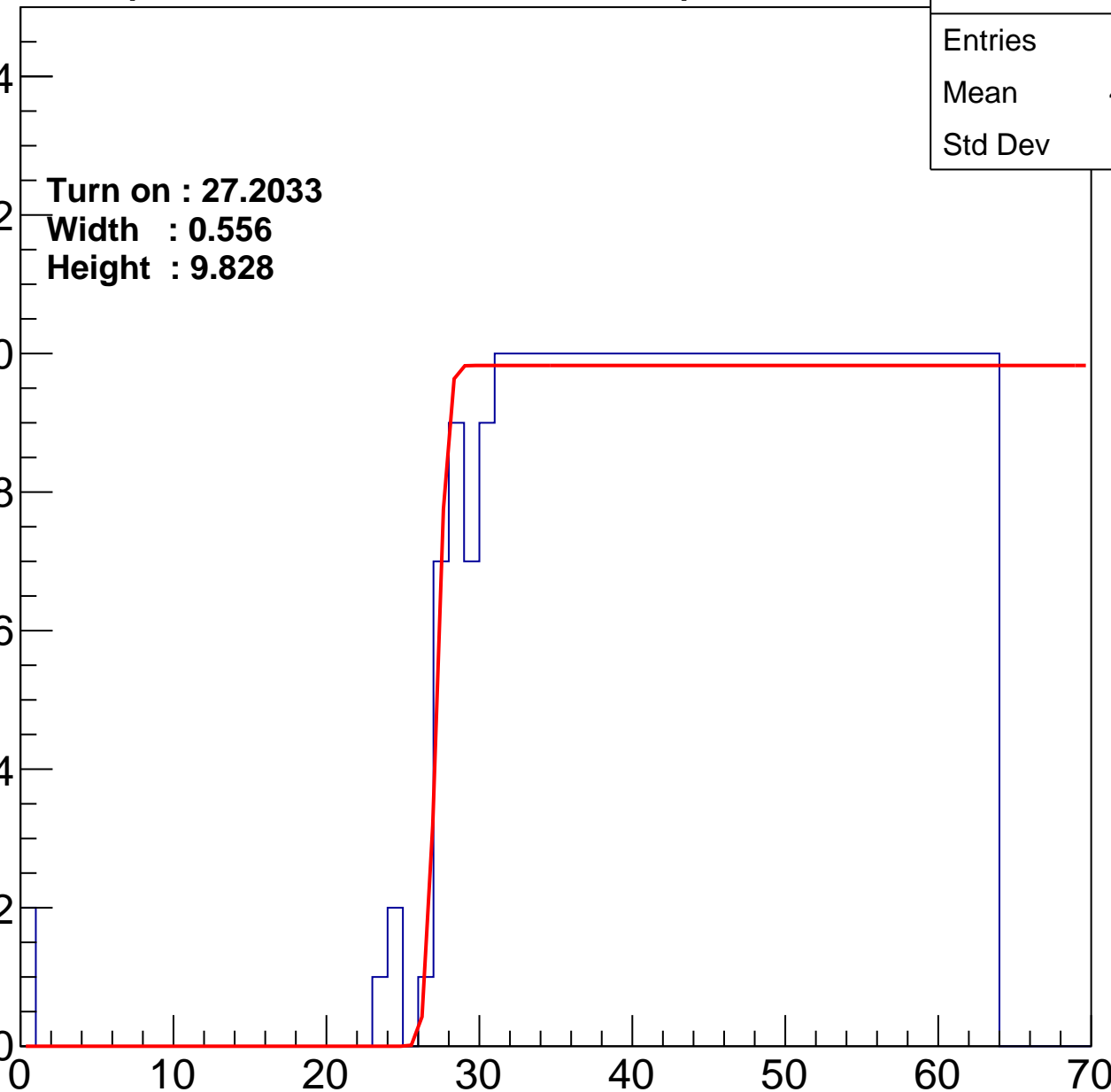
Width : 0.556

Height : 9.828

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.5202

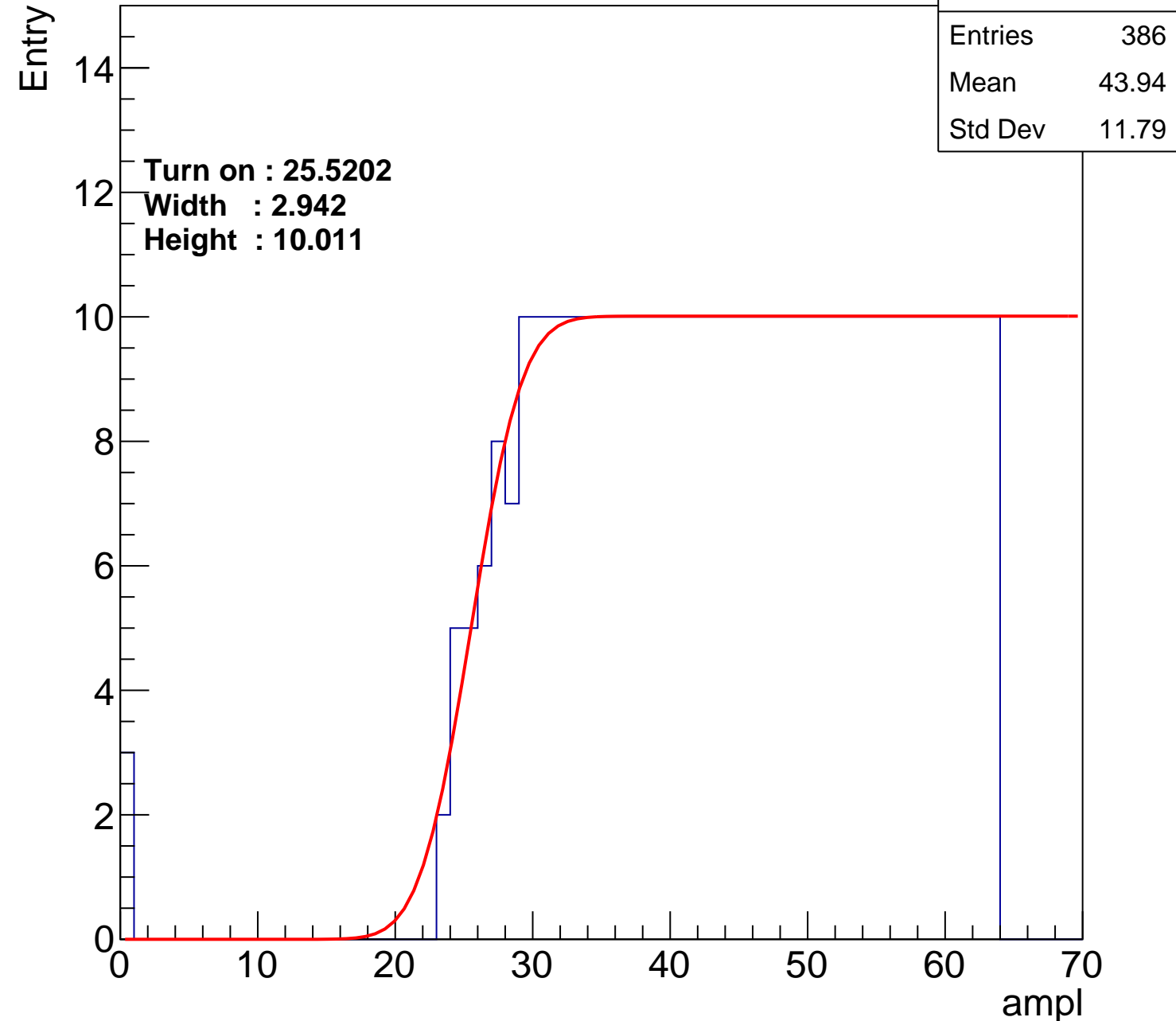
Width : 2.942

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch121

calib_packv5_042523_0143.root, FC#10, port B3

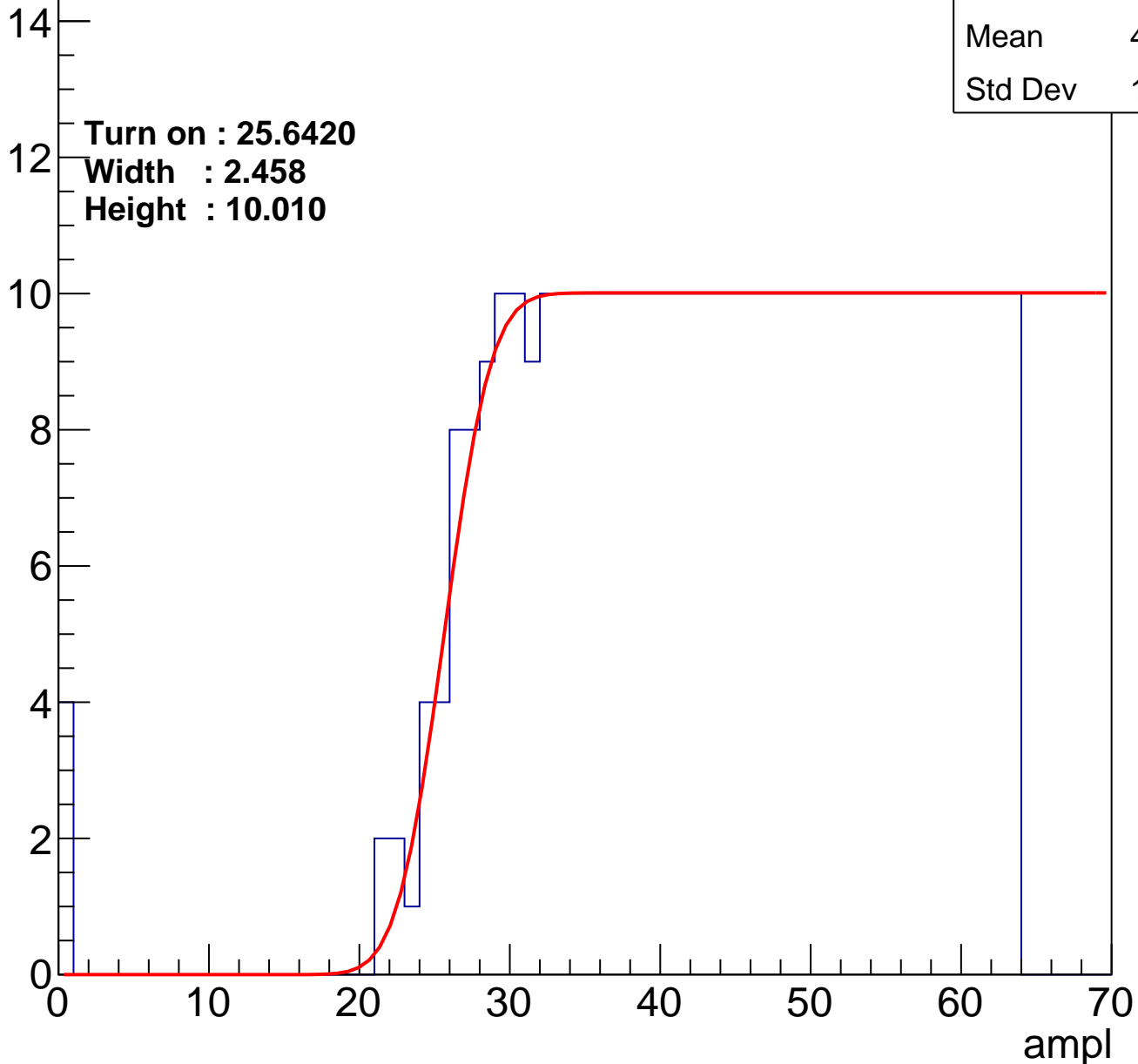
Entries	391
Mean	43.61
Std Dev	12.11

Turn on : 25.6420

Width : 2.458

Height : 10.010

Entry



B1L002S, U23-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.33
Std Dev	11.31

Turn on : 25.9645

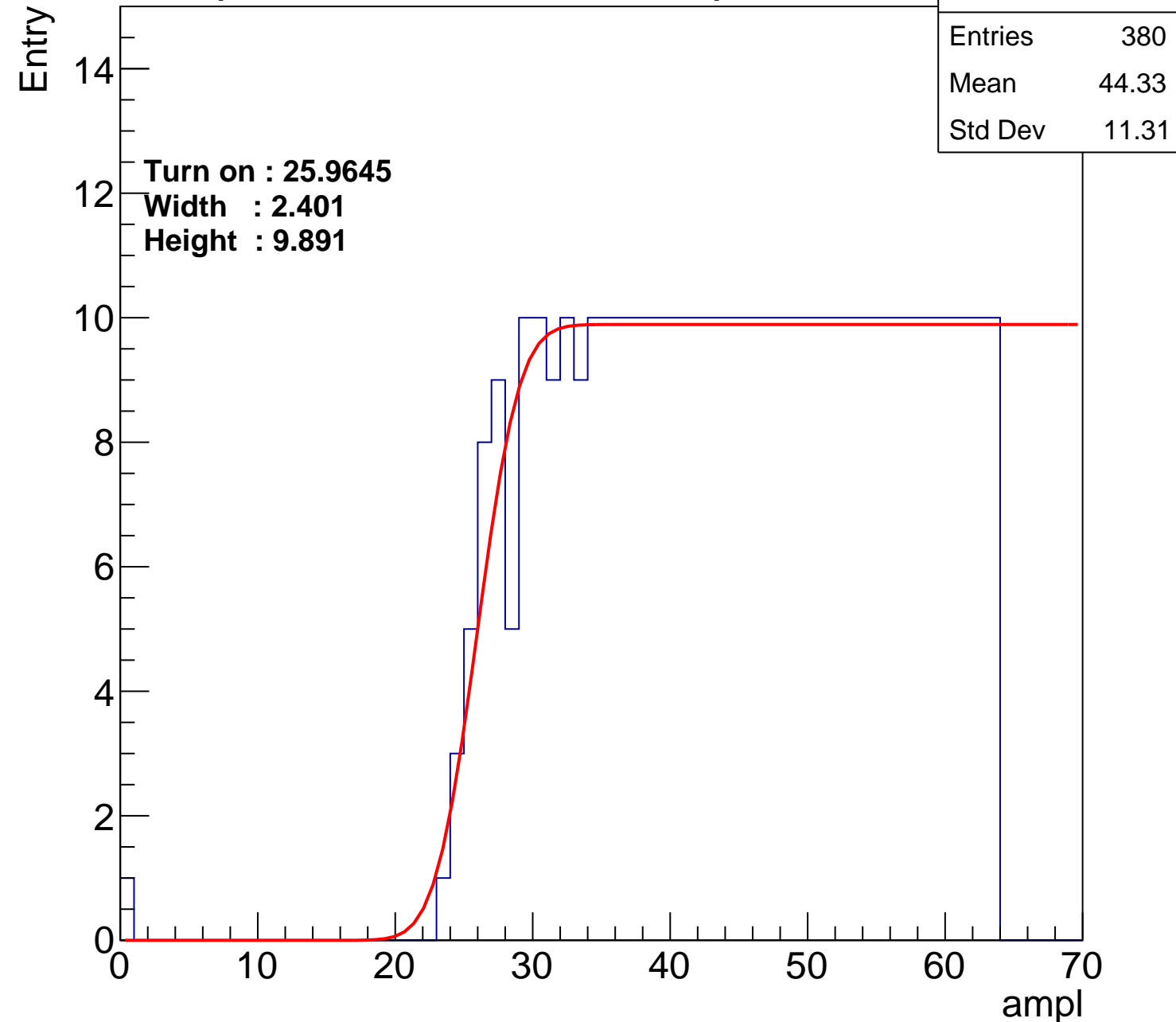
Width : 2.401

Height : 9.891

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.01
Std Dev	11.49

Turn on : 25.8053

Width : 1.959

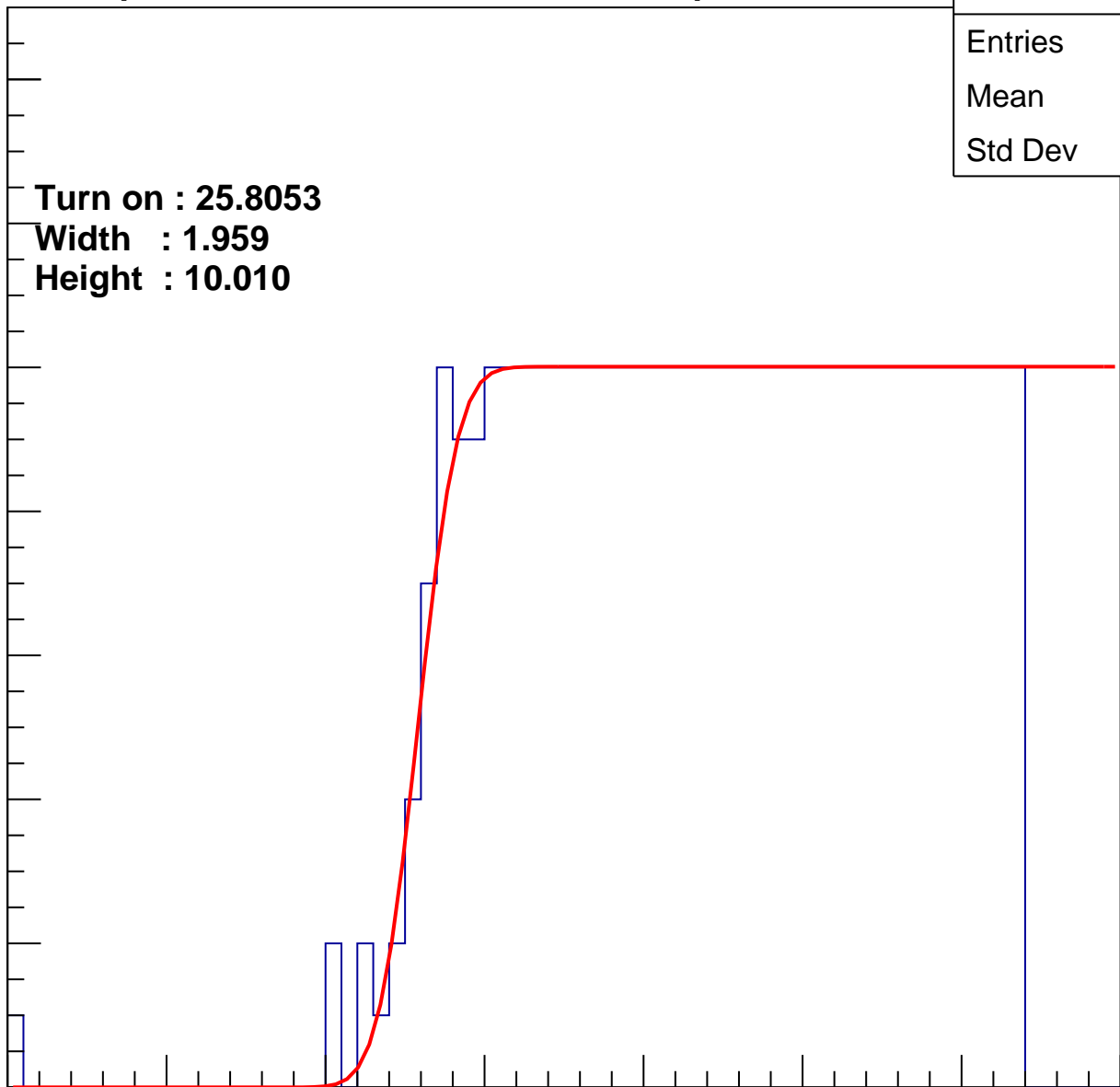
Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U23-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.48
Std Dev	12.1

Turn on : 26.1905

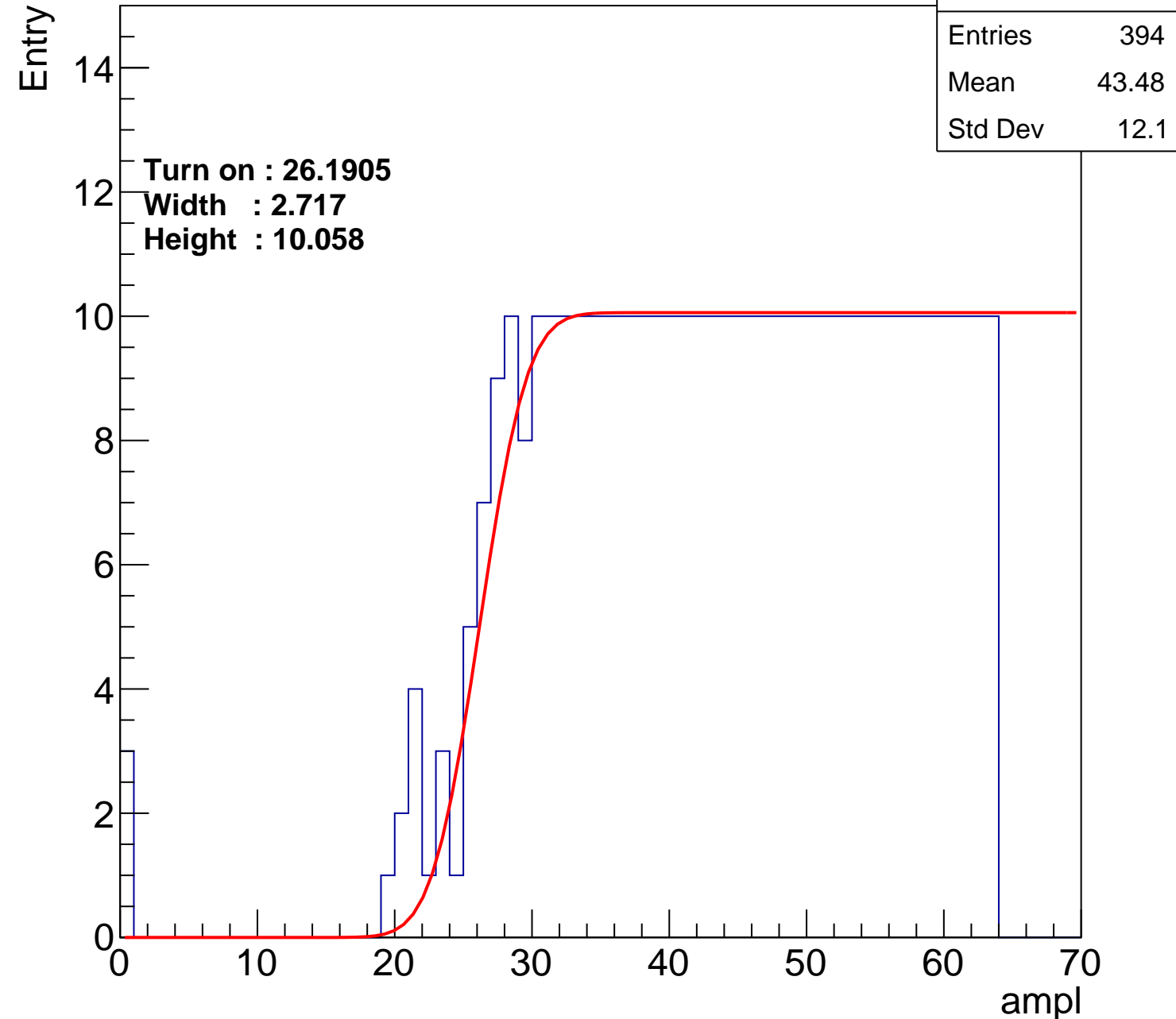
Width : 2.717

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
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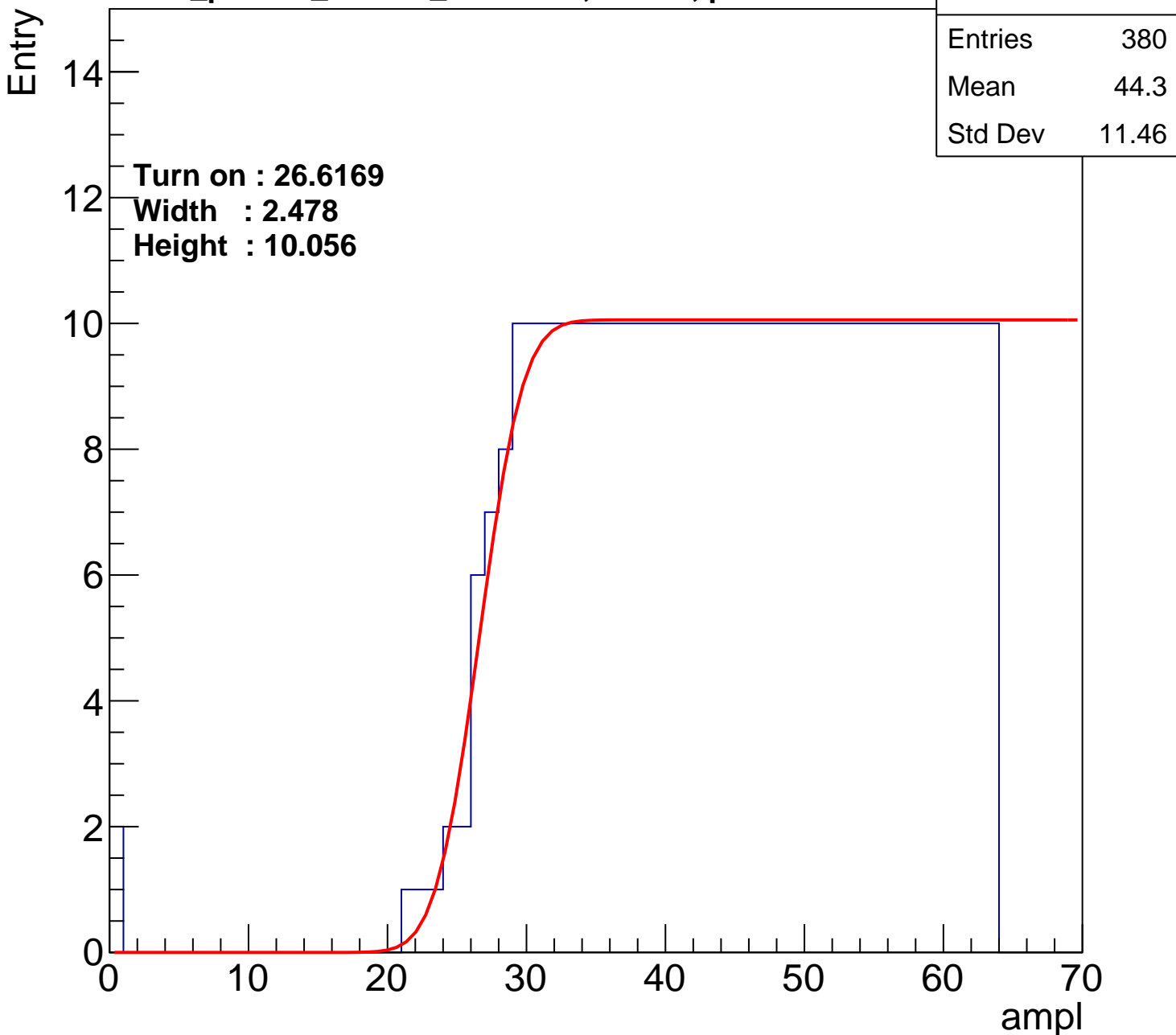
Mean	44.3
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Std Dev	11.46
---------	-------

Turn on : 26.6169

Width : 2.478

Height : 10.056



B1L002S, U23-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 25.8521

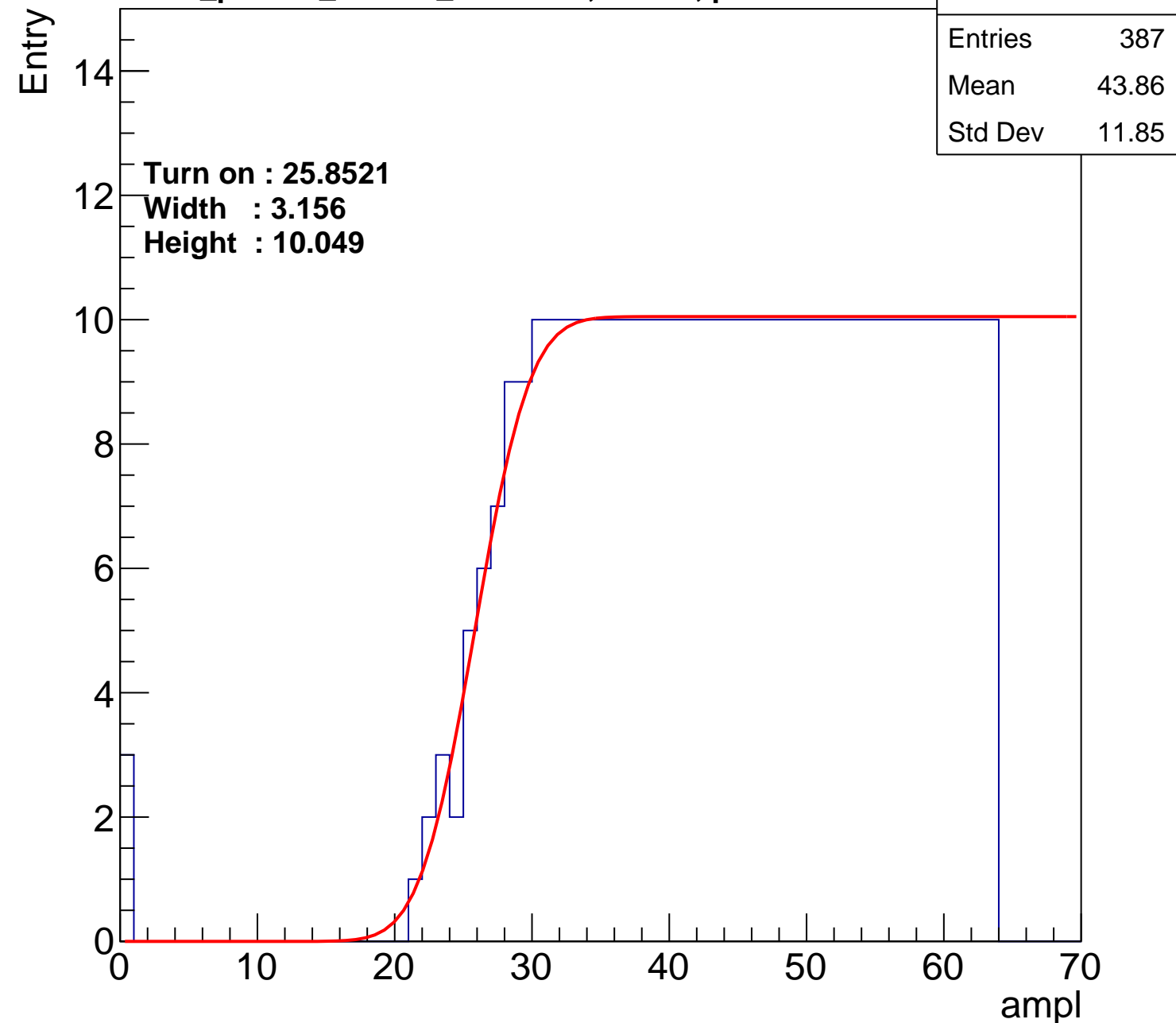
Width : 3.156

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U23-ch127

calib_packv5_042523_0143.root, FC#10, port B3

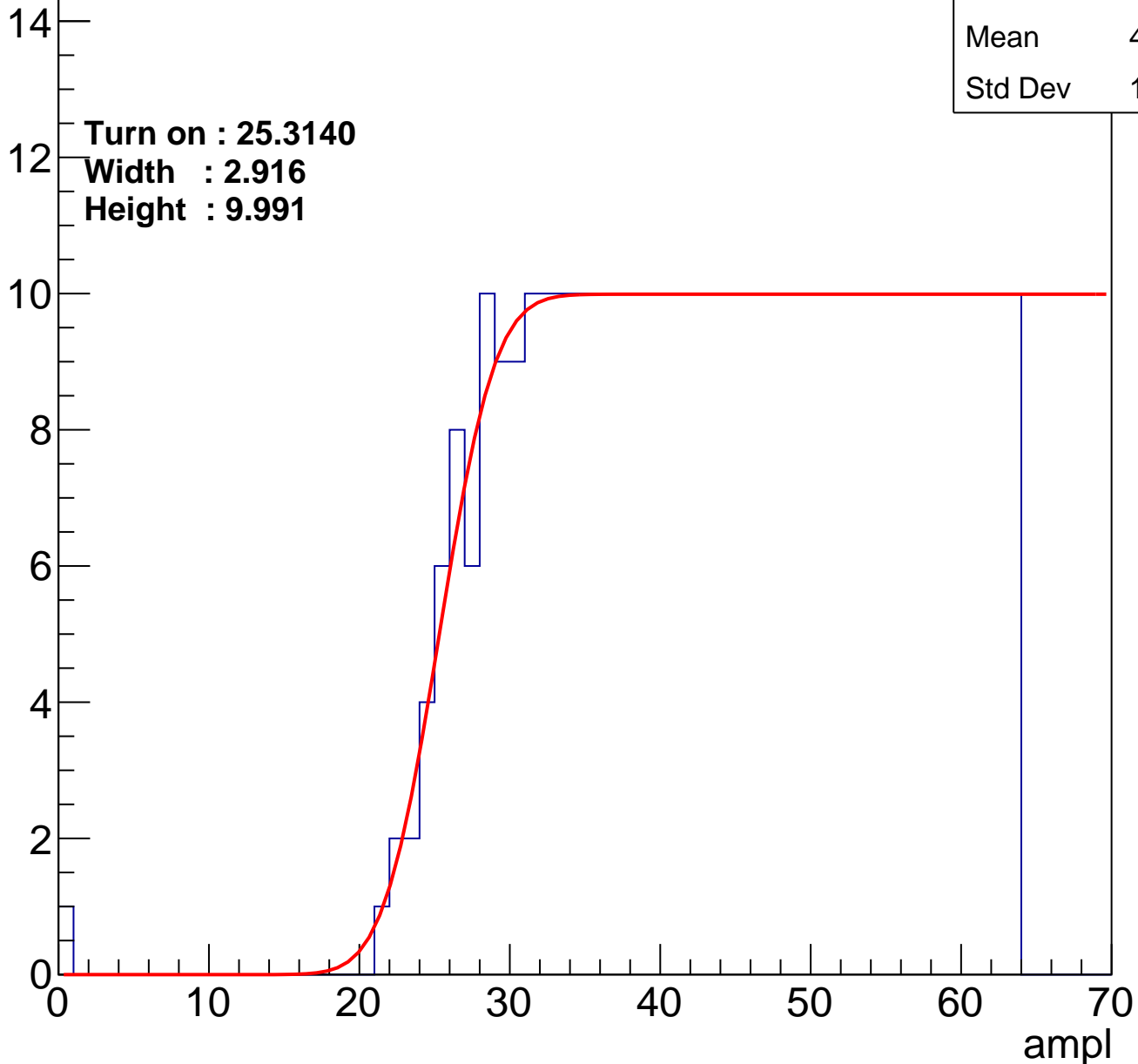
Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.3140

Width : 2.916

Height : 9.991

Entry



B1L002S, U23-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.3140

Width : 2.916

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl

