



# B1L000S, U25-ch0, adc0

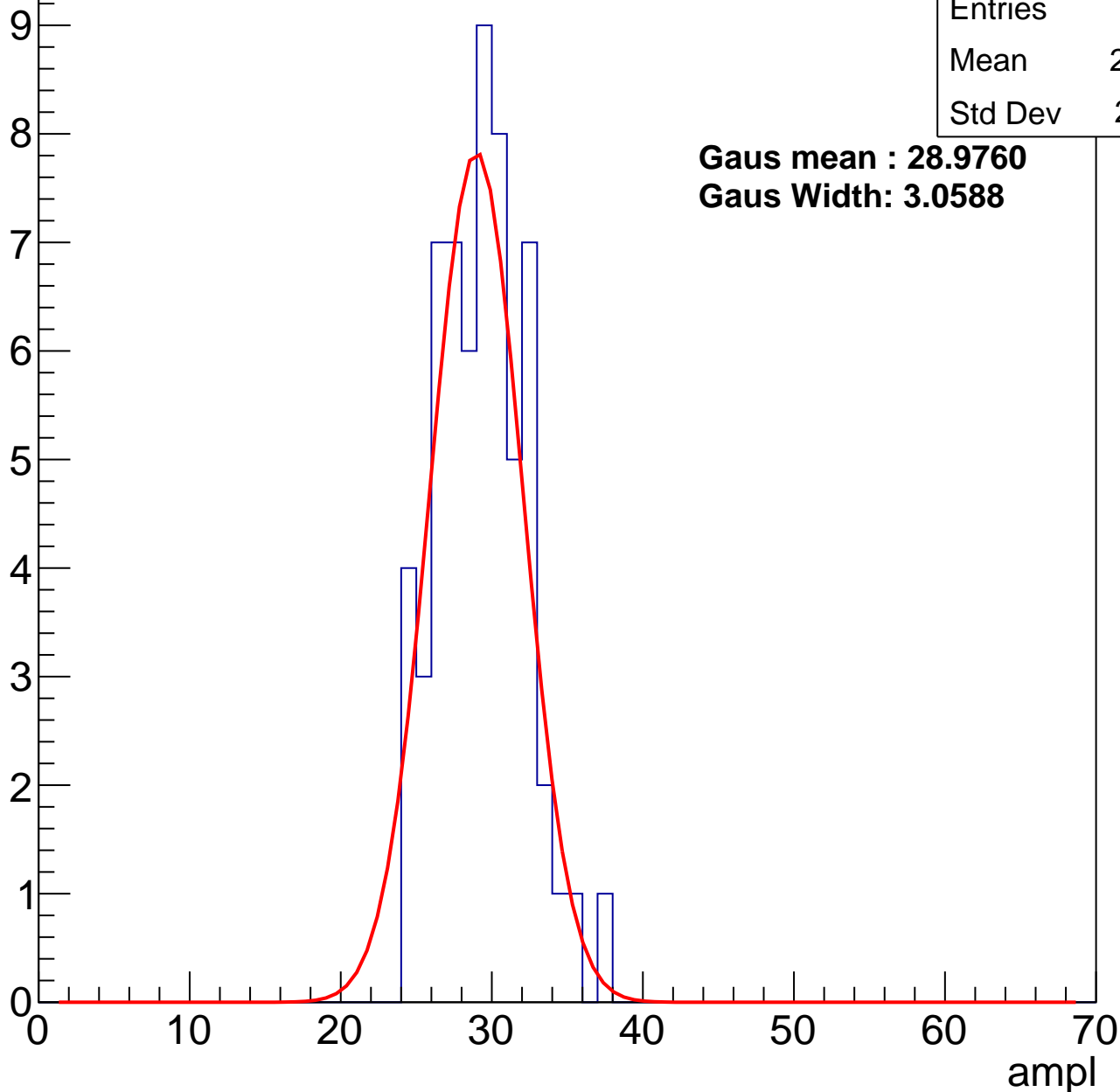
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	28.89
Std Dev	2.841

**Gaus mean : 28.9760**

**Gaus Width: 3.0588**



# B1L000S, U25-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	35.88
Std Dev	3.333

**Gaus mean : 36.5037**

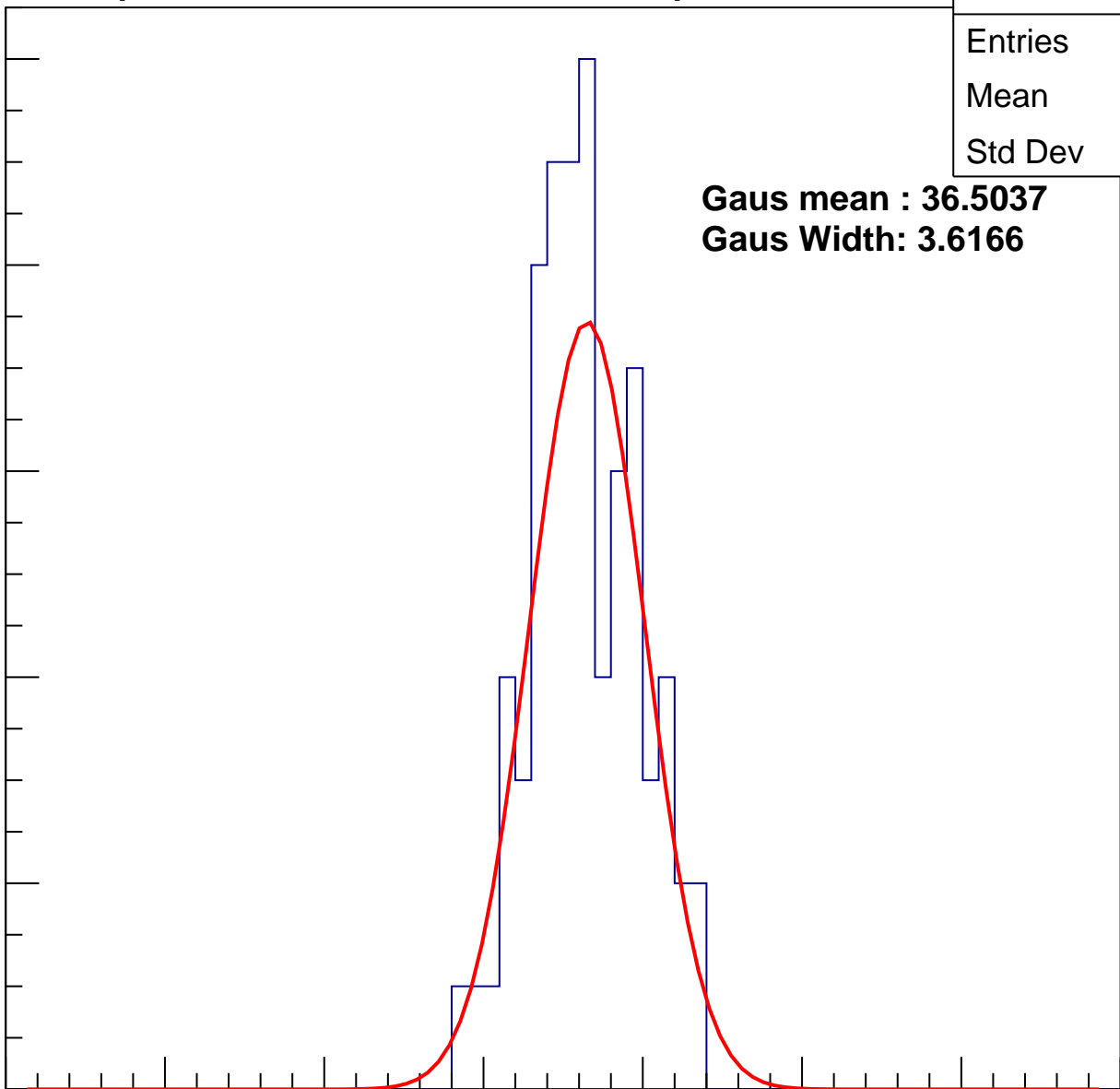
**Gaus Width: 3.6166**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	42.76
Std Dev	3.262

**Gaus mean : 42.9051**

**Gaus Width: 3.6411**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

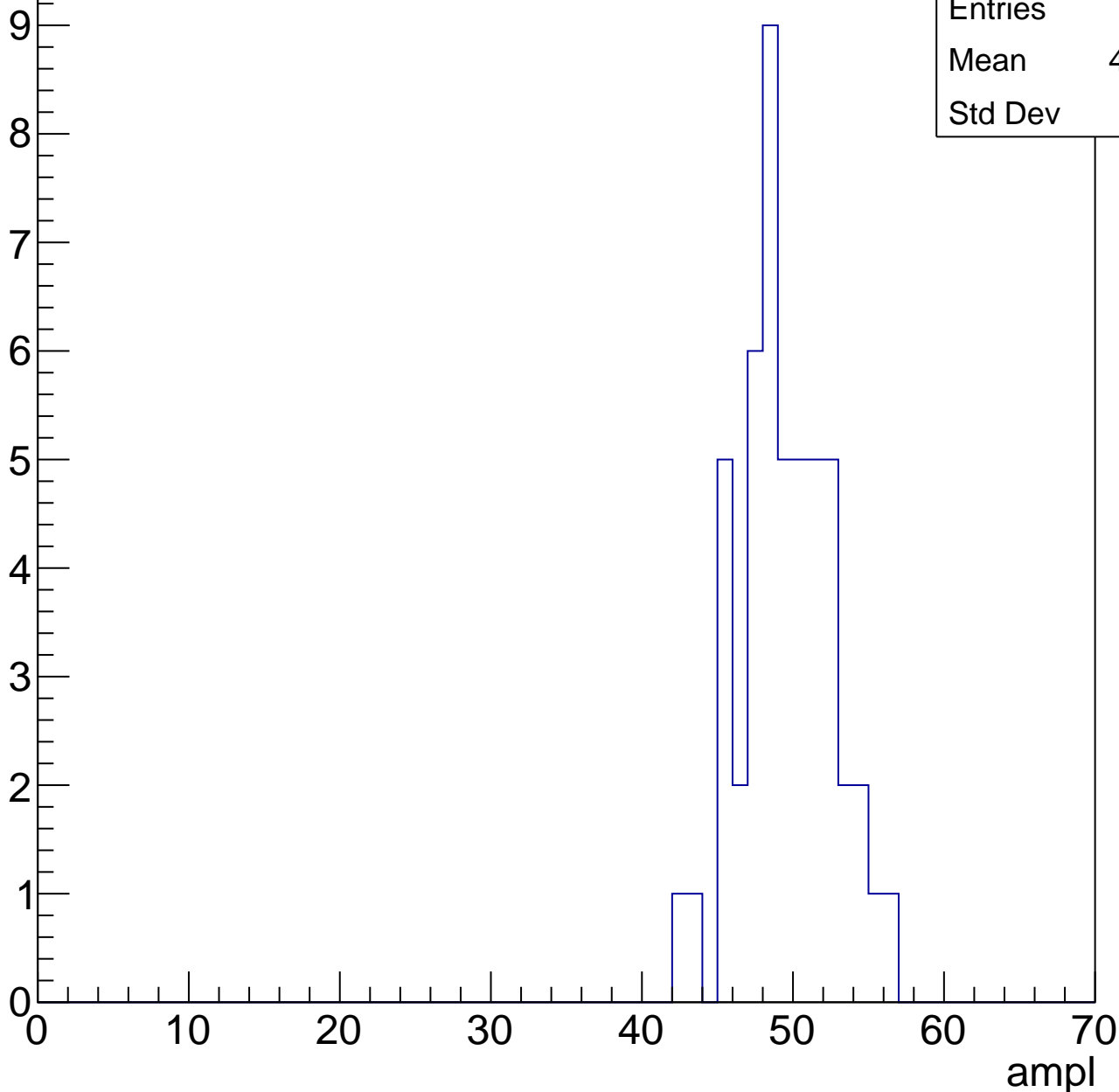
70

# B1L000S, U25-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

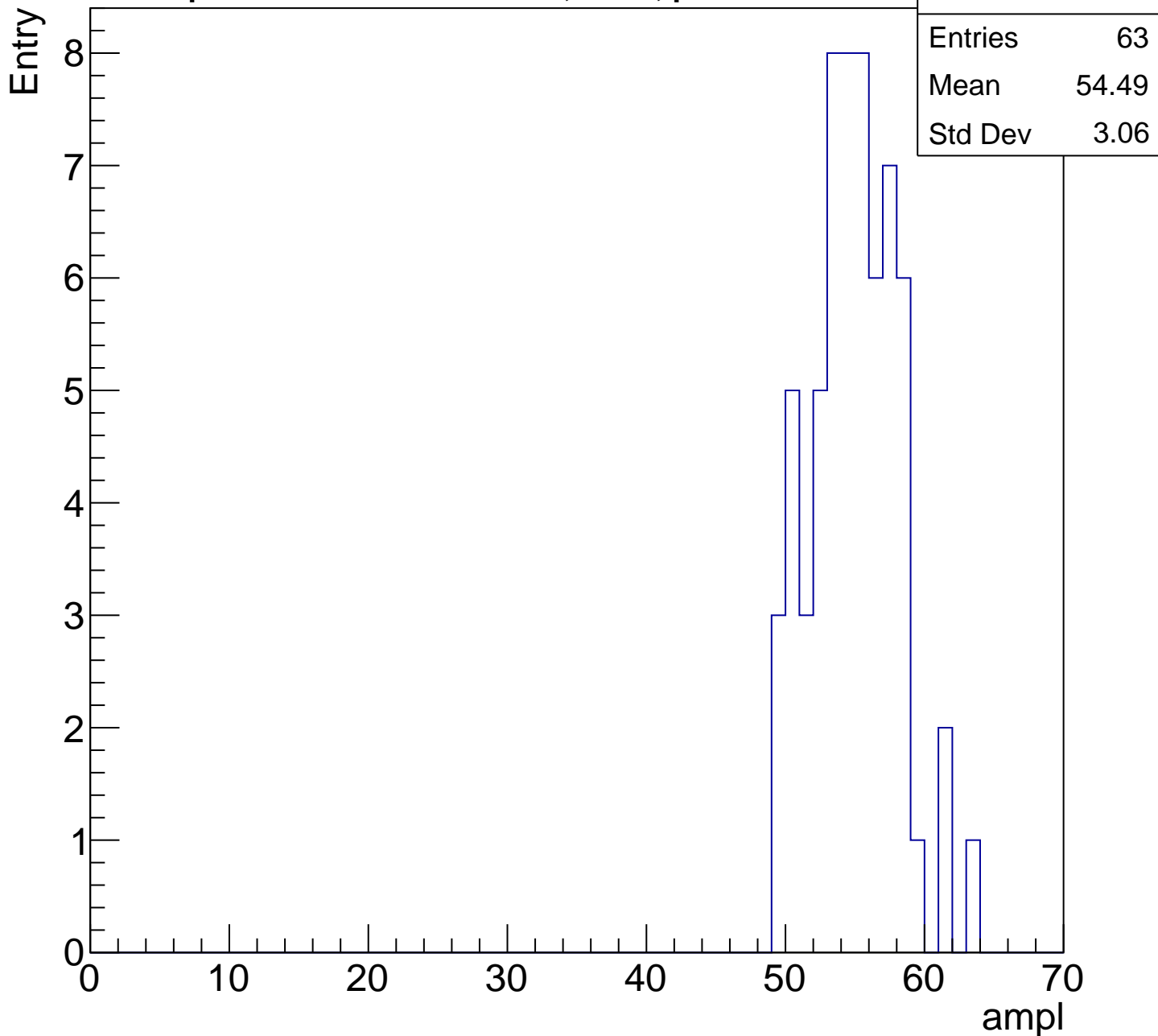
Entry

Entries	50
Mean	49.02
Std Dev	3.01



# B1L000S, U25-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

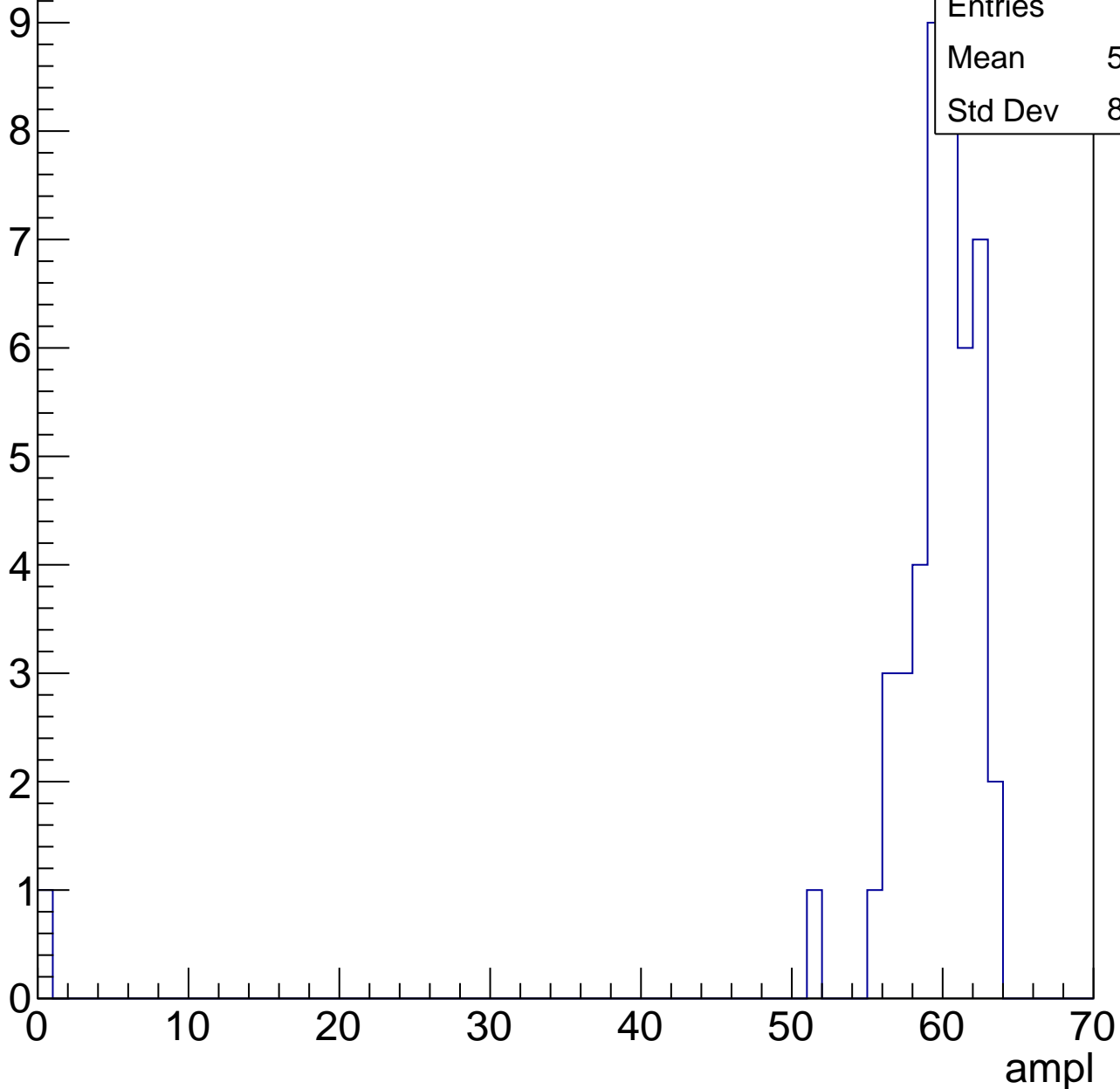


# B1L000S, U25-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

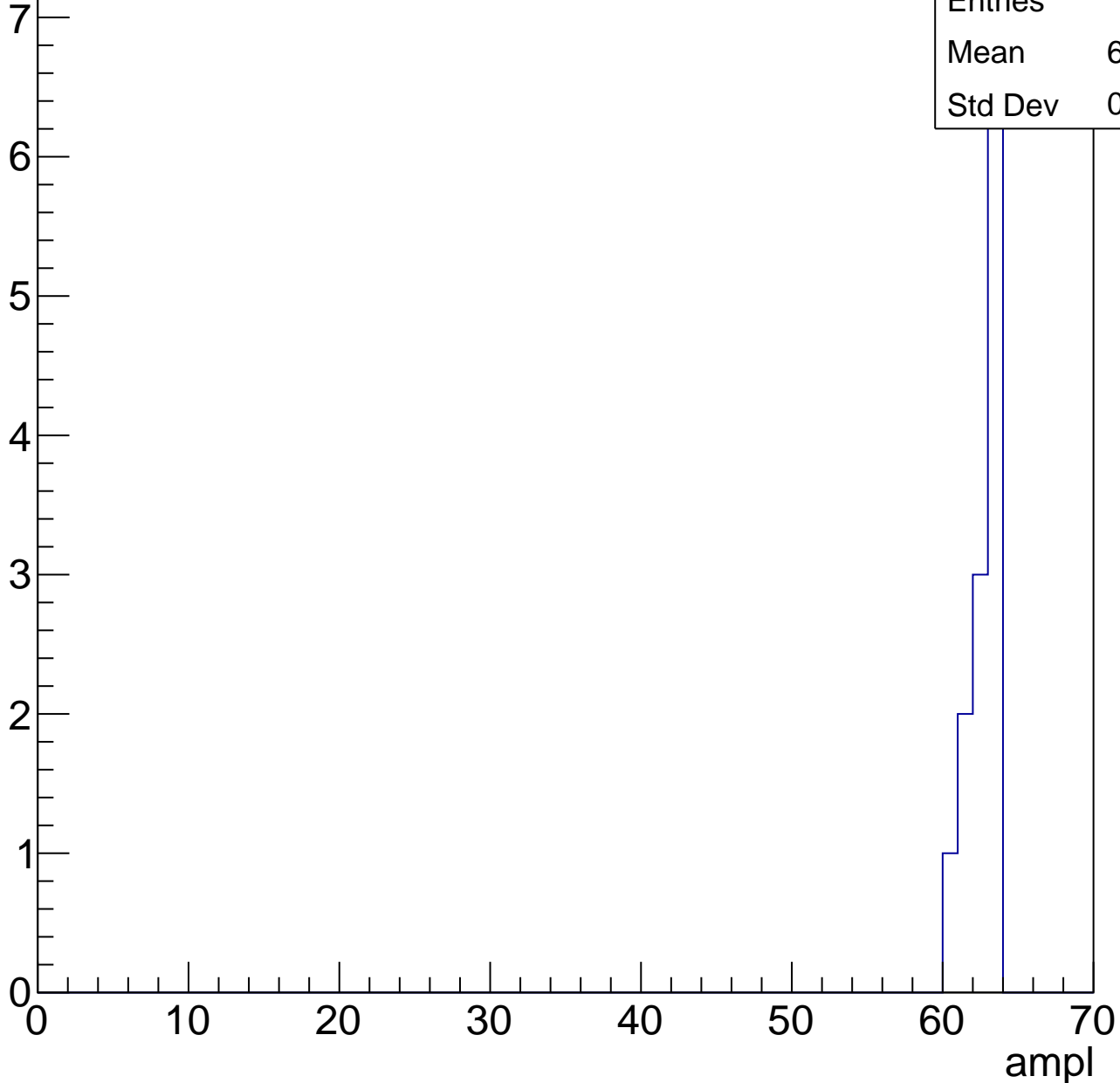
Entries	46
Mean	58.13
Std Dev	8.965



# B1L000S, U25-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

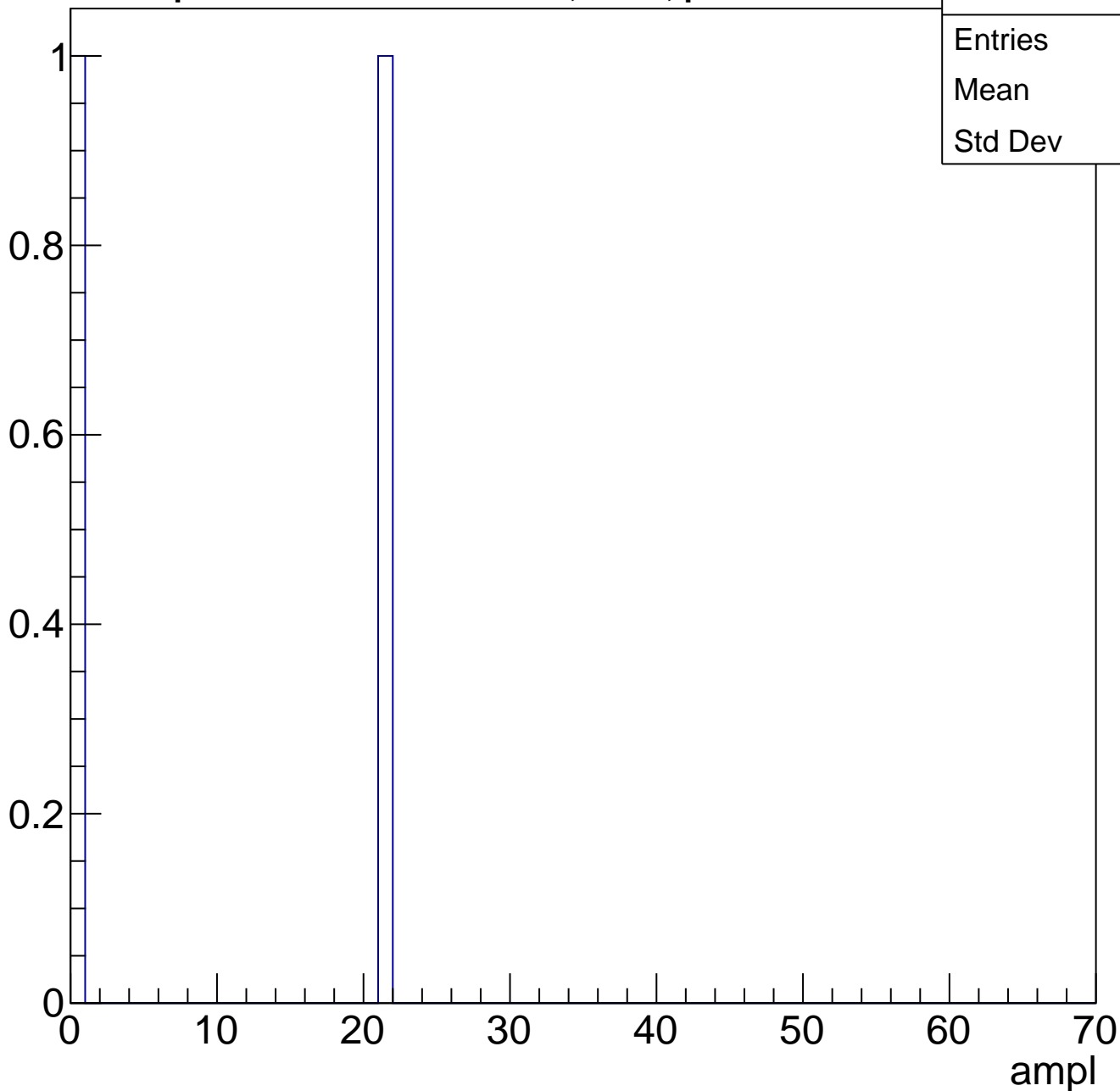




# B1L000S, U25-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B1L000S, U25-ch1, adc0

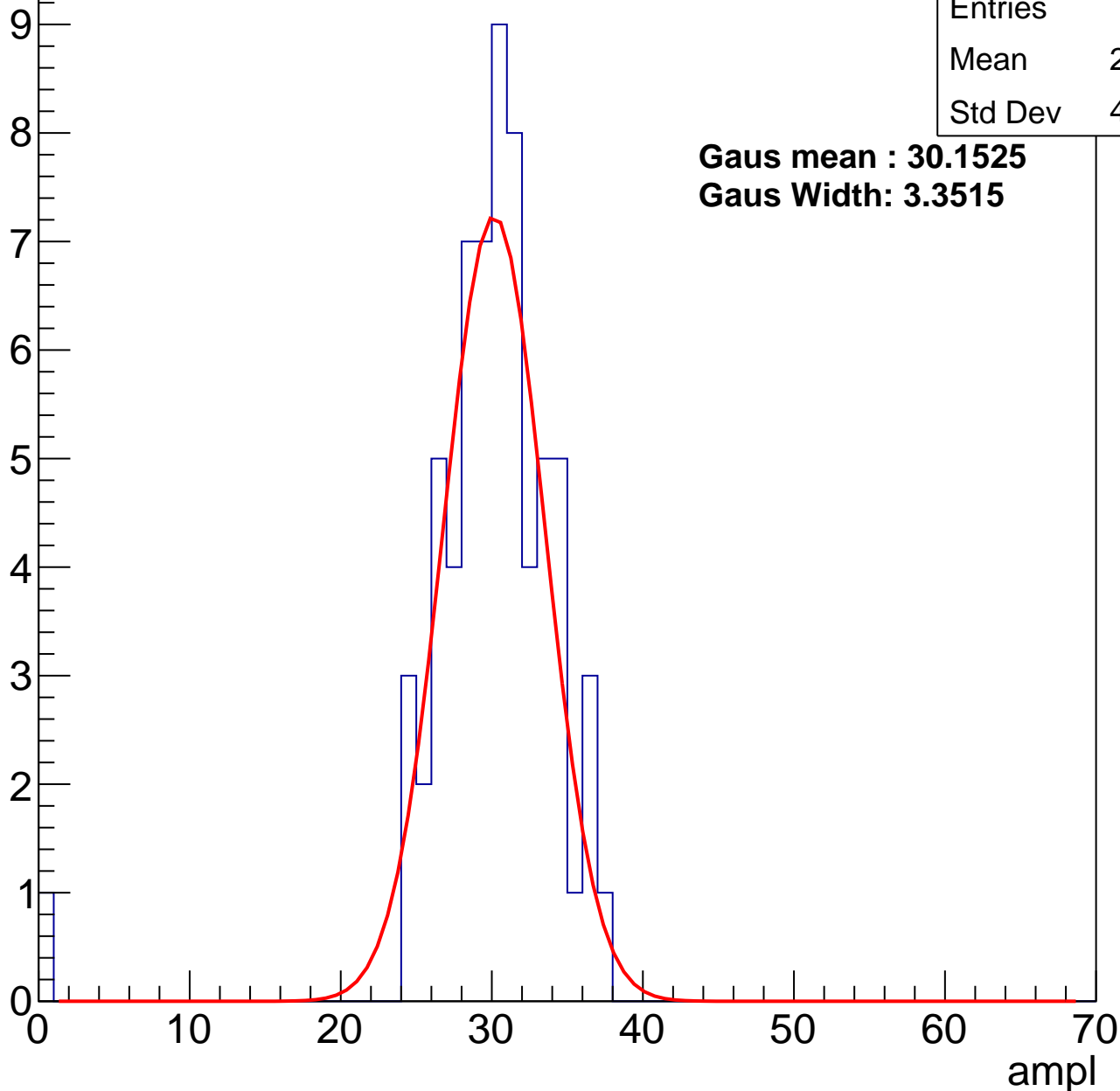
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	29.54
Std Dev	4.846

**Gaus mean : 30.1525**

**Gaus Width: 3.3515**



# B1L000S, U25-ch1, adc1

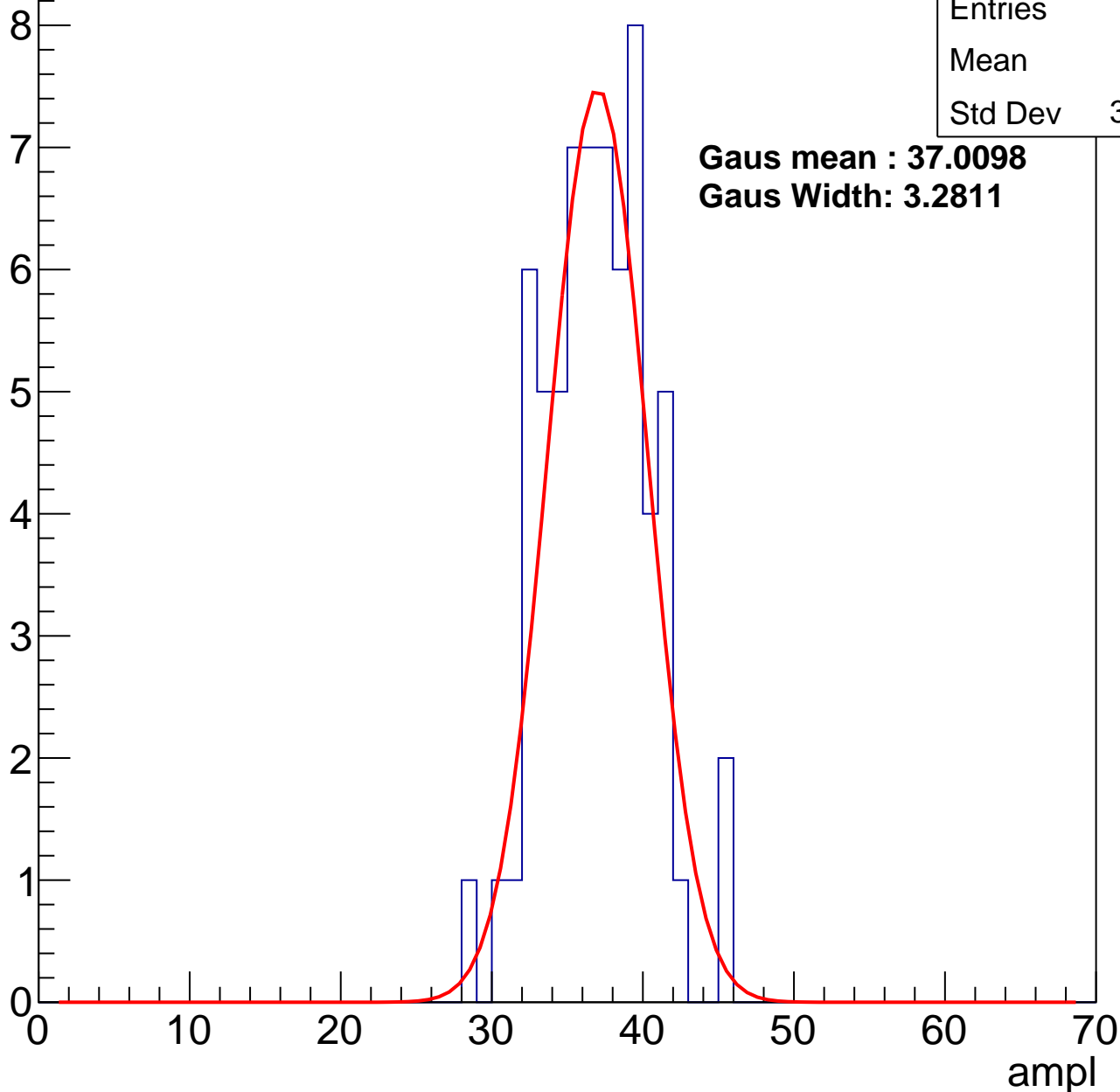
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	36.5
Std Dev	3.408

**Gaus mean : 37.0098**

**Gaus Width: 3.2811**



# B1L000S, U25-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	42.72
Std Dev	3.416

**Gaus mean : 42.8901**

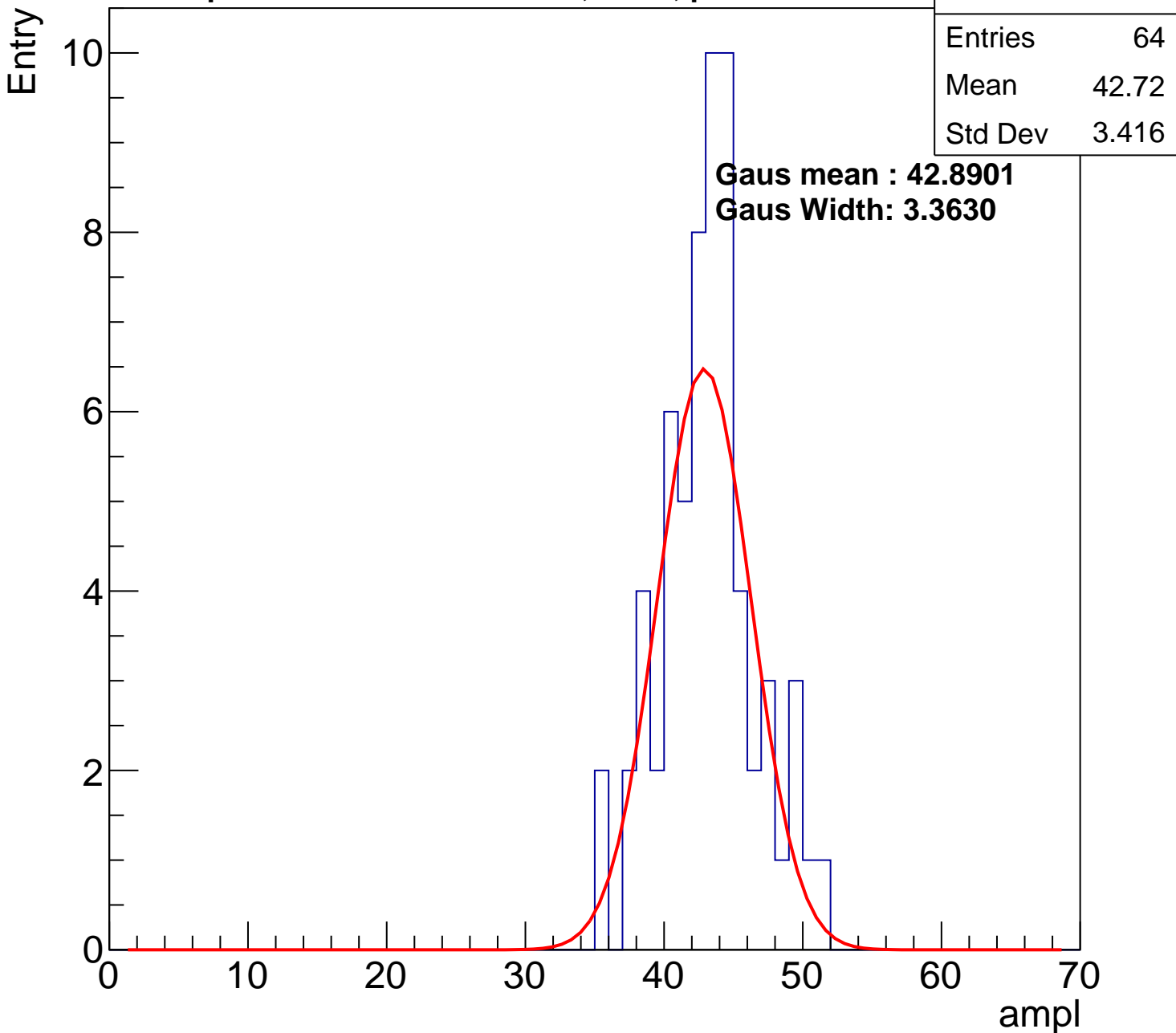
**Gaus Width: 3.3630**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

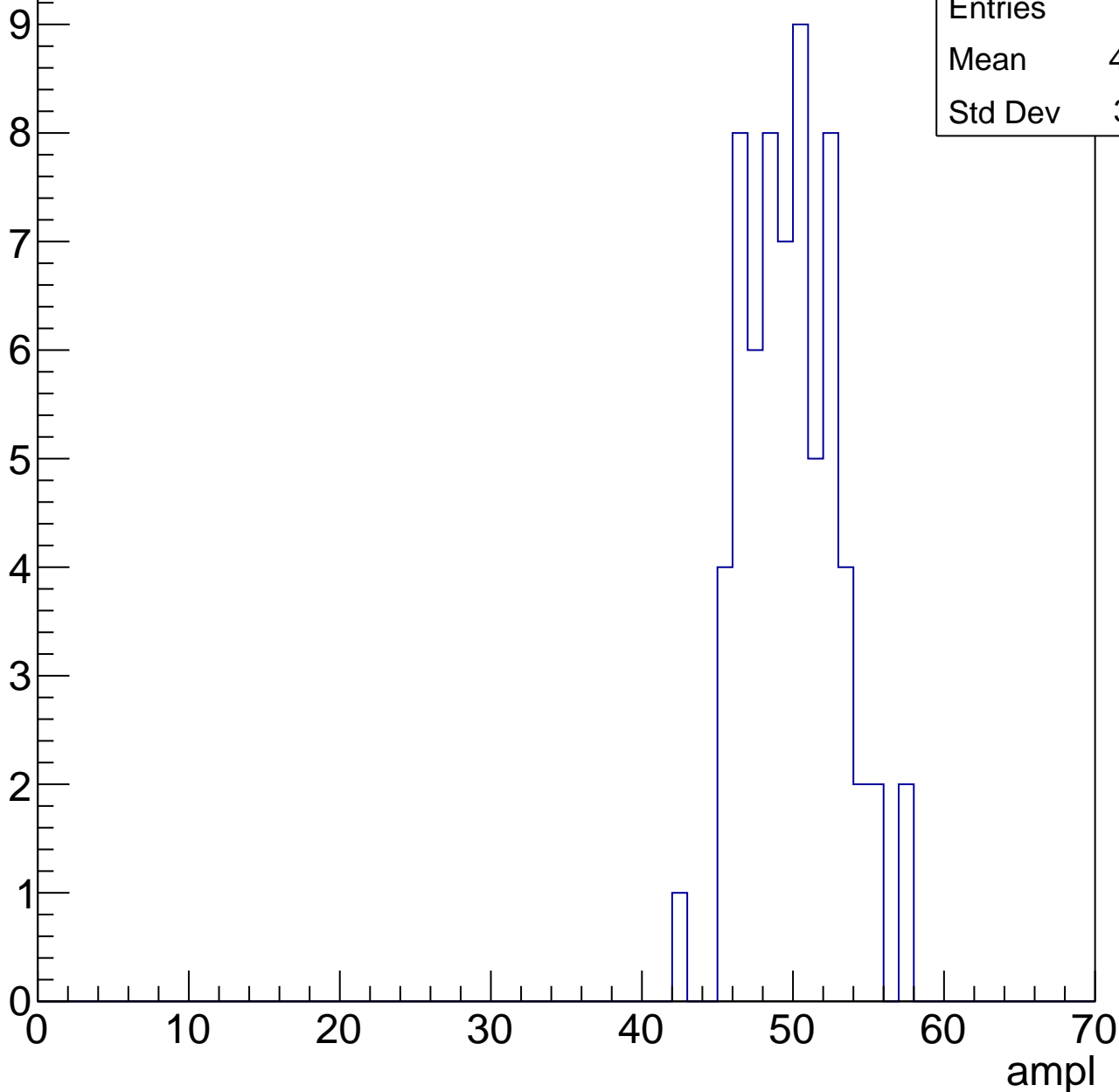


# B1L000S, U25-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

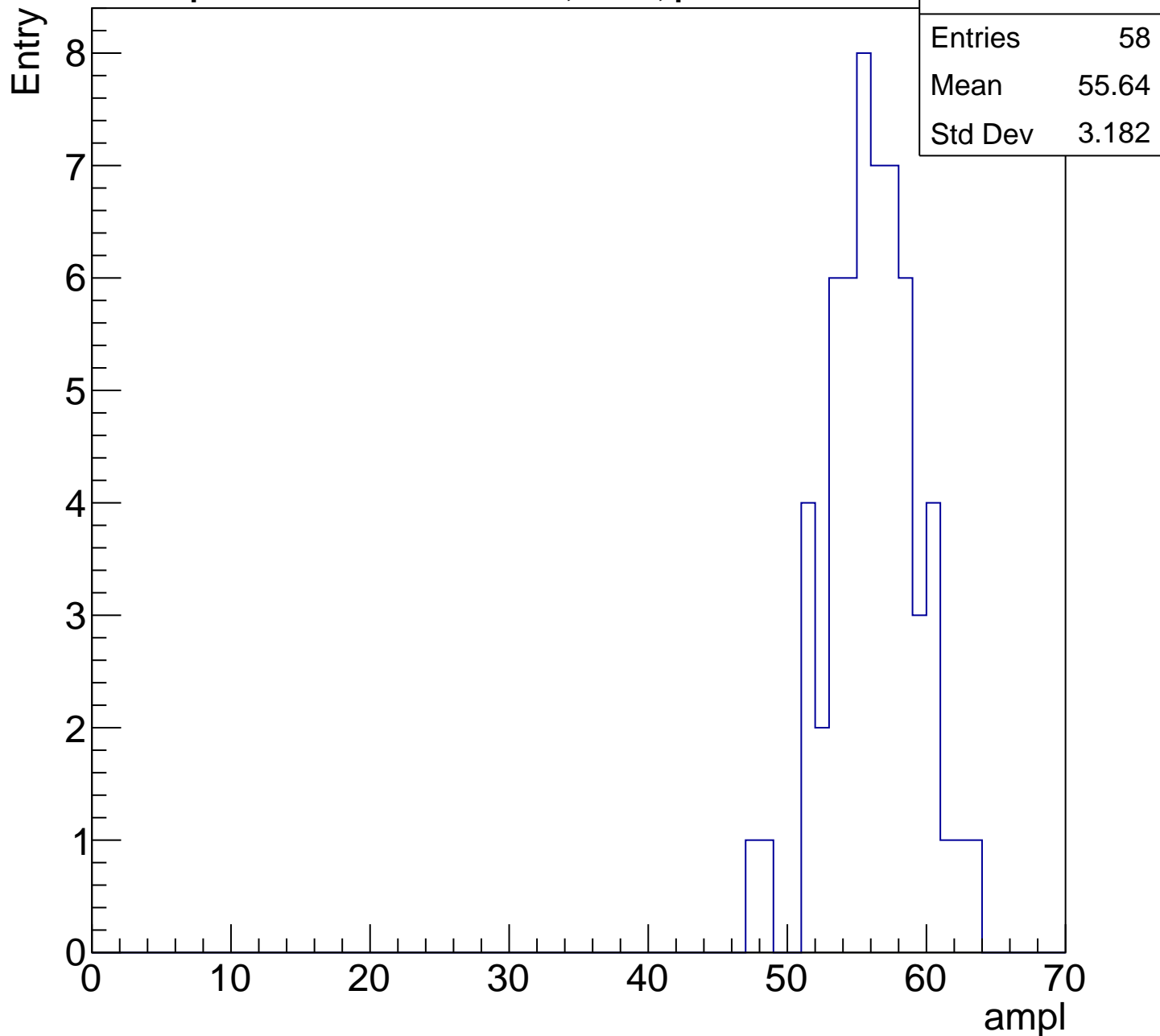
Entry

Entries	66
Mean	49.45
Std Dev	3.061



# B1L000S, U25-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

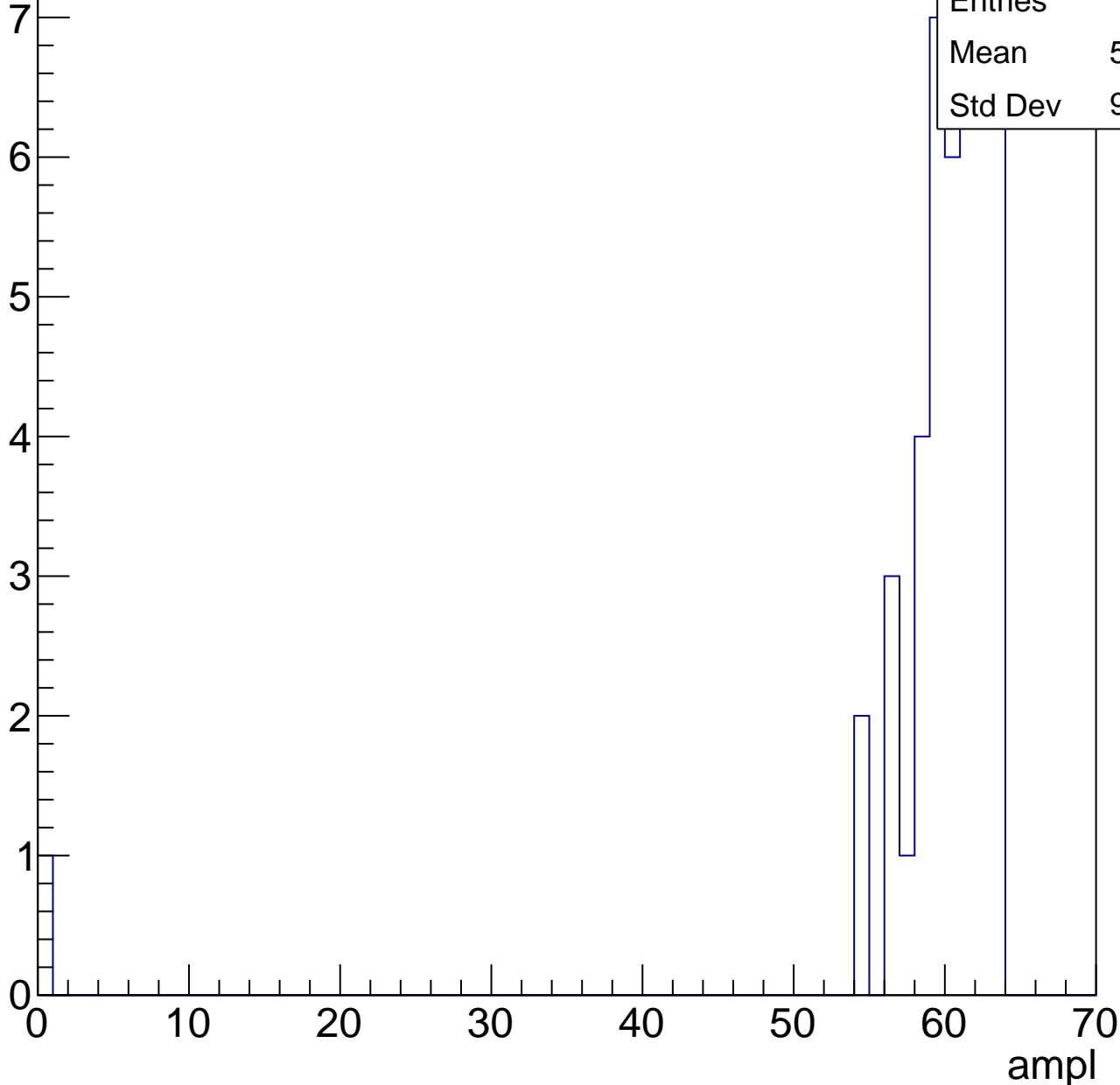


# B1L000S, U25-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

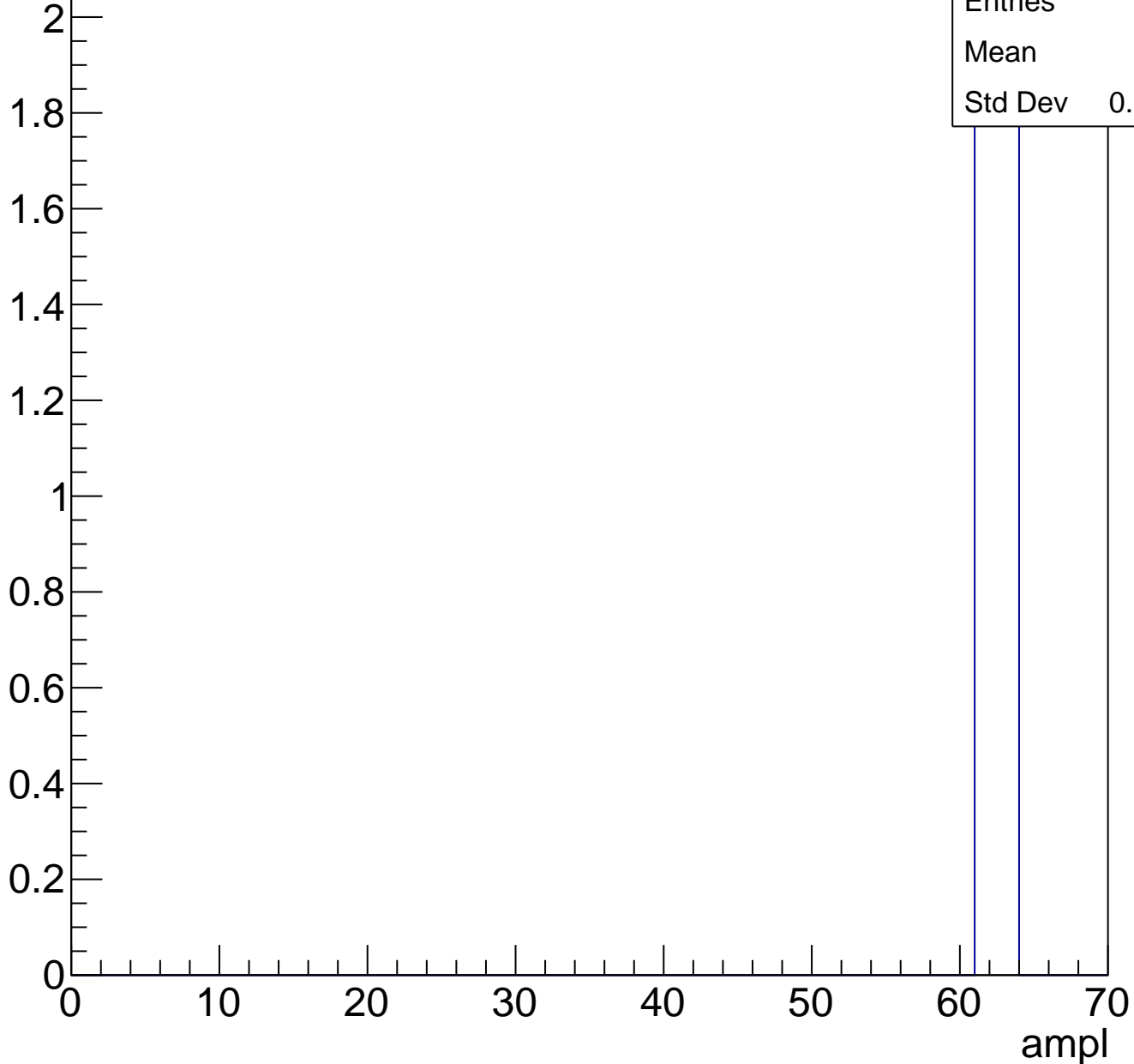
Entries	45
Mean	58.67
Std Dev	9.153



# B1L000S, U25-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch2, adc0

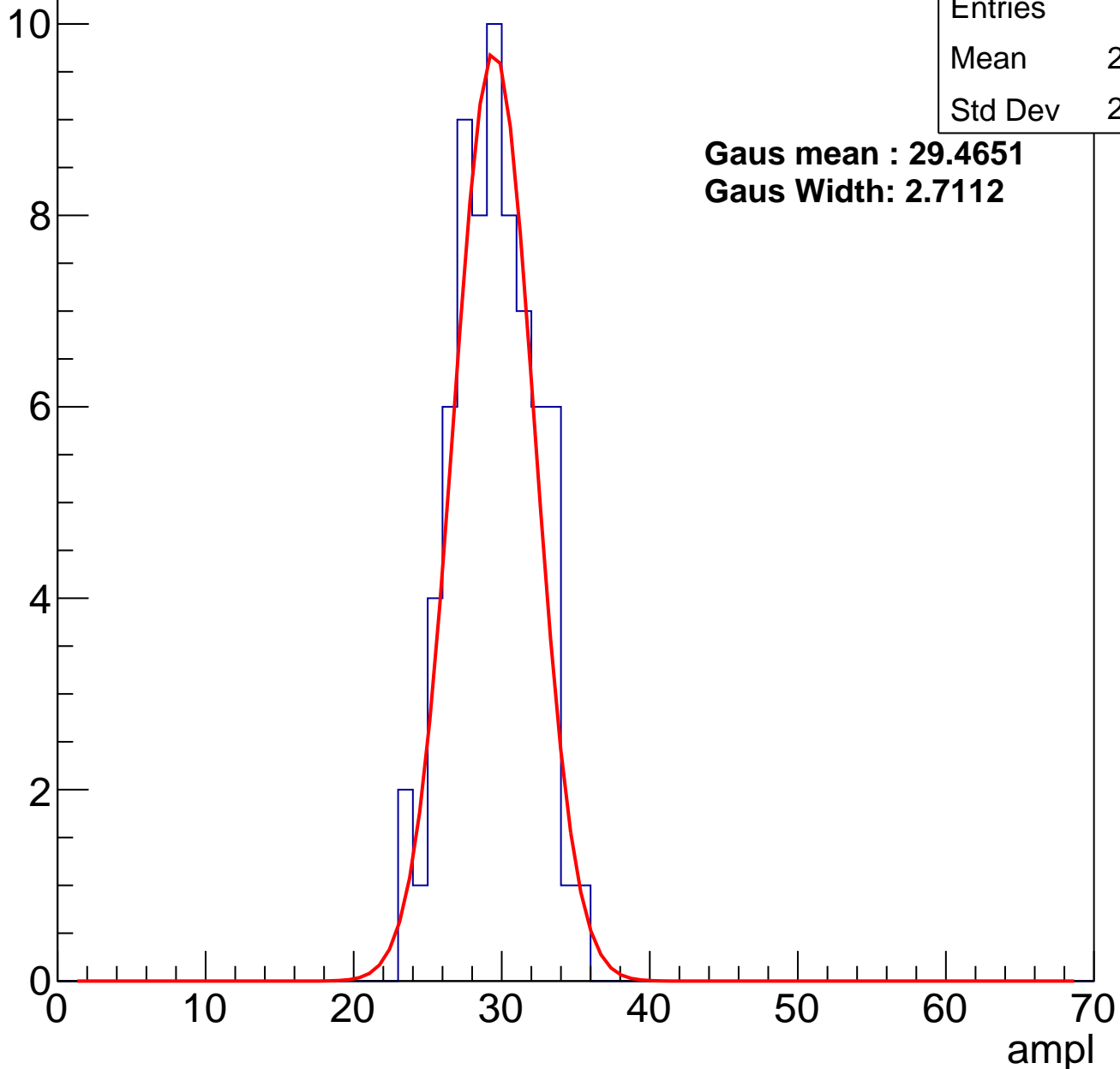
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.97
Std Dev	2.708

**Gaus mean : 29.4651**

**Gaus Width: 2.7112**

Entry



# B1L000S, U25-ch2, adc1

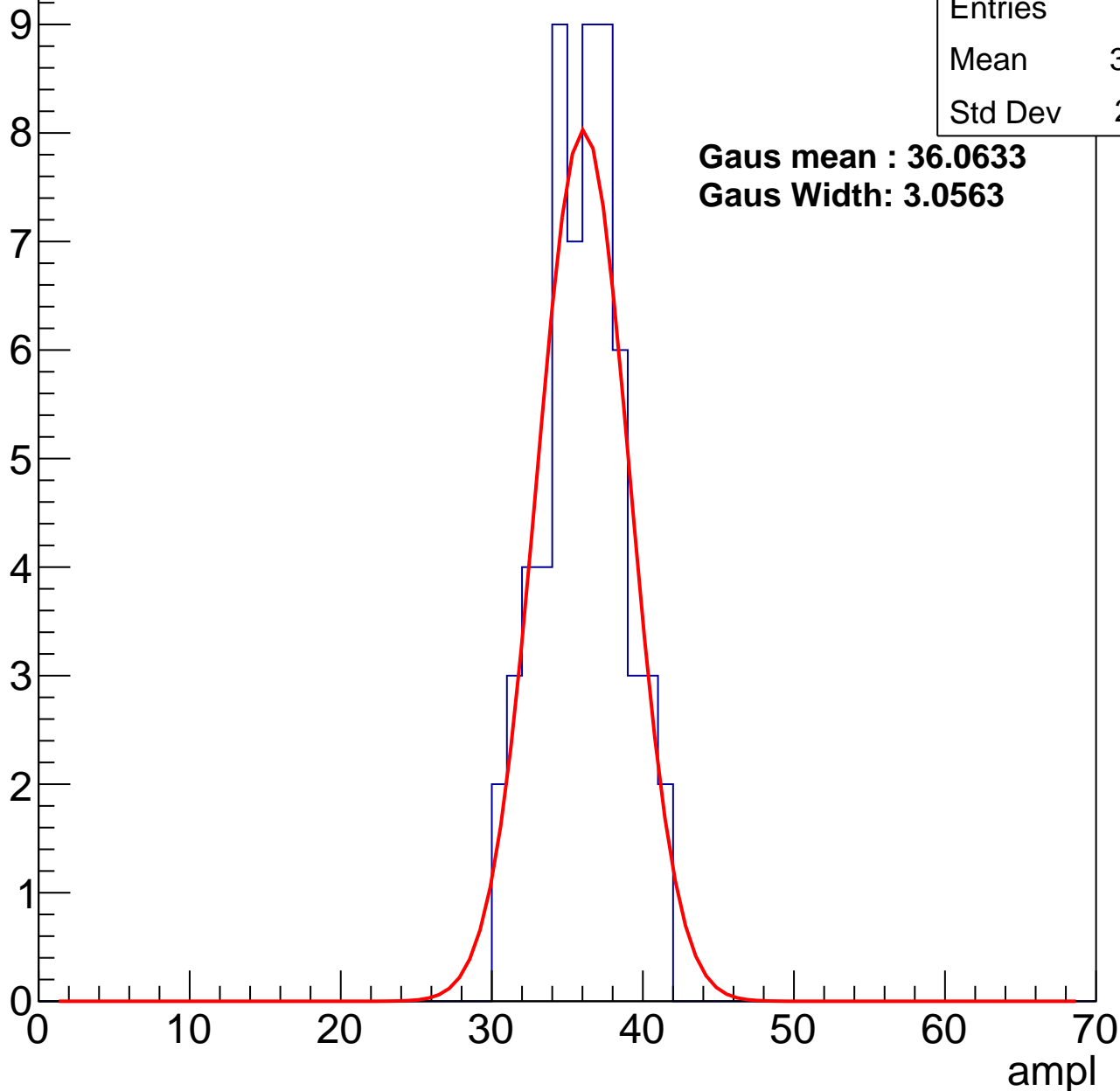
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.54
Std Dev	2.671

**Gaus mean : 36.0633**

**Gaus Width: 3.0563**



# B1L000S, U25-ch2, adc2

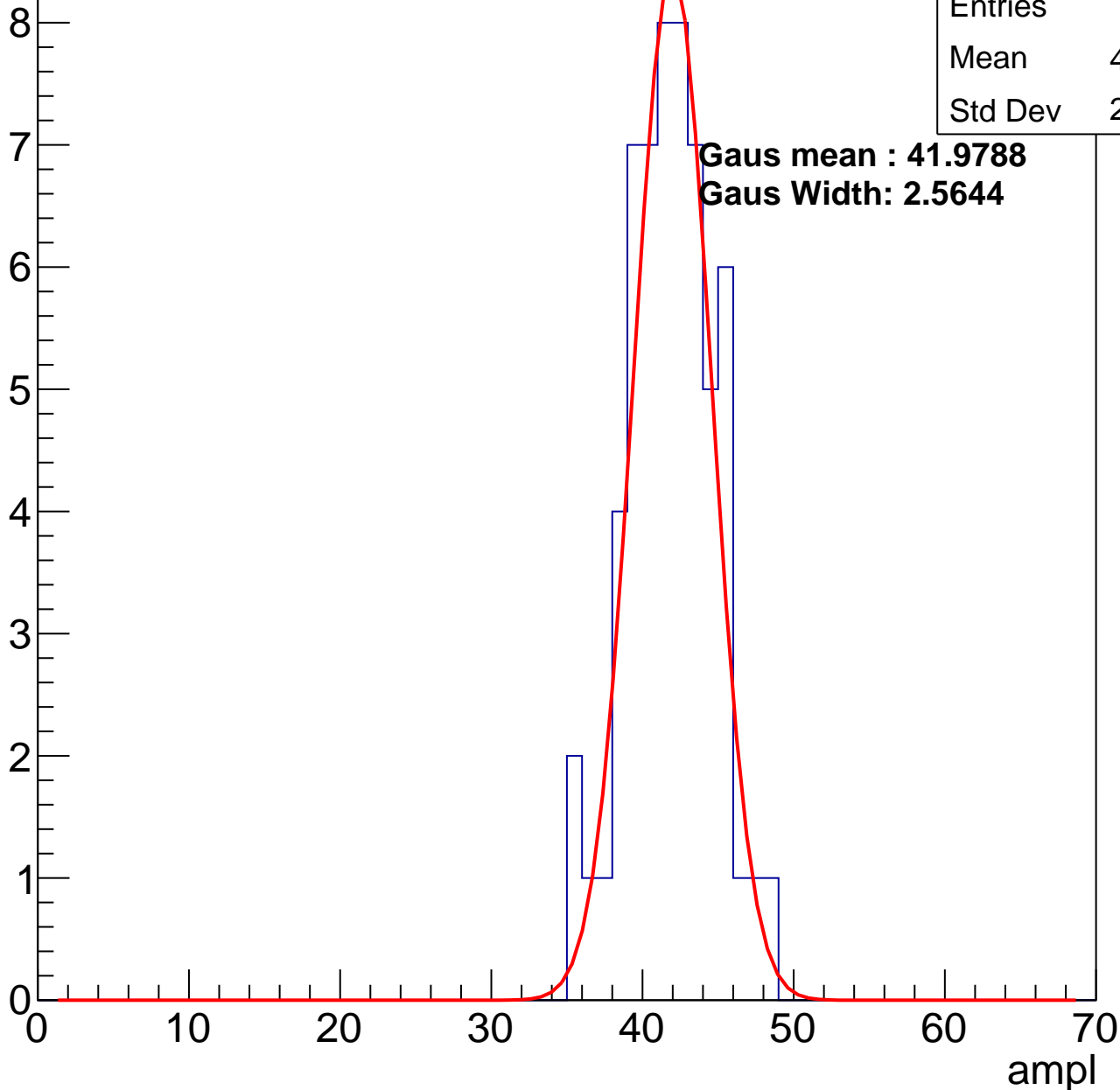
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.42
Std Dev	2.793

**Gaus mean : 41.9788**

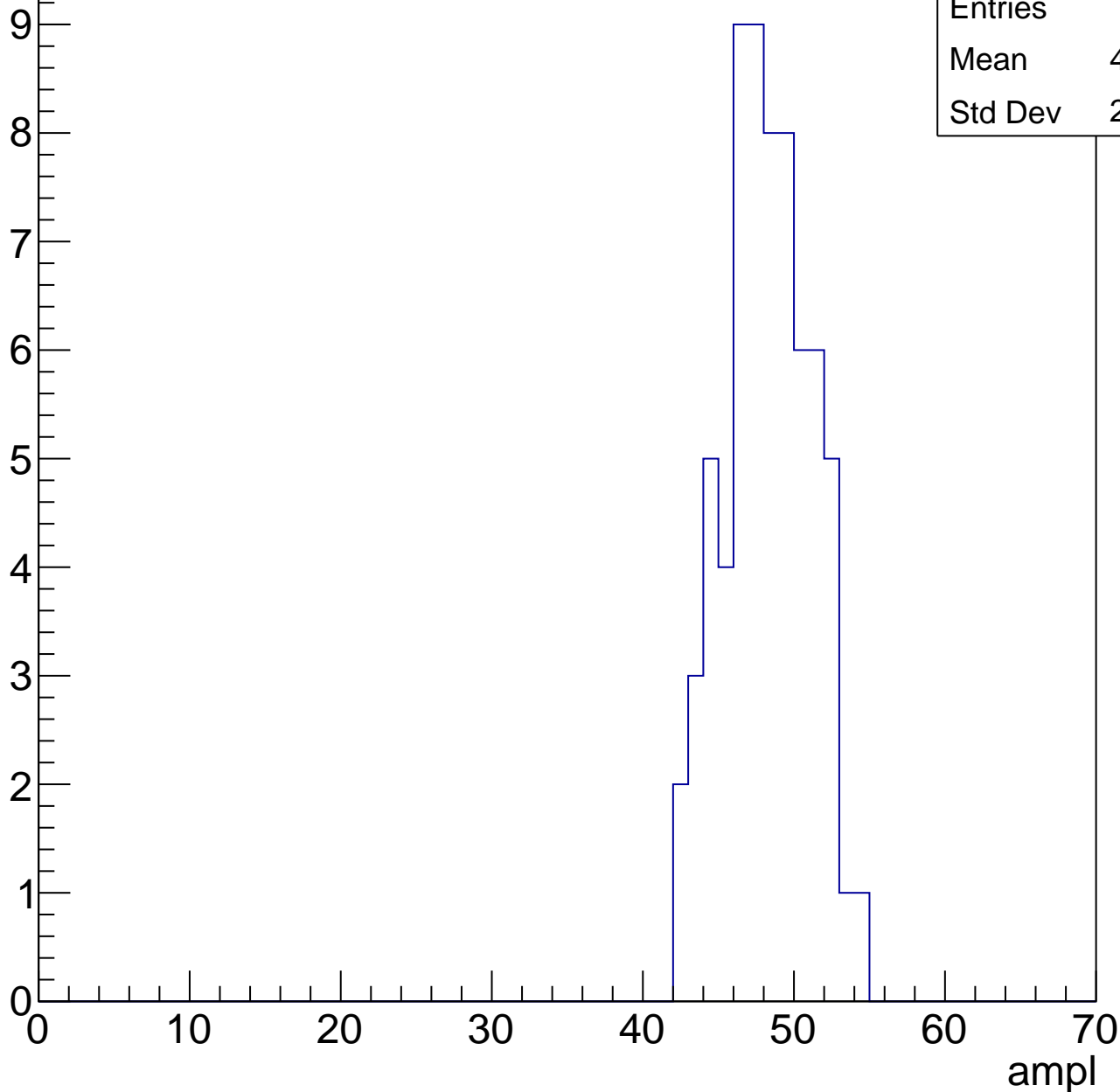
**Gaus Width: 2.5644**



# B1L000S, U25-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

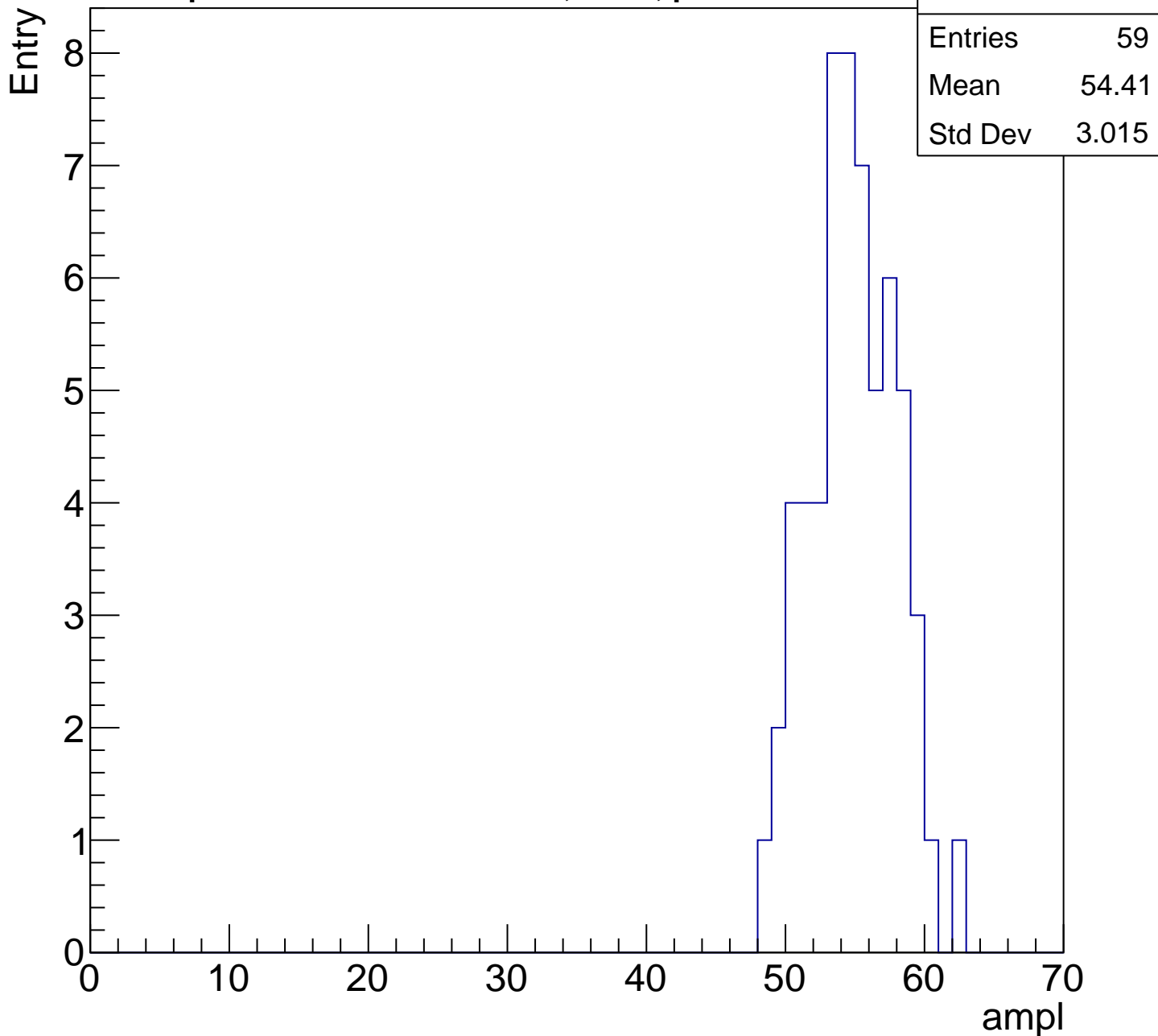
Entry



Entries	67
Mean	47.75
Std Dev	2.814

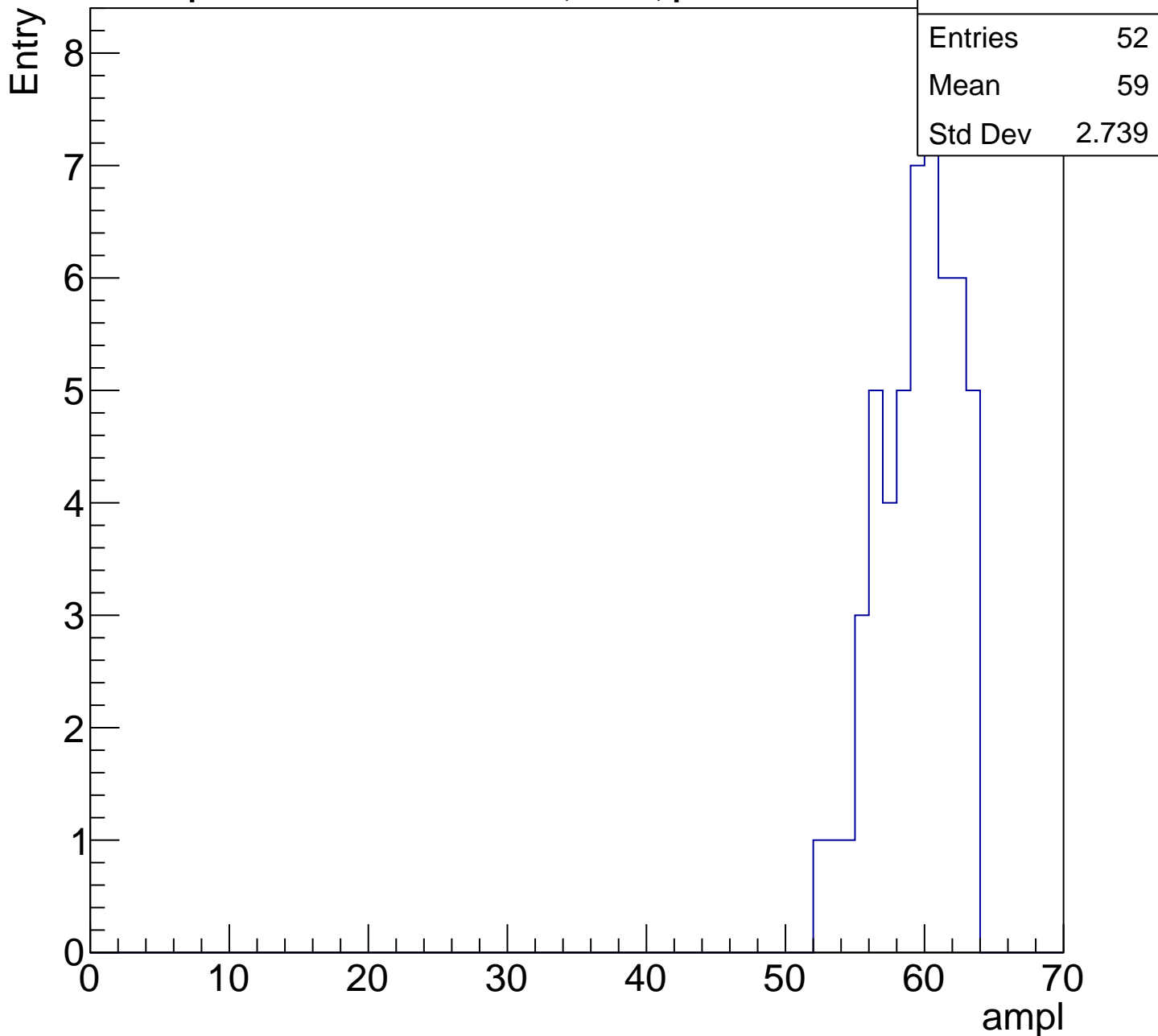
# B1L000S, U25-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch2, adc5

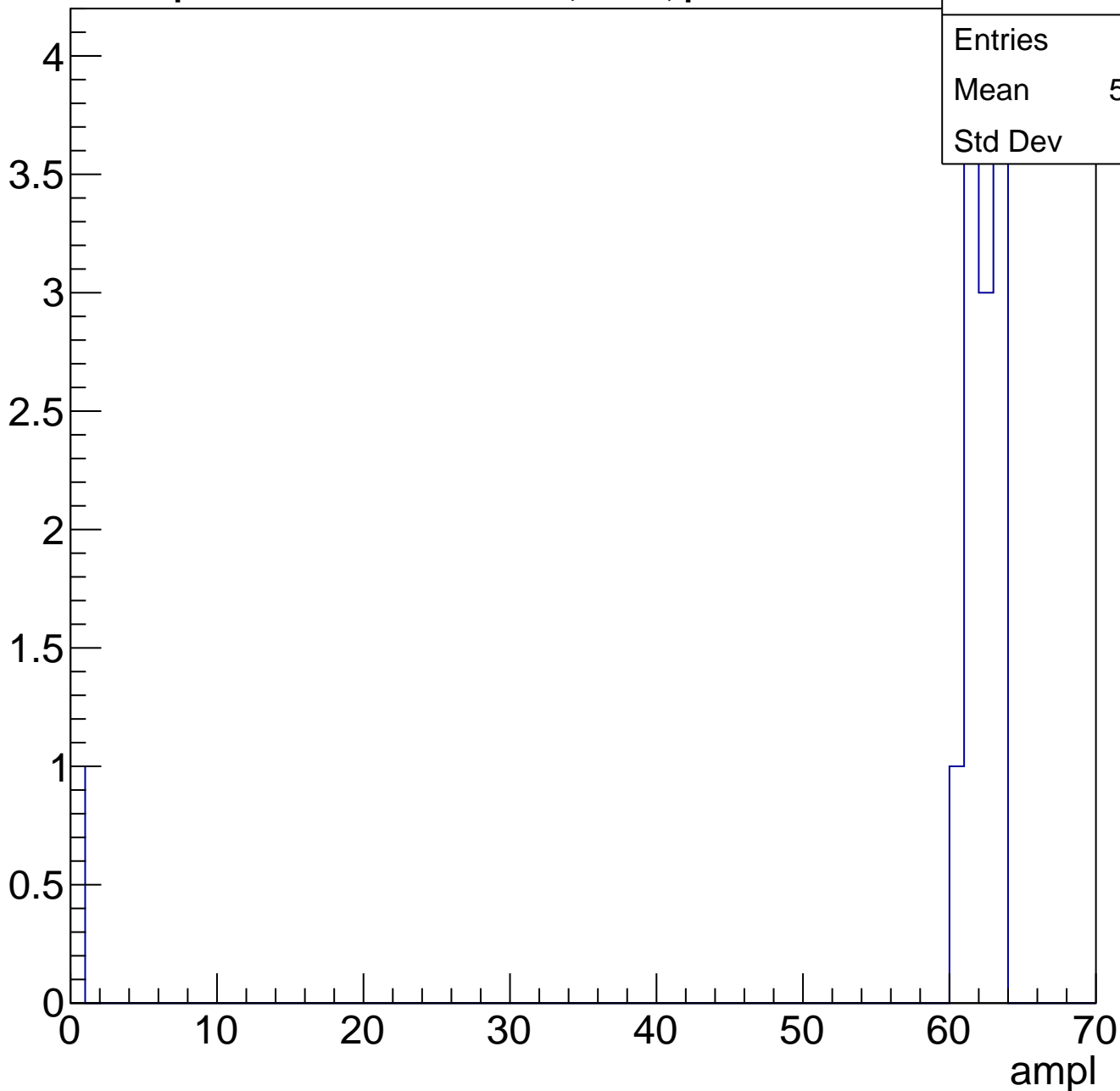
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch2, adc7

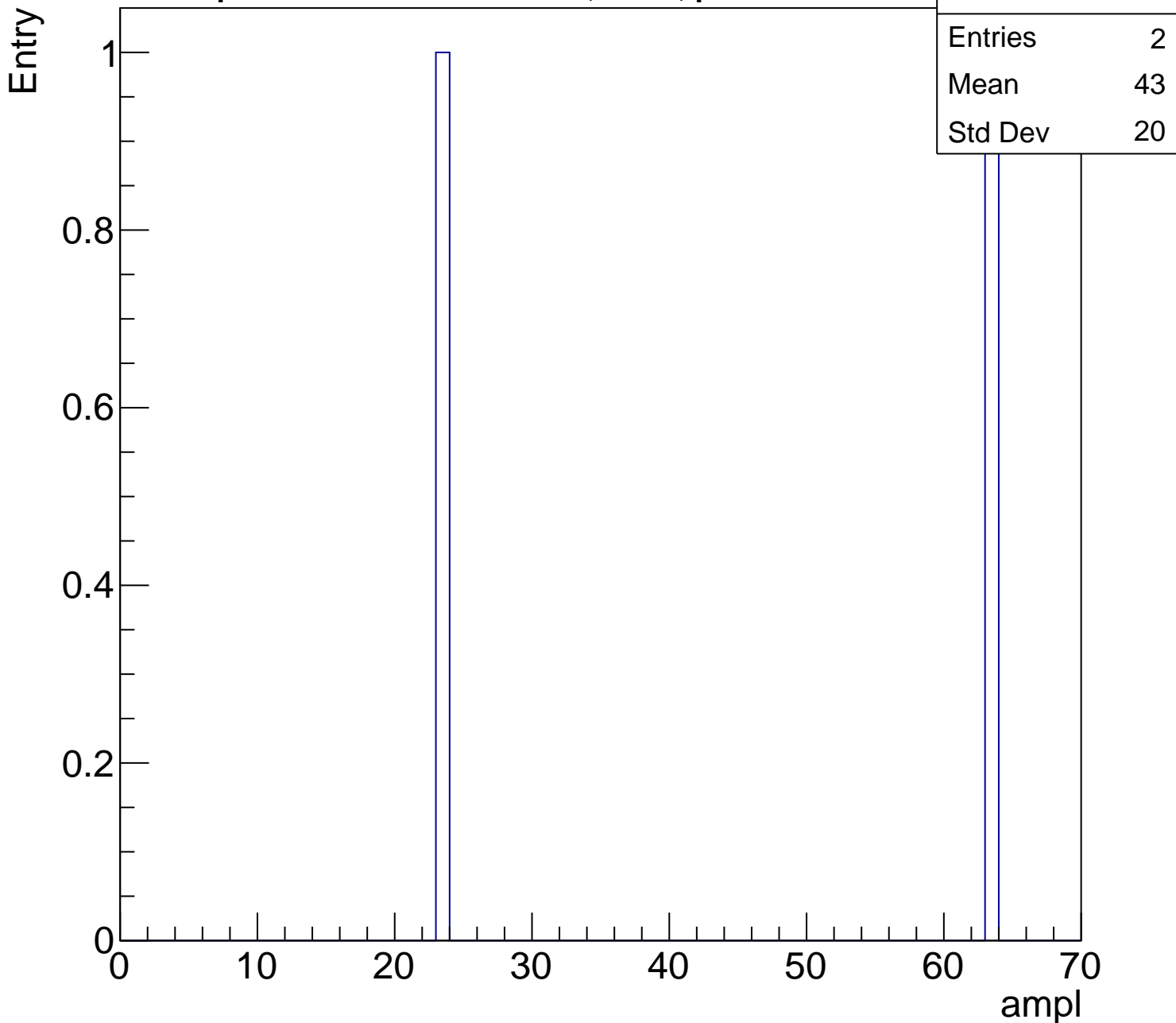
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	43
Std Dev	20

ampl



# B1L000S, U25-ch3, adc0

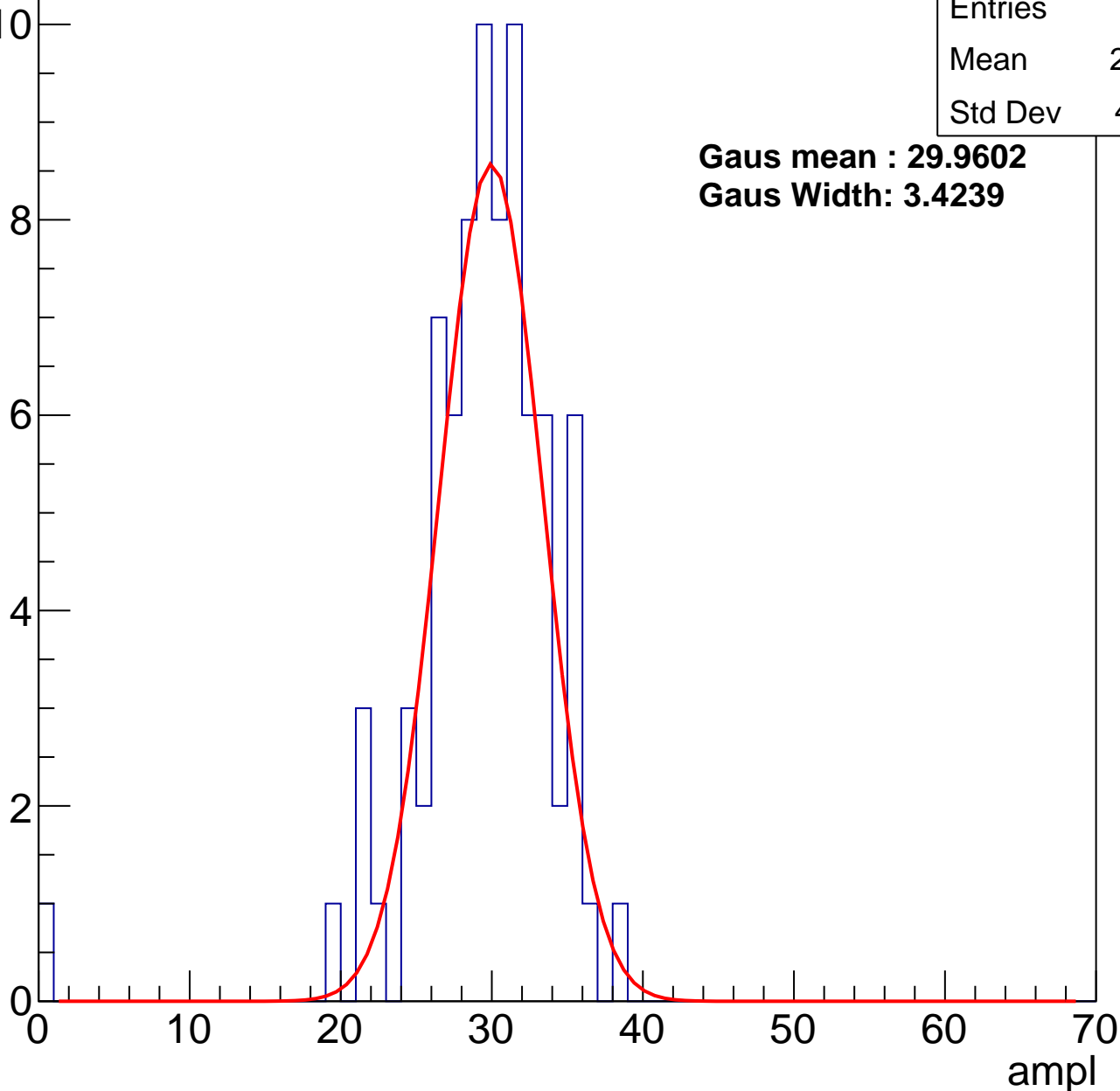
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	28.98
Std Dev	4.911

**Gaus mean : 29.9602**

**Gaus Width: 3.4239**



# B1L000S, U25-ch3, adc1

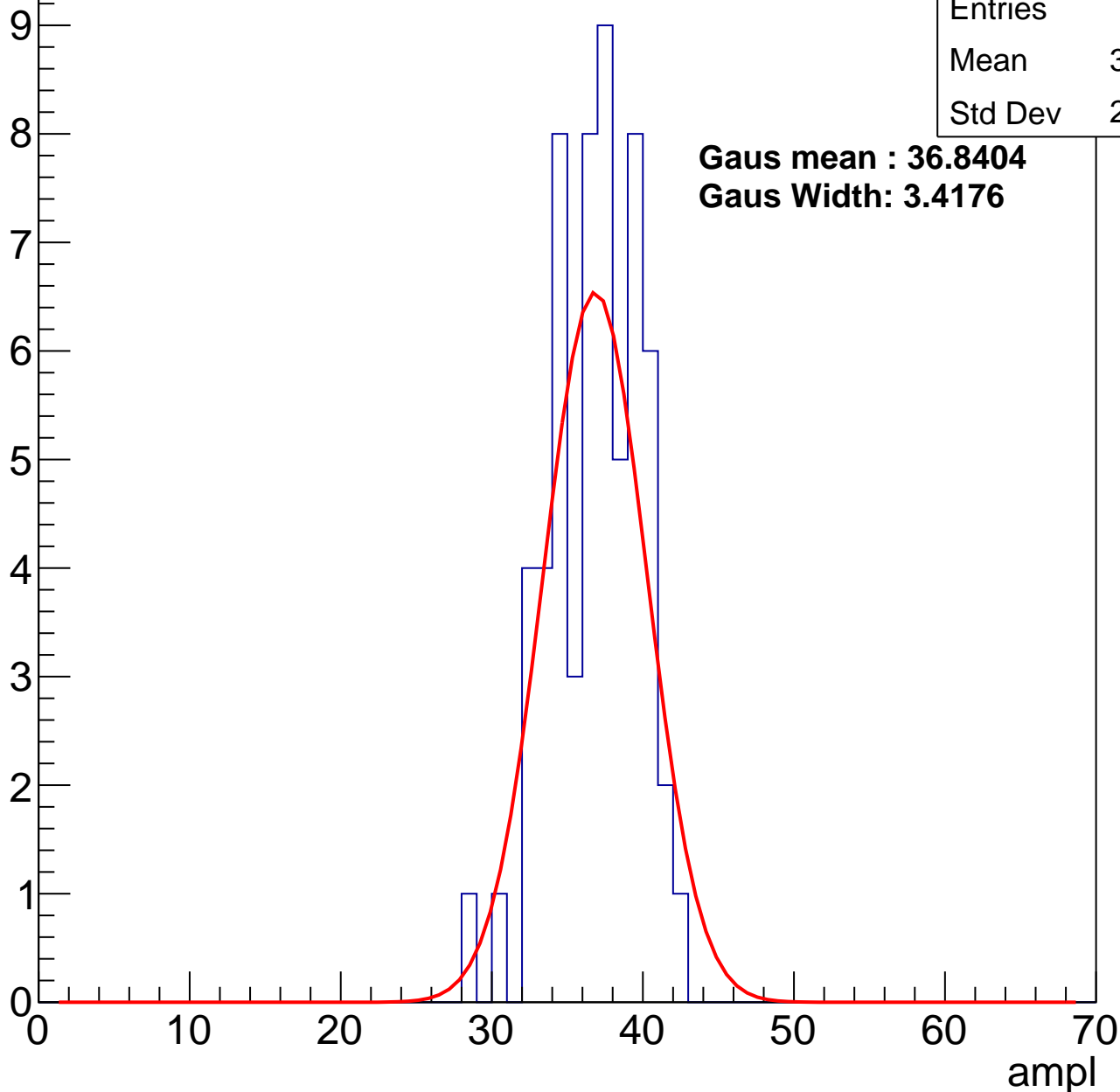
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	36.37
Std Dev	2.915

**Gaus mean : 36.8404**

**Gaus Width: 3.4176**



# B1L000S, U25-ch3, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	42.99
Std Dev	3.169

**Gaus mean : 43.7166**

**Gaus Width: 3.1658**

10

8

6

4

2

0

0

10

20

30

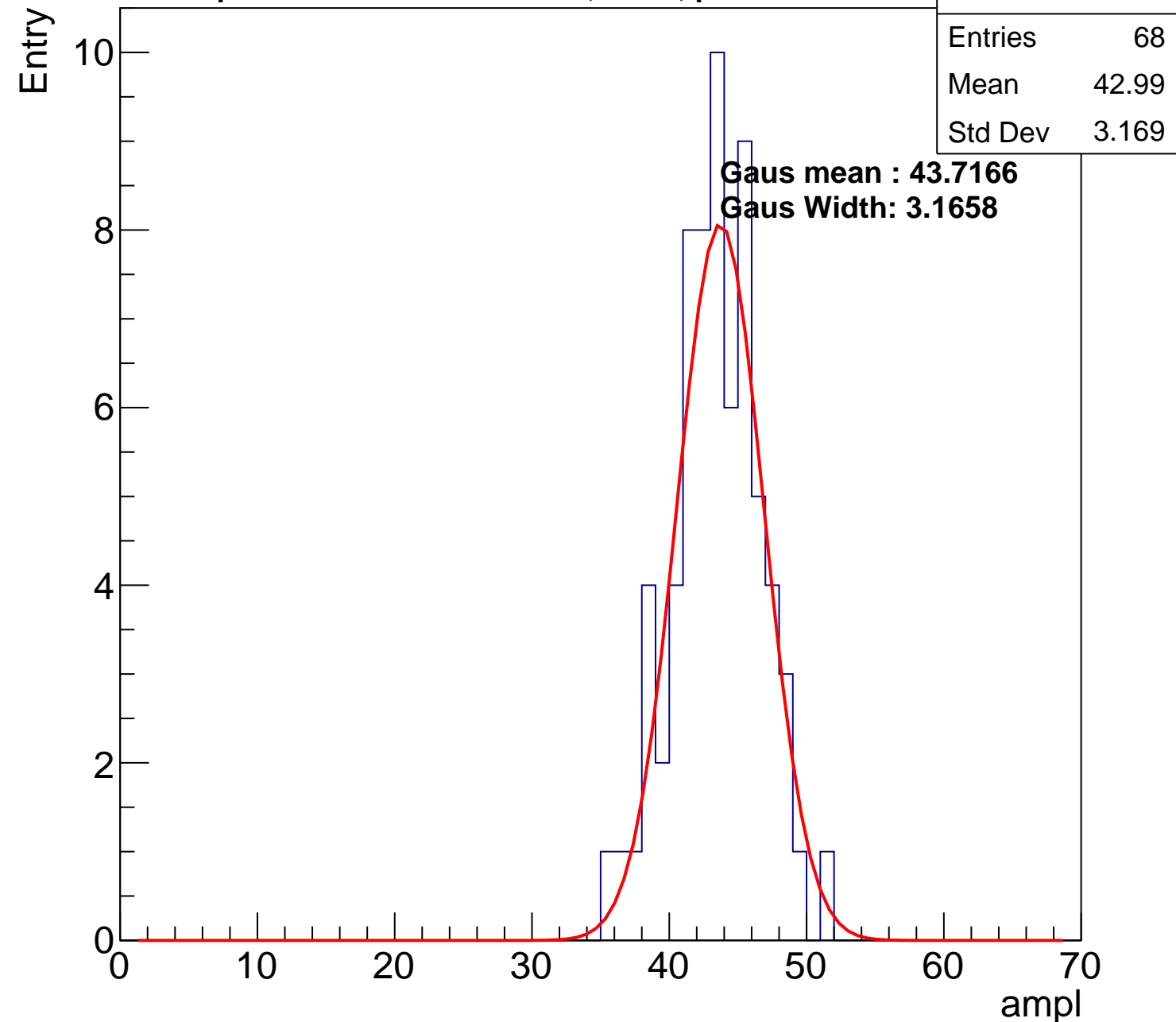
40

50

60

70

ampl

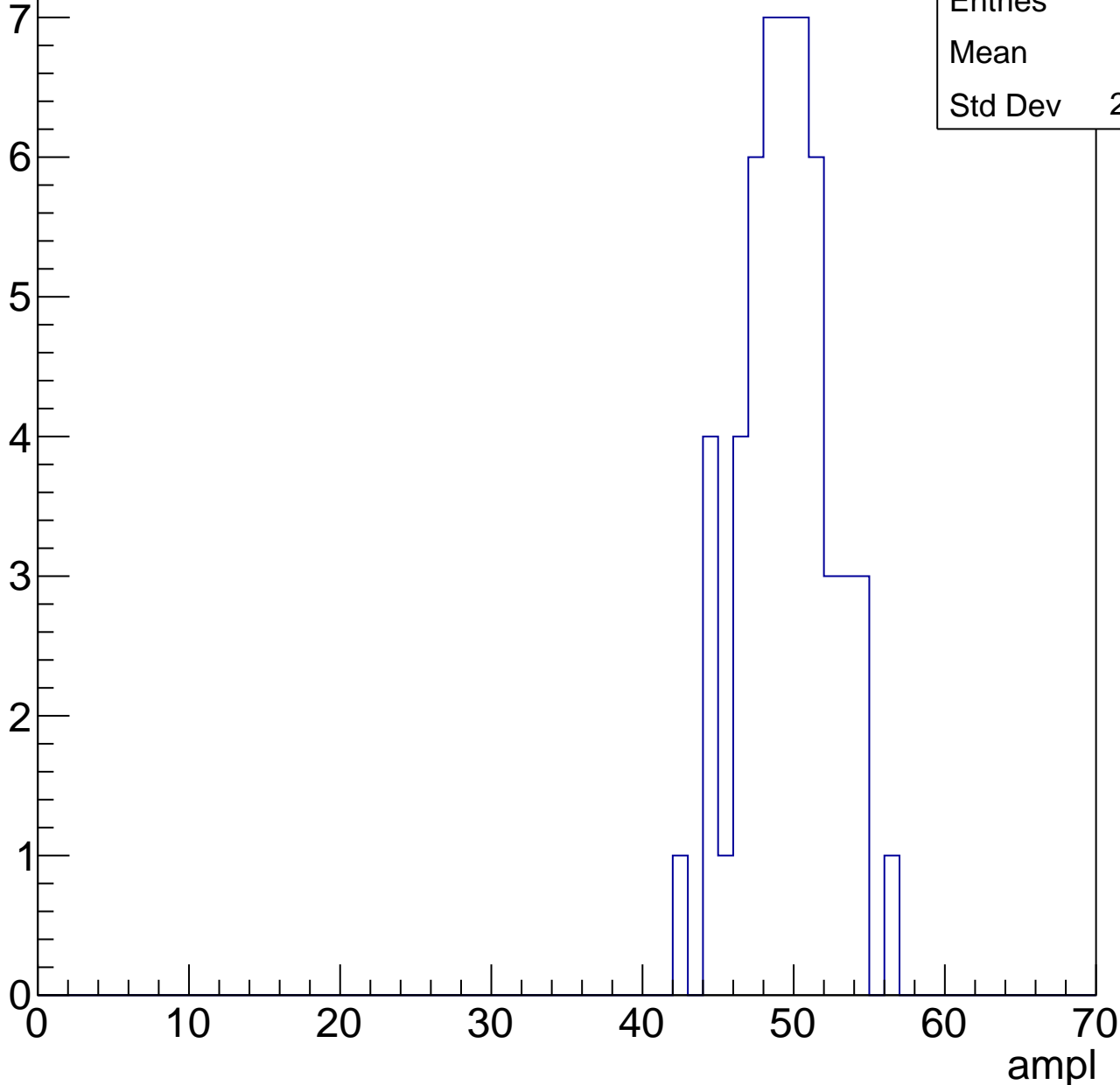


# B1L000S, U25-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	49
Std Dev	2.952

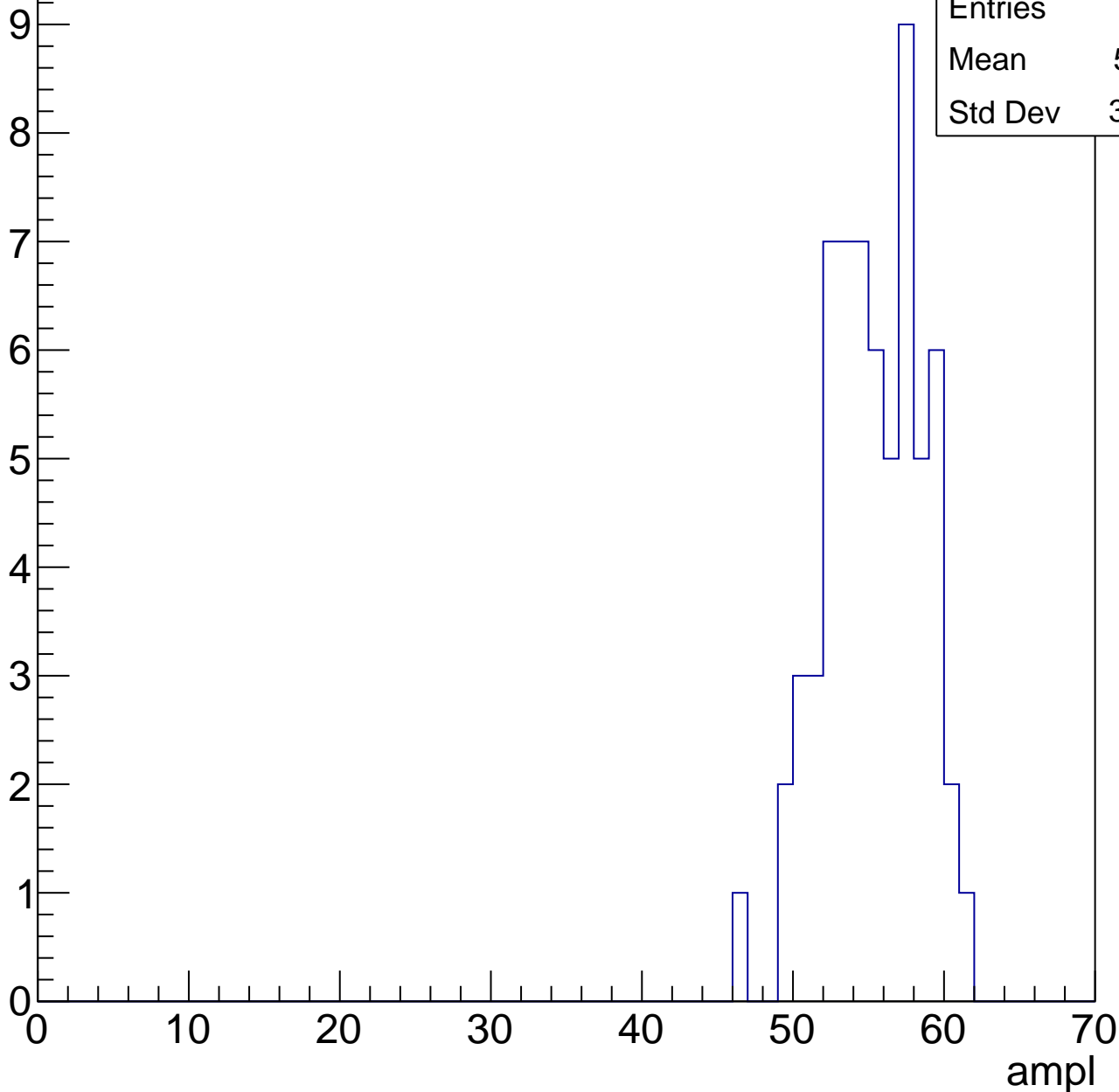


# B1L000S, U25-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	54.81
Std Dev	3.162

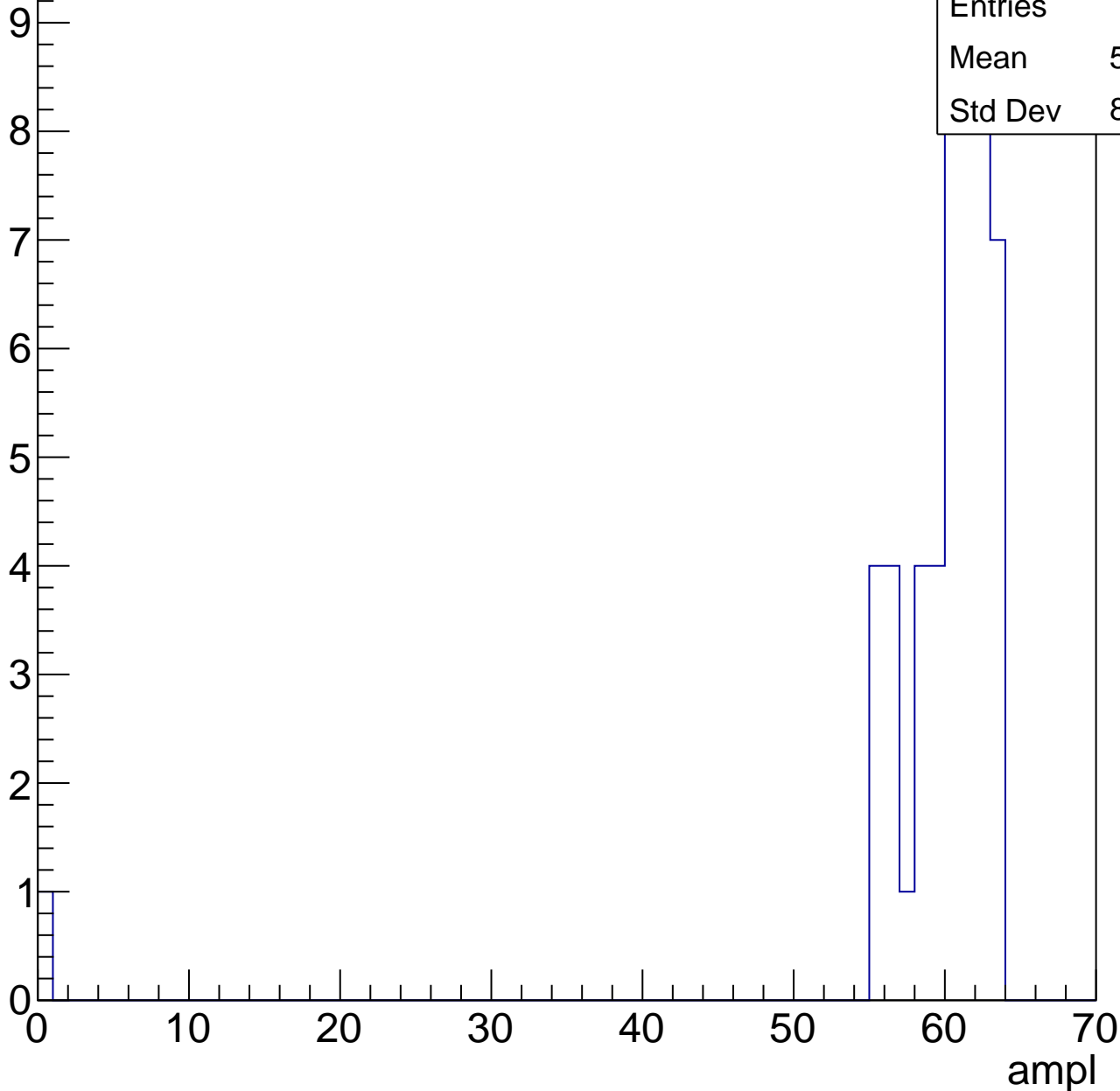


# B1L000S, U25-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

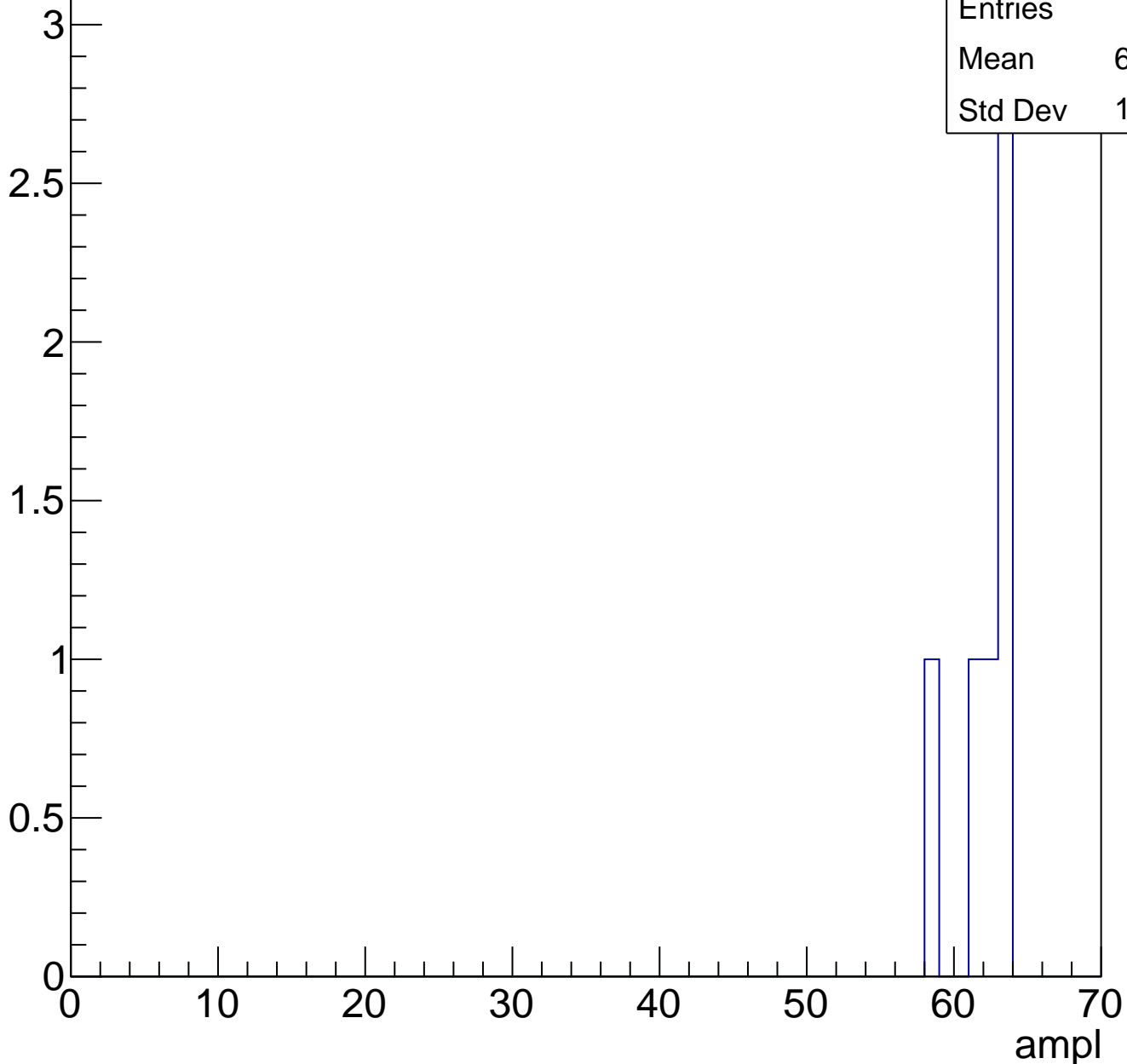
Entries	50
Mean	58.72
Std Dev	8.739



# B1L000S, U25-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch4, adc0

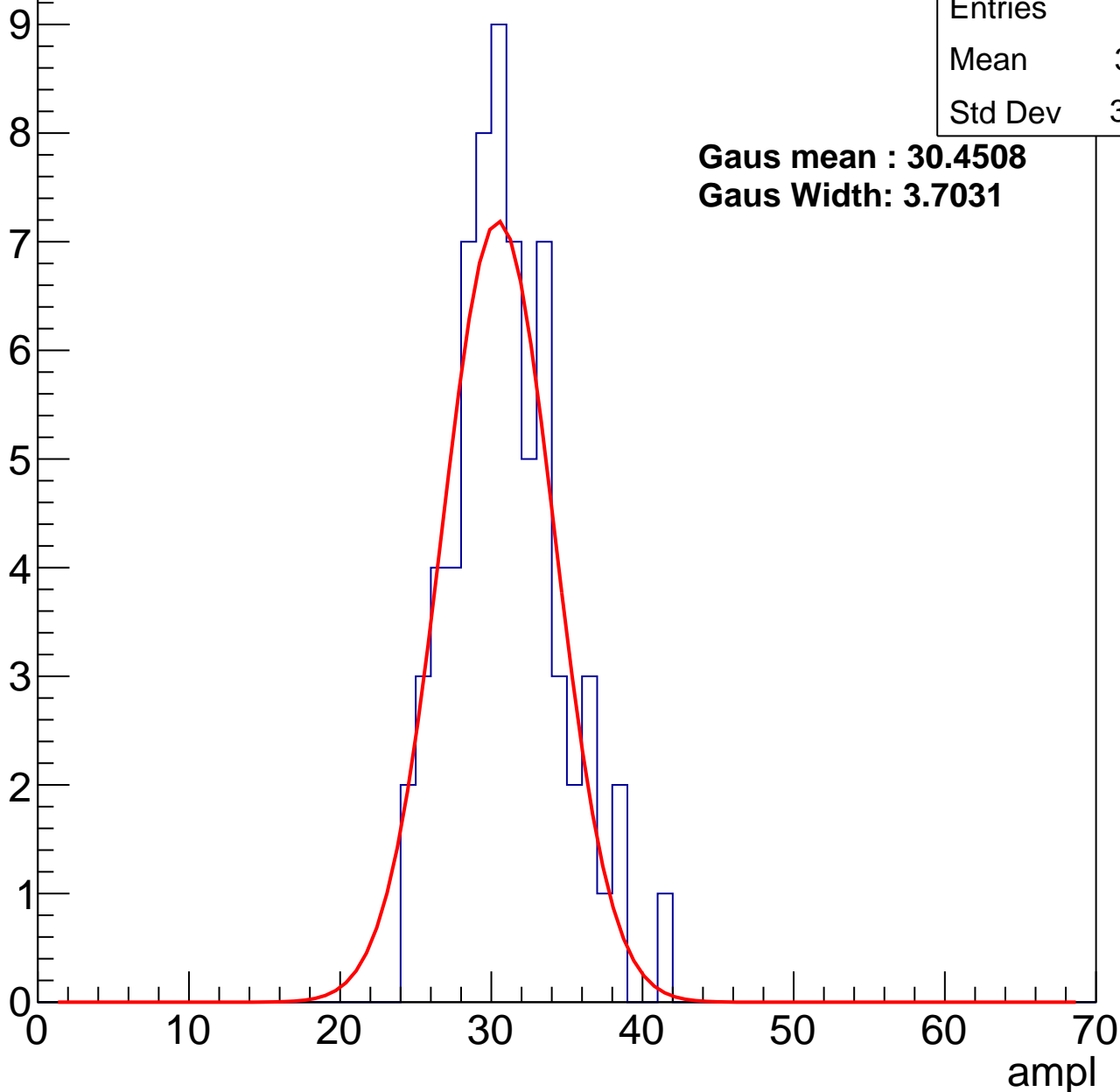
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	30.51
Std Dev	3.554

**Gaus mean : 30.4508**

**Gaus Width: 3.7031**



# B1L000S, U25-ch4, adc1

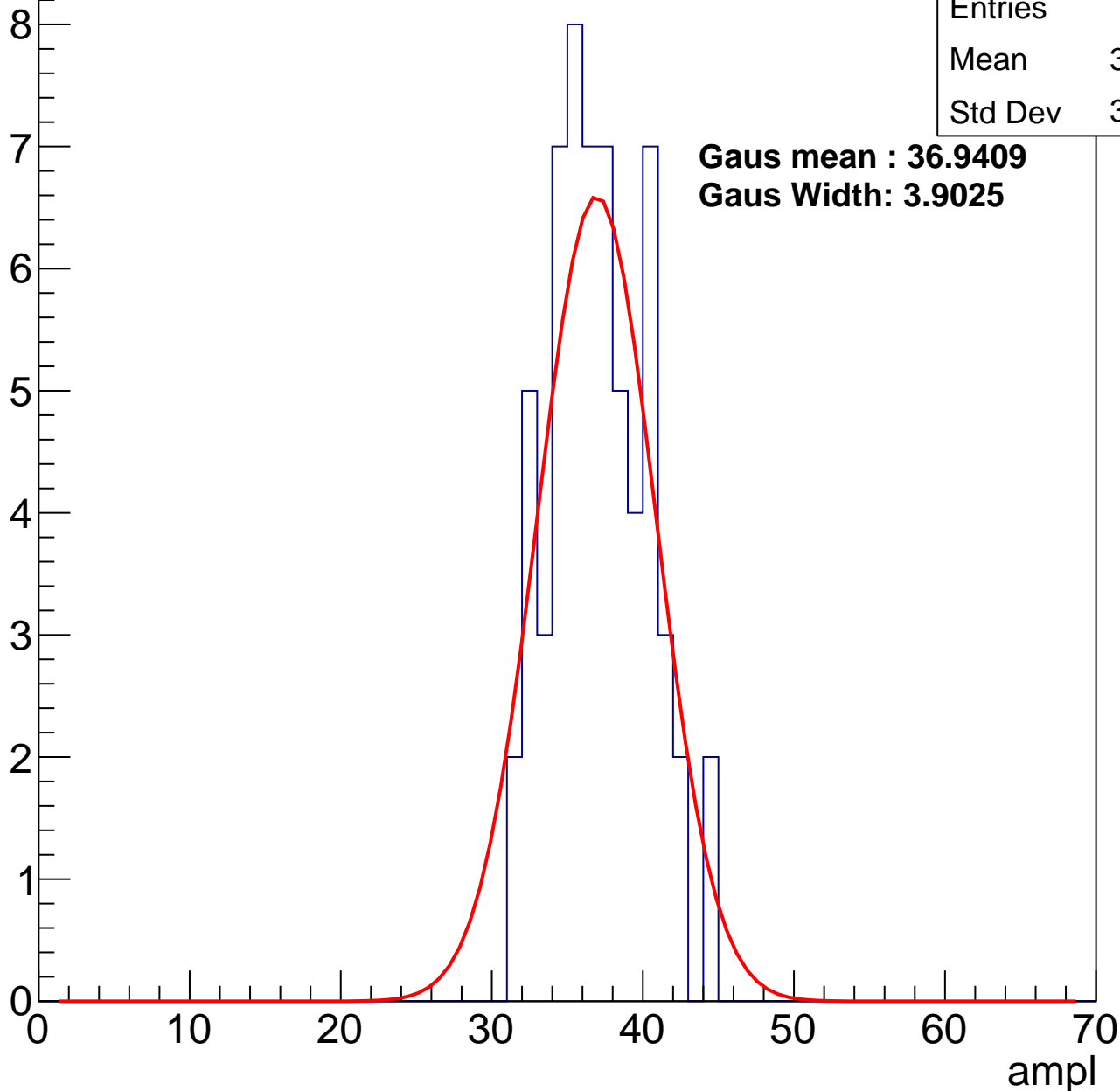
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.63
Std Dev	3.158

**Gaus mean : 36.9409**

**Gaus Width: 3.9025**



# B1L000S, U25-ch4, adc2

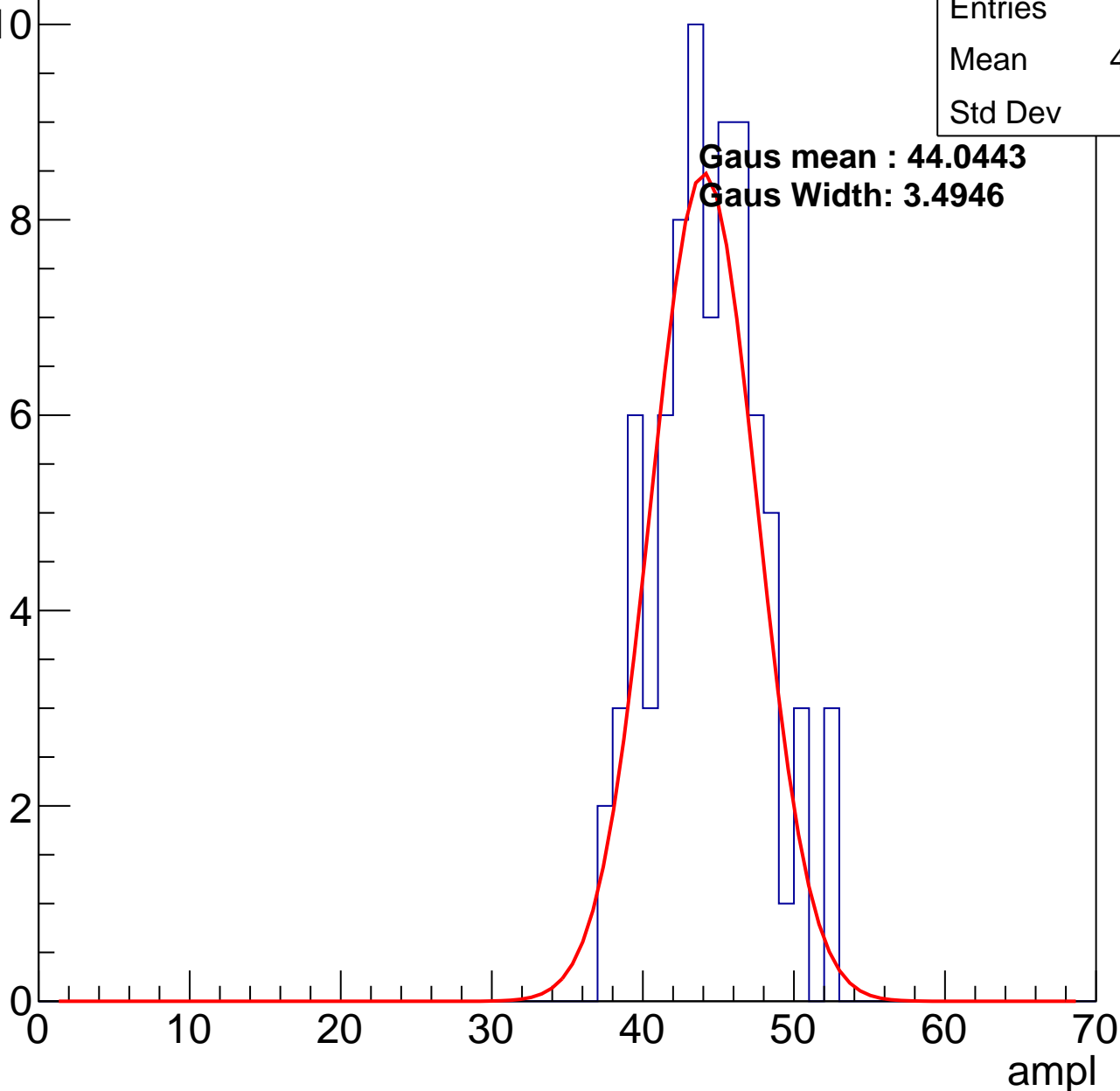
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	43.93
Std Dev	3.52

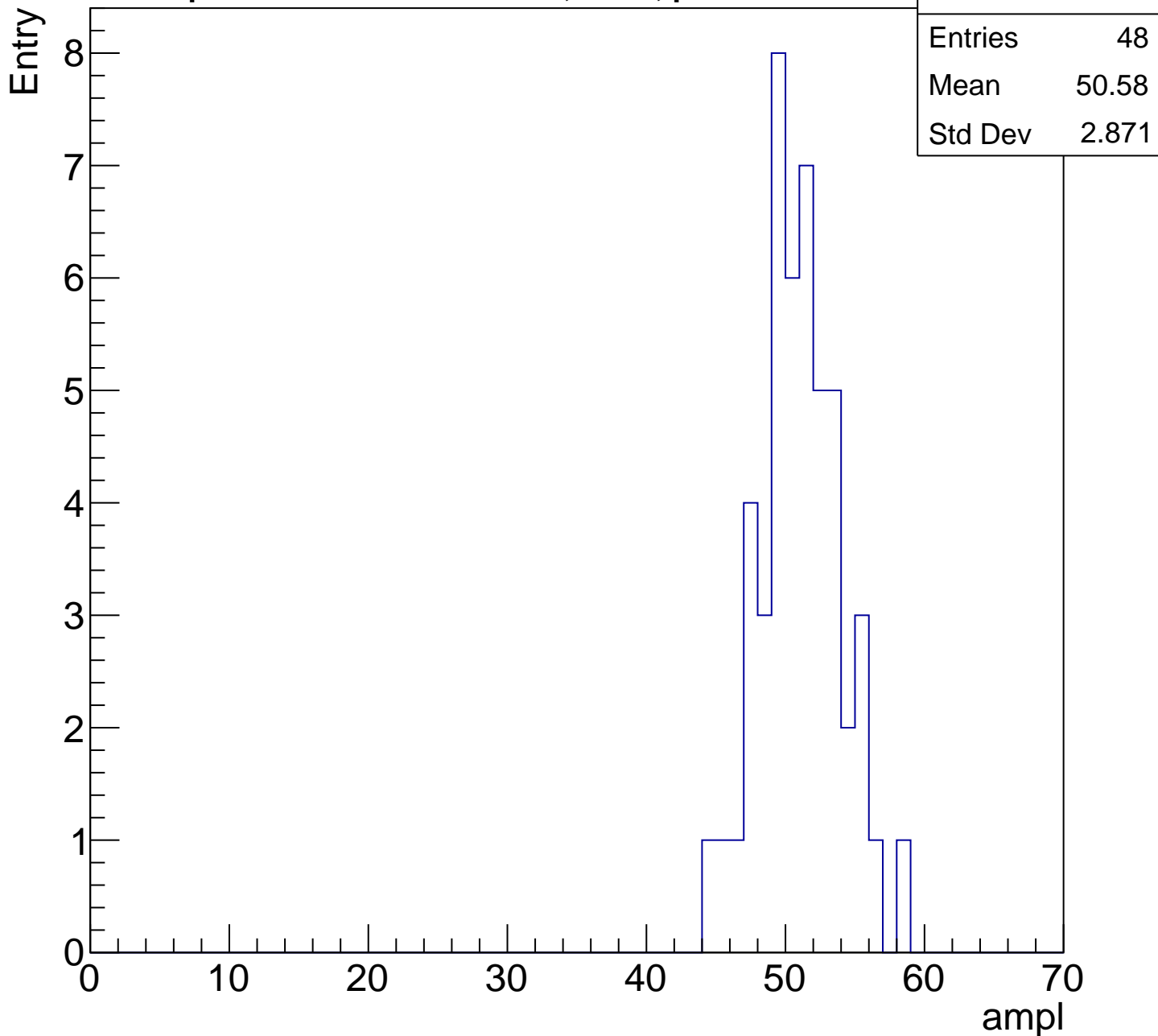
**Gaus mean : 44.0443**

**Gaus Width: 3.4946**



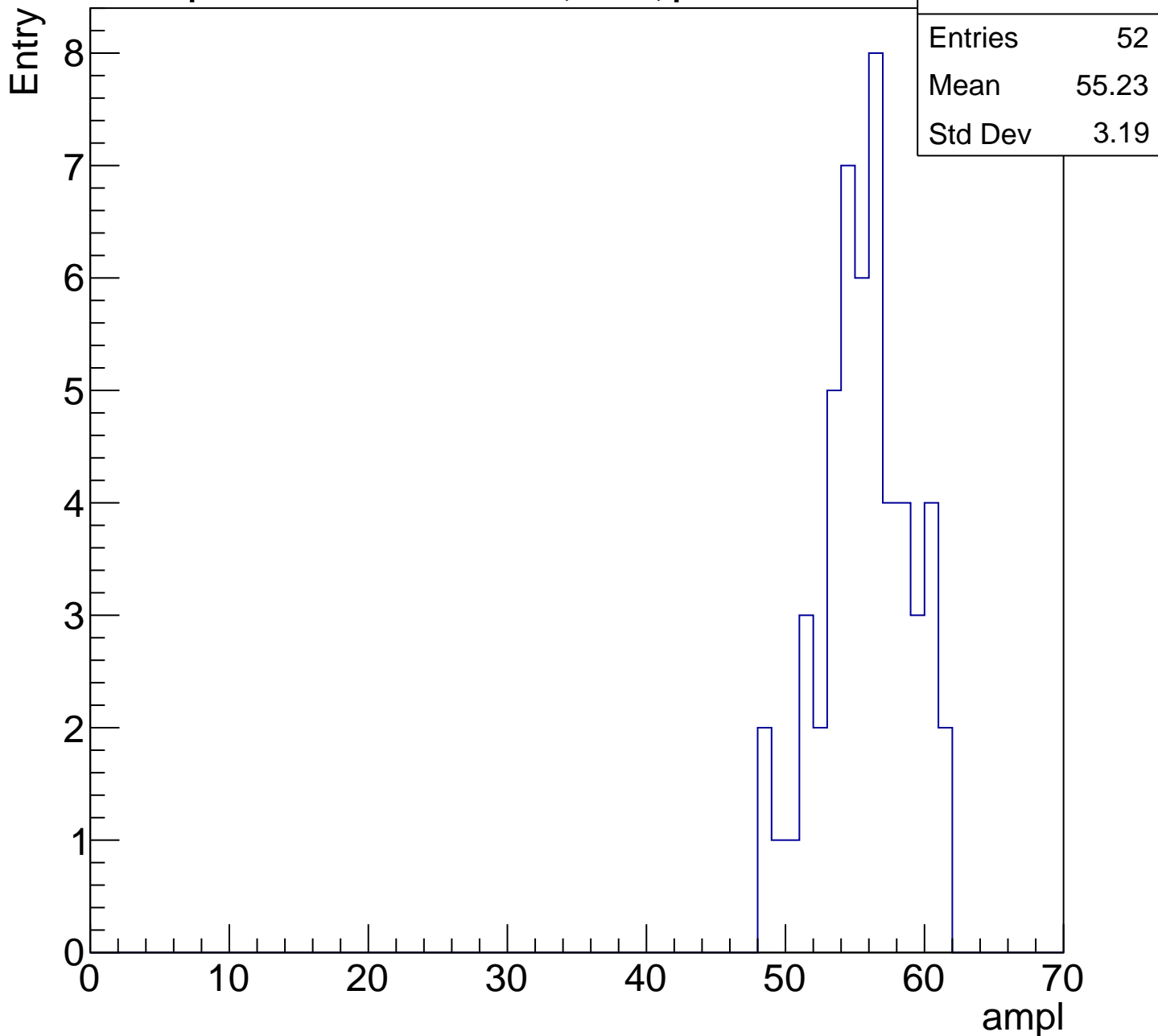
# B1L000S, U25-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.65
Std Dev	2.209

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

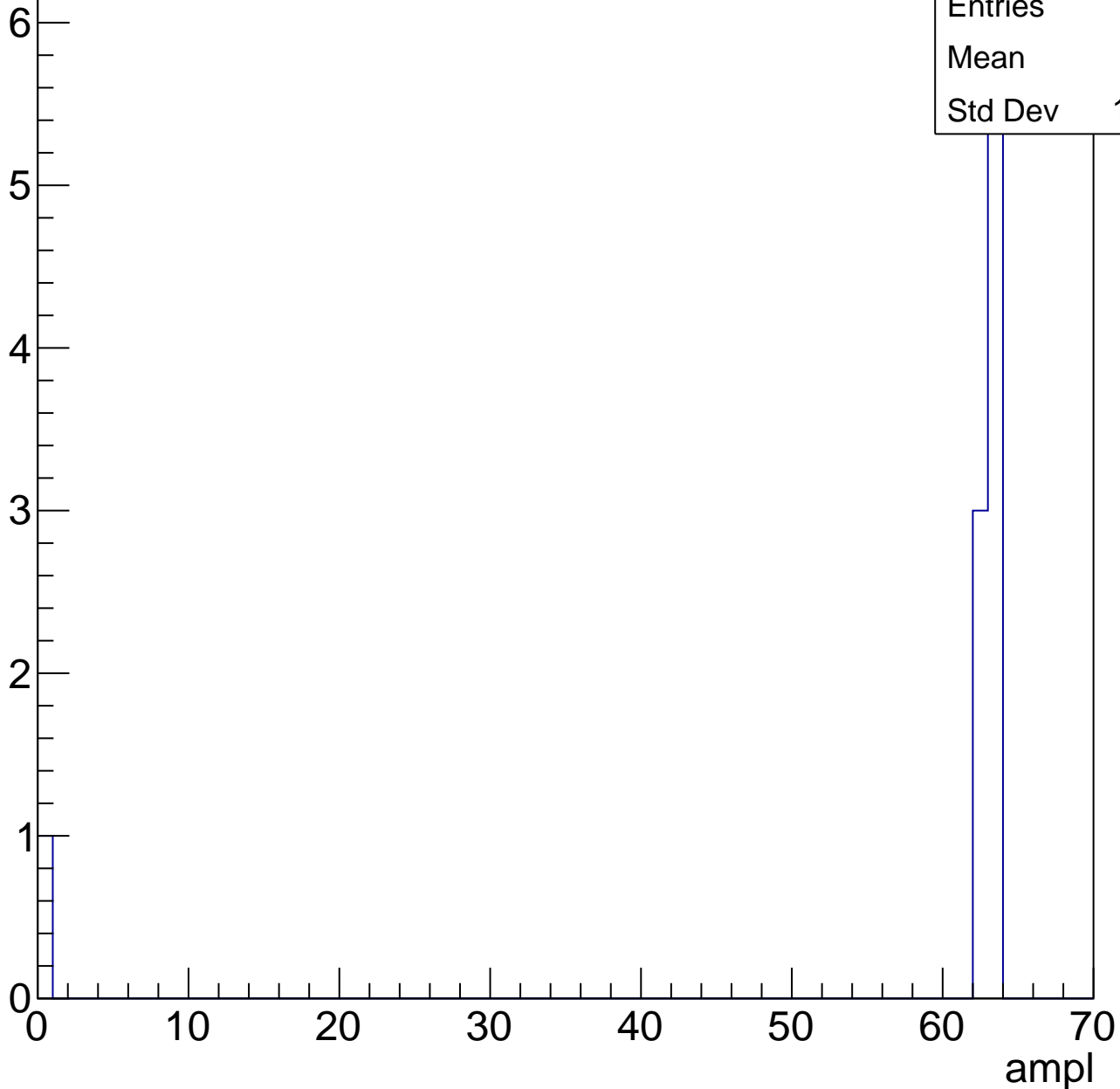
8

# B1L000S, U25-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	10
Mean	56.4
Std Dev	18.81





# B1L000S, U25-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch5, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	29.93
Std Dev	3.191

**Gaus mean : 30.6081**

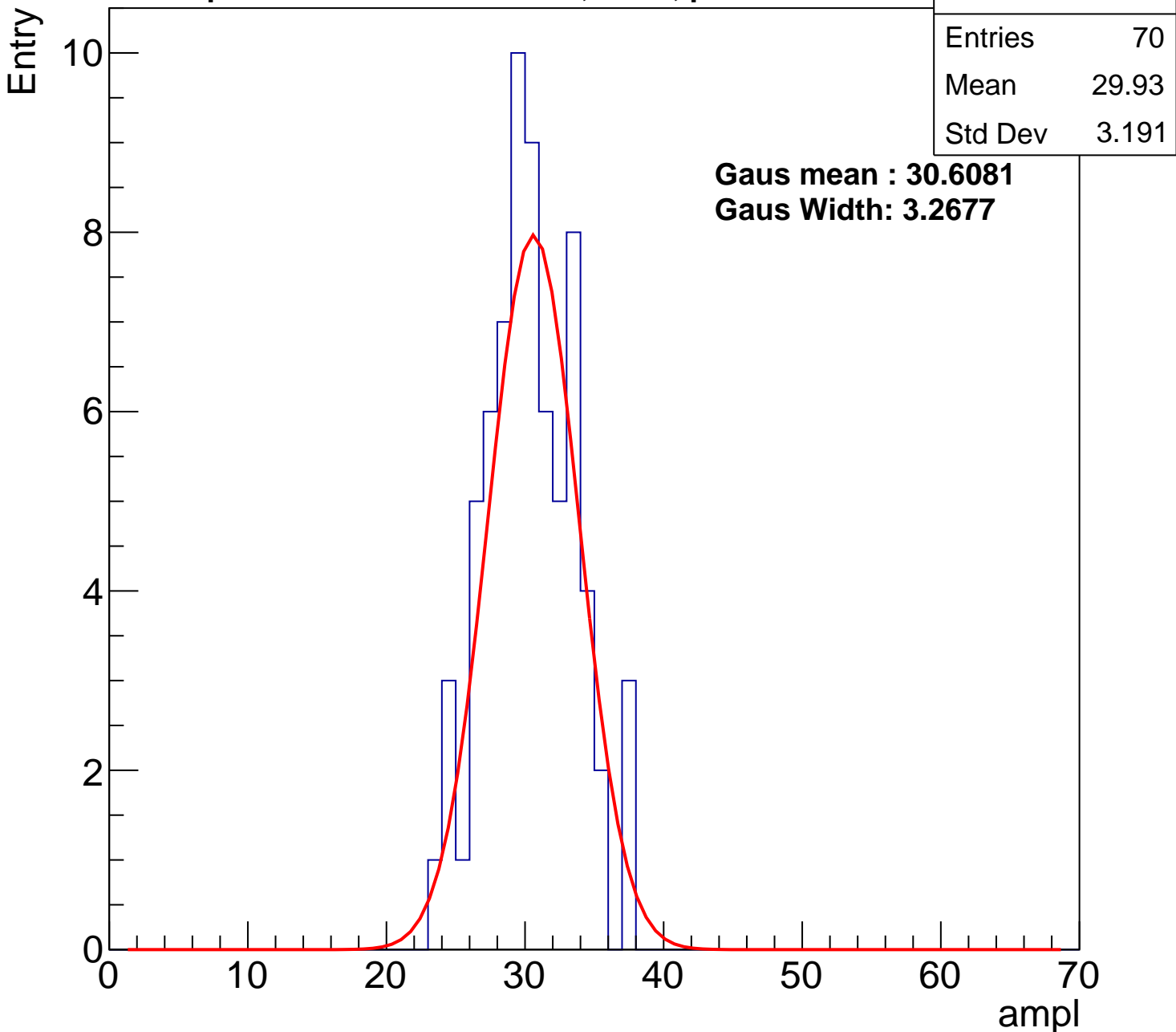
**Gaus Width: 3.2677**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch5, adc1

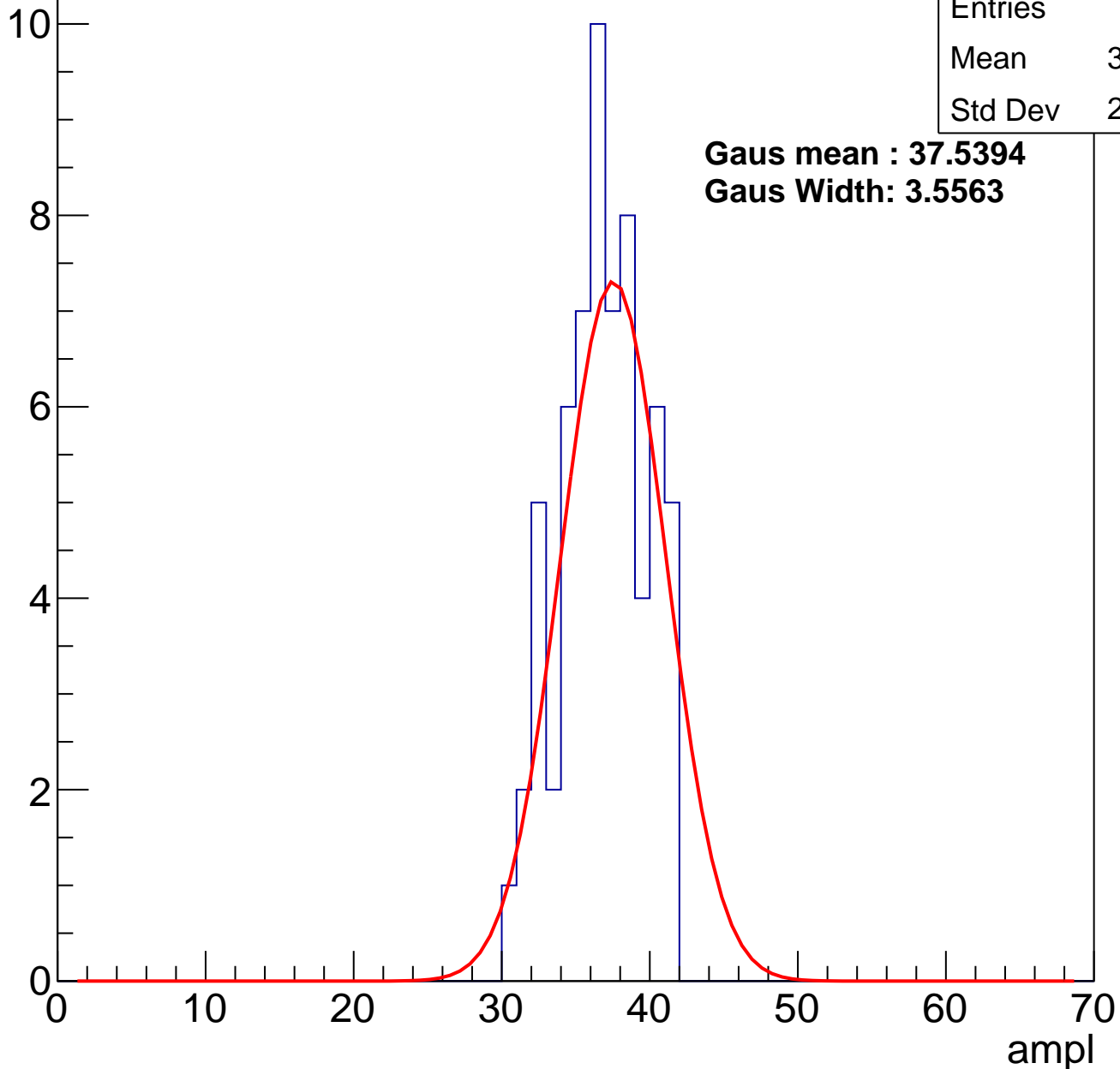
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	36.37
Std Dev	2.825

**Gaus mean : 37.5394**

**Gaus Width: 3.5563**

Entry



# B1L000S, U25-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	43.39
Std Dev	3.164

**Gaus mean : 43.8462**

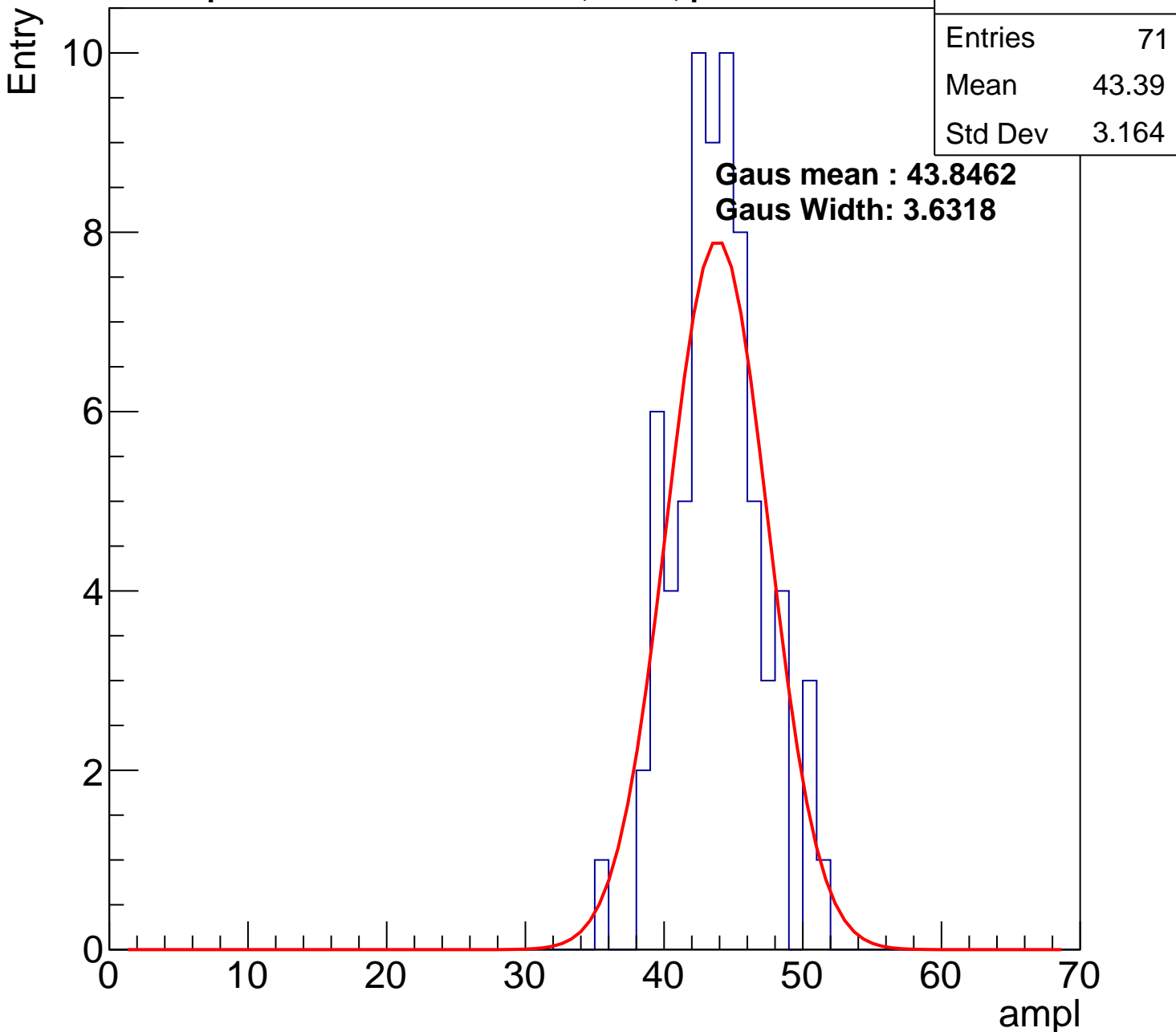
**Gaus Width: 3.6318**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

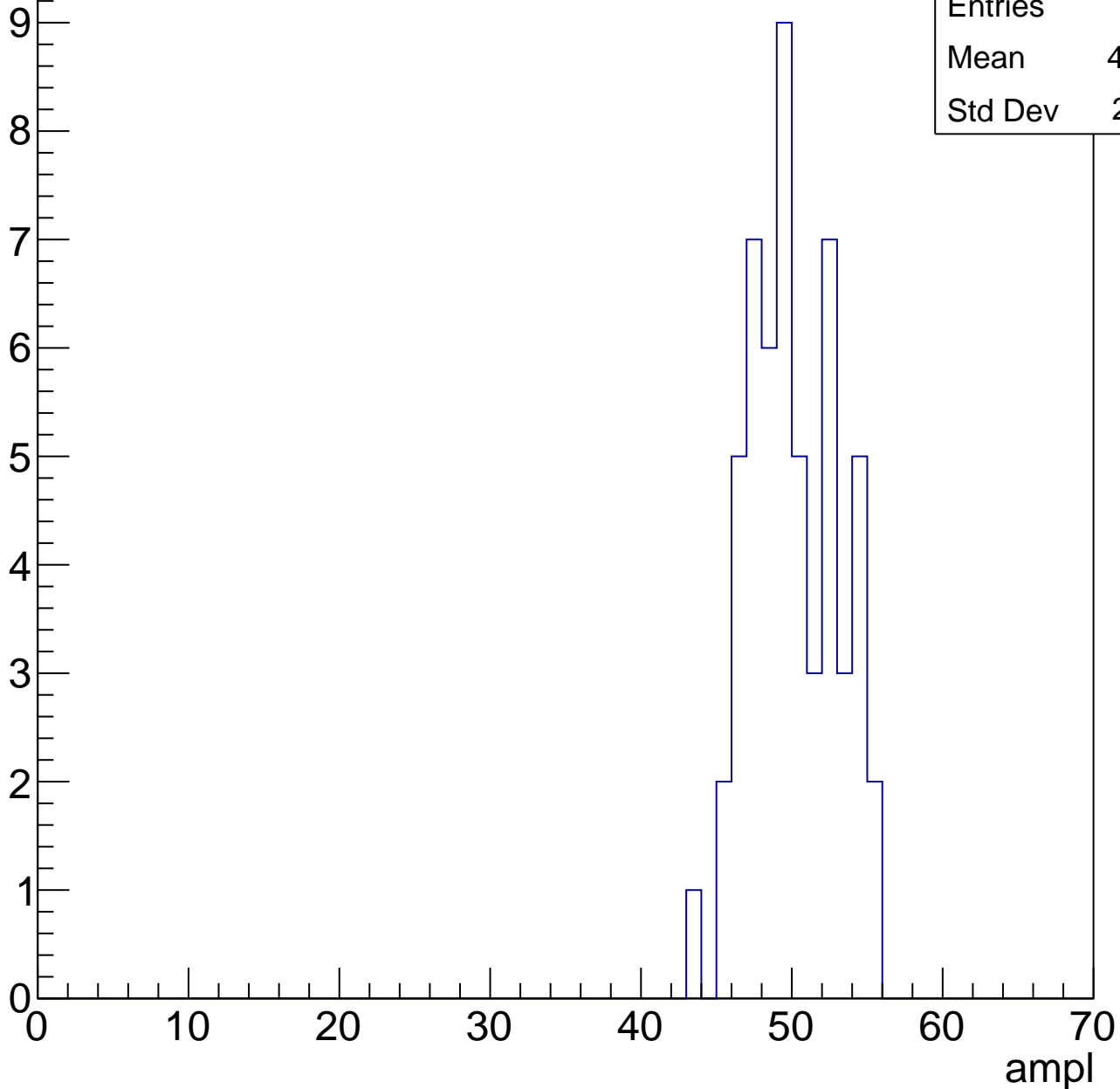


# B1L000S, U25-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	49.58
Std Dev	2.871



# B1L000S, U25-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

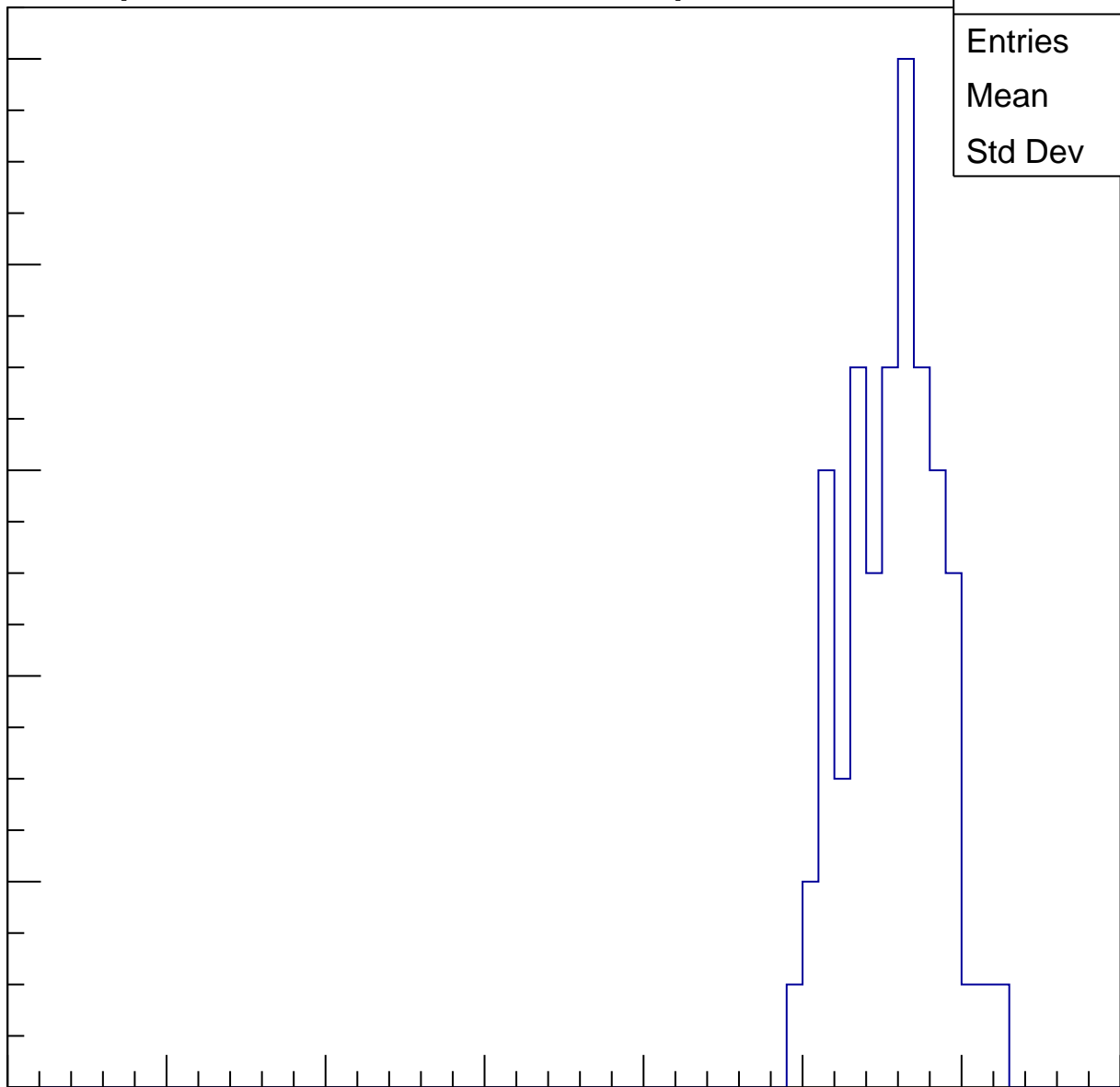
Entries	62
Mean	55.19
Std Dev	2.901

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch5, adc5

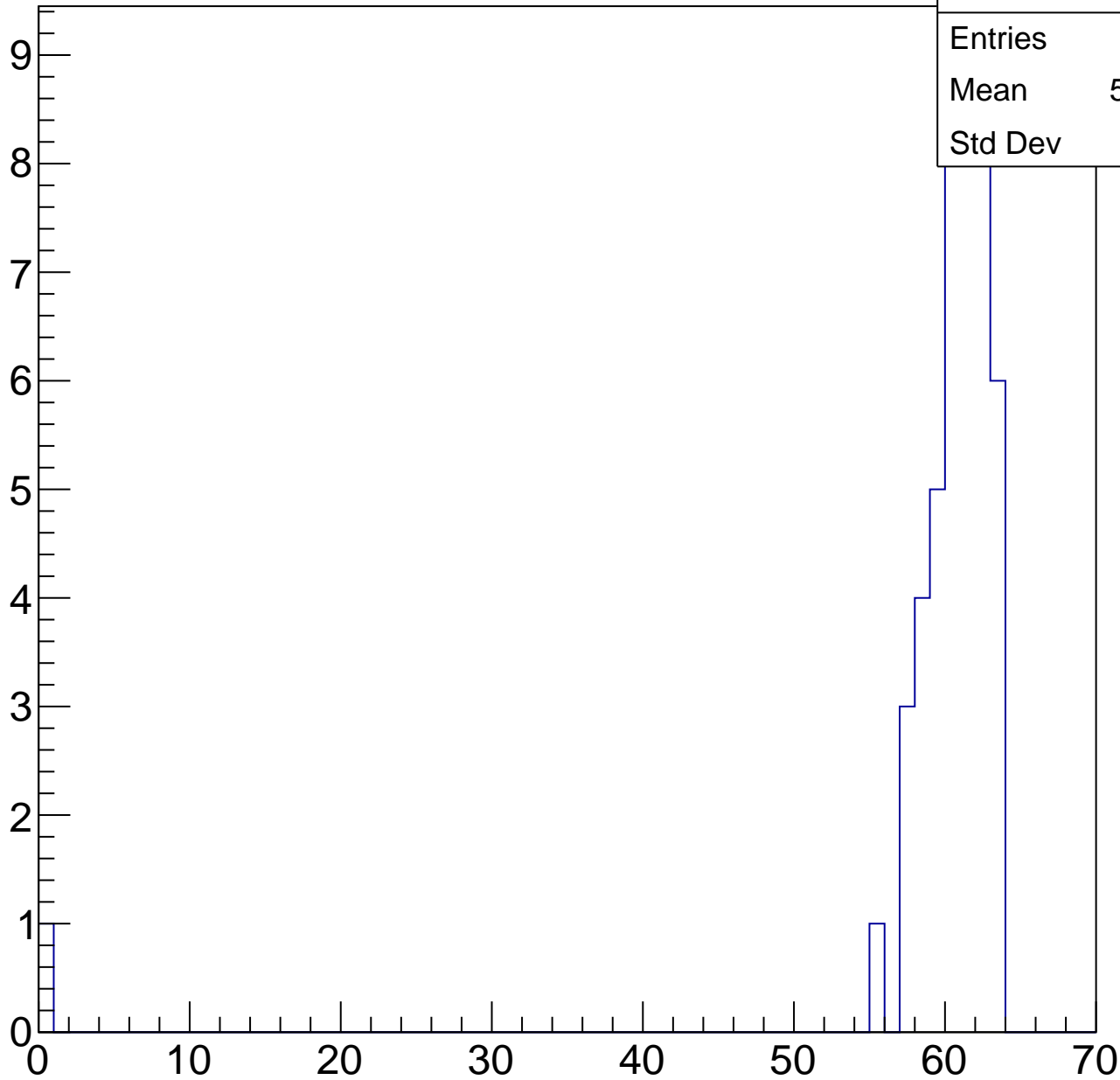
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	59.04
Std Dev	9

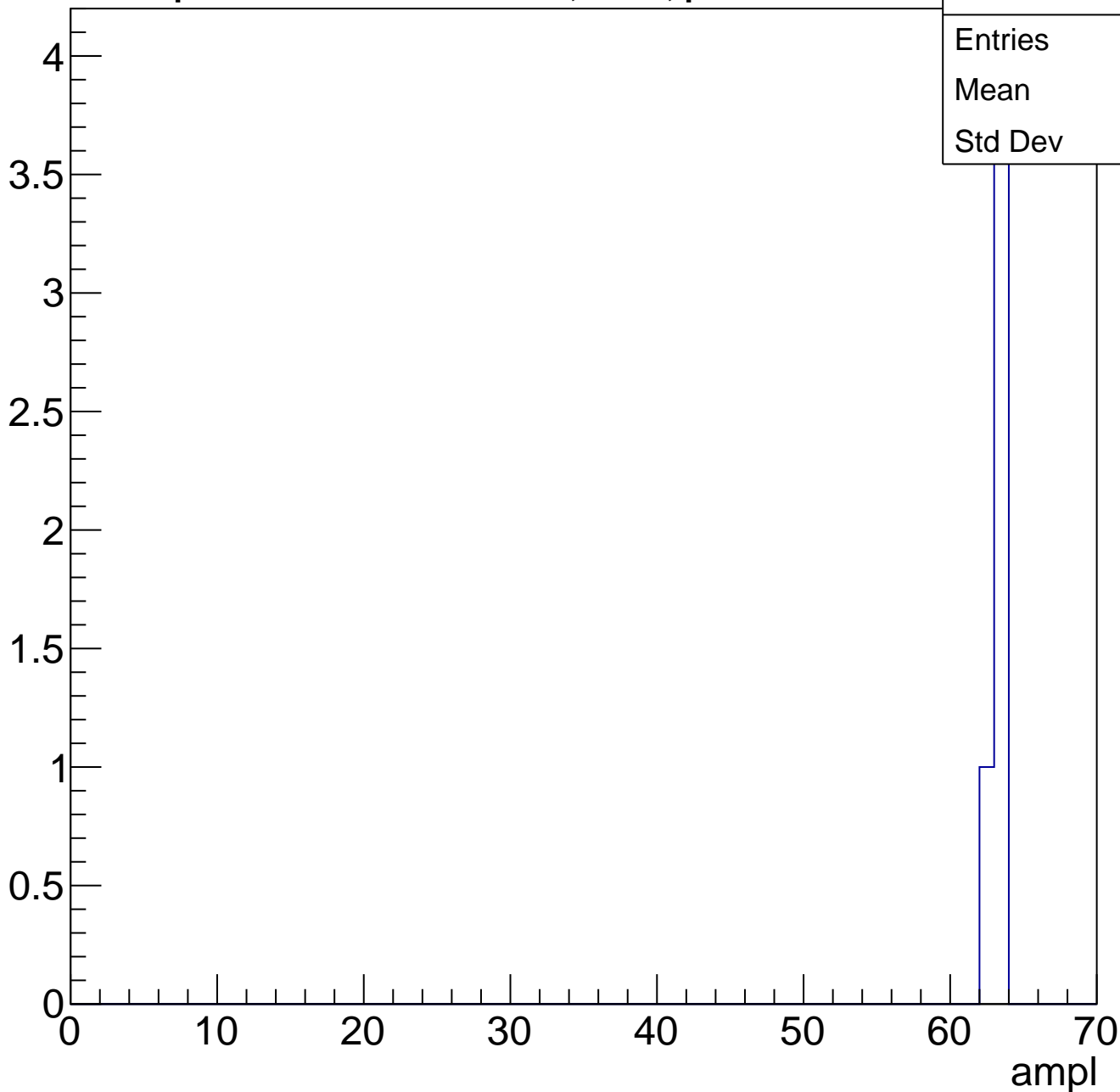
ampl



# B1L000S, U25-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U25-ch6, adc0

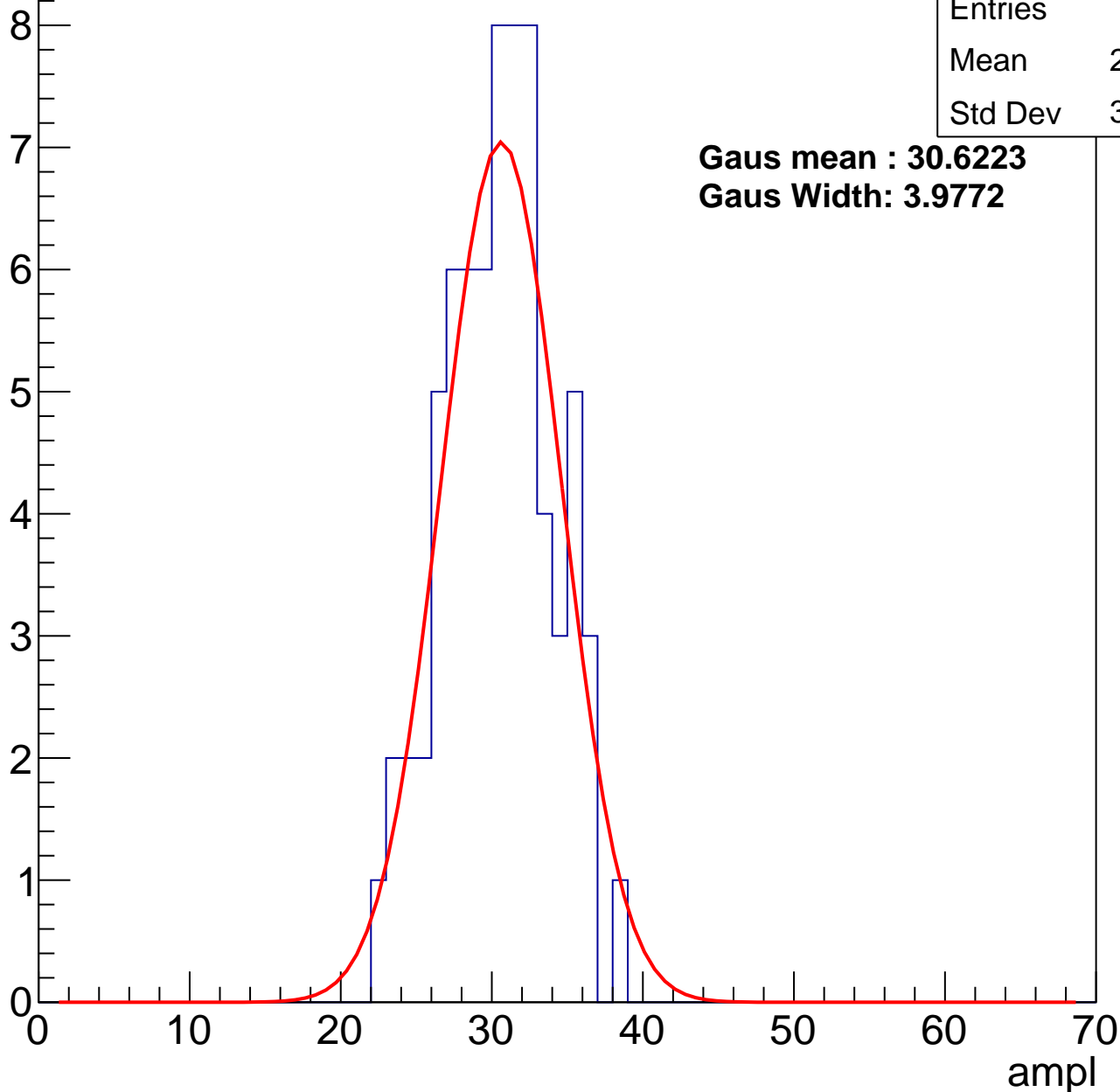
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	29.99
Std Dev	3.523

**Gaus mean : 30.6223**

**Gaus Width: 3.9772**



# B1L000S, U25-ch6, adc1

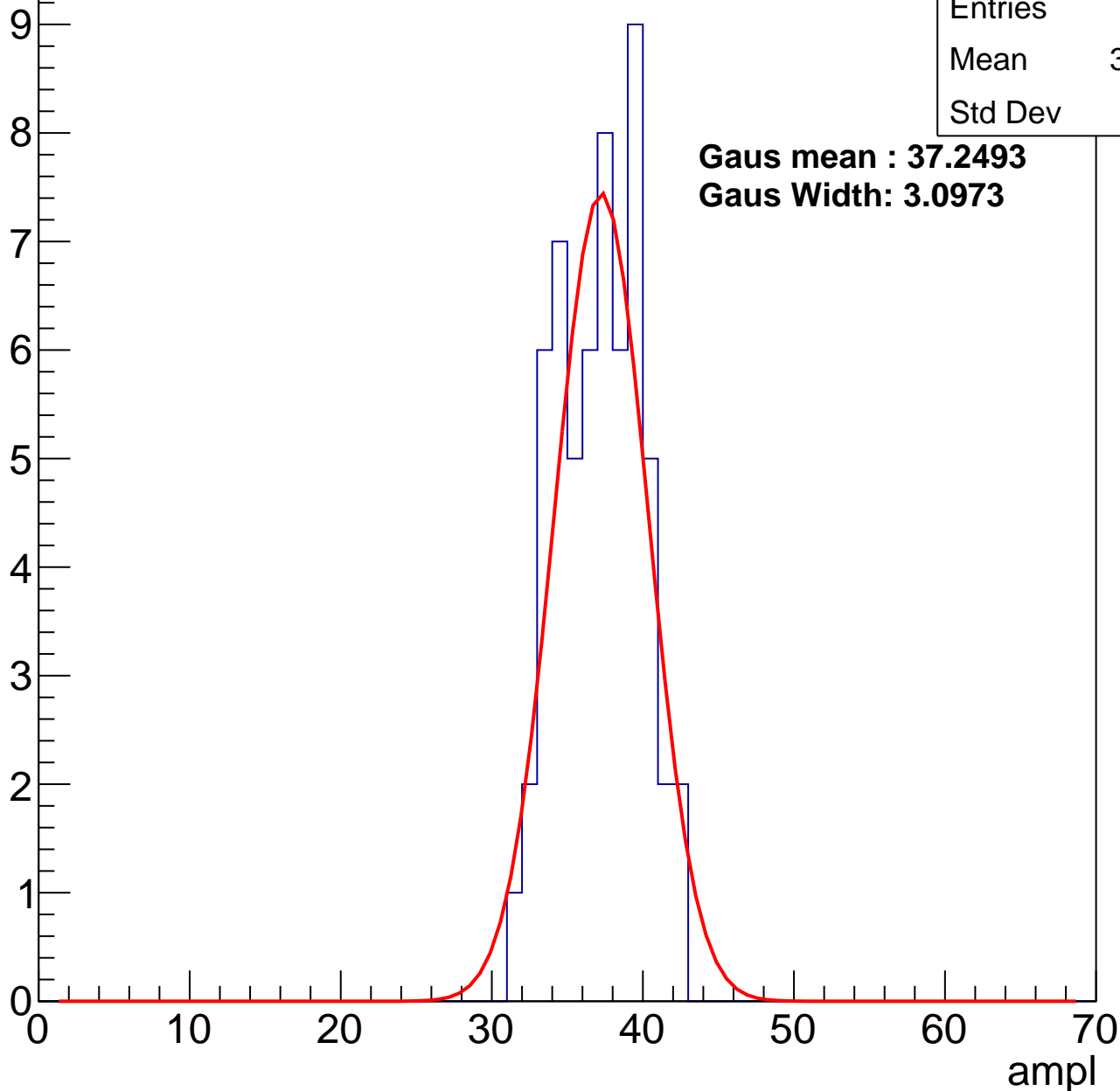
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	36.66
Std Dev	2.71

**Gaus mean : 37.2493**

**Gaus Width: 3.0973**



# B1L000S, U25-ch6, adc2

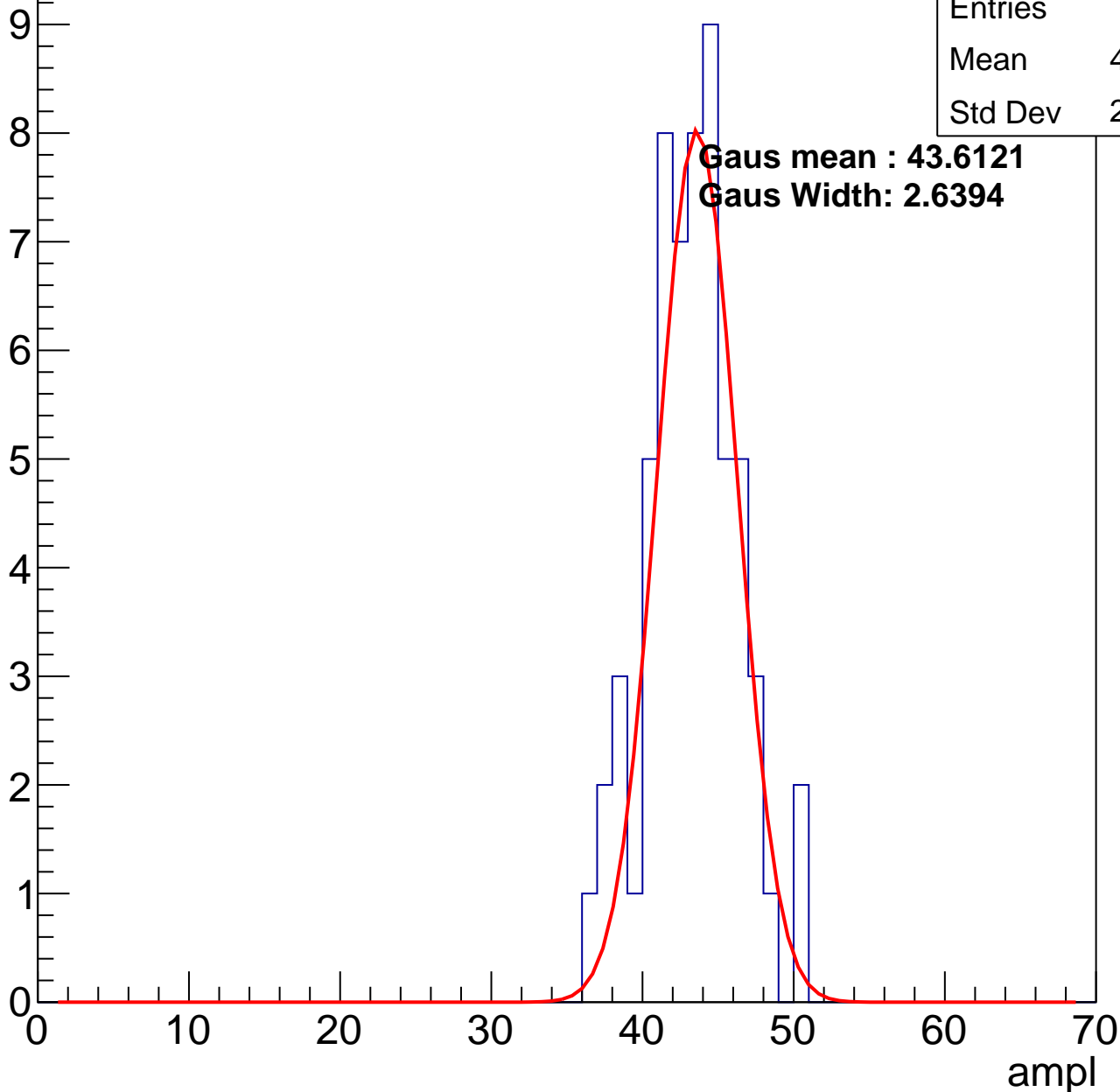
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.82
Std Dev	2.997

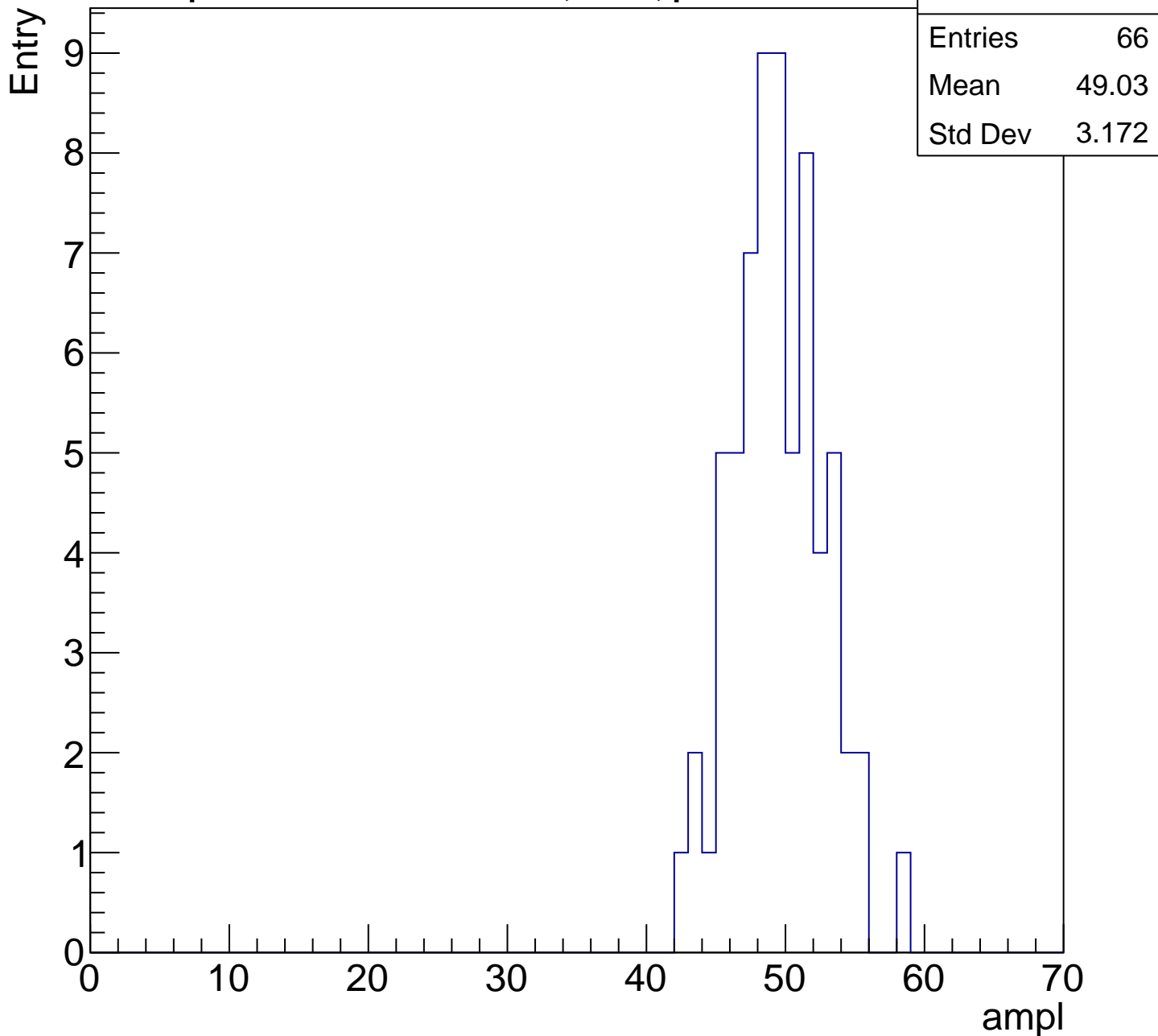
**Gaus mean : 43.6121**

**Gaus Width: 2.6394**



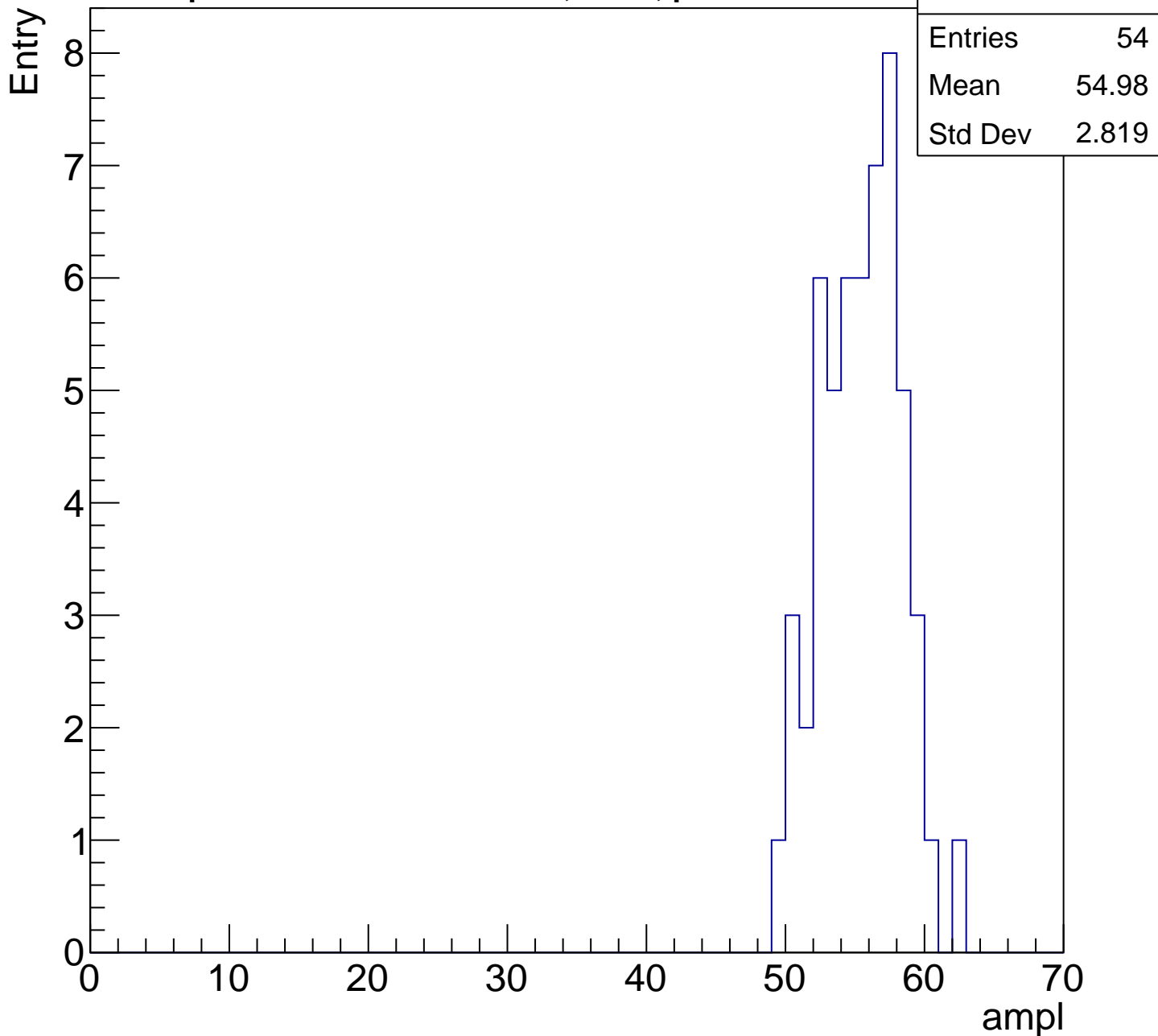
# B1L000S, U25-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.61
Std Dev	8.648

ampl

0

10

20

30

40

50

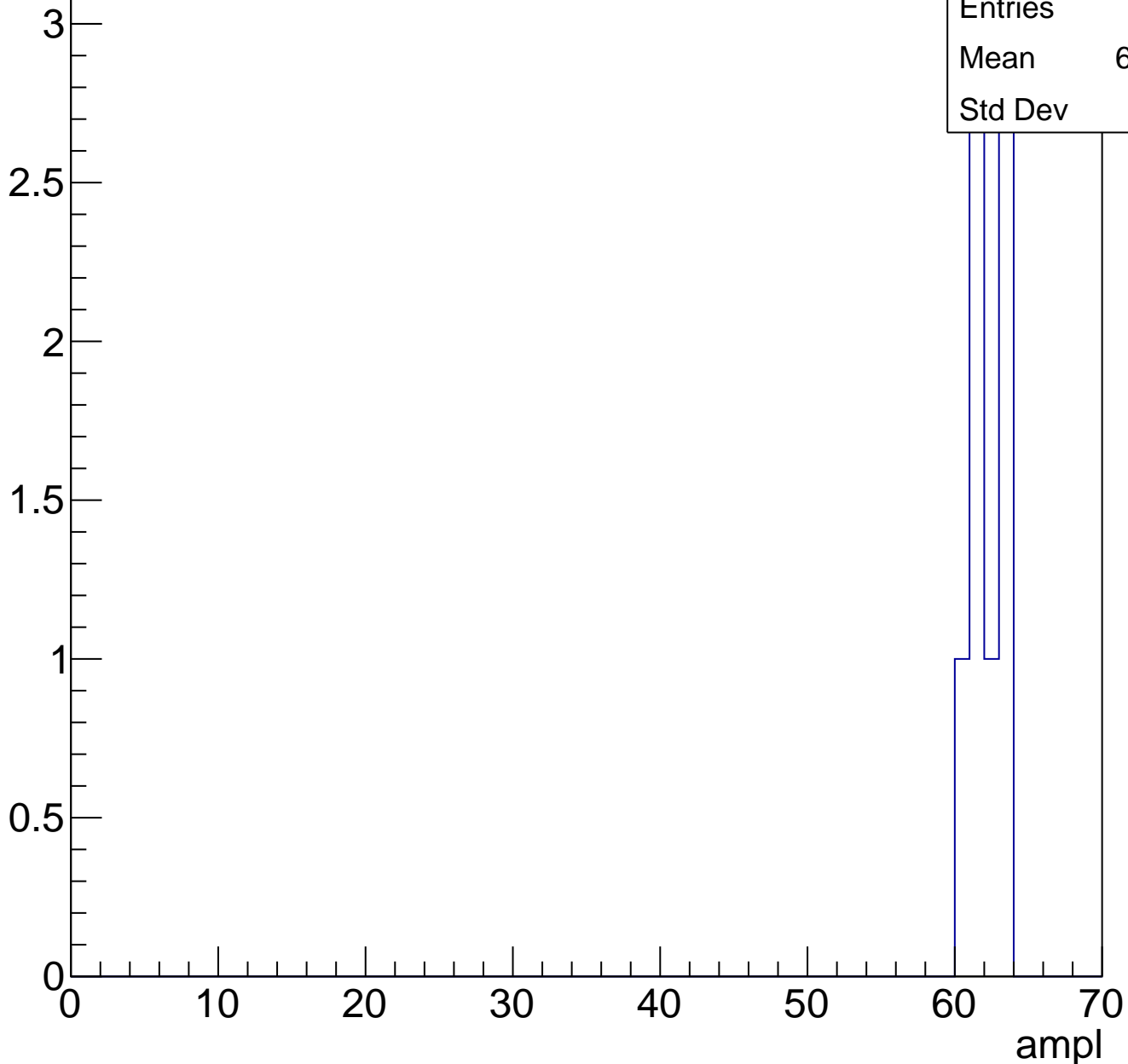
60

70

# B1L000S, U25-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	8
Mean	61.75
Std Dev	1.09



# B1L000S, U25-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch7, adc0

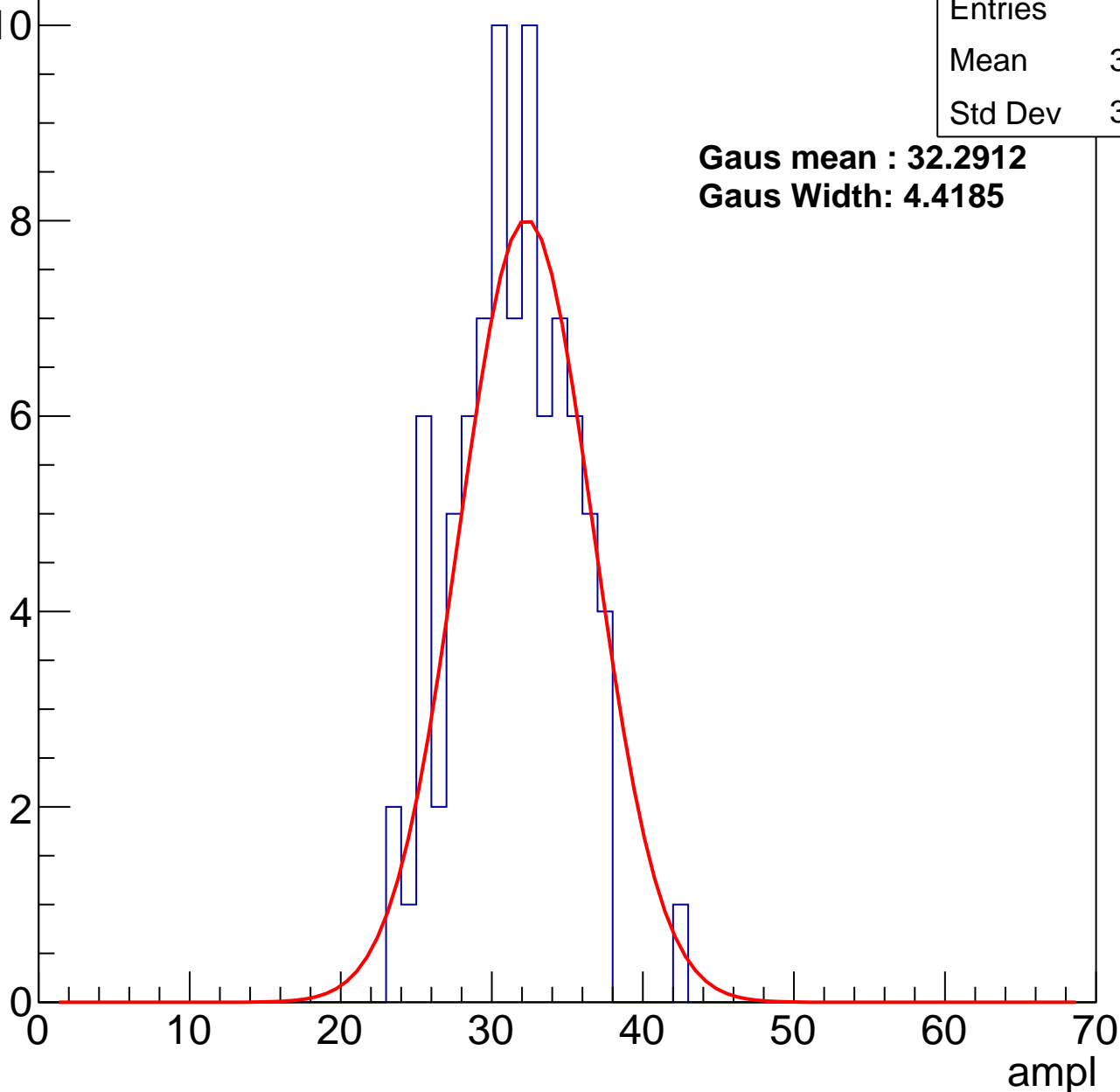
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	85
Mean	30.95
Std Dev	3.754

**Gaus mean : 32.2912**

**Gaus Width: 4.4185**



# B1L000S, U25-ch7, adc1

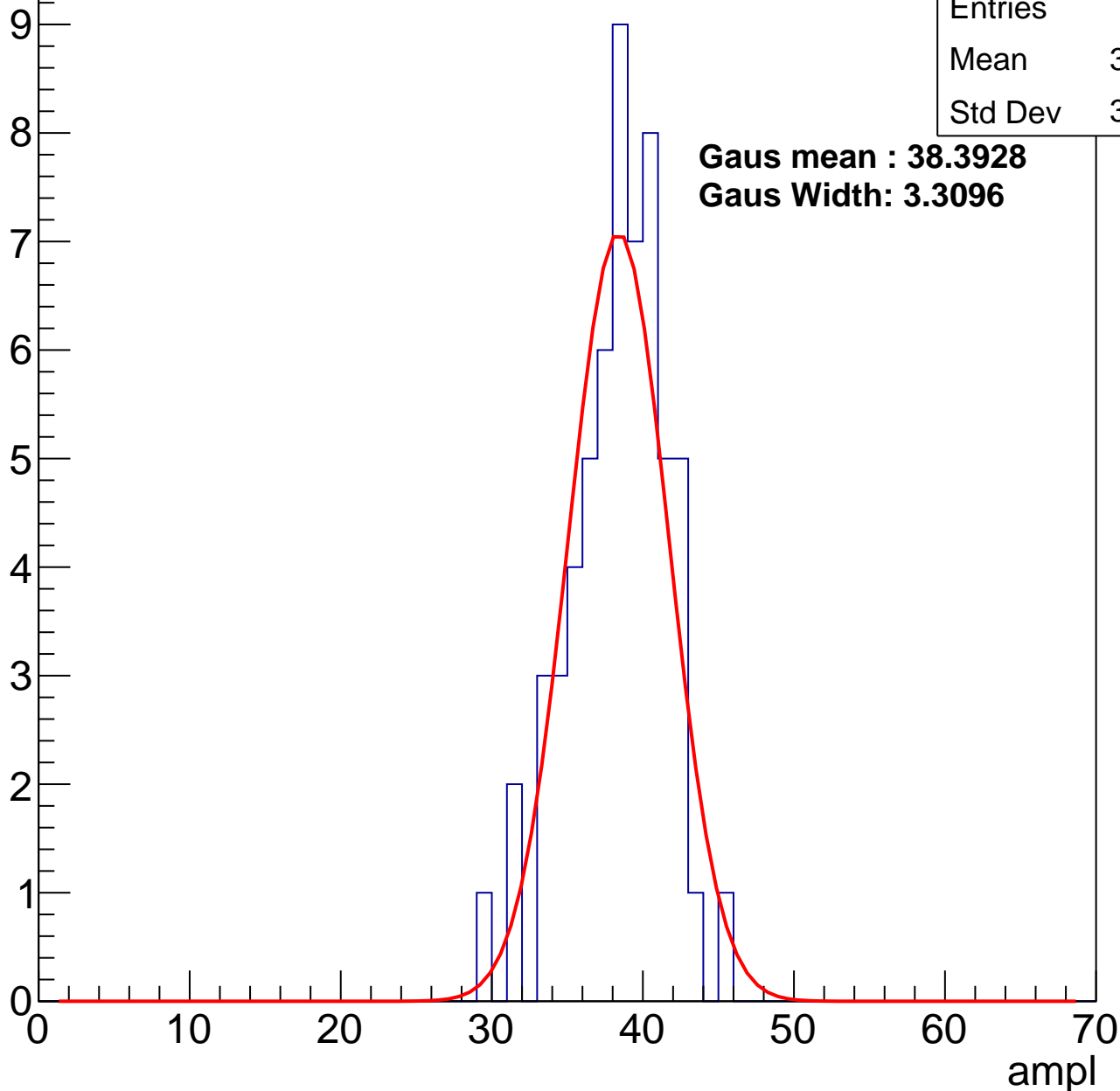
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	37.87
Std Dev	3.165

**Gaus mean : 38.3928**

**Gaus Width: 3.3096**



# B1L000S, U25-ch7, adc2

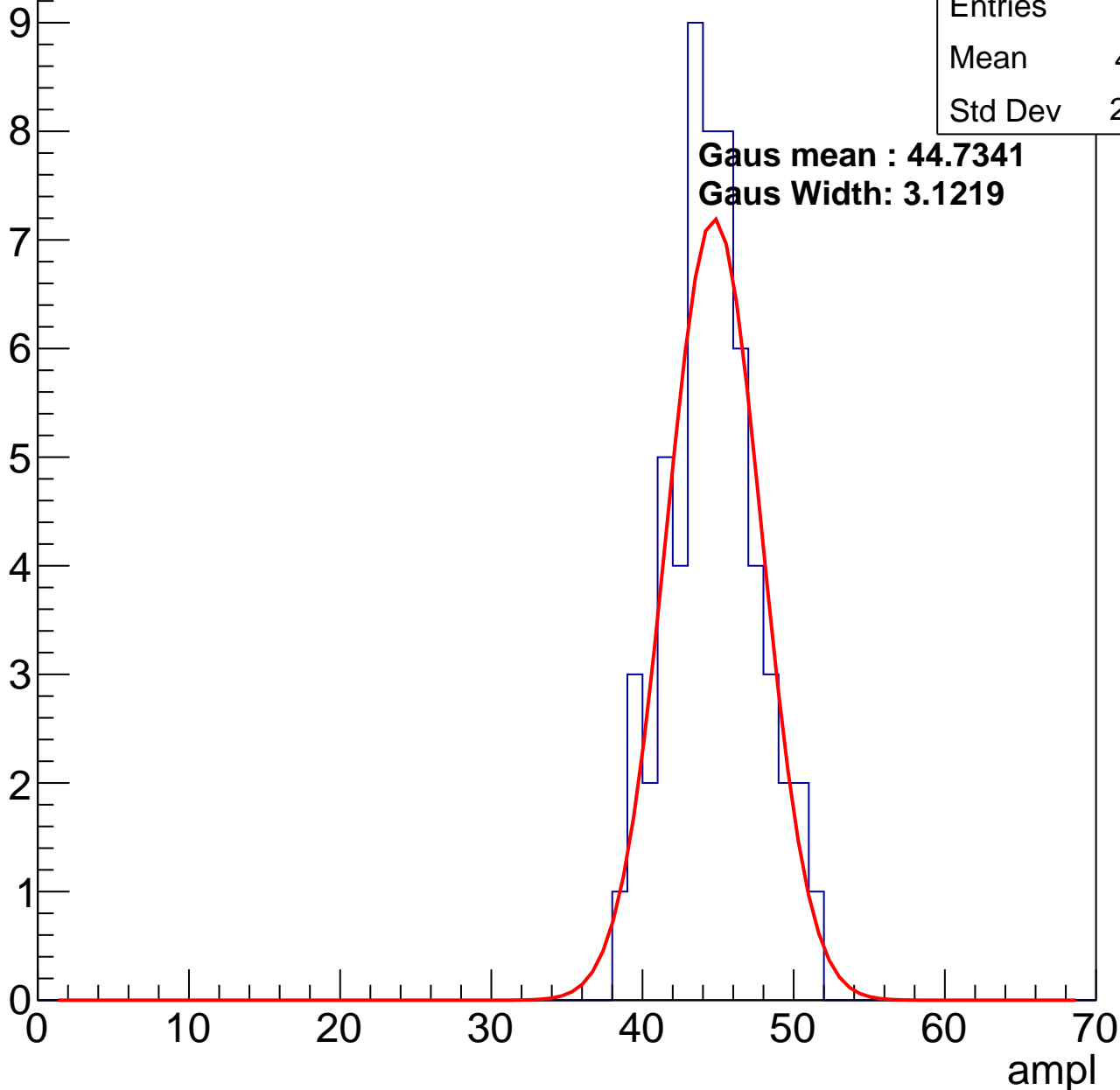
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	44.21
Std Dev	2.929

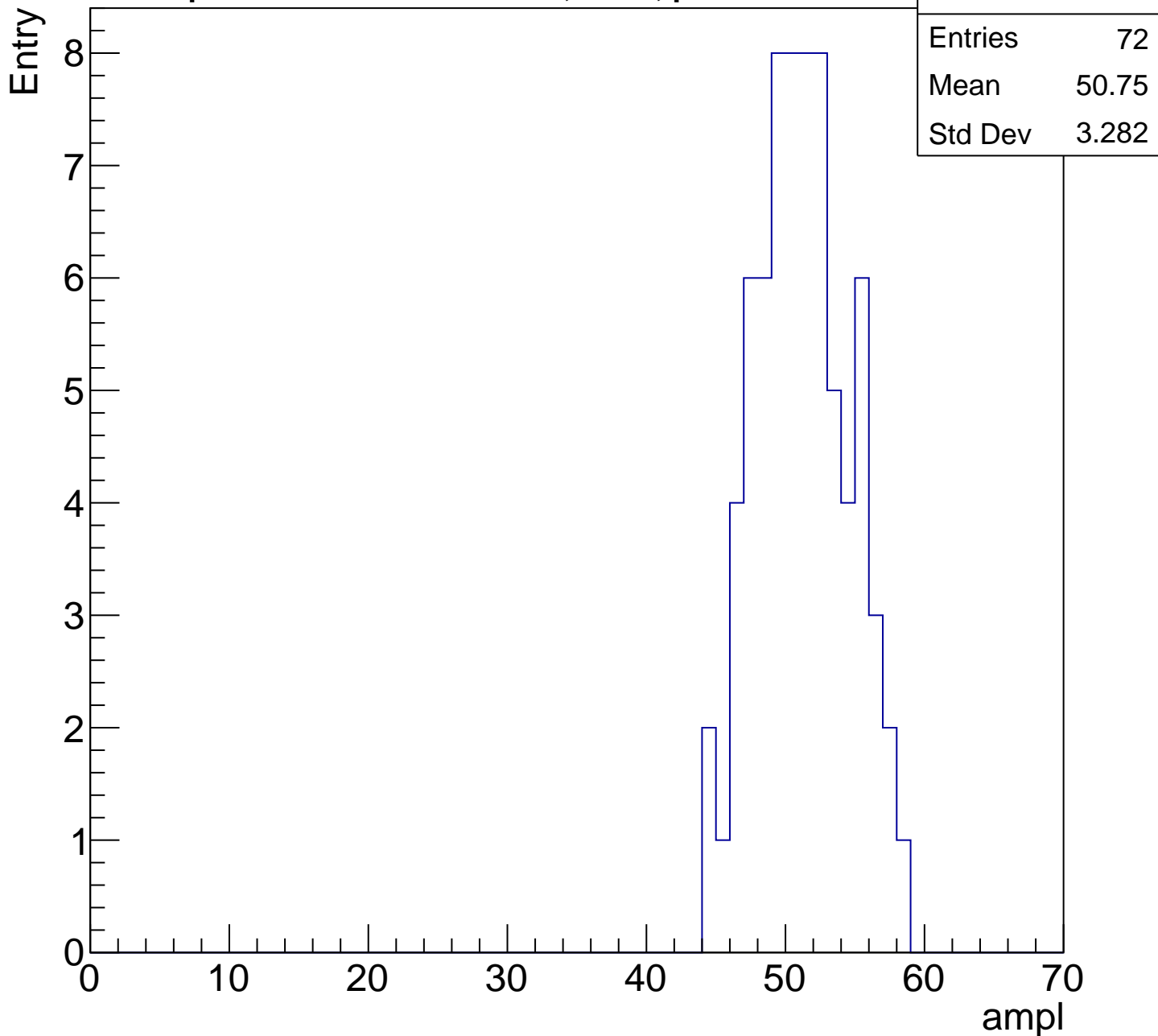
**Gaus mean : 44.7341**

**Gaus Width: 3.1219**



# B1L000S, U25-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

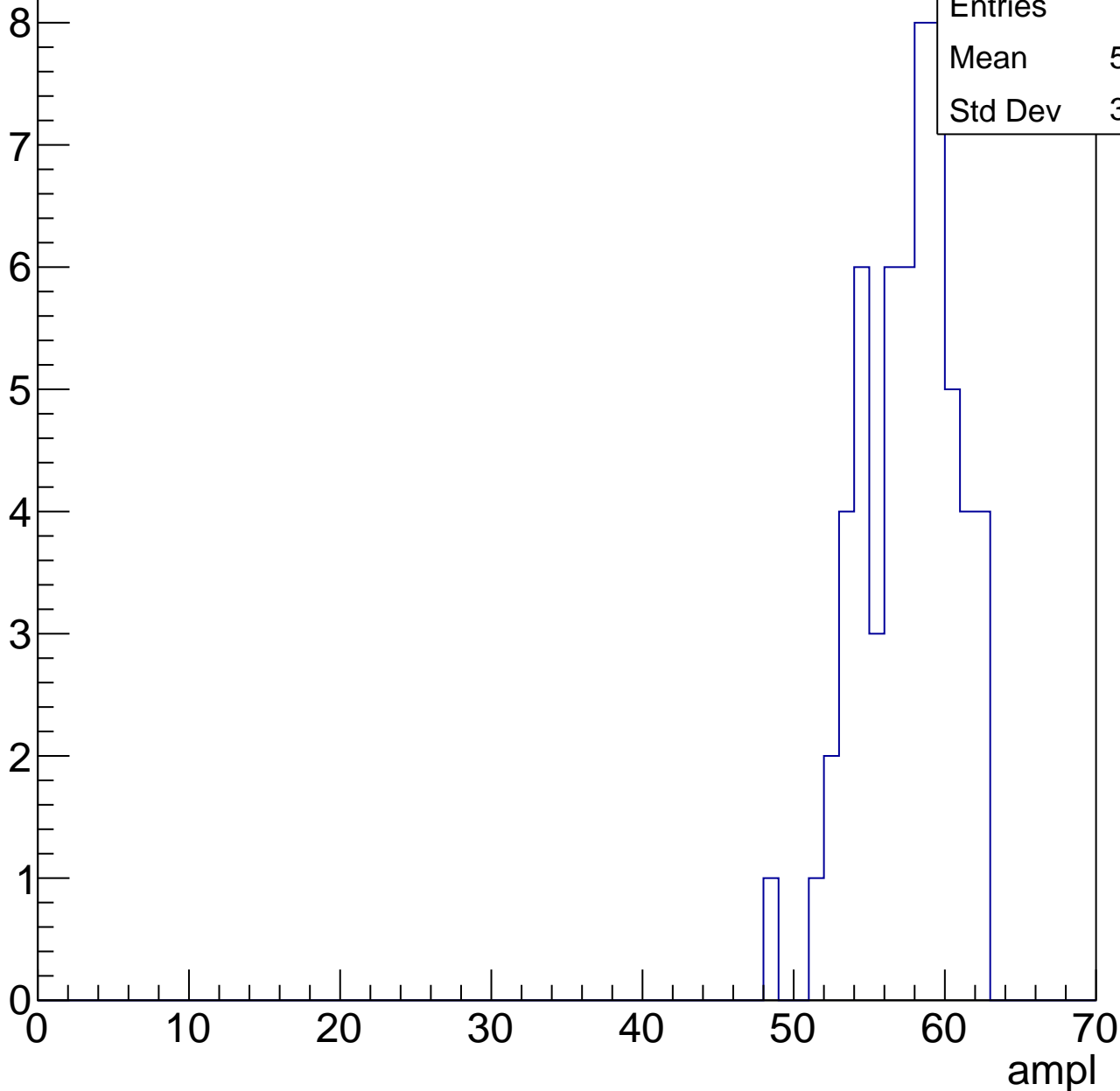


# B1L000S, U25-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	57.07
Std Dev	3.084

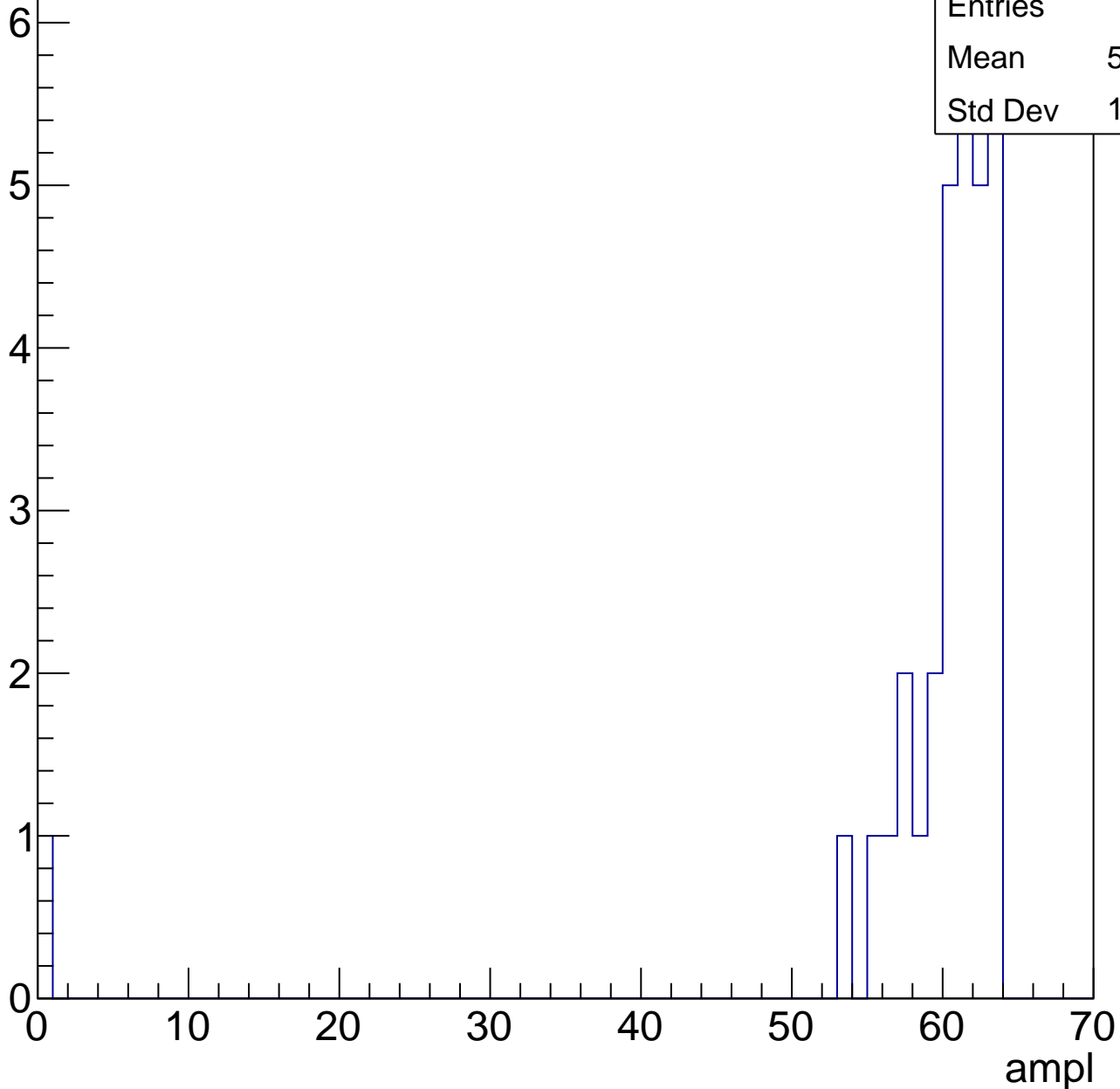


# B1L000S, U25-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

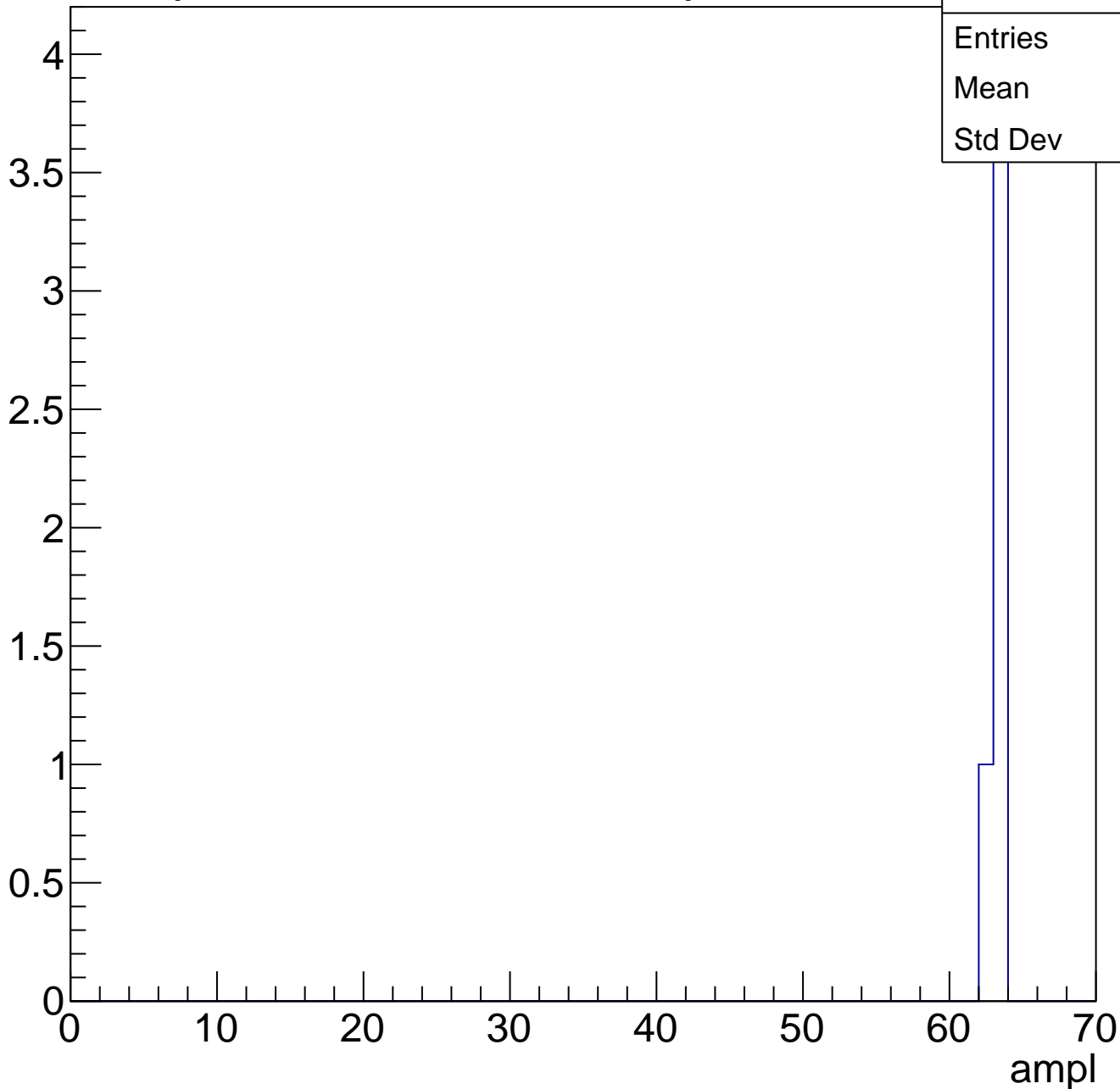
Entries	31
Mean	58.32
Std Dev	10.93



# B1L000S, U25-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch8, adc0

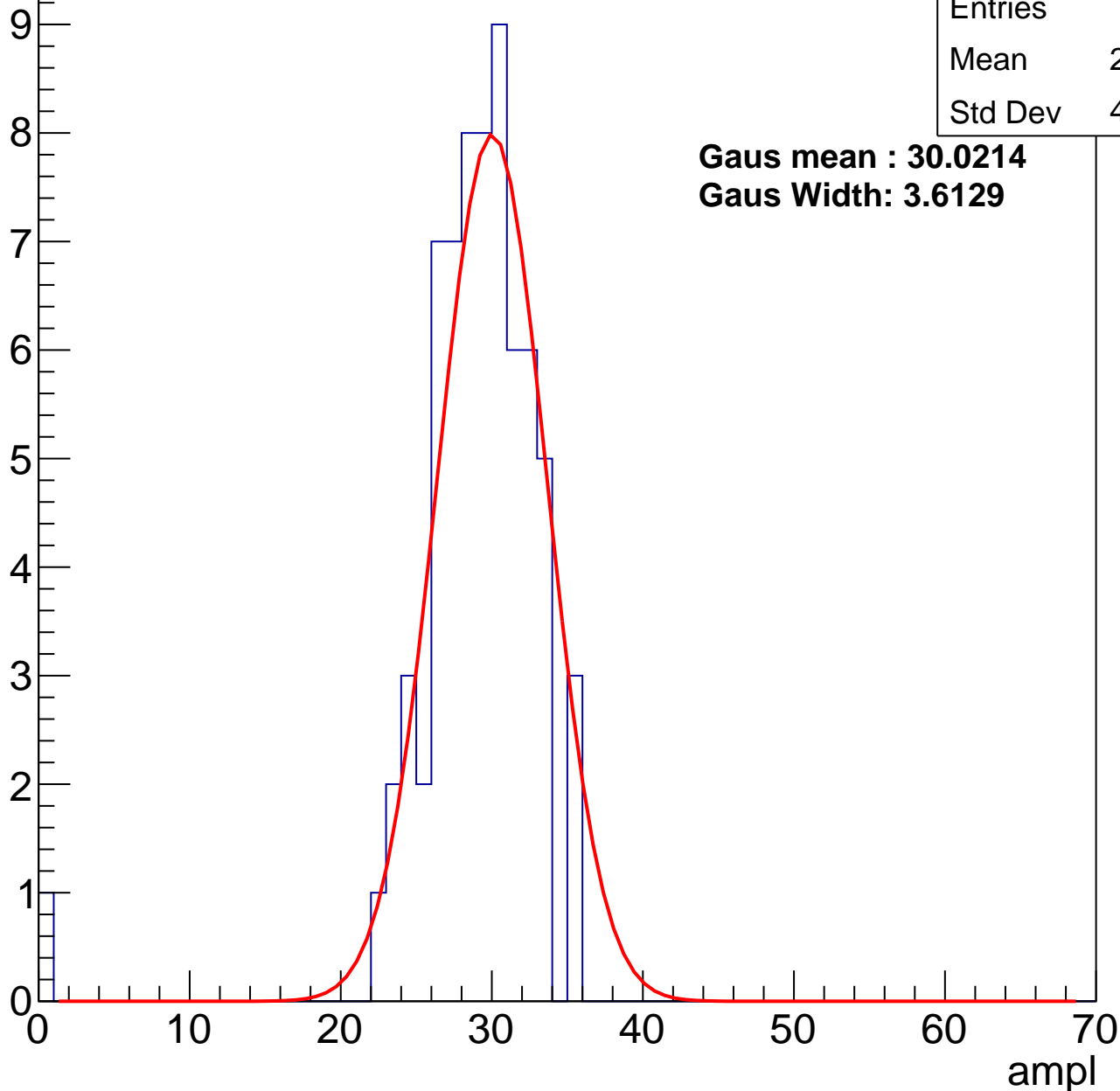
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.46
Std Dev	4.574

**Gaus mean : 30.0214**

**Gaus Width: 3.6129**



# B1L000S, U25-ch8, adc1

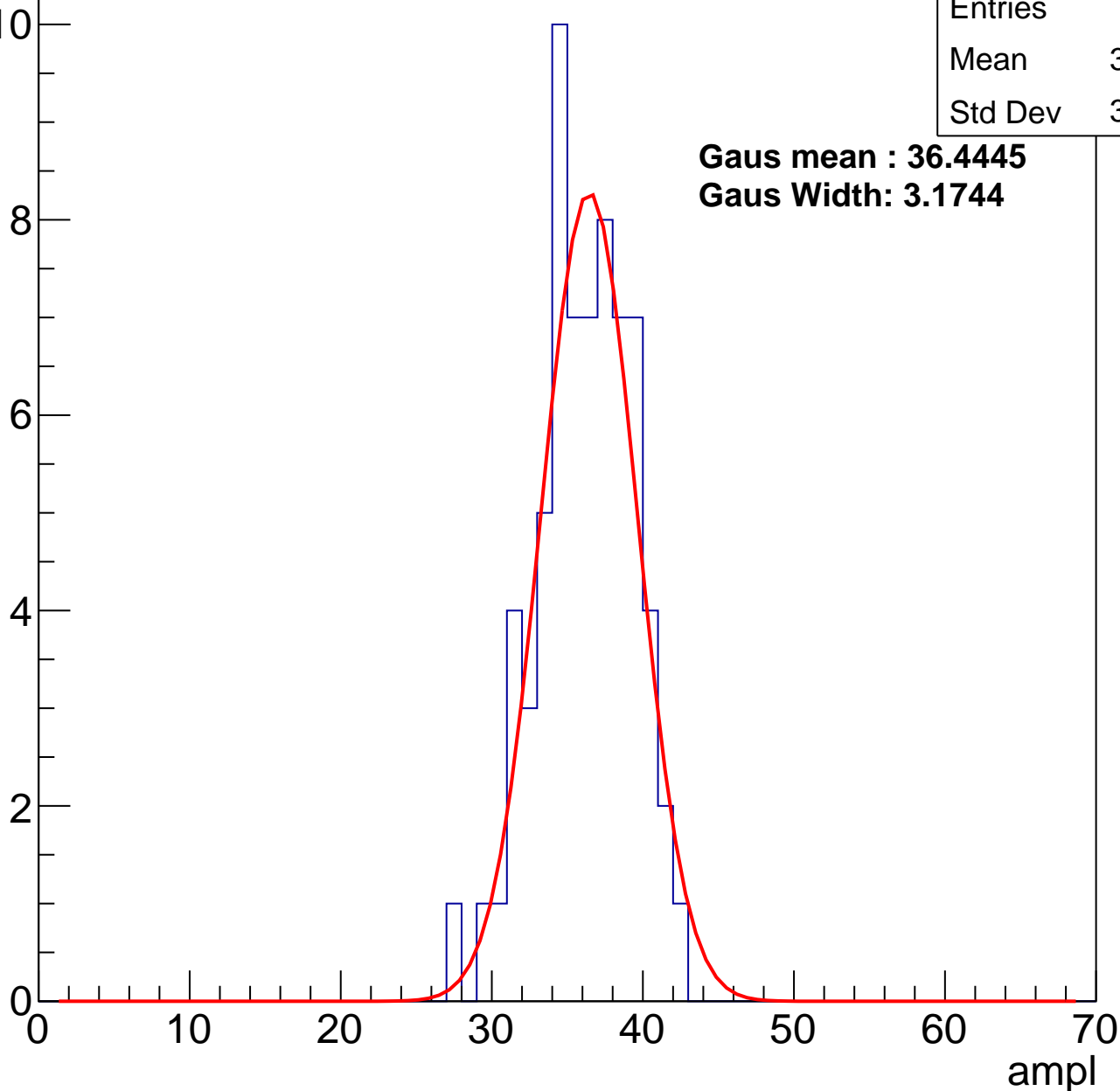
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	35.69
Std Dev	3.088

**Gaus mean : 36.4445**

**Gaus Width: 3.1744**



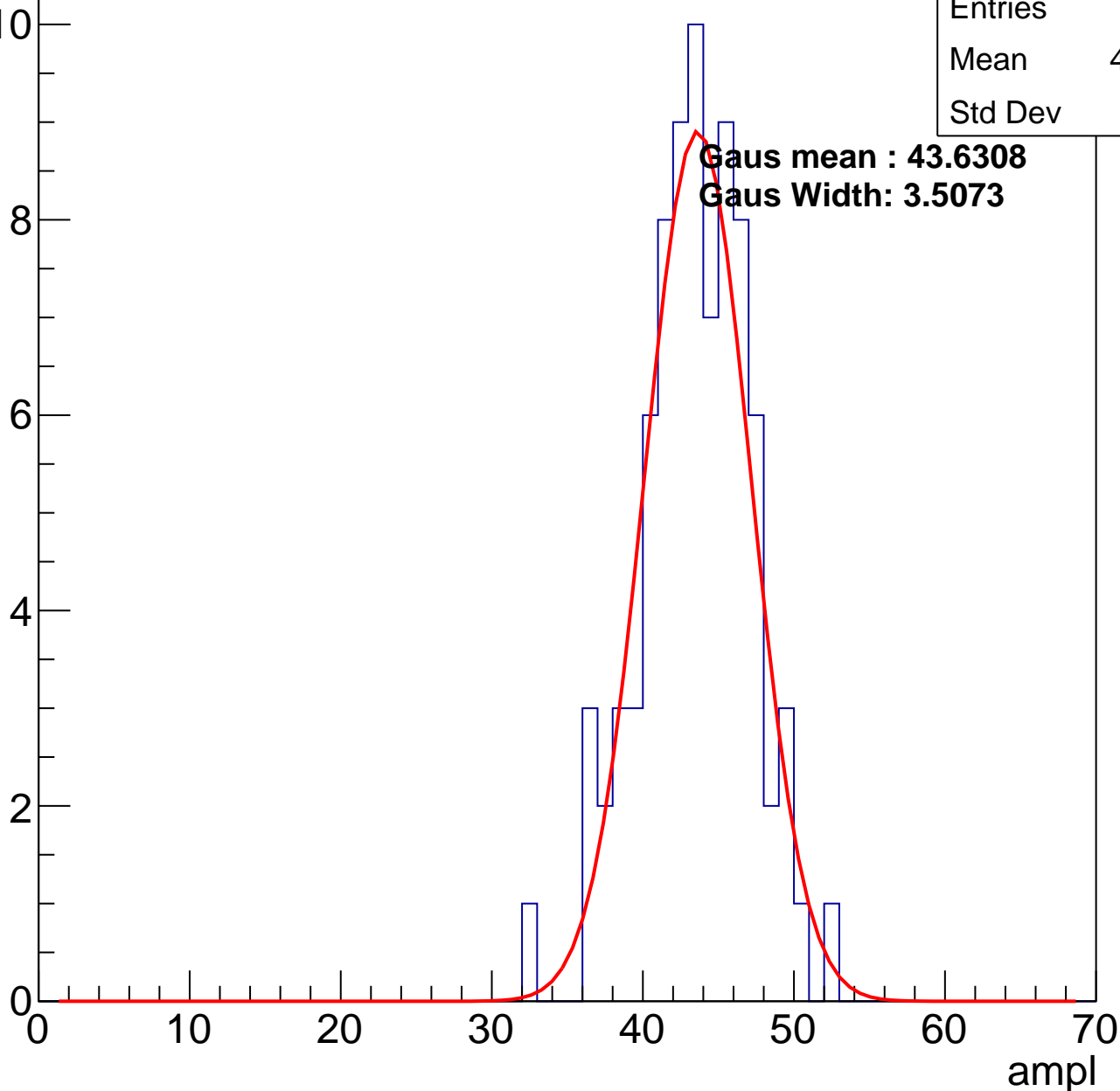
# B1L000S, U25-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	82
Mean	43.04
Std Dev	3.59

**Gaus mean : 43.6308**  
**Gaus Width: 3.5073**

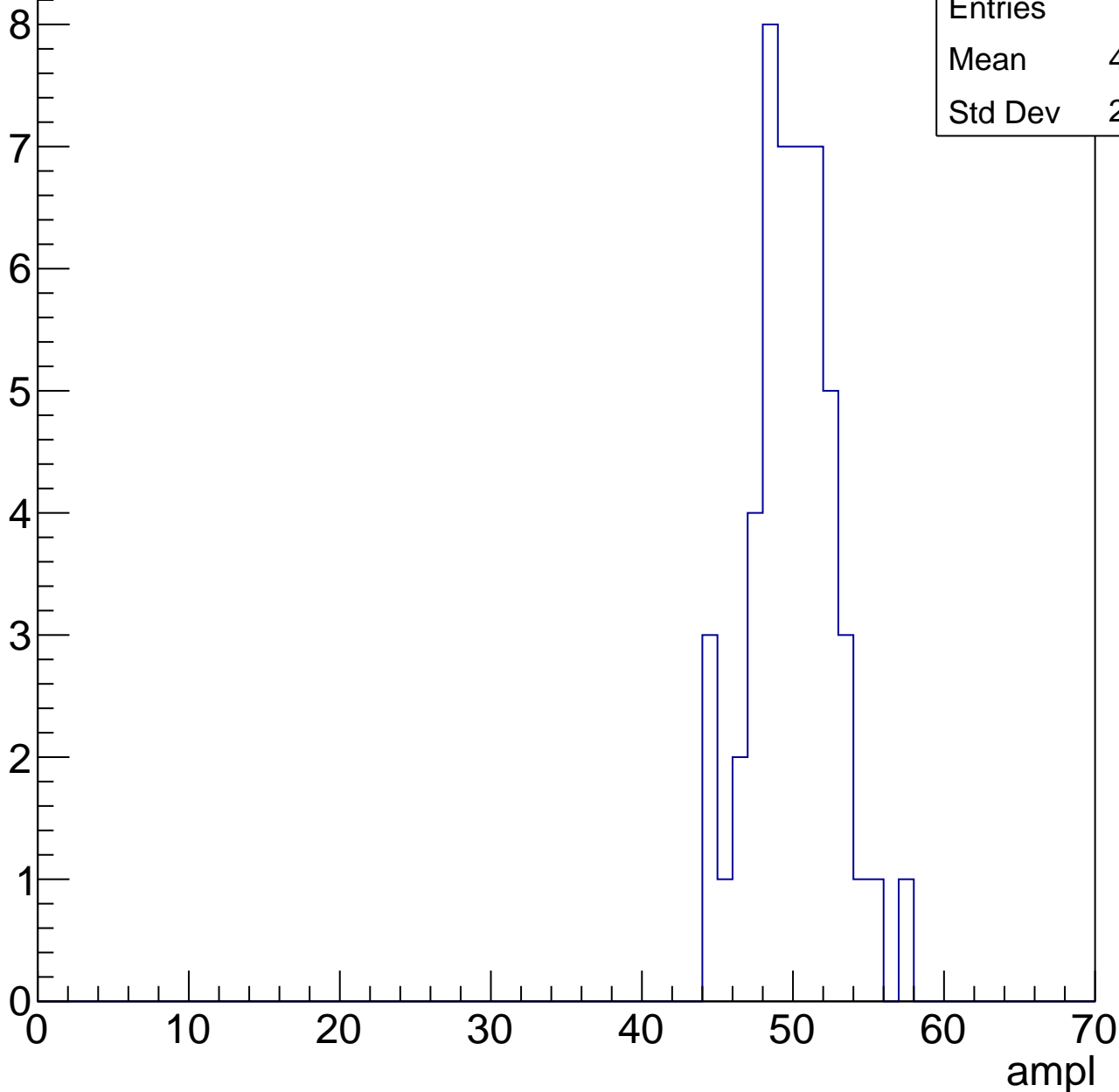


# B1L000S, U25-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

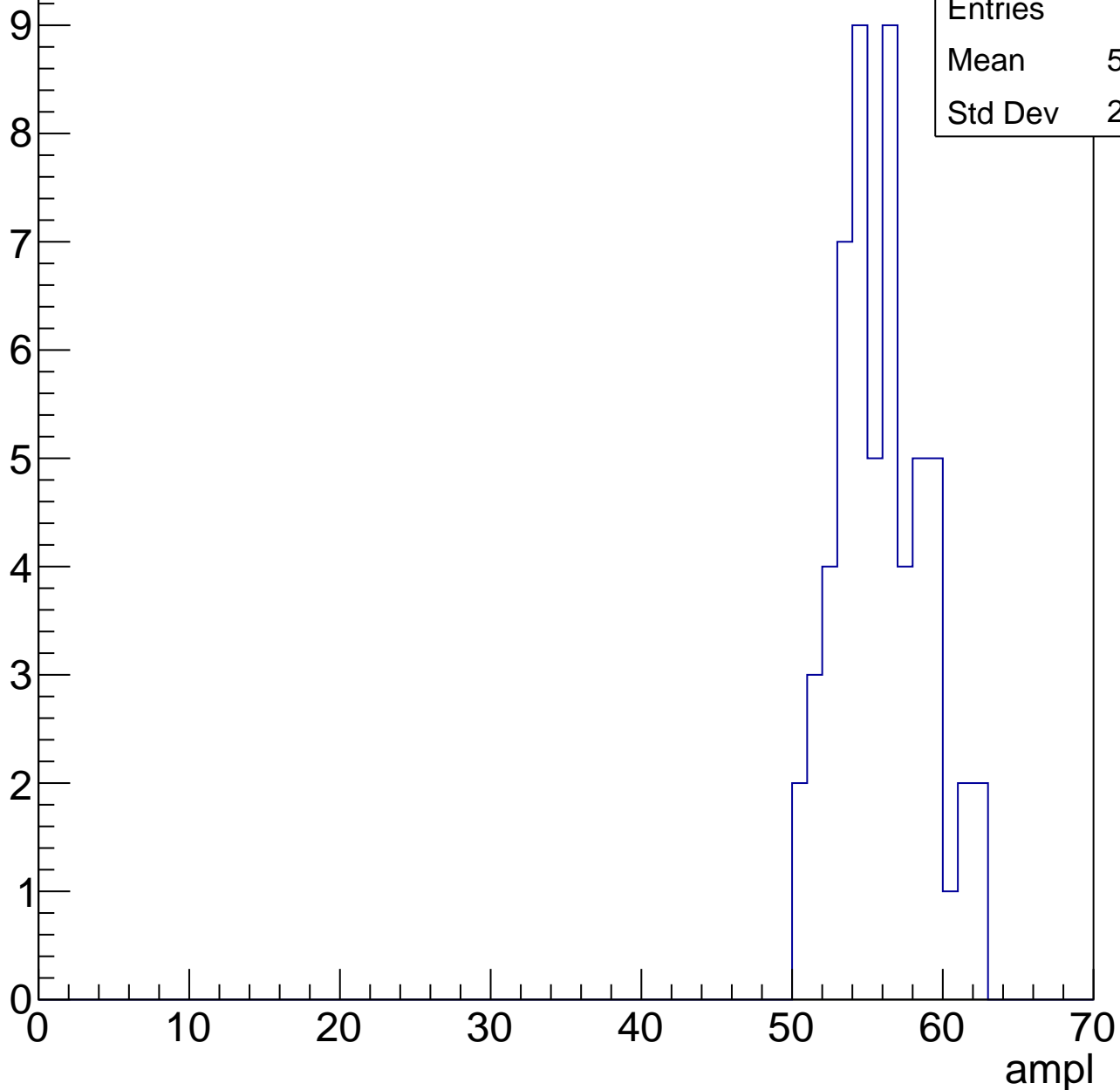
Entries	50
Mean	49.52
Std Dev	2.729



# B1L000S, U25-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



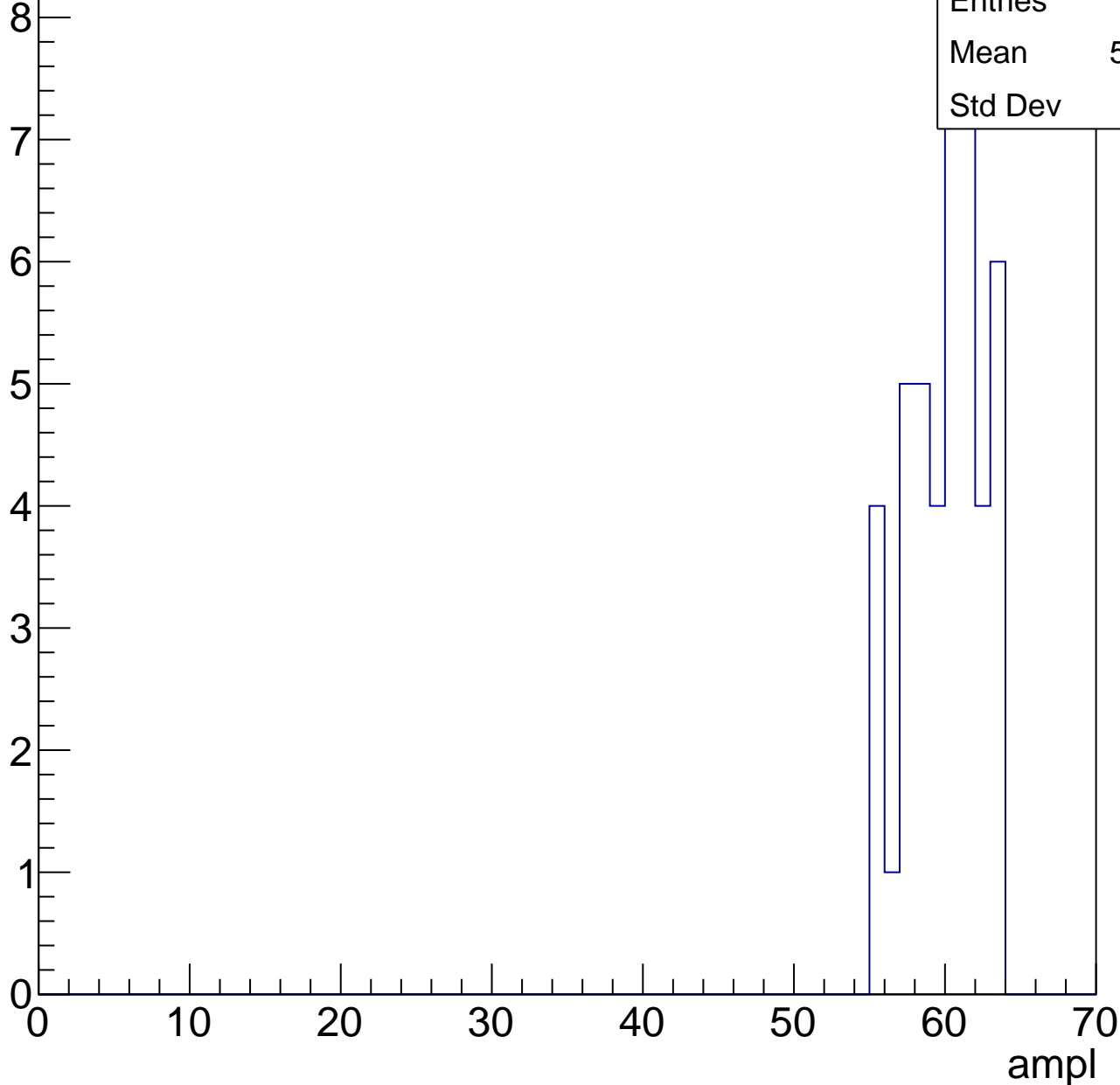
Entries	58
Mean	55.45
Std Dev	2.949

# B1L000S, U25-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

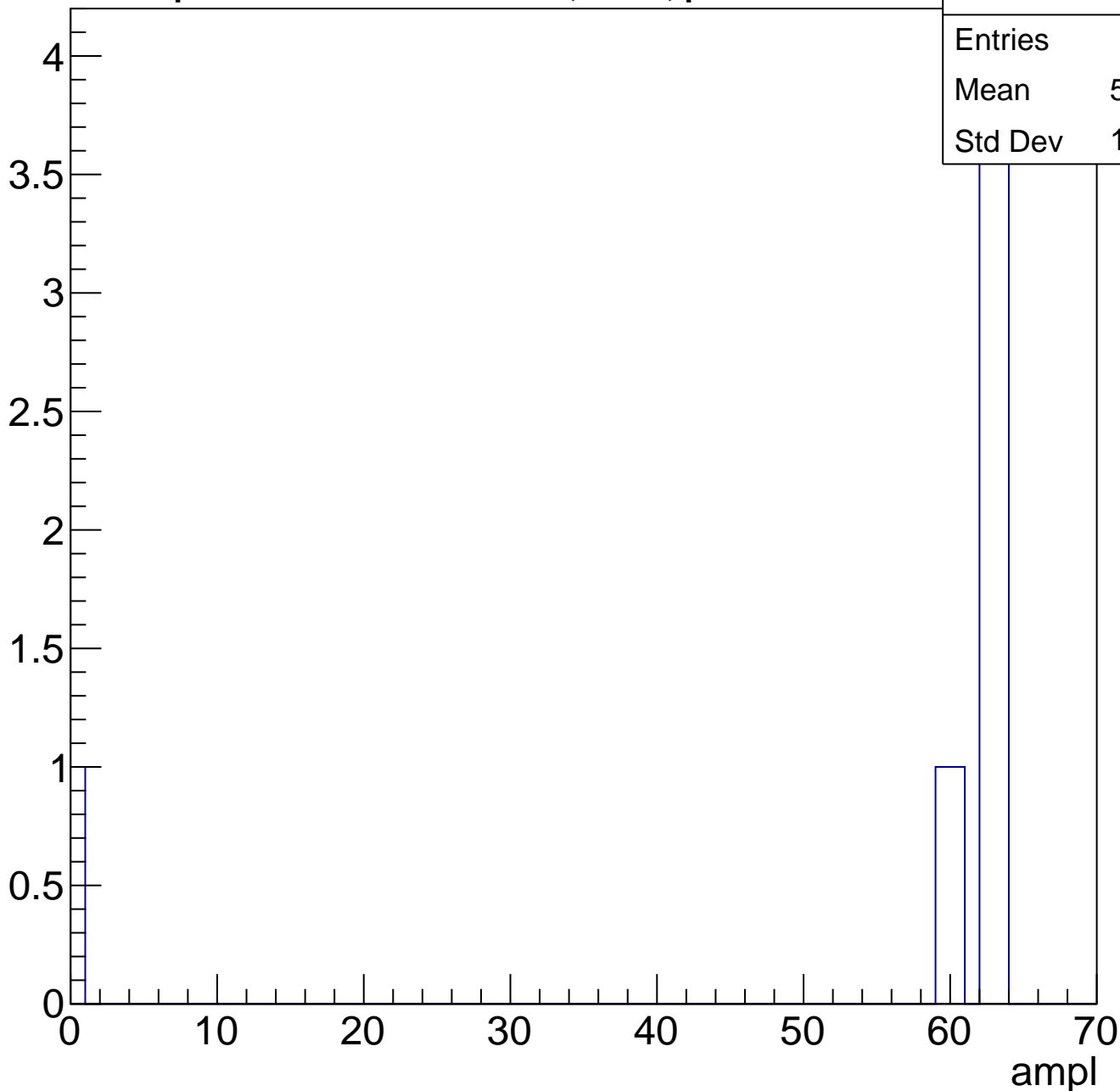
Entries	45
Mean	59.58
Std Dev	2.38



# B1L000S, U25-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

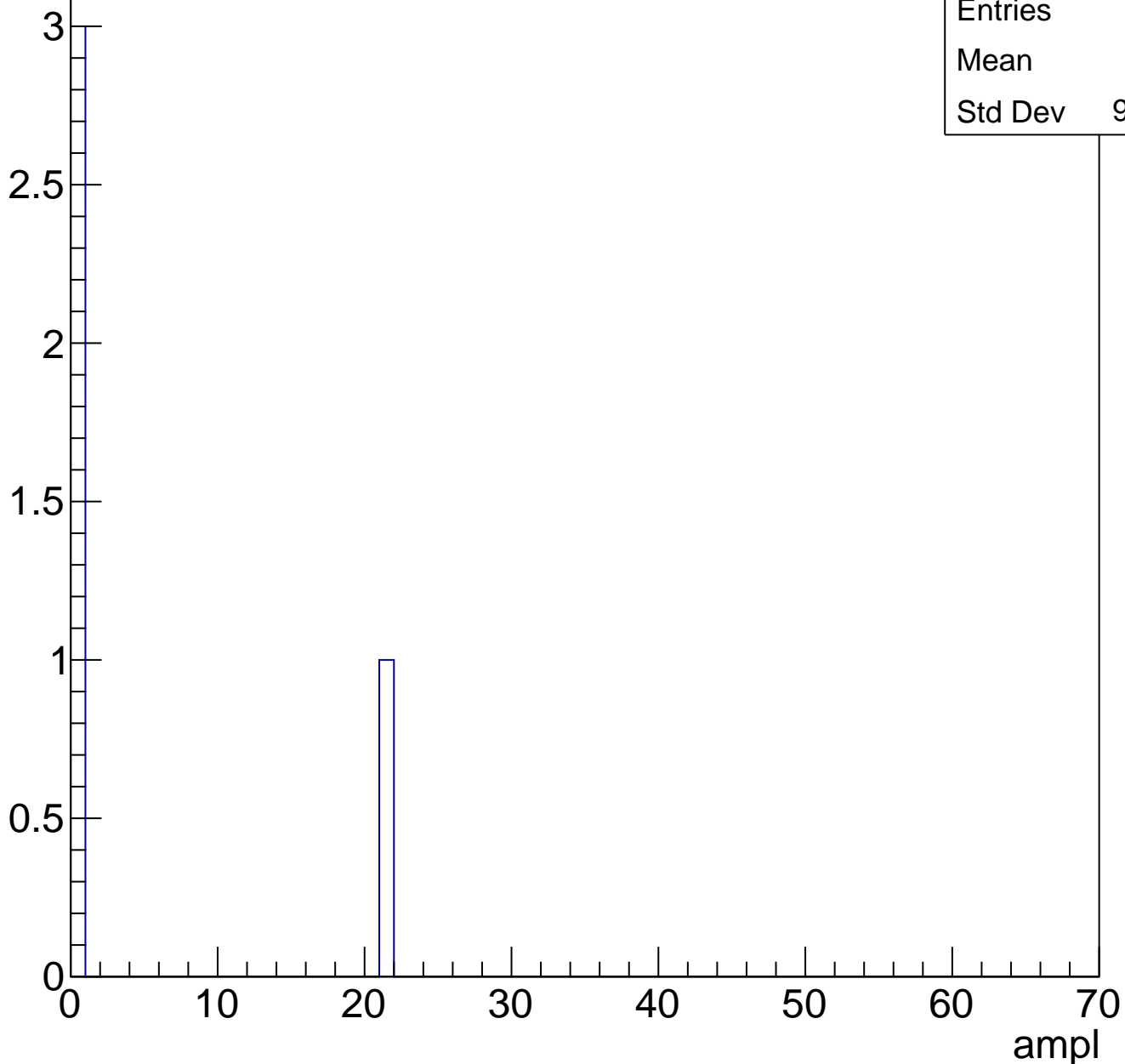




# B1L000S, U25-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	5.25
Std Dev	9.093

# B1L000S, U25-ch9, adc0

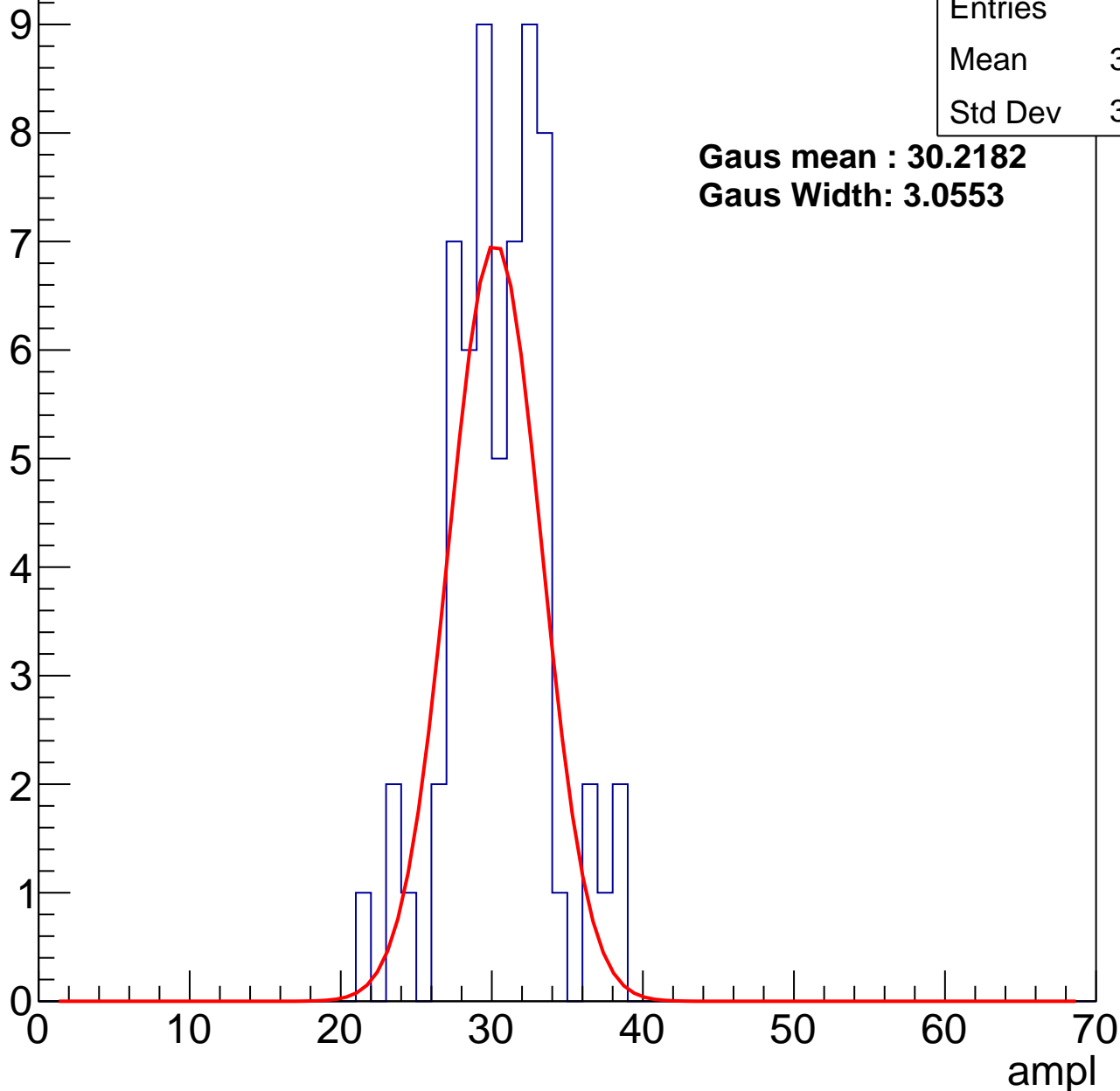
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	30.14
Std Dev	3.385

**Gaus mean : 30.2182**

**Gaus Width: 3.0553**



# B1L000S, U25-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	36.72
Std Dev	3.069

**Gaus mean : 37.2530**

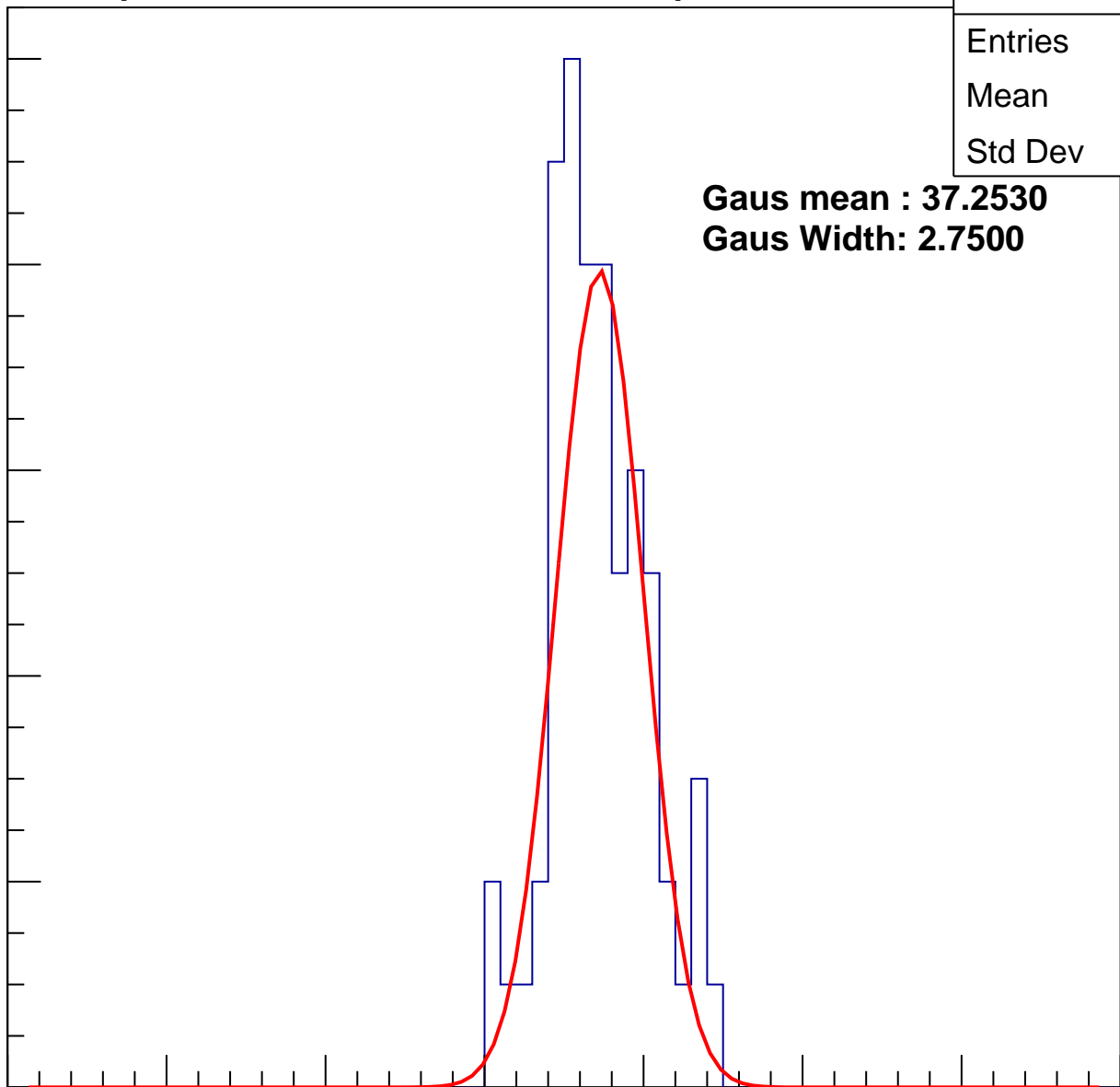
**Gaus Width: 2.7500**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch9, adc2

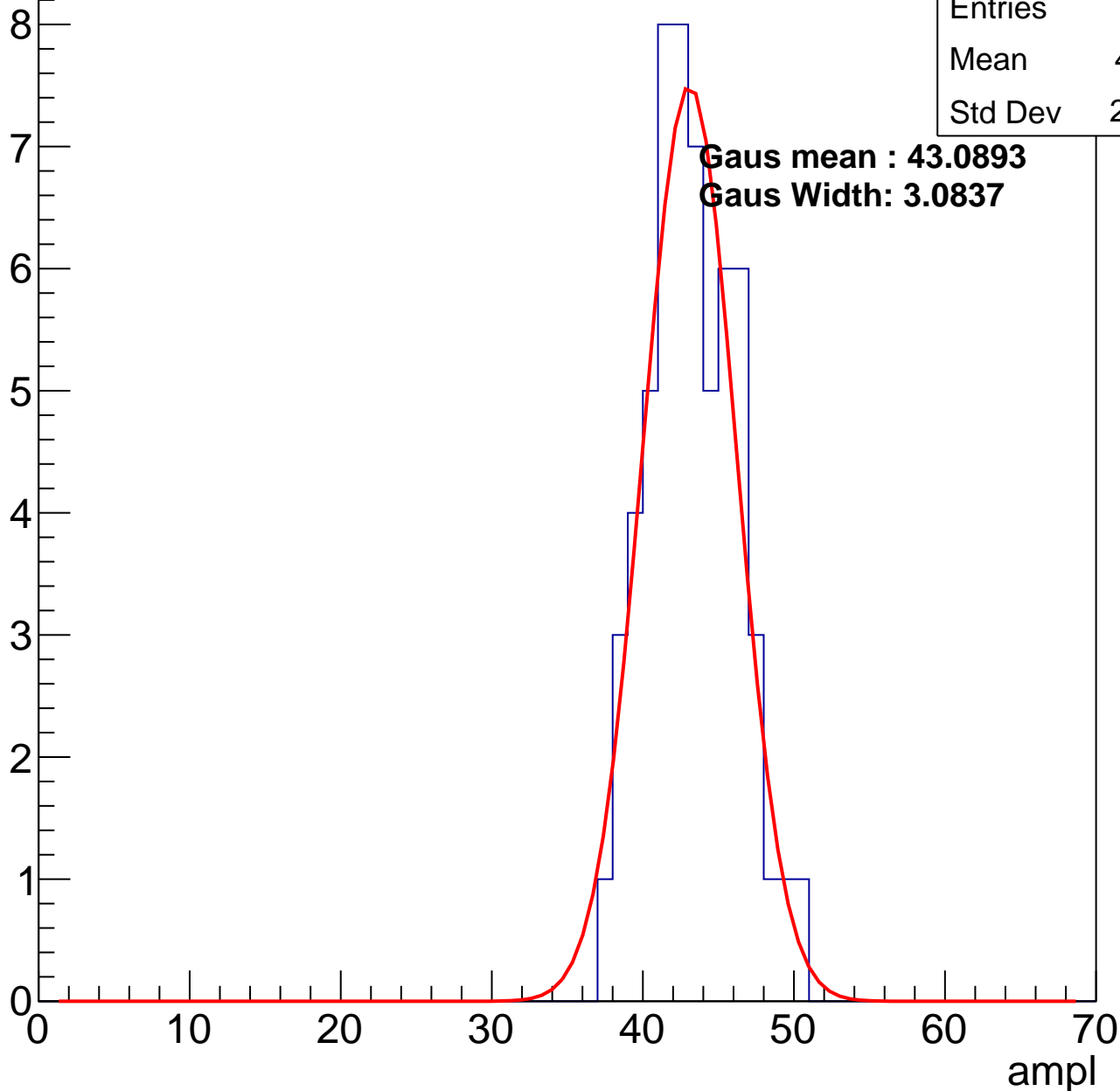
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.81
Std Dev	2.908

**Gaus mean : 43.0893**

**Gaus Width: 3.0837**

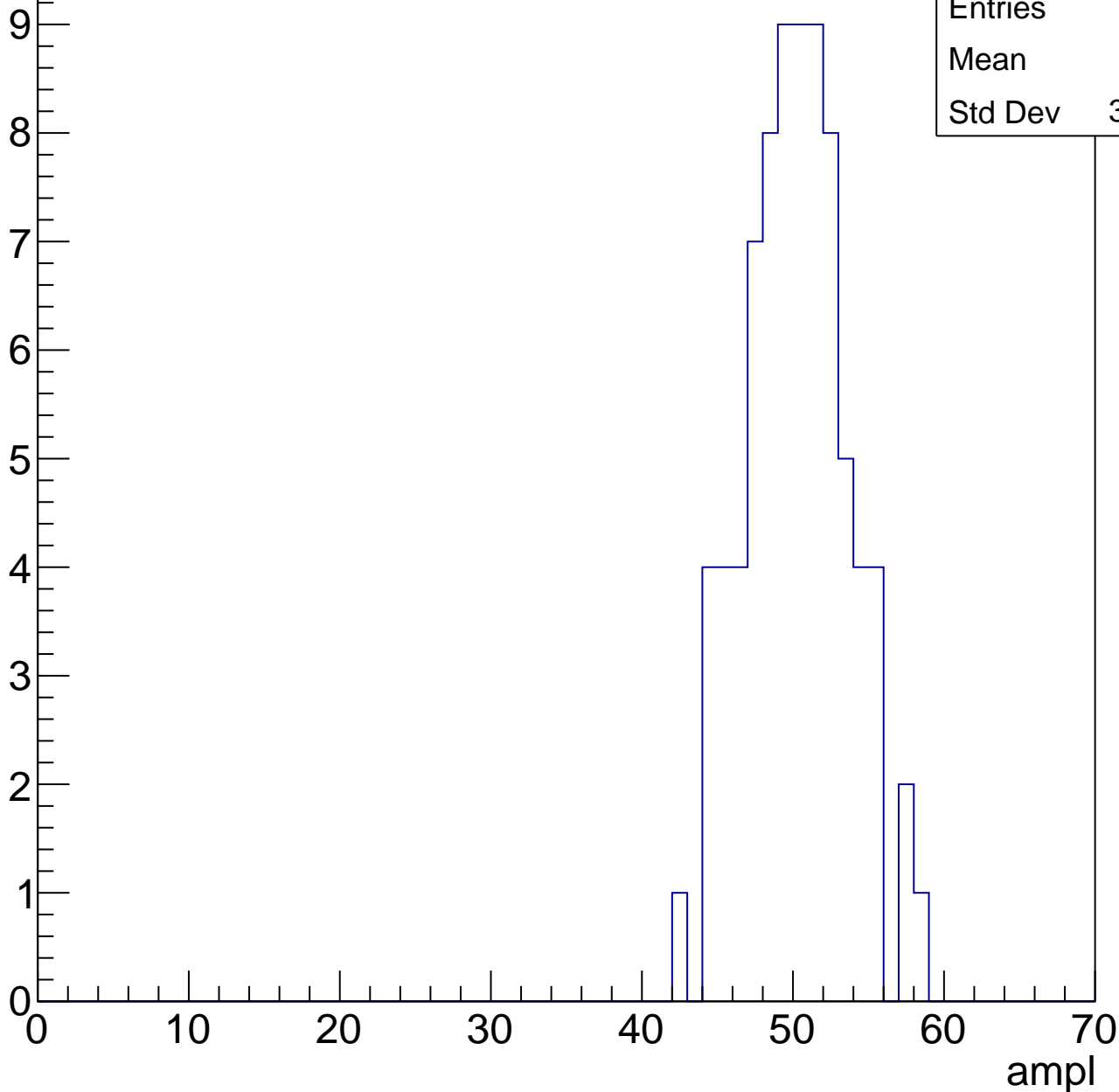


# B1L000S, U25-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

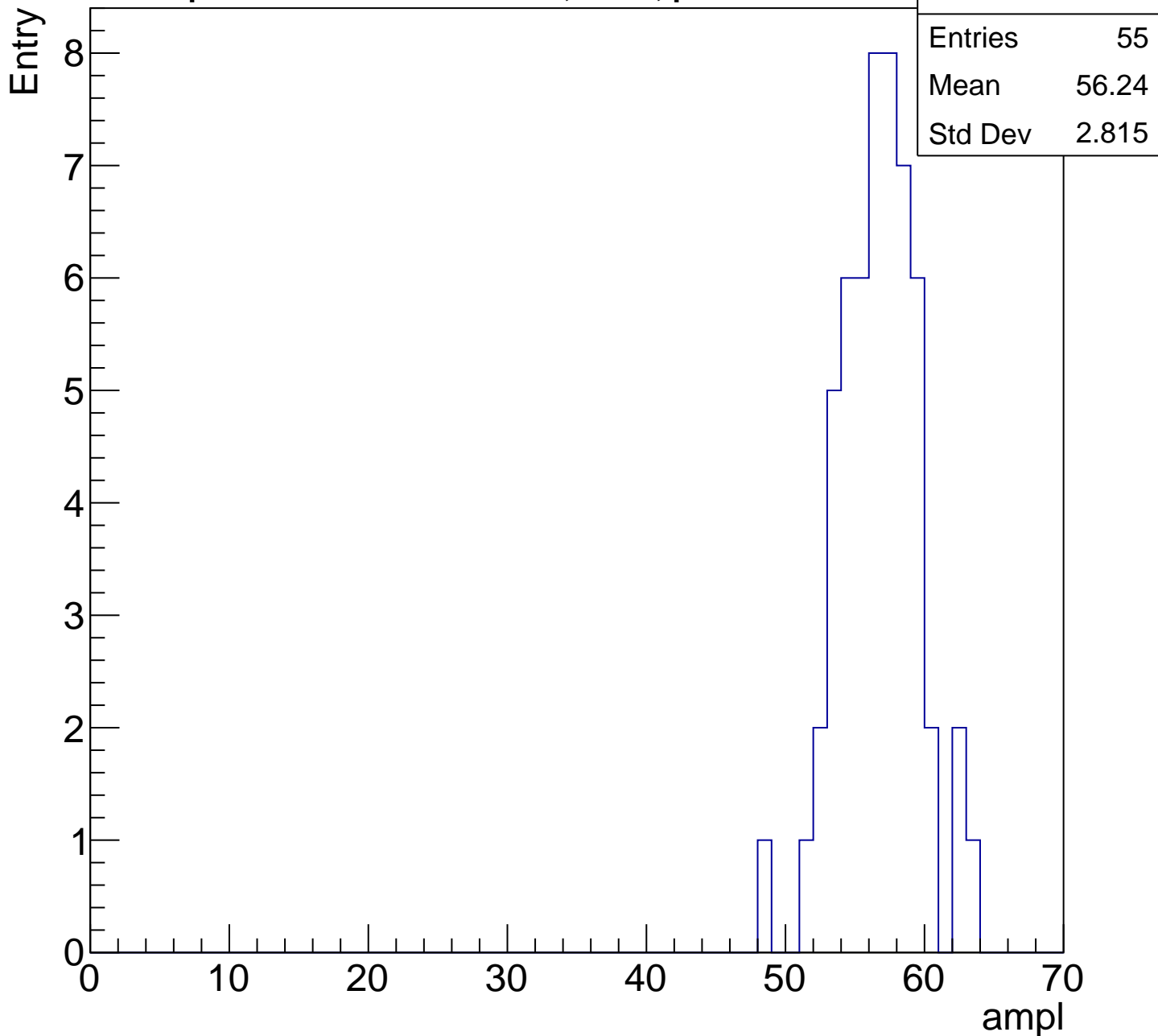
Entry

Entries	79
Mean	49.8
Std Dev	3.347



# B1L000S, U25-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

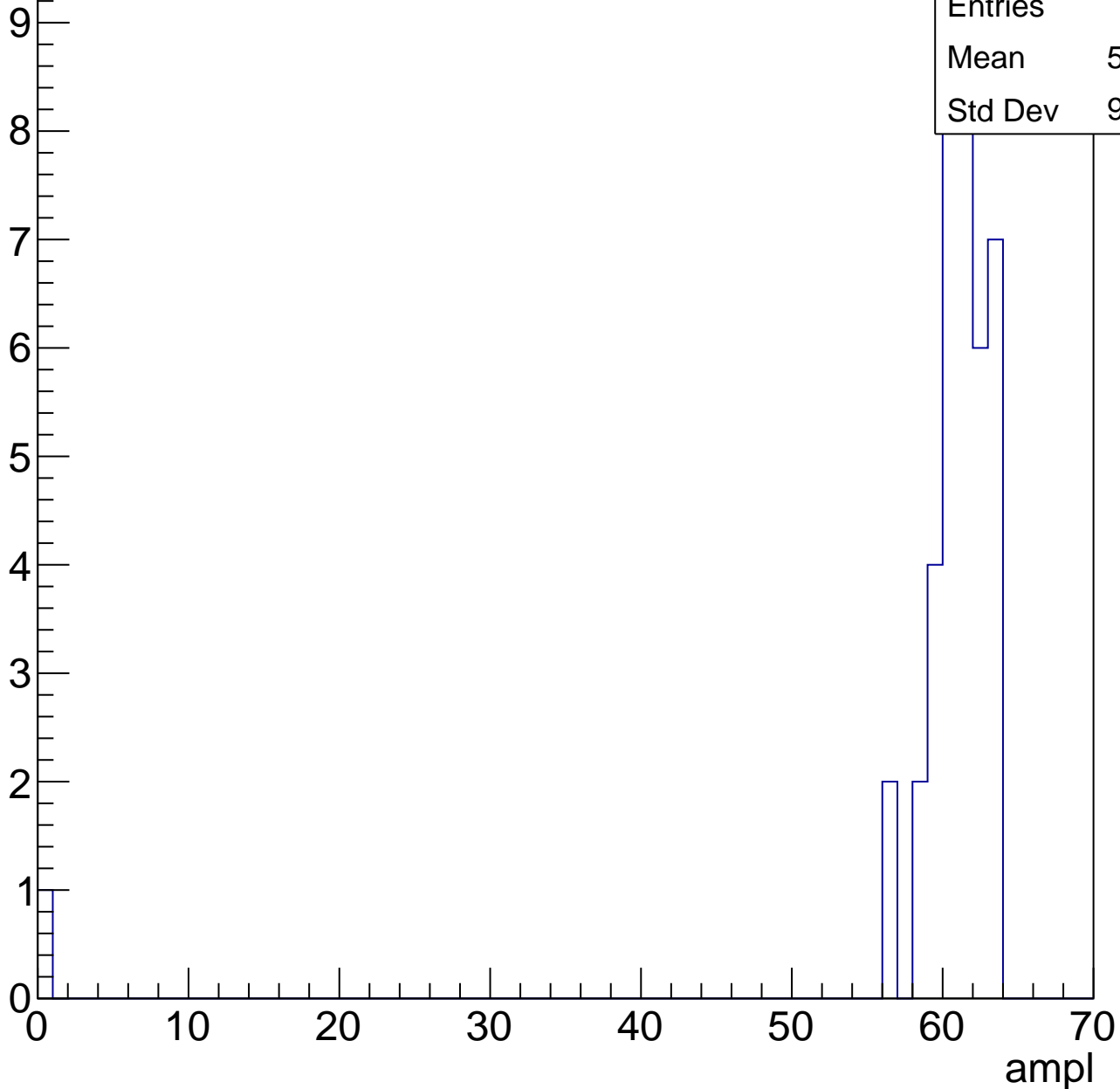


# B1L000S, U25-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	59.13
Std Dev	9.754



# B1L000S, U25-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	28.15
Std Dev	4.539

**Gaus mean : 28.8548**

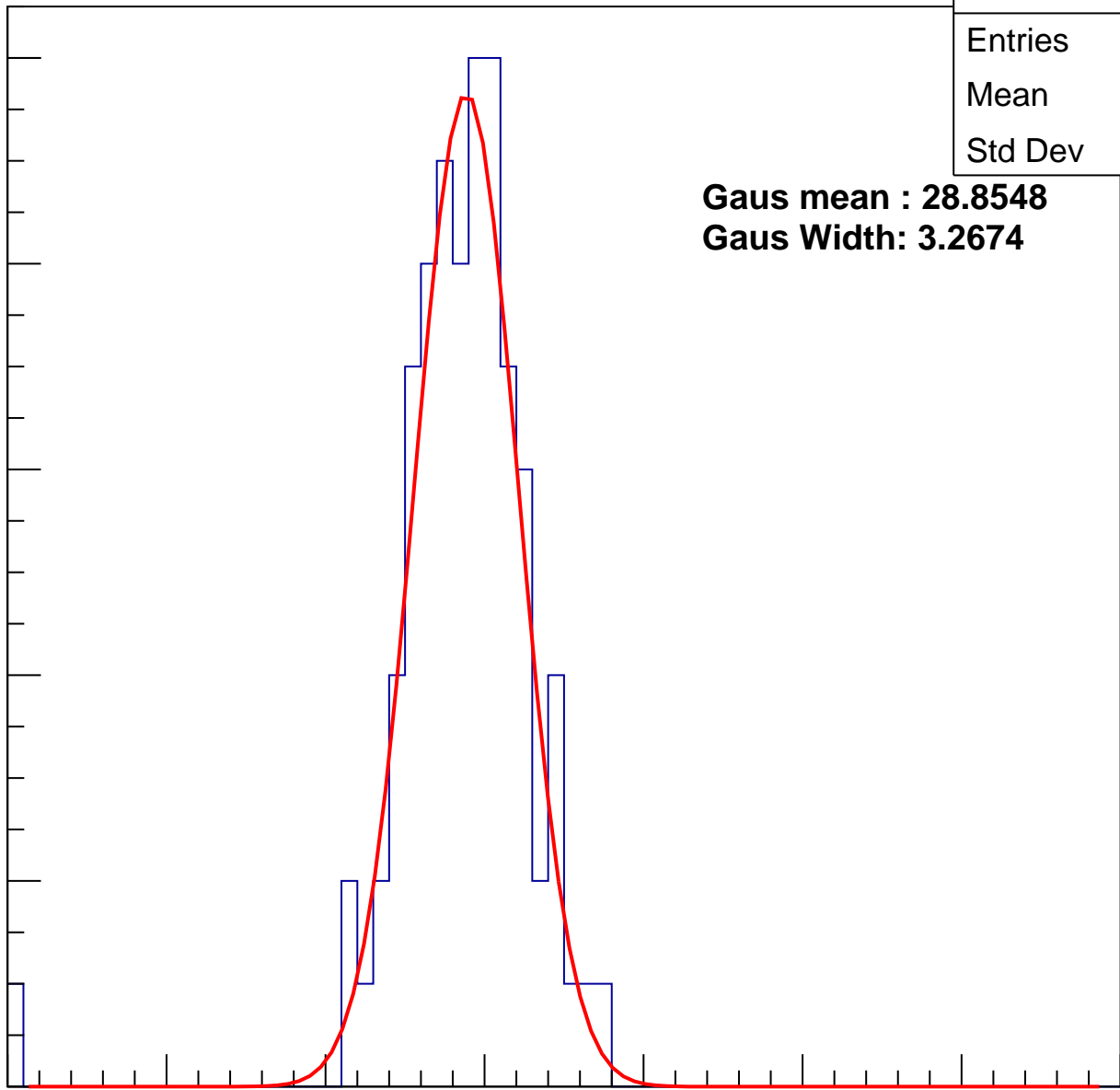
**Gaus Width: 3.2674**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch10, adc1

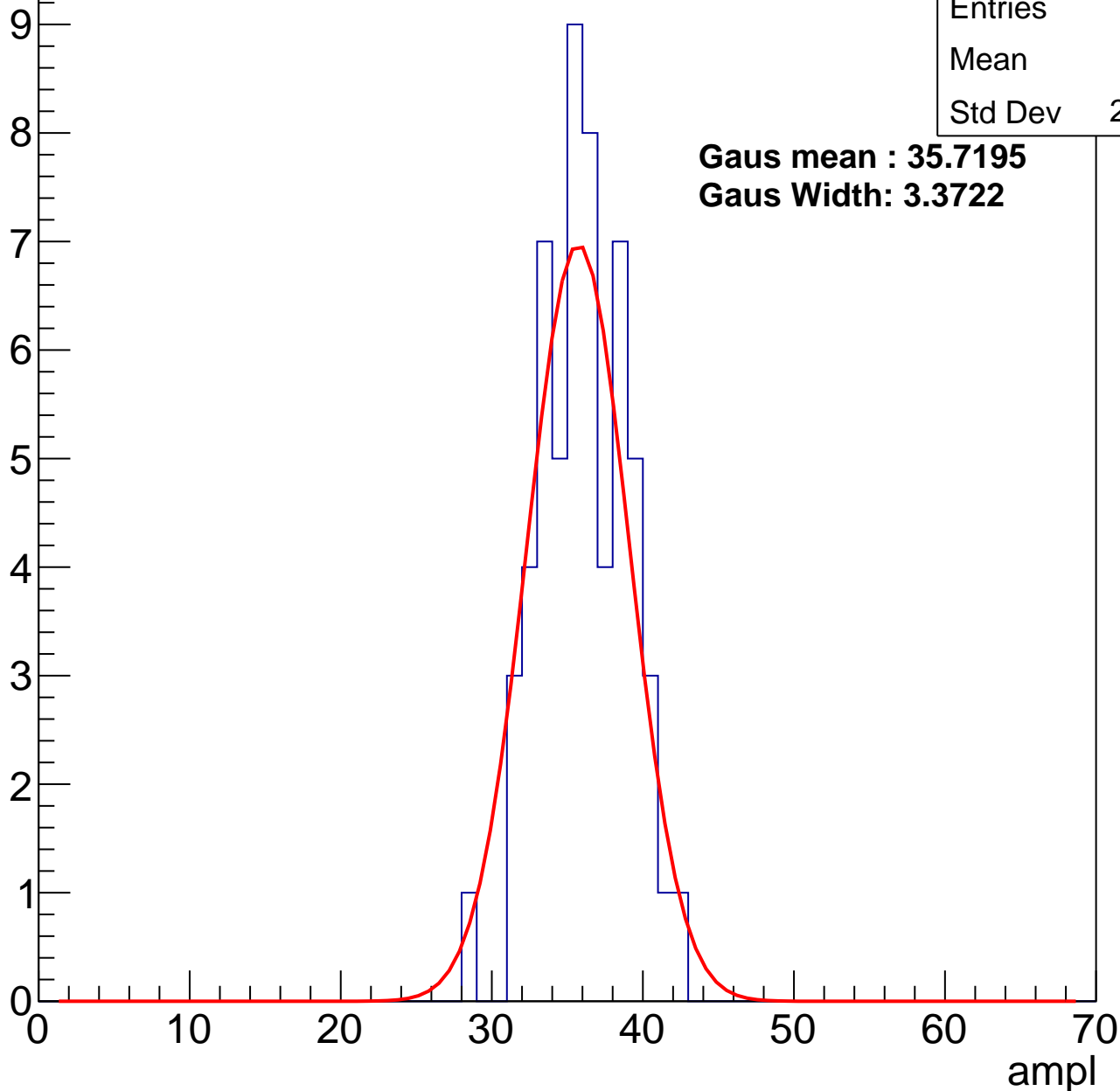
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.6
Std Dev	2.852

**Gaus mean : 35.7195**

**Gaus Width: 3.3722**



# B1L000S, U25-ch10, adc2

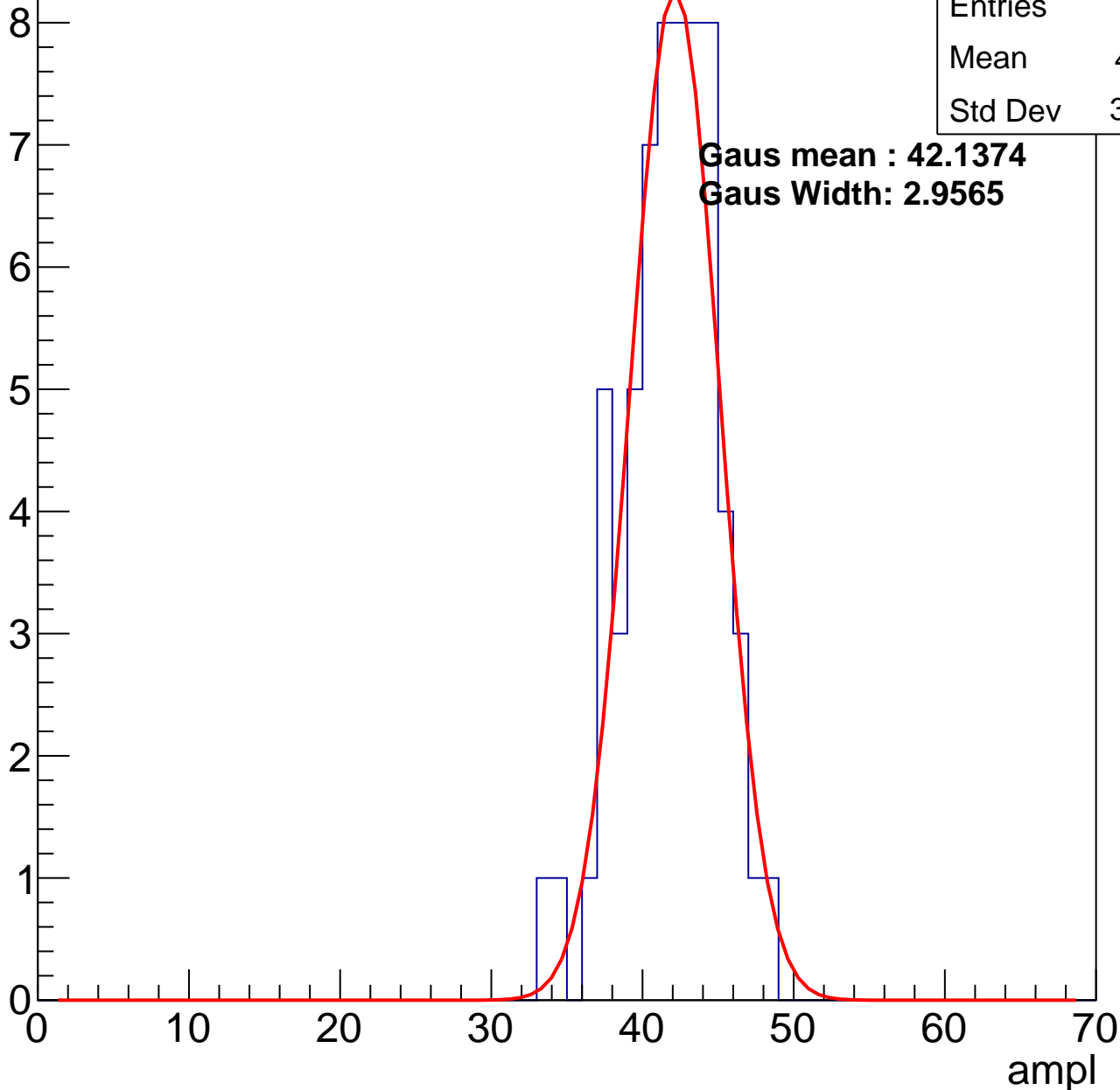
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.41
Std Dev	3.055

**Gaus mean : 42.1374**

**Gaus Width: 2.9565**



# B1L000S, U25-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	48.53
Std Dev	3.066

Entry

10

8

6

4

2

0

0

10

20

30

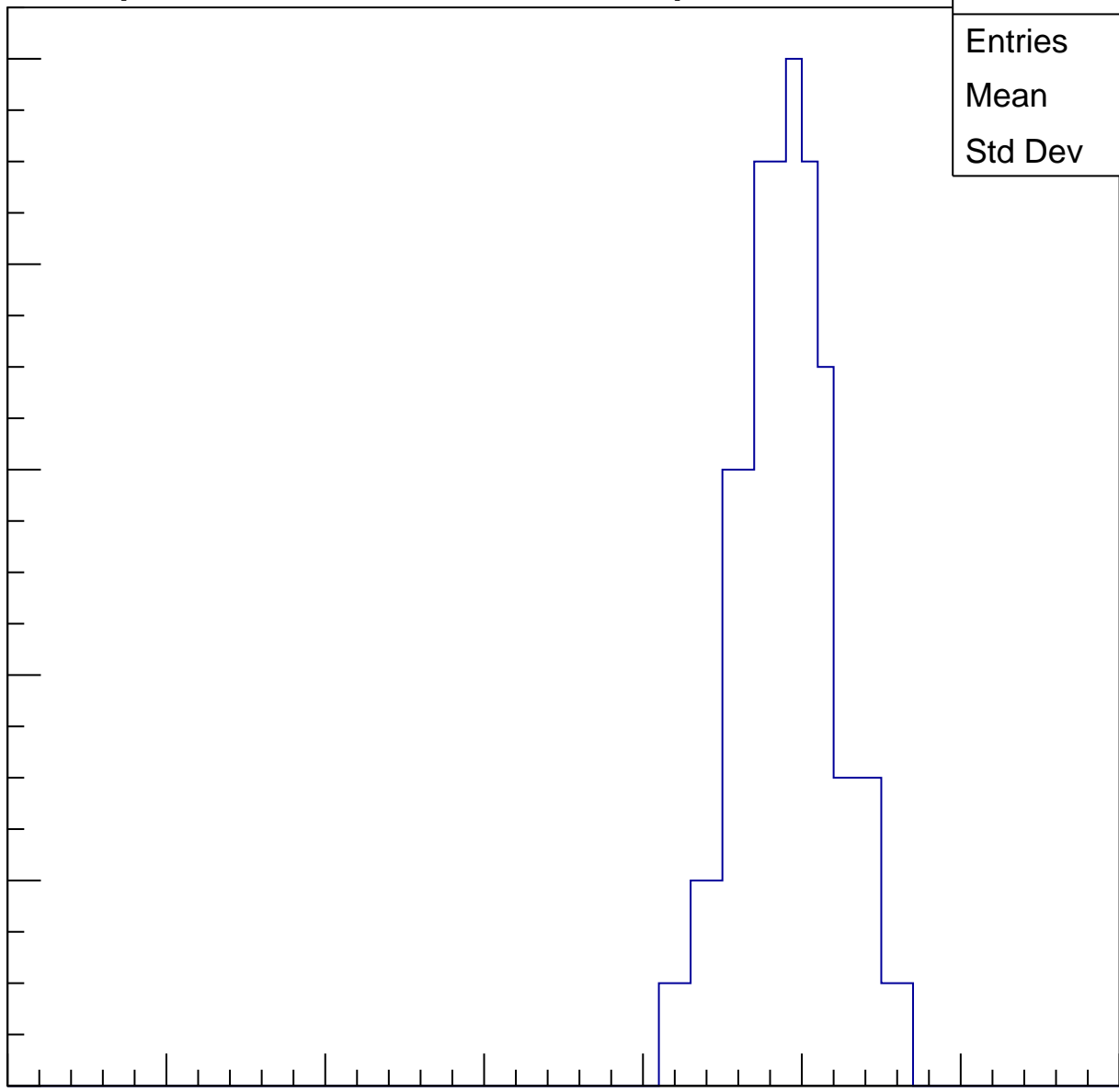
40

50

60

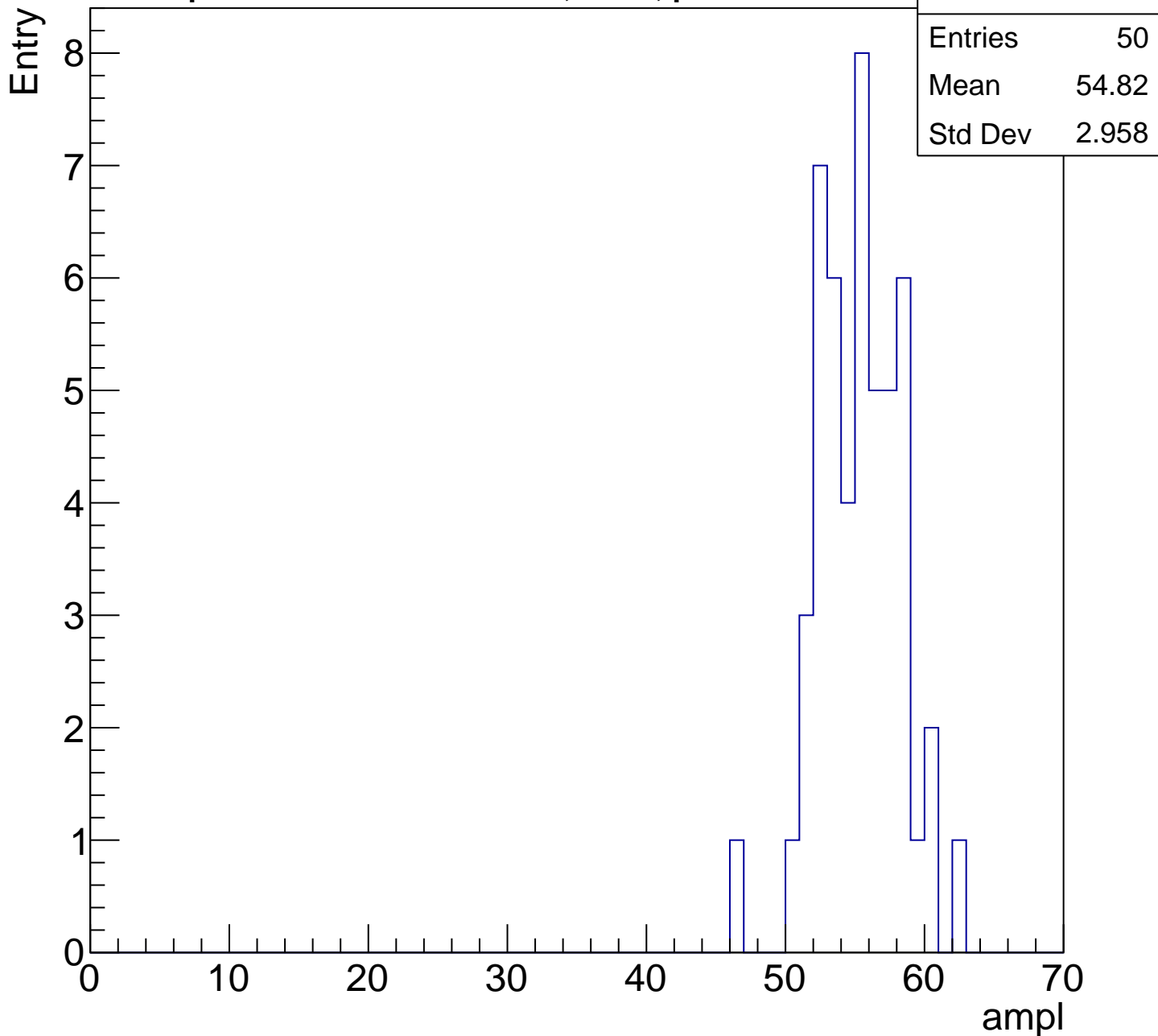
70

ampl



# B1L000S, U25-ch10, adc4

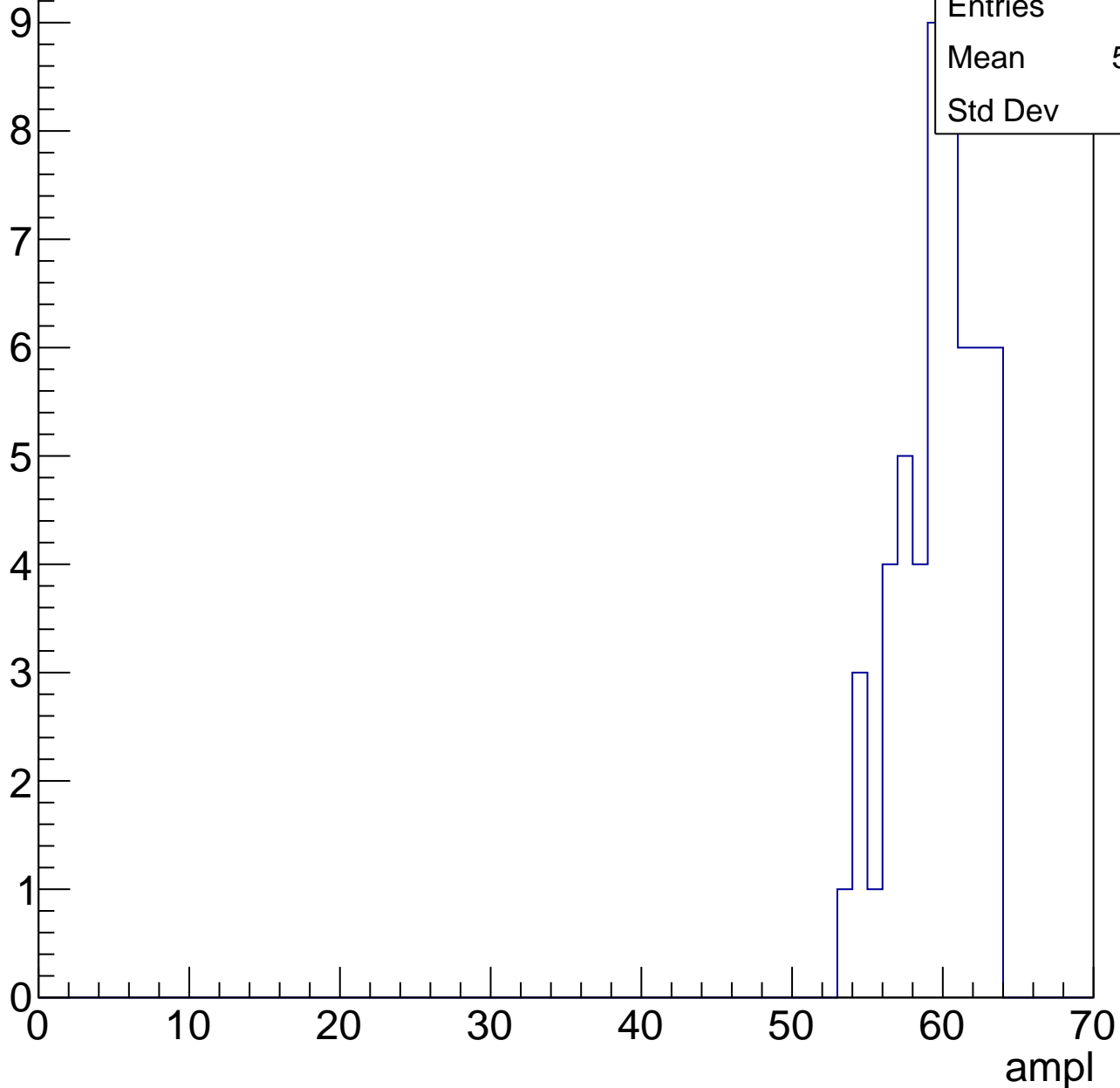
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

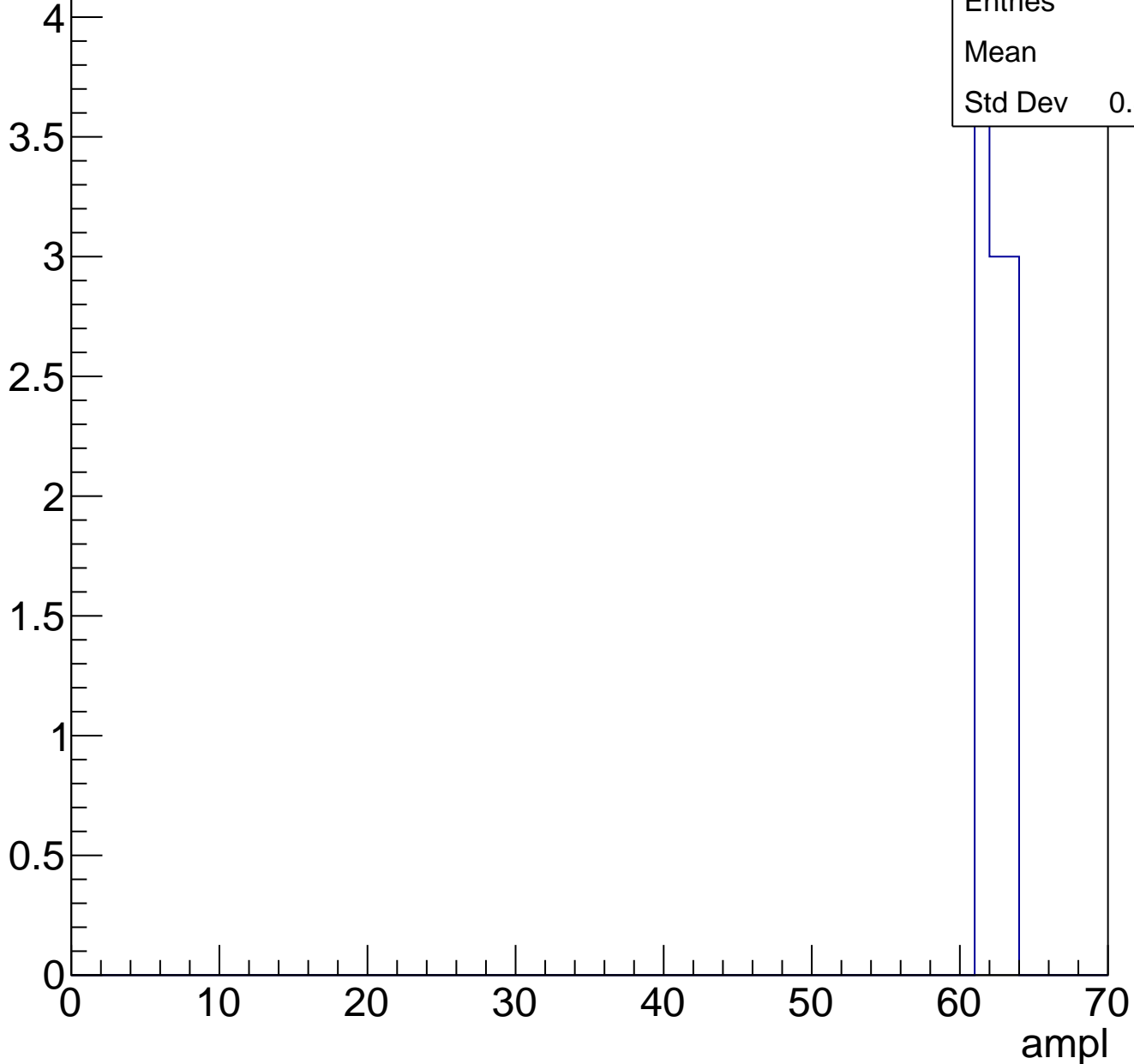
Entry



# B1L000S, U25-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L000S, U25-ch11, adc0

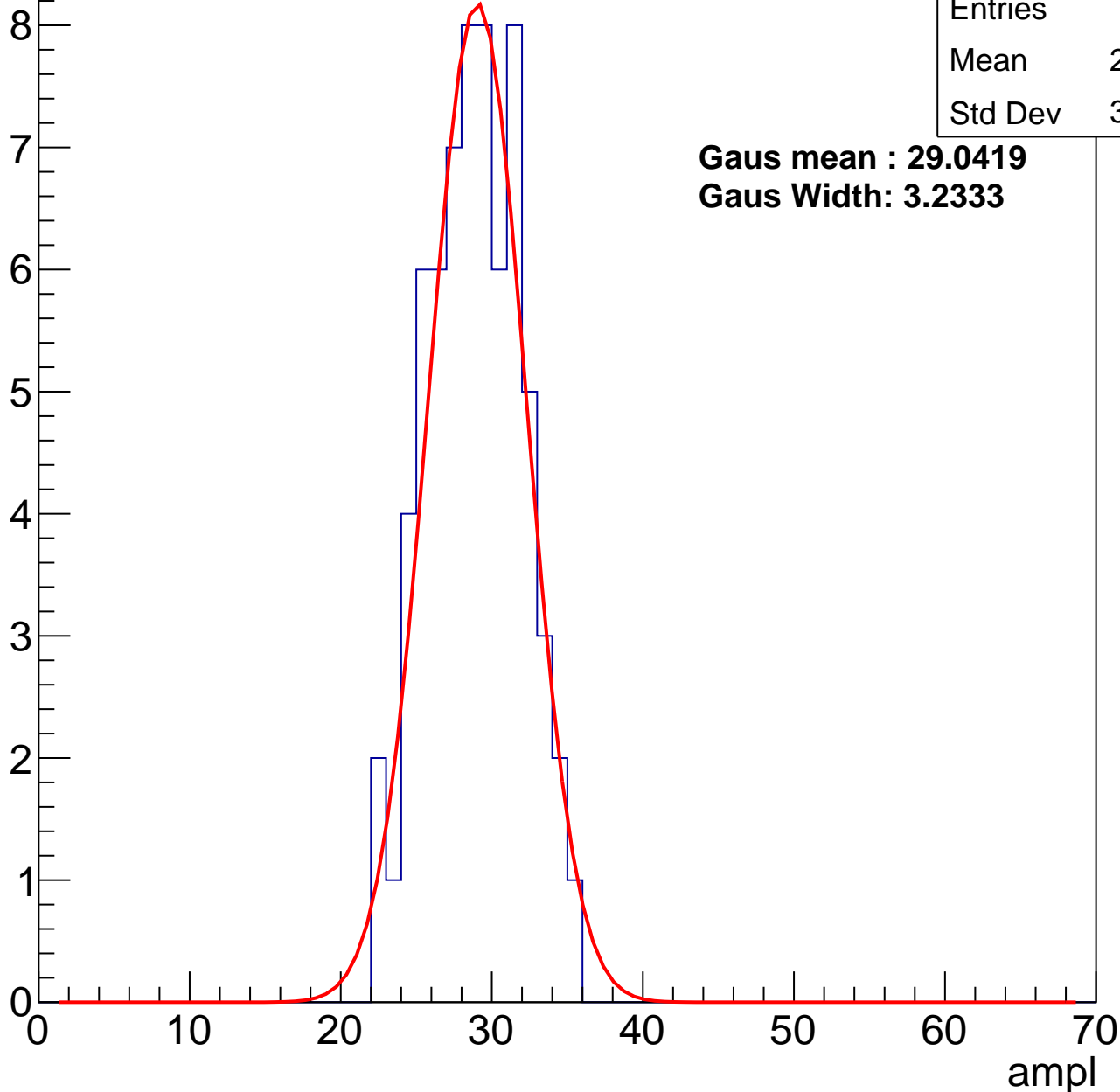
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.42
Std Dev	3.028

**Gaus mean : 29.0419**

**Gaus Width: 3.2333**



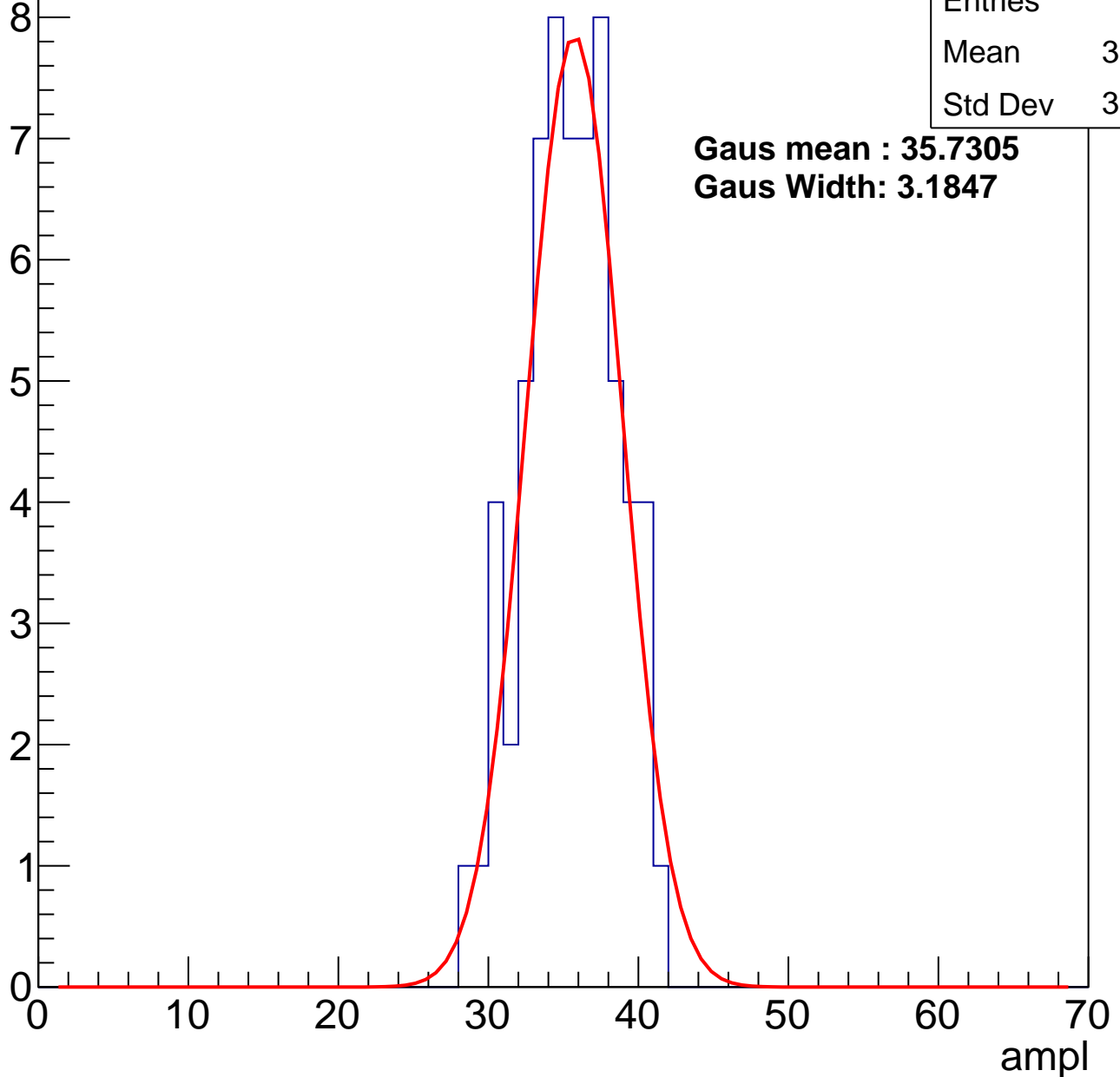
# B1L000S, U25-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.03
Std Dev	3.015

**Gaus mean : 35.7305**  
**Gaus Width: 3.1847**



# B1L000S, U25-ch11, adc2

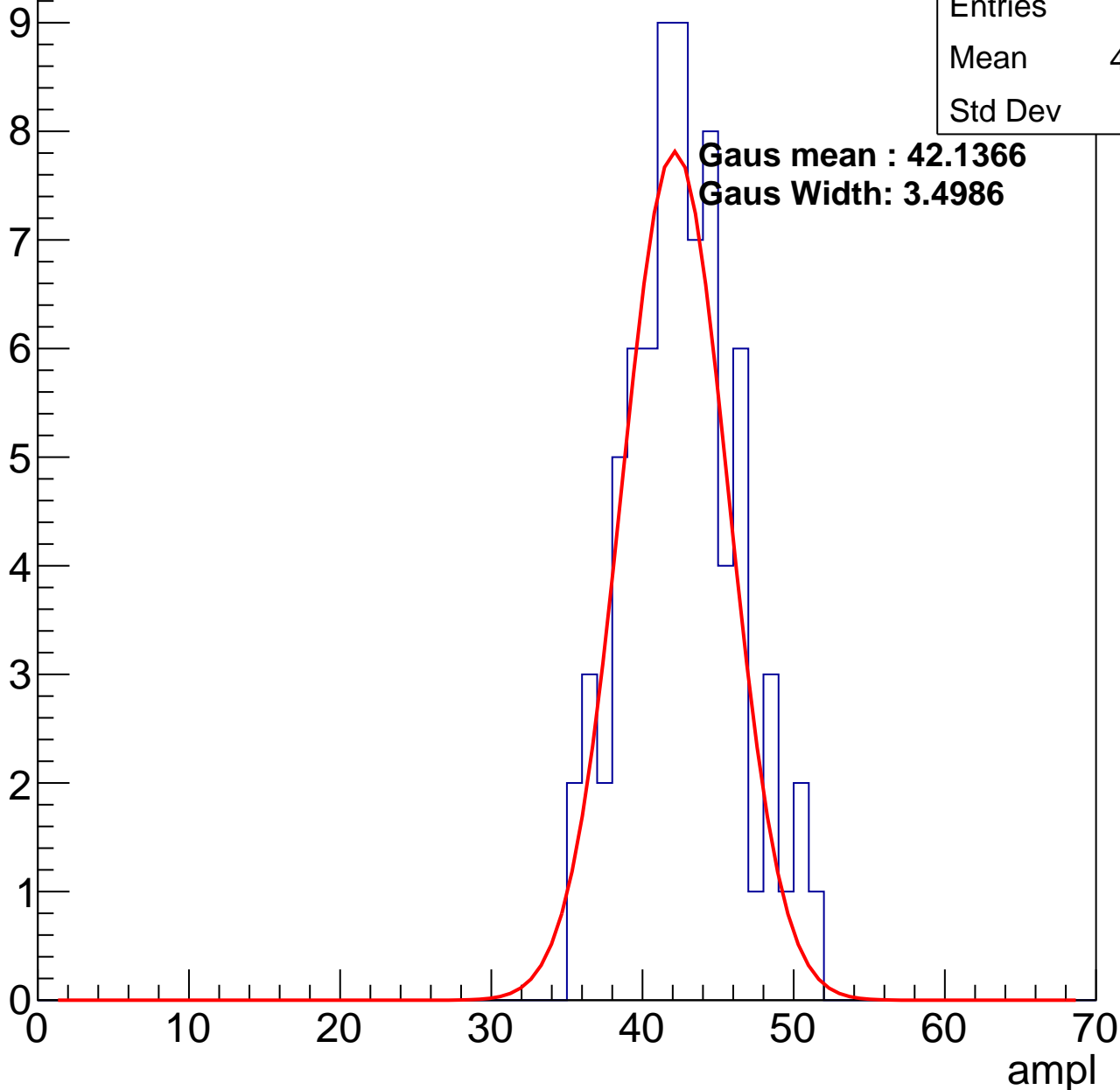
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	42.17
Std Dev	3.62

**Gaus mean : 42.1366**

**Gaus Width: 3.4986**

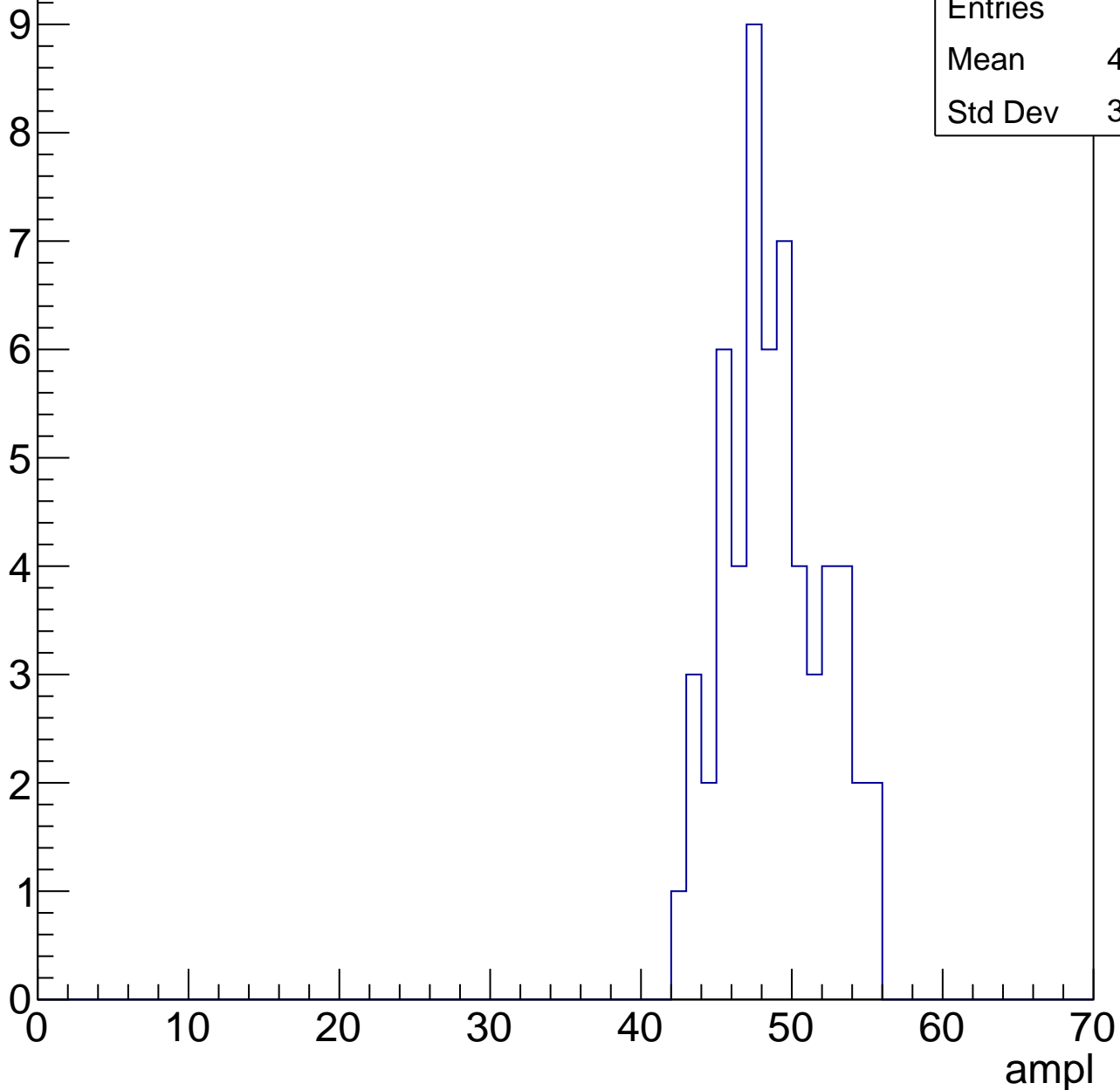


# B1L000S, U25-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	48.39
Std Dev	3.238

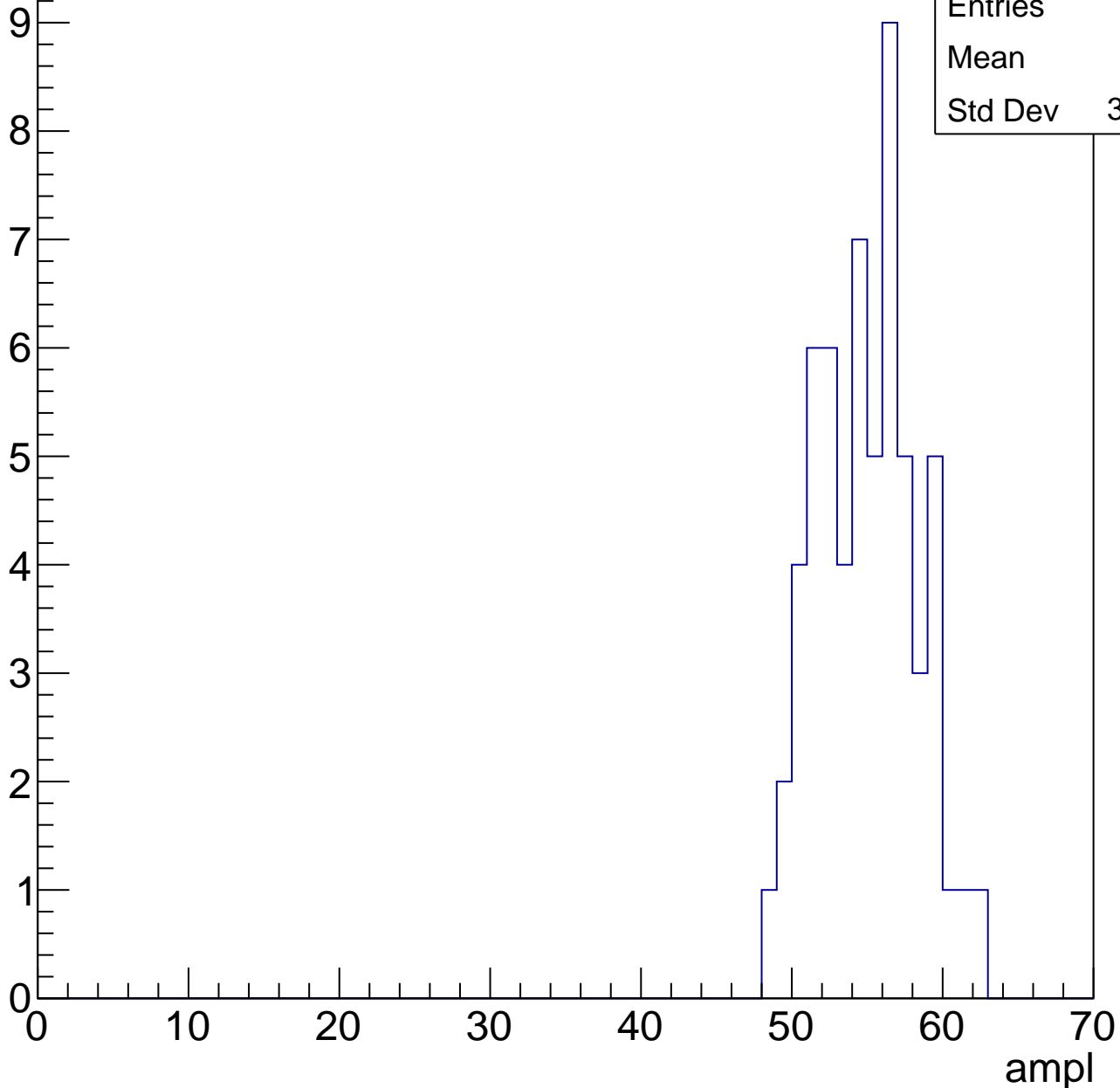


# B1L000S, U25-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.5
Std Dev	3.227



# B1L000S, U25-ch11, adc5

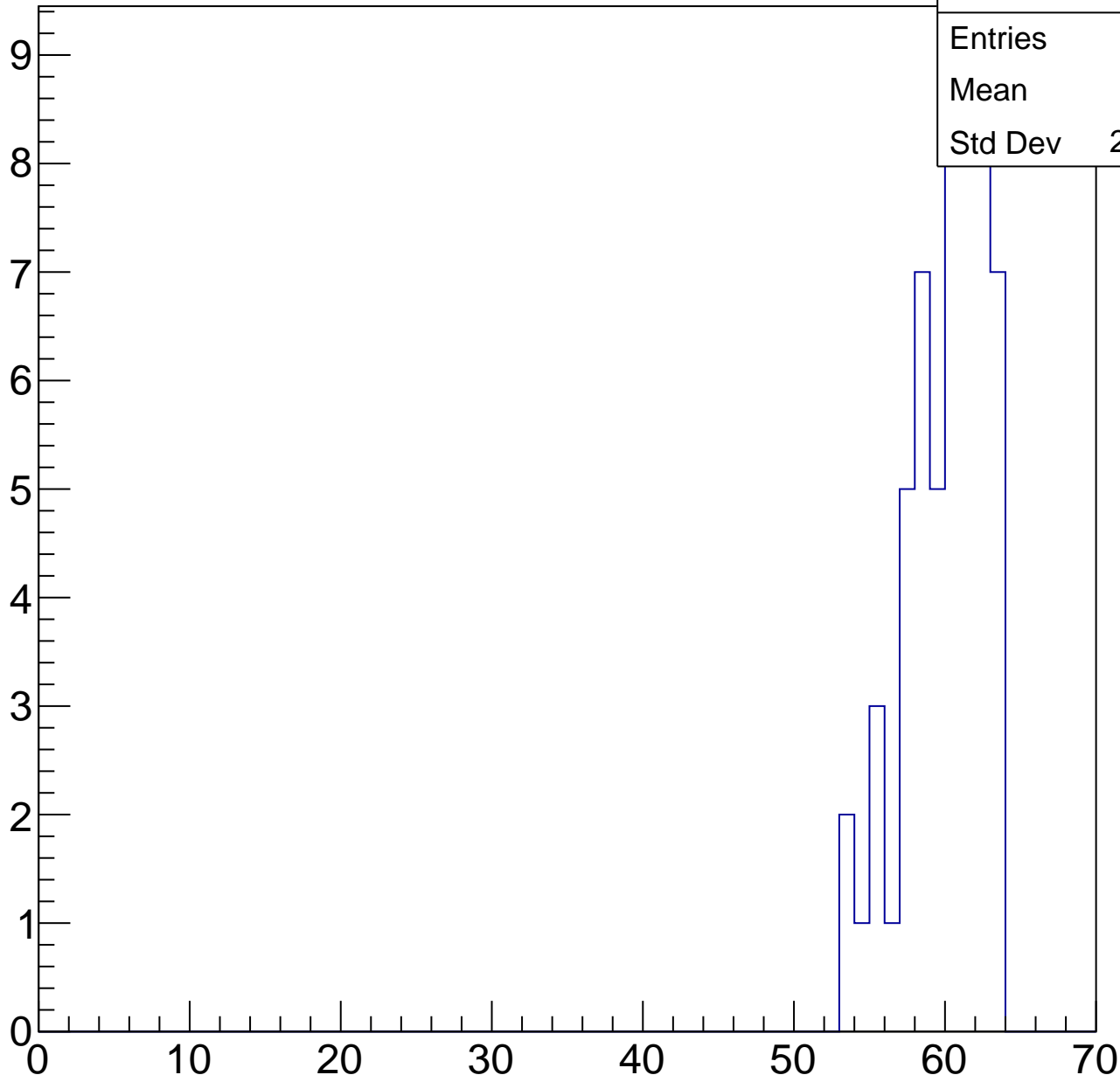
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	59.5
Std Dev	2.646

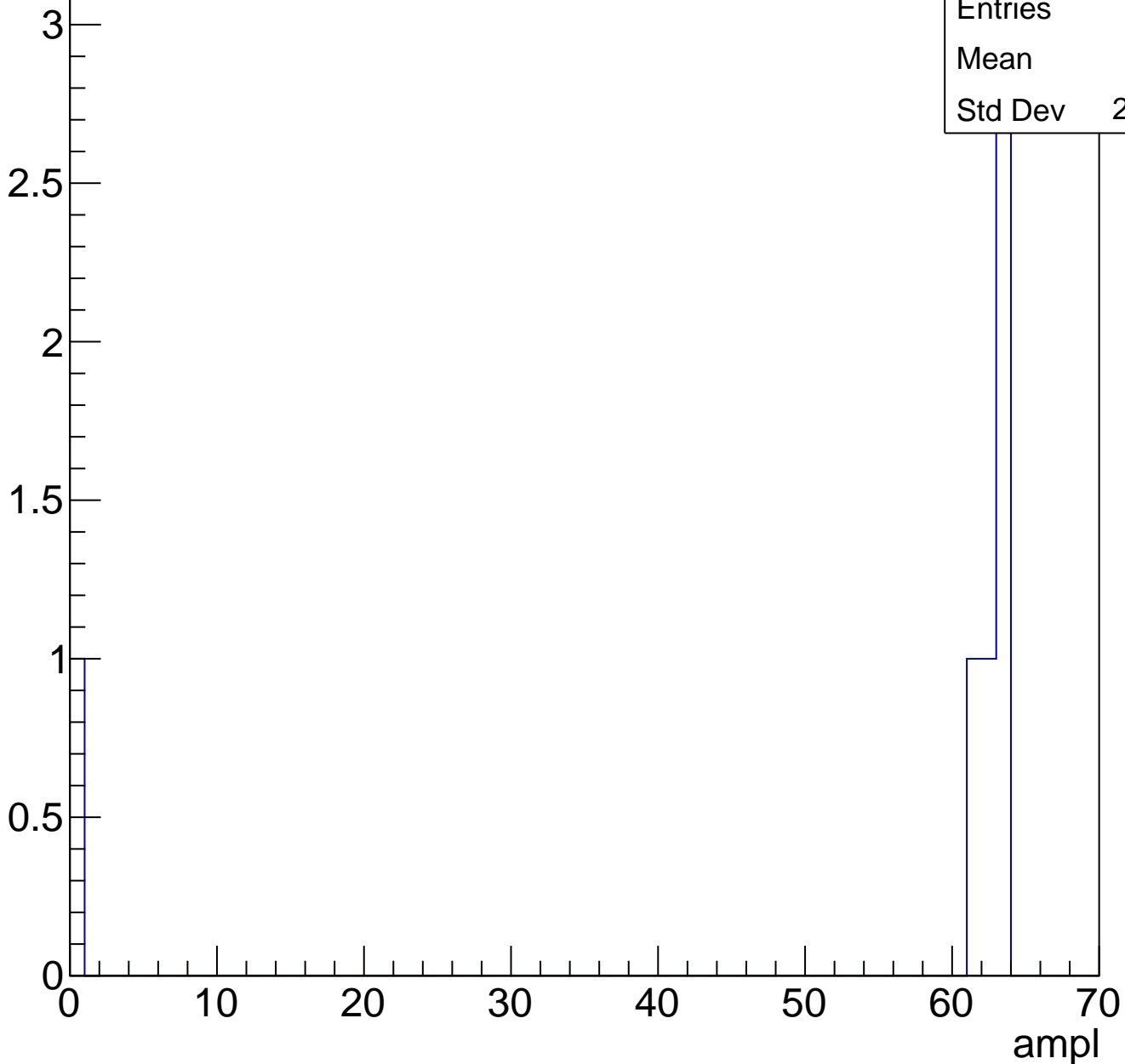
ampl



# B1L000S, U25-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	52
Std Dev	23.27



# B1L000S, U25-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch12, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	29.51
Std Dev	3.012

**Gaus mean : 30.2048**

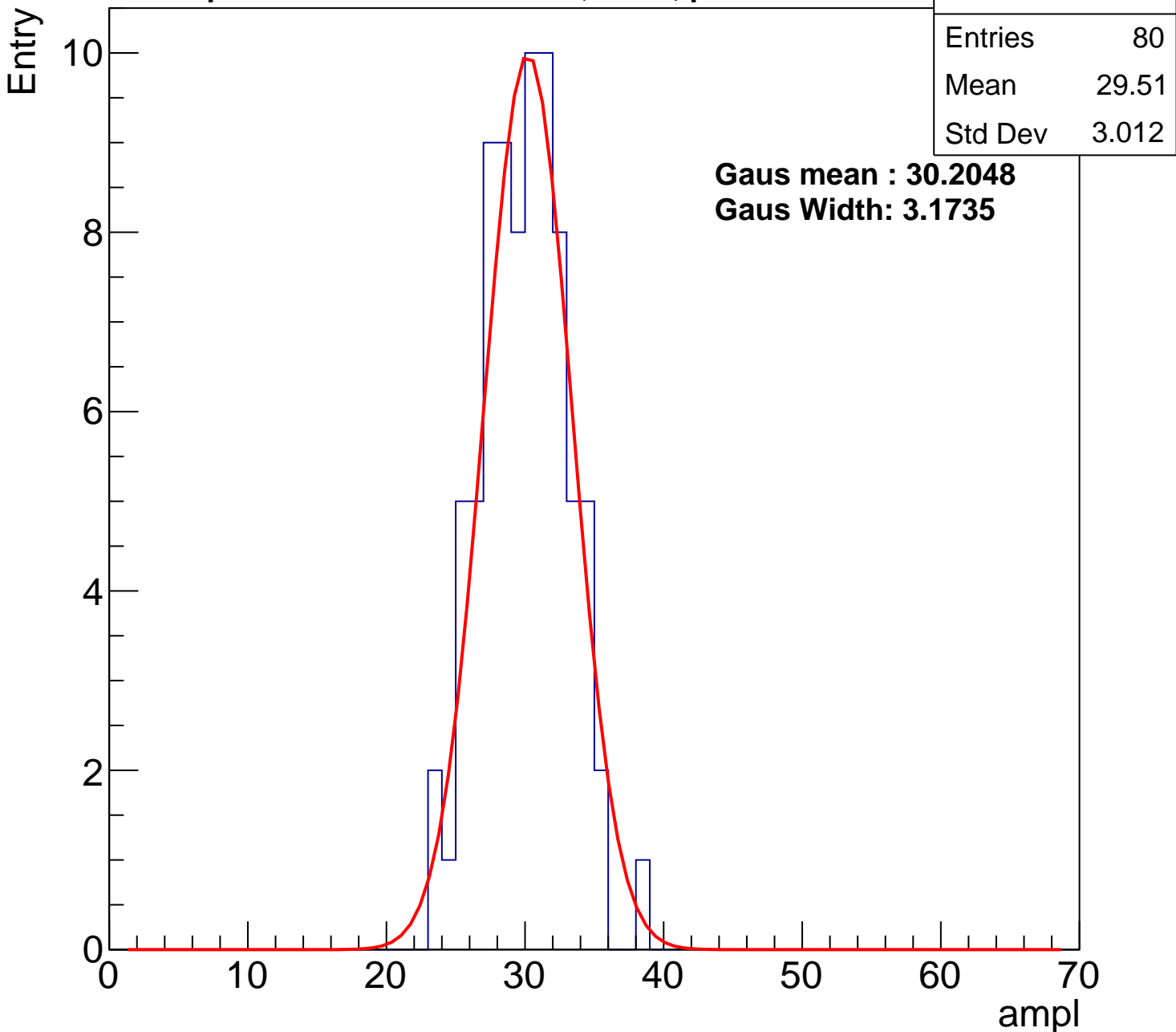
**Gaus Width: 3.1735**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



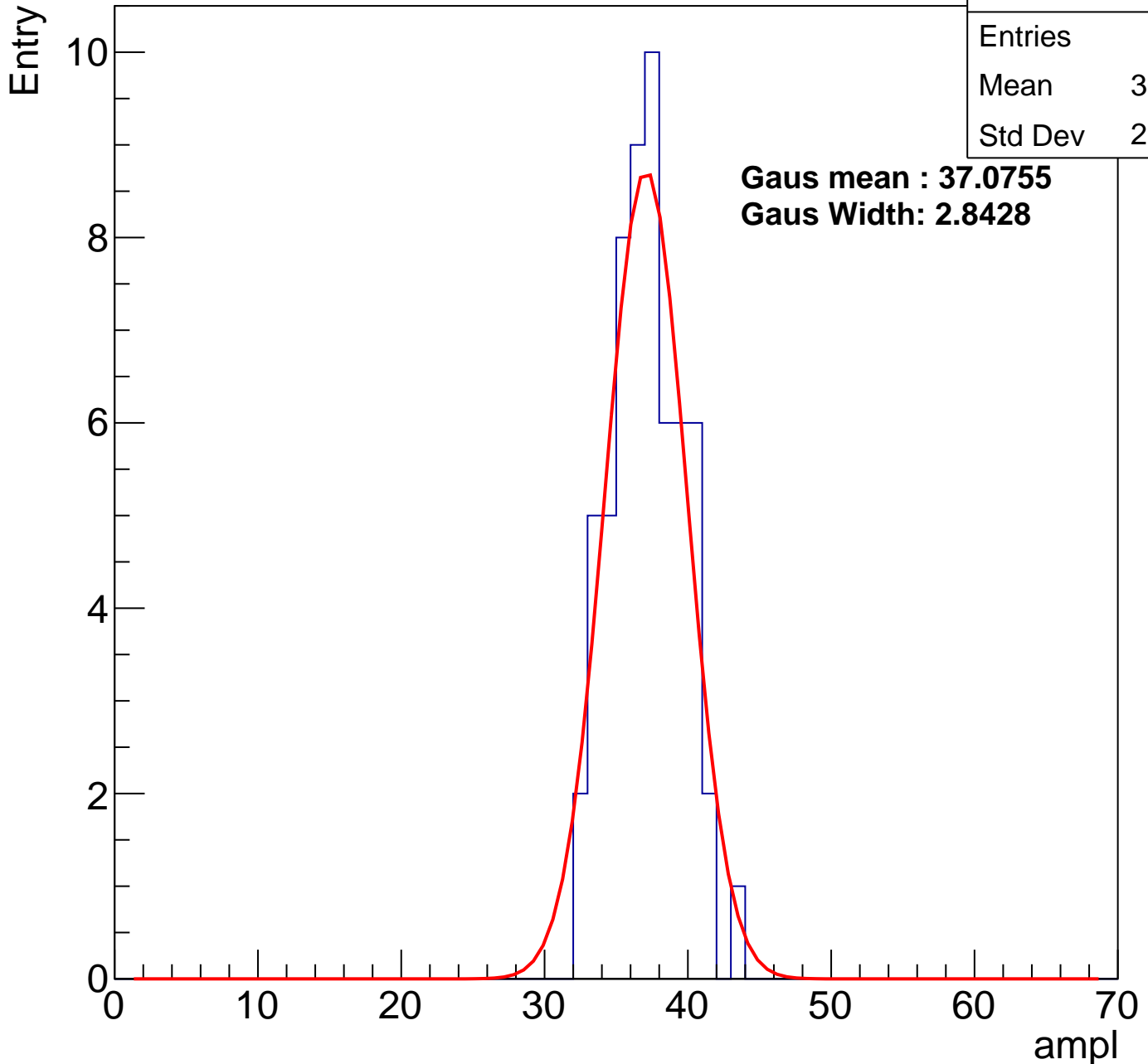
# B1L000S, U25-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	36.67
Std Dev	2.454

**Gaus mean : 37.0755**

**Gaus Width: 2.8428**



# B1L000S, U25-ch12, adc2

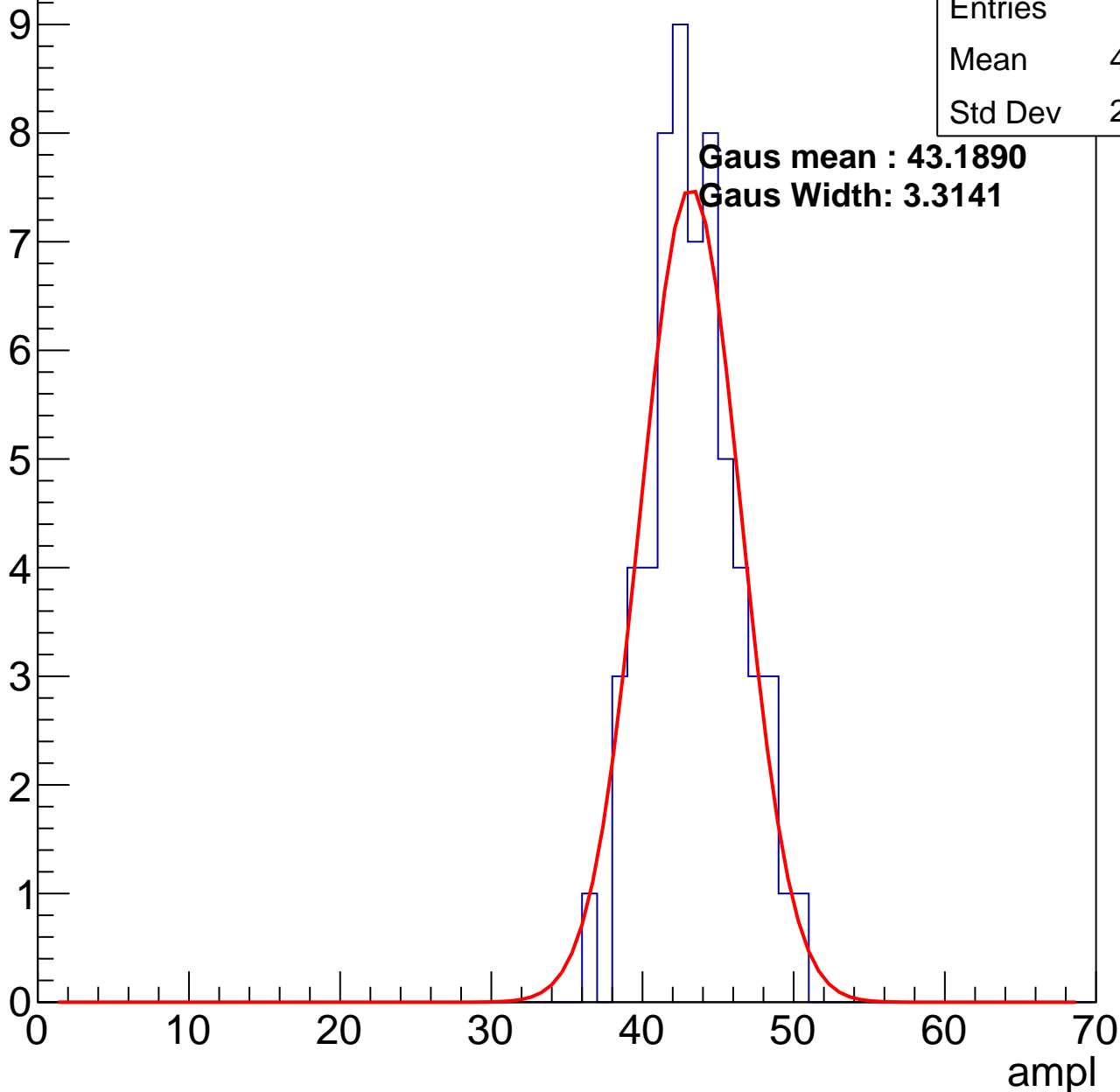
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.92
Std Dev	2.966

**Gaus mean : 43.1890**

**Gaus Width: 3.3141**

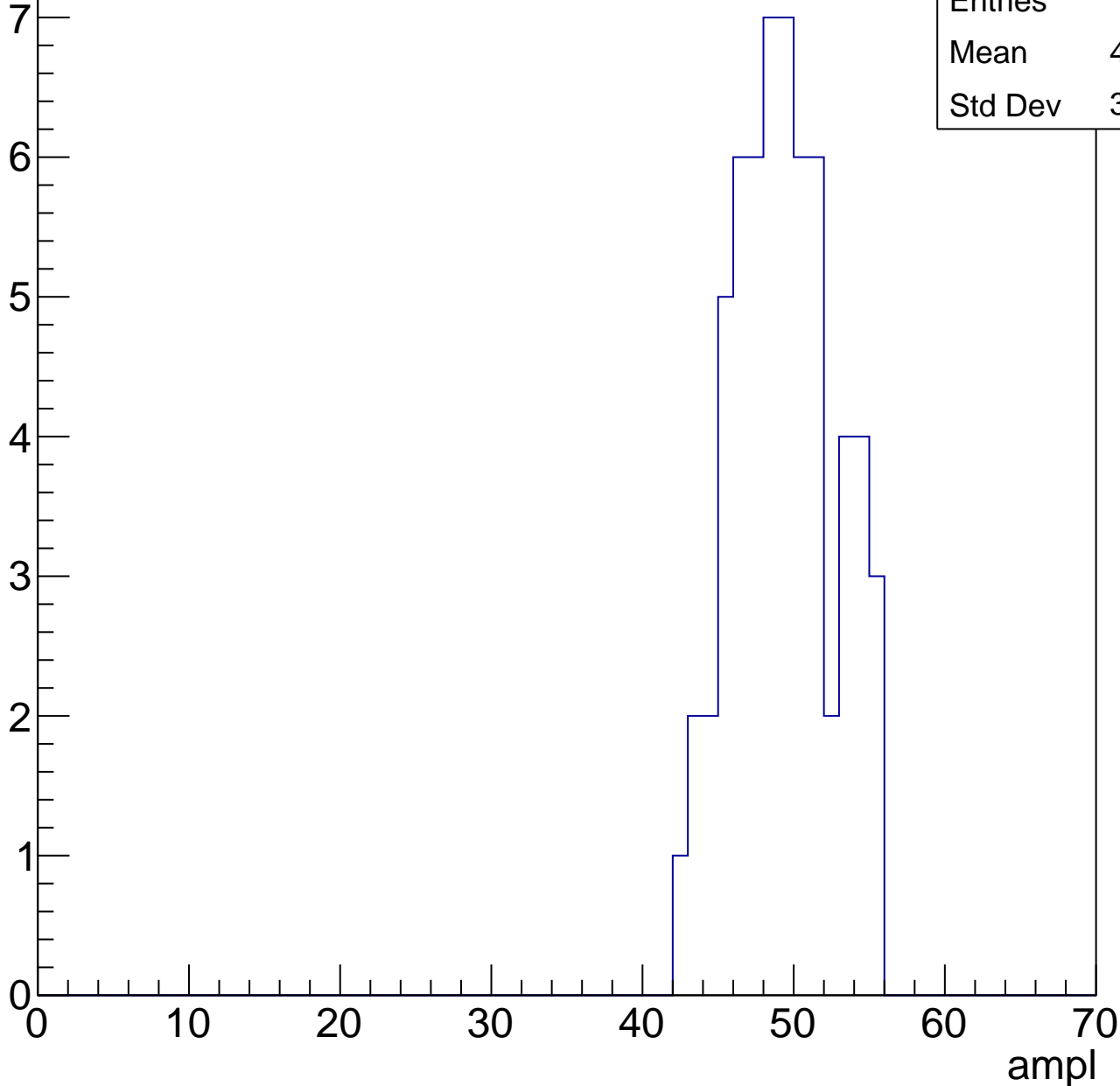


# B1L000S, U25-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	48.87
Std Dev	3.277

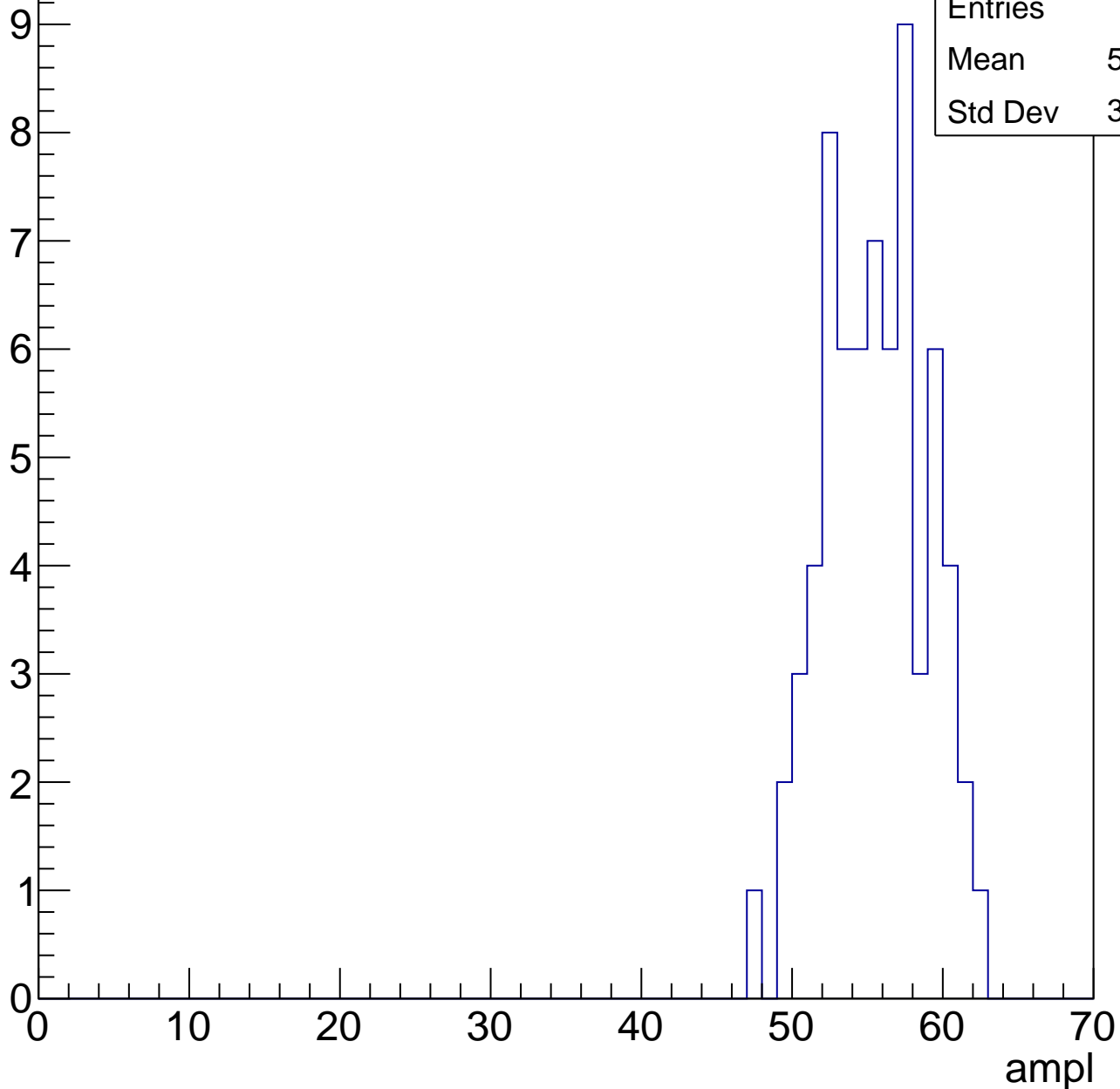


# B1L000S, U25-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

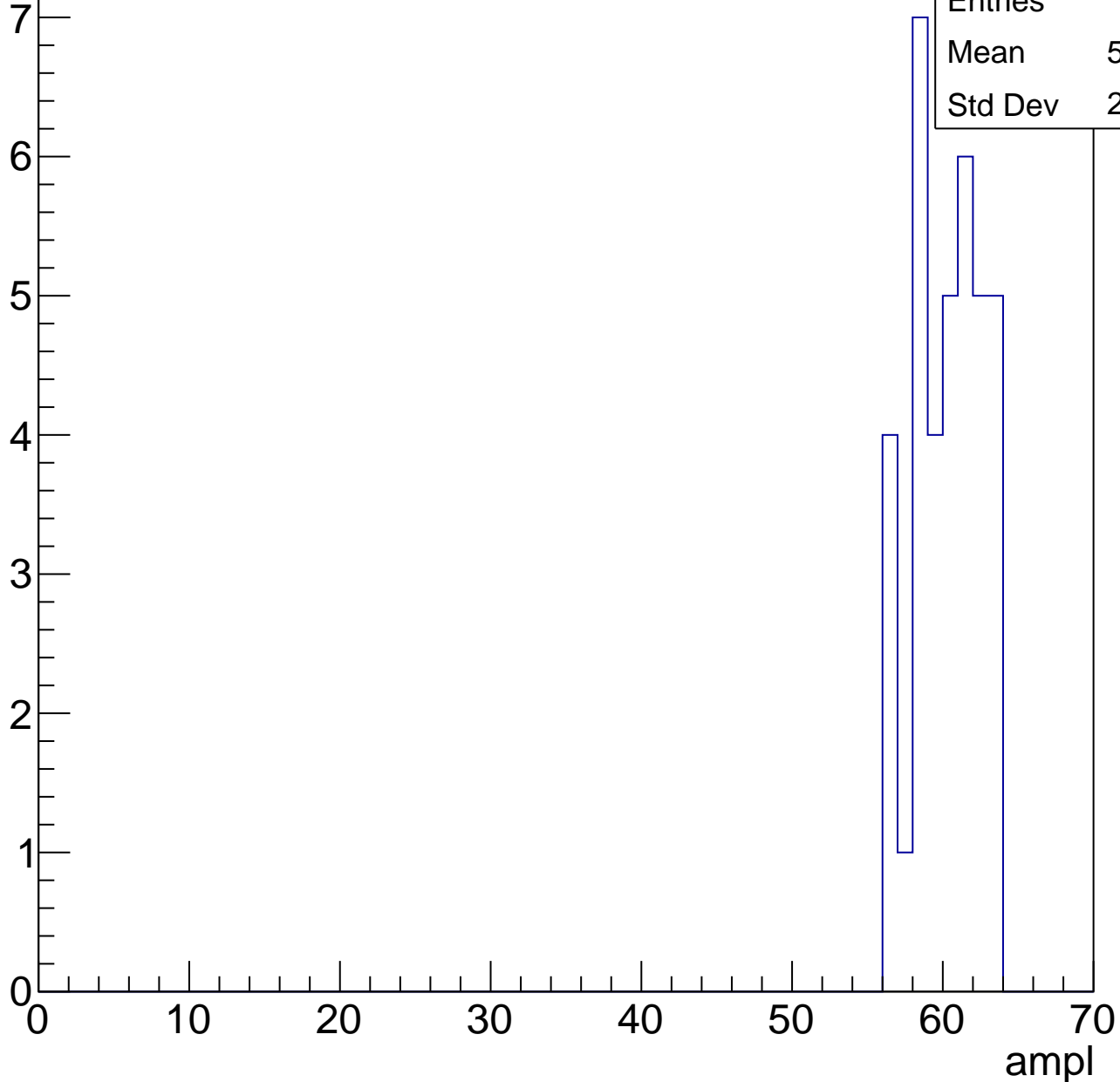
Entries	68
Mean	55.04
Std Dev	3.349



# B1L000S, U25-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



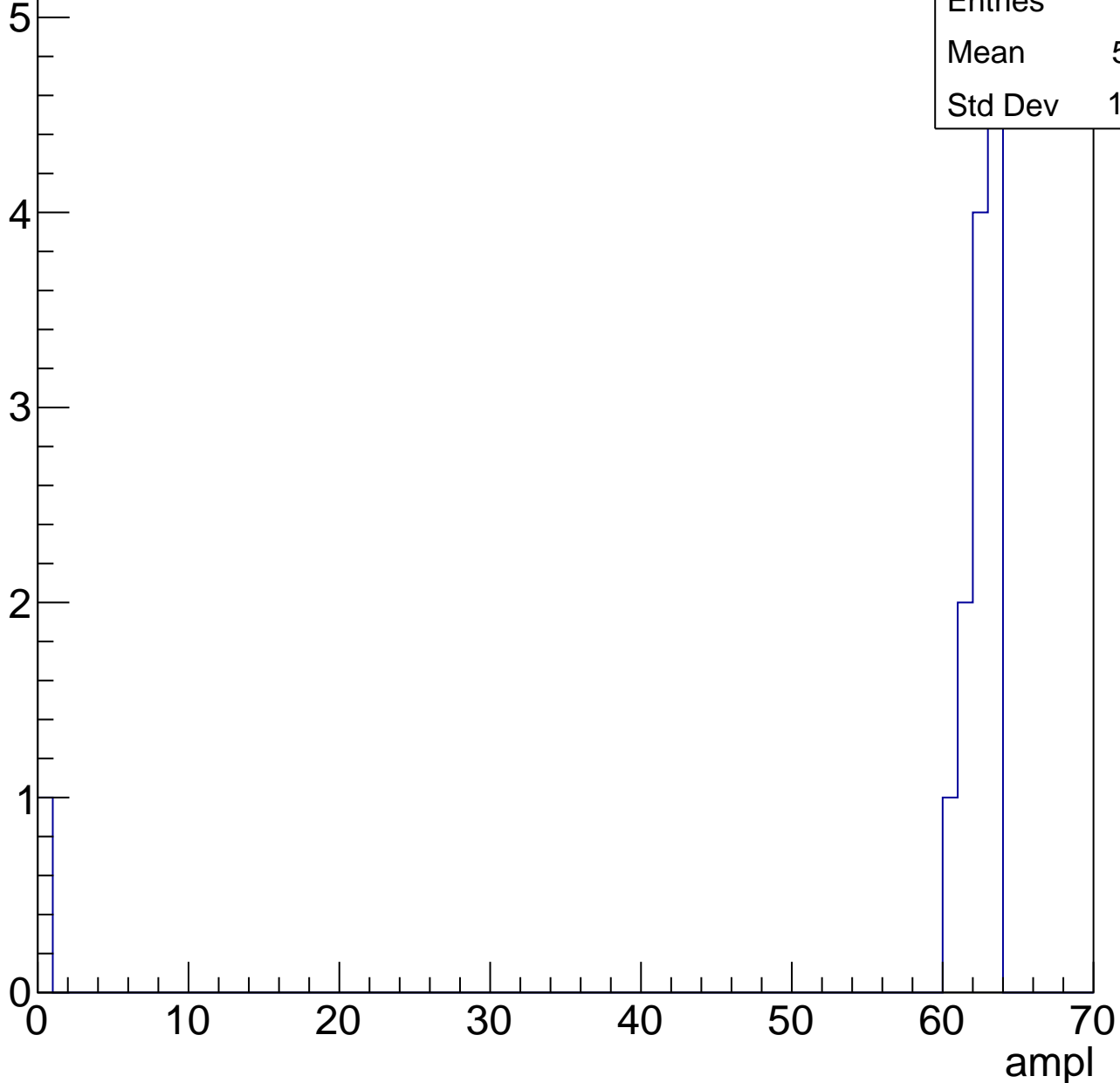
Entries	37
Mean	59.84
Std Dev	2.175

# B1L000S, U25-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	57.31
Std Dev	16.57





# B1L000S, U25-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch13, adc0

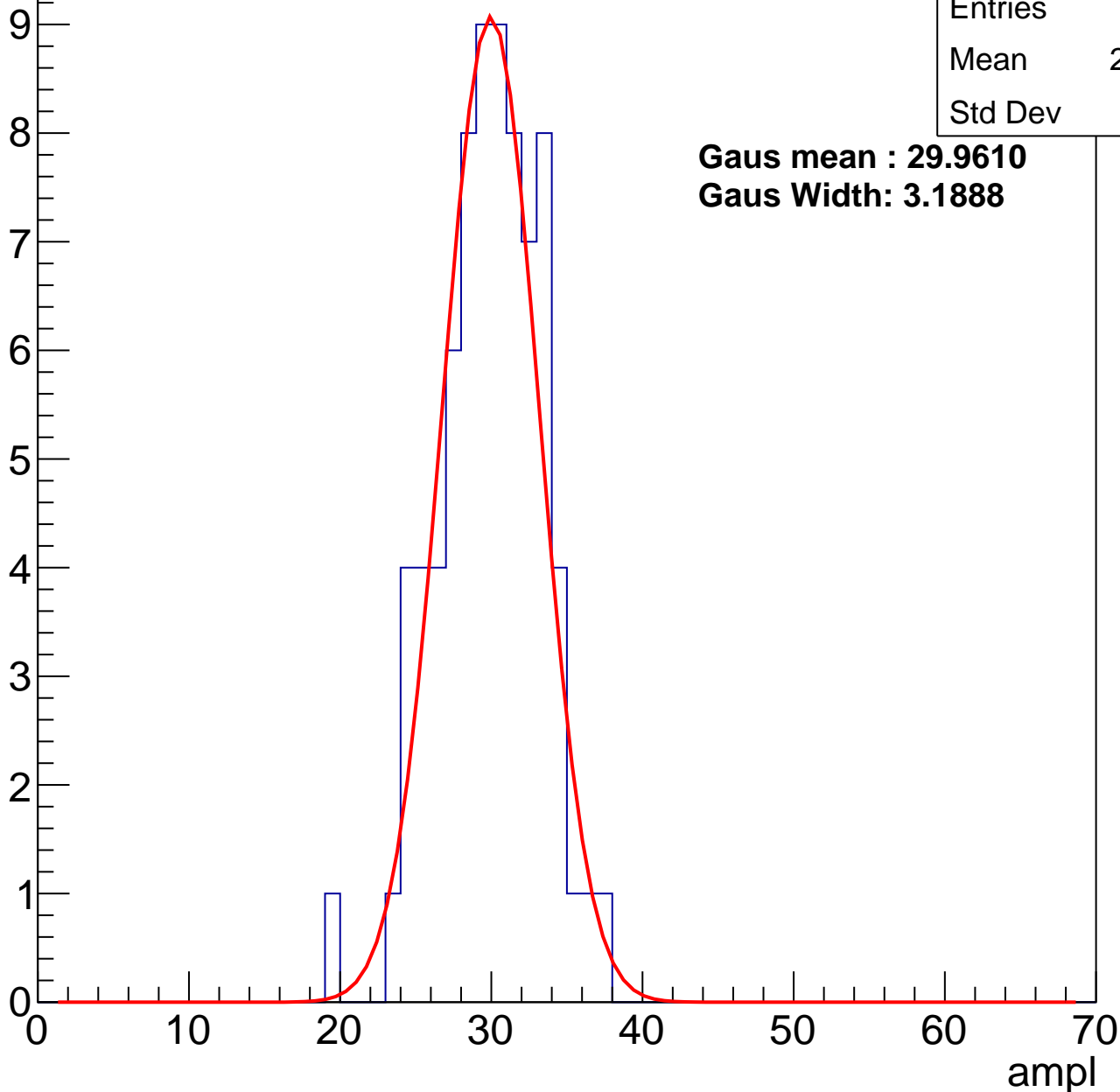
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	29.46
Std Dev	3.31

**Gaus mean : 29.9610**

**Gaus Width: 3.1888**



# B1L000S, U25-ch13, adc1

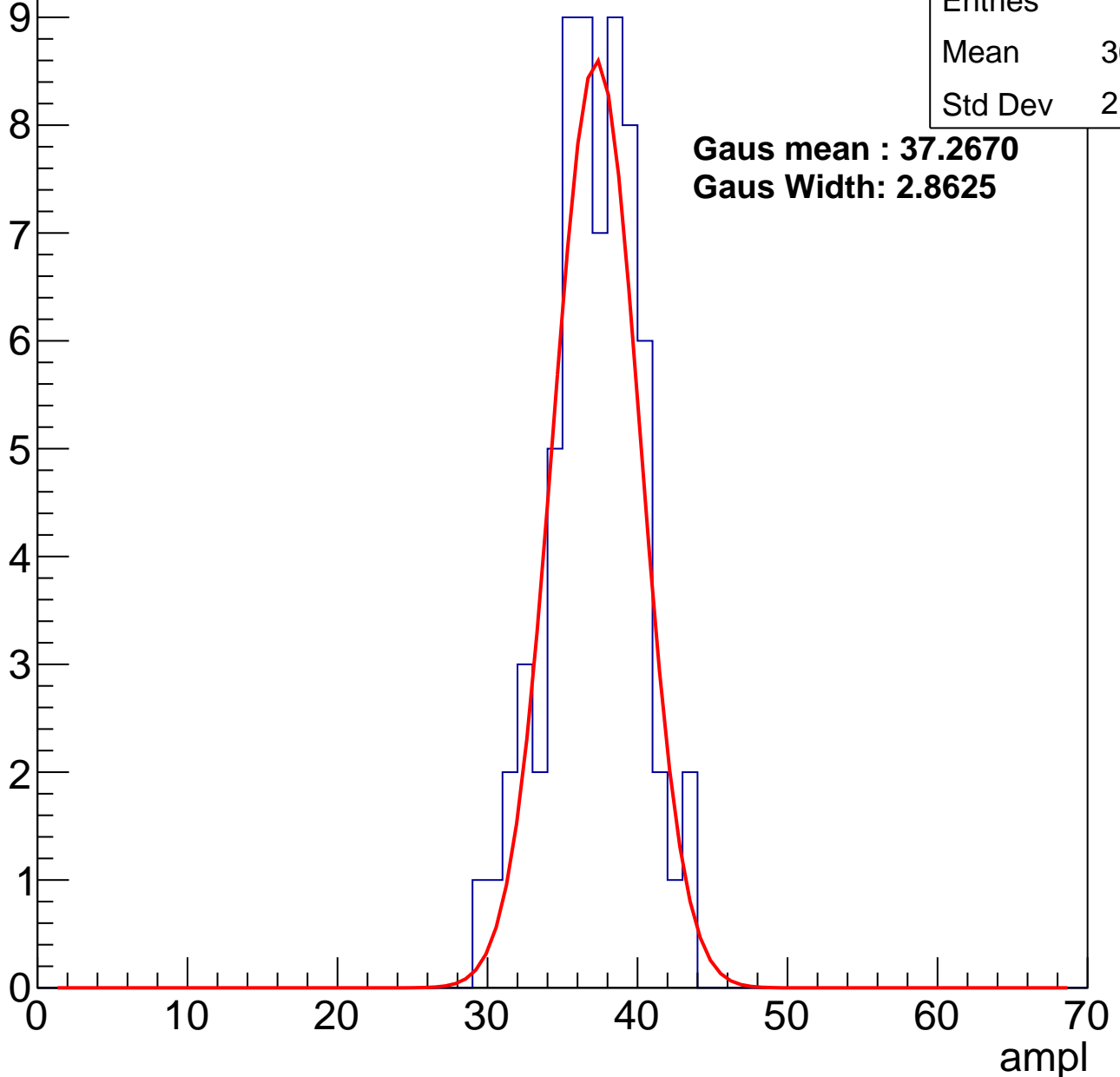
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.64
Std Dev	2.986

**Gaus mean : 37.2670**

**Gaus Width: 2.8625**



# B1L000S, U25-ch13, adc2

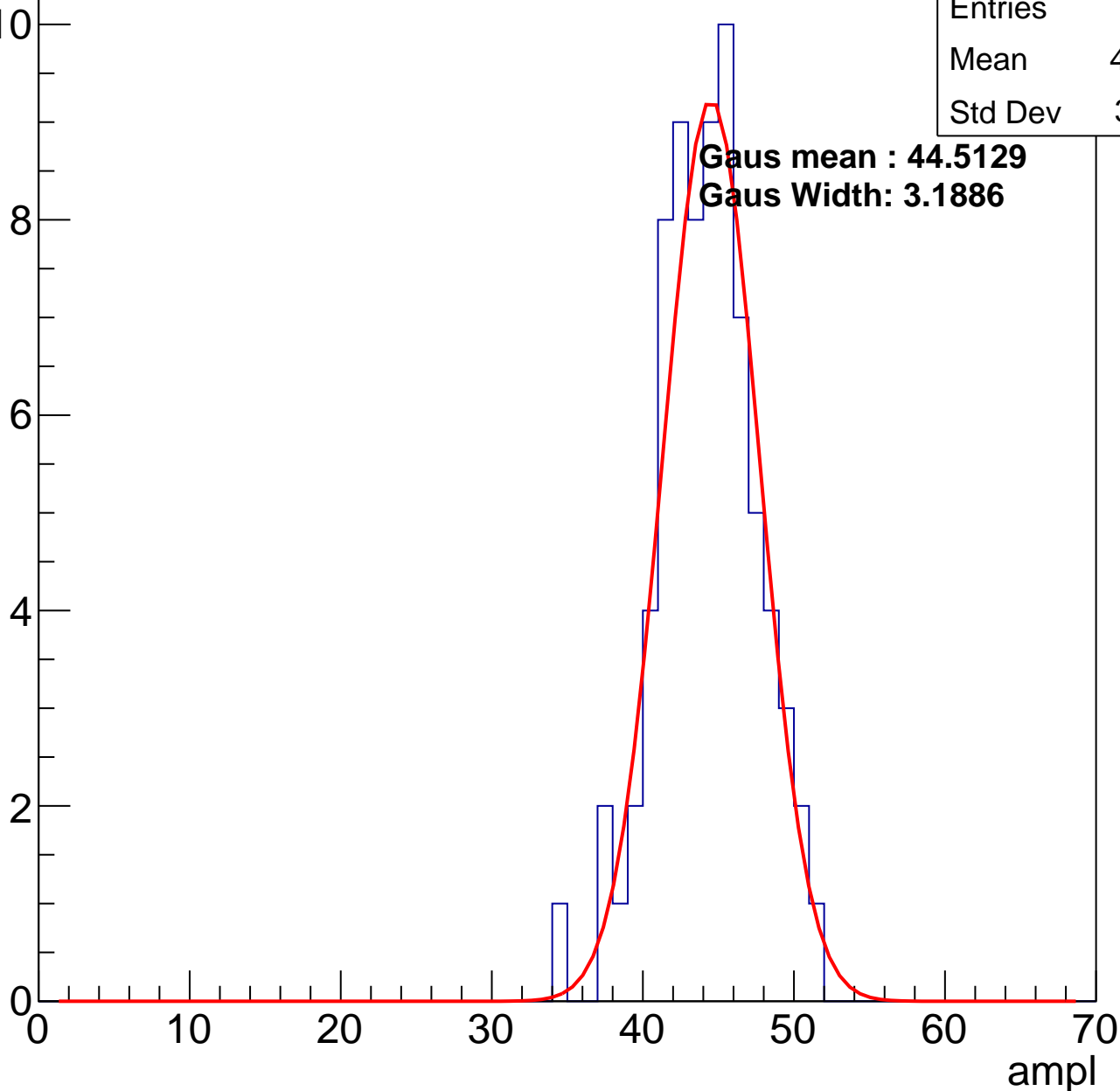
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	43.78
Std Dev	3.251

**Gaus mean : 44.5129**

**Gaus Width: 3.1886**

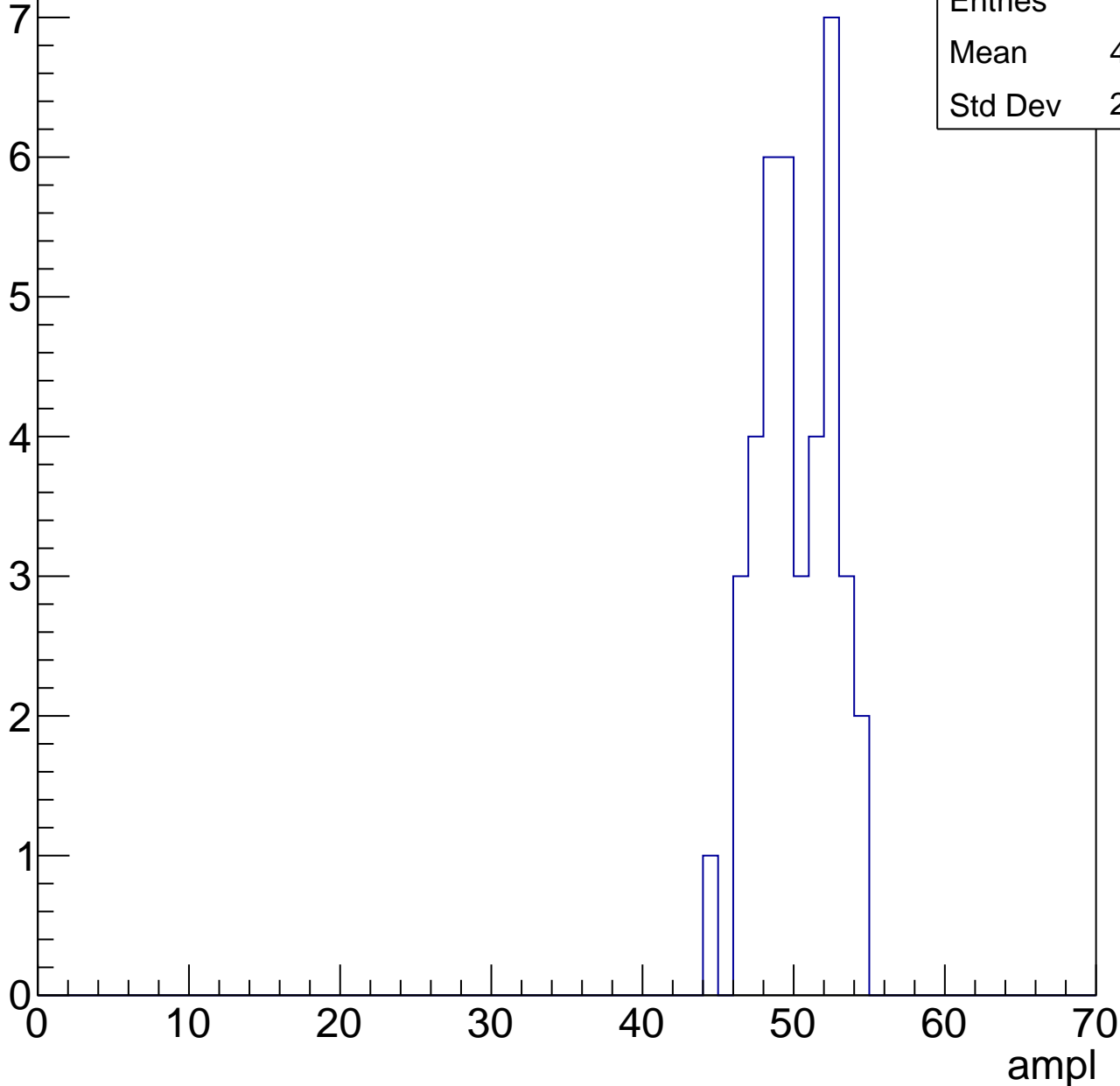


# B1L000S, U25-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	49.67
Std Dev	2.463

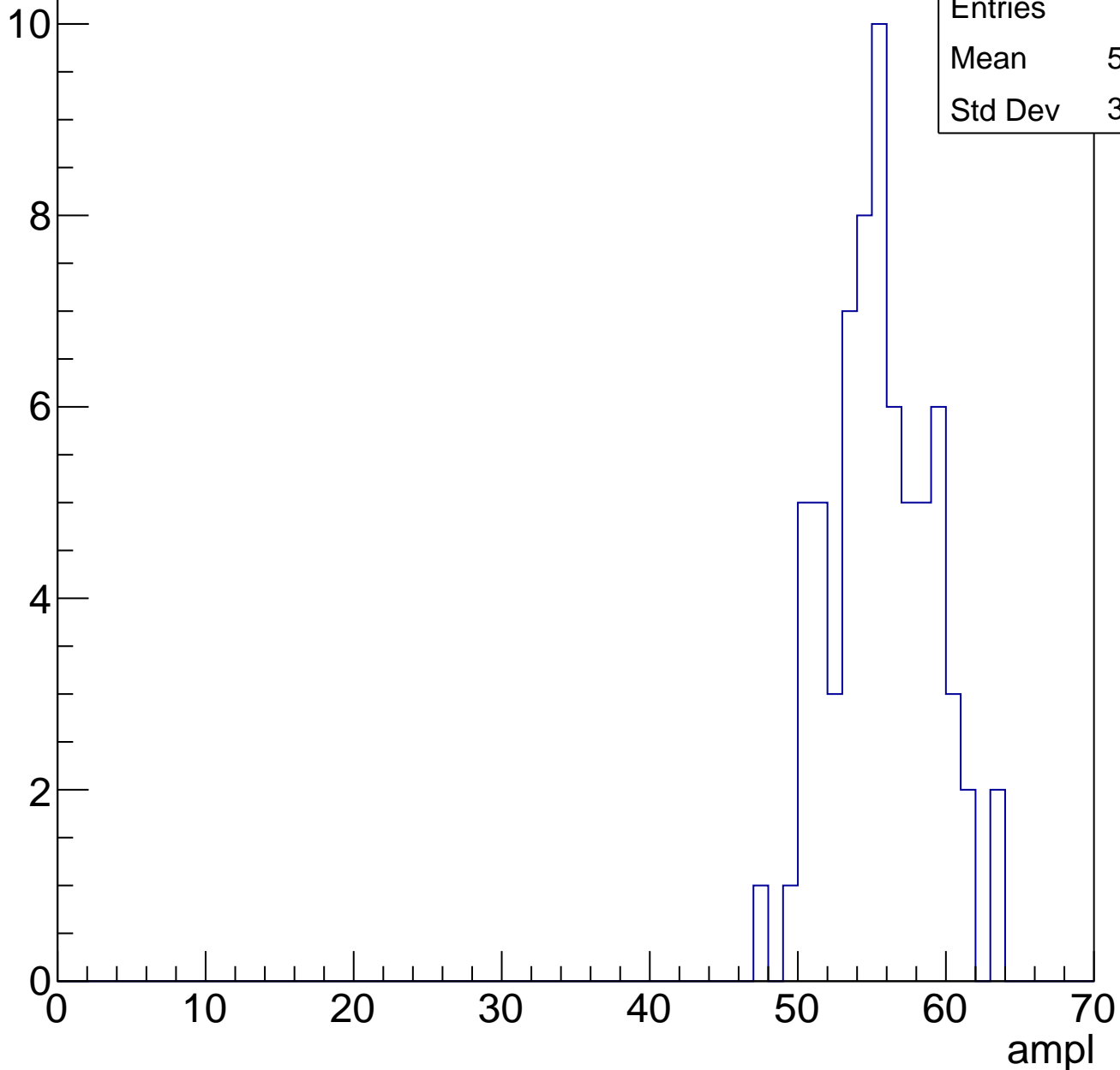


# B1L000S, U25-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	55.12
Std Dev	3.424

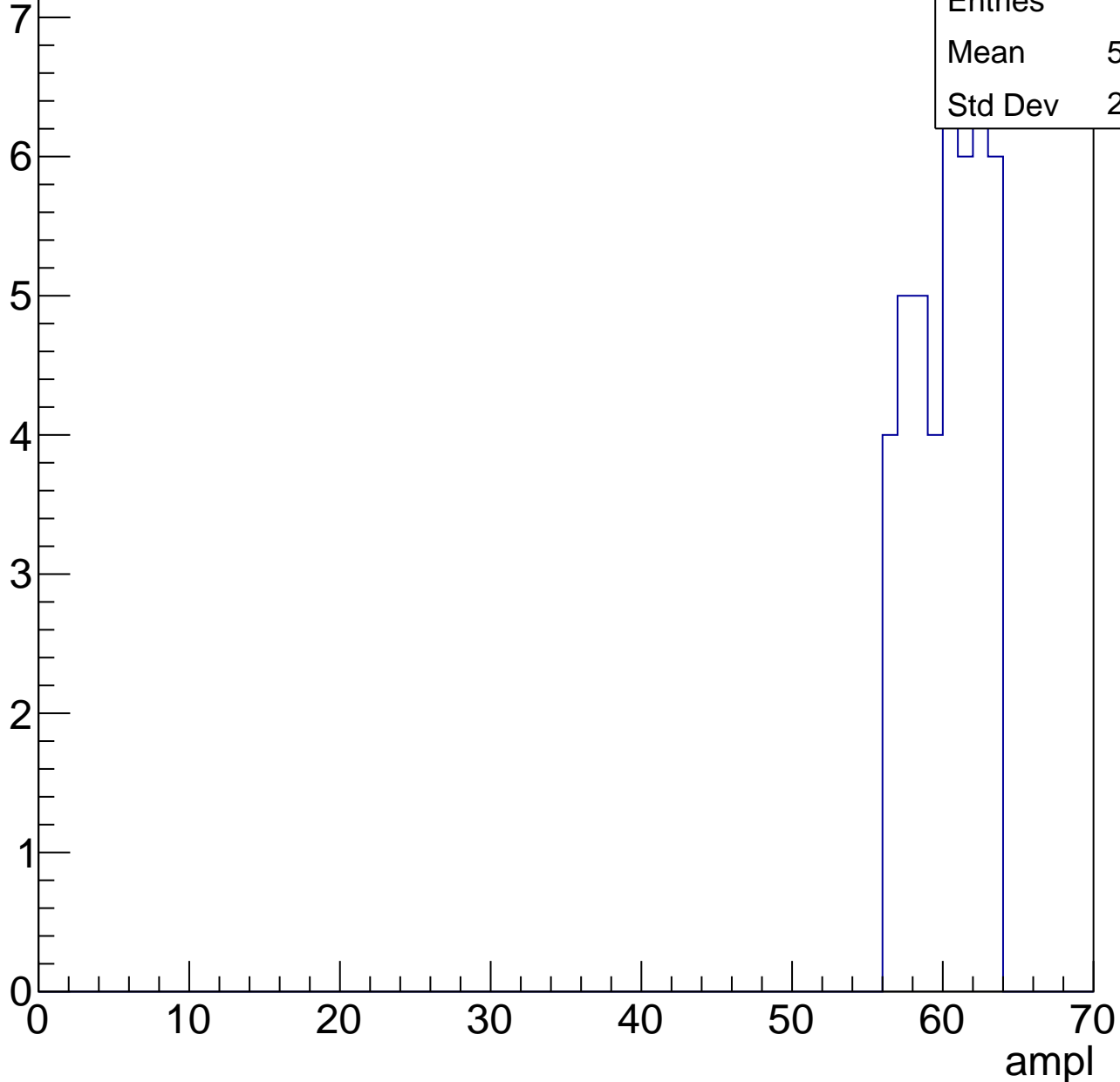
Entry



# B1L000S, U25-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

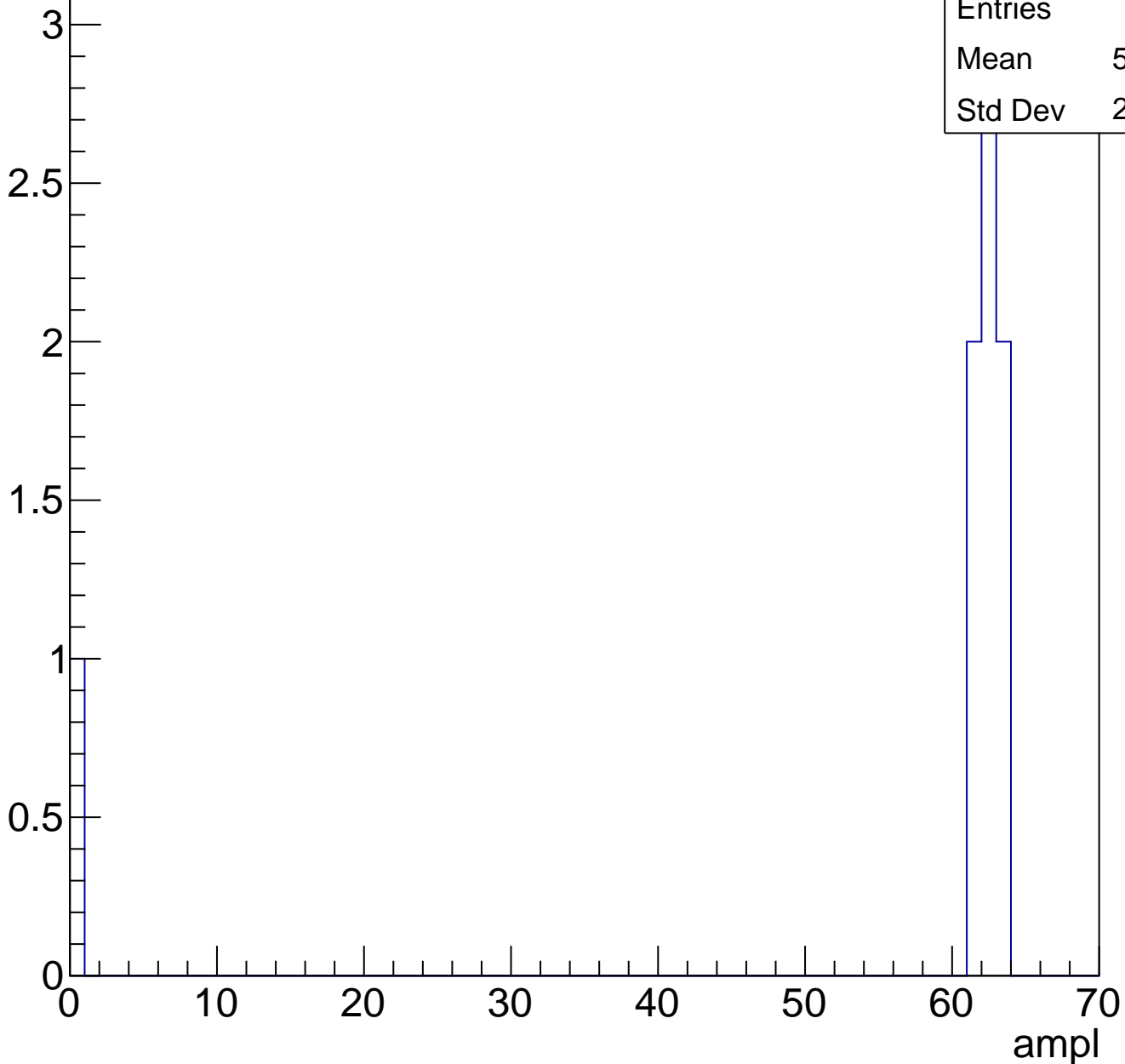
Entry



# B1L000S, U25-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch14, adc0

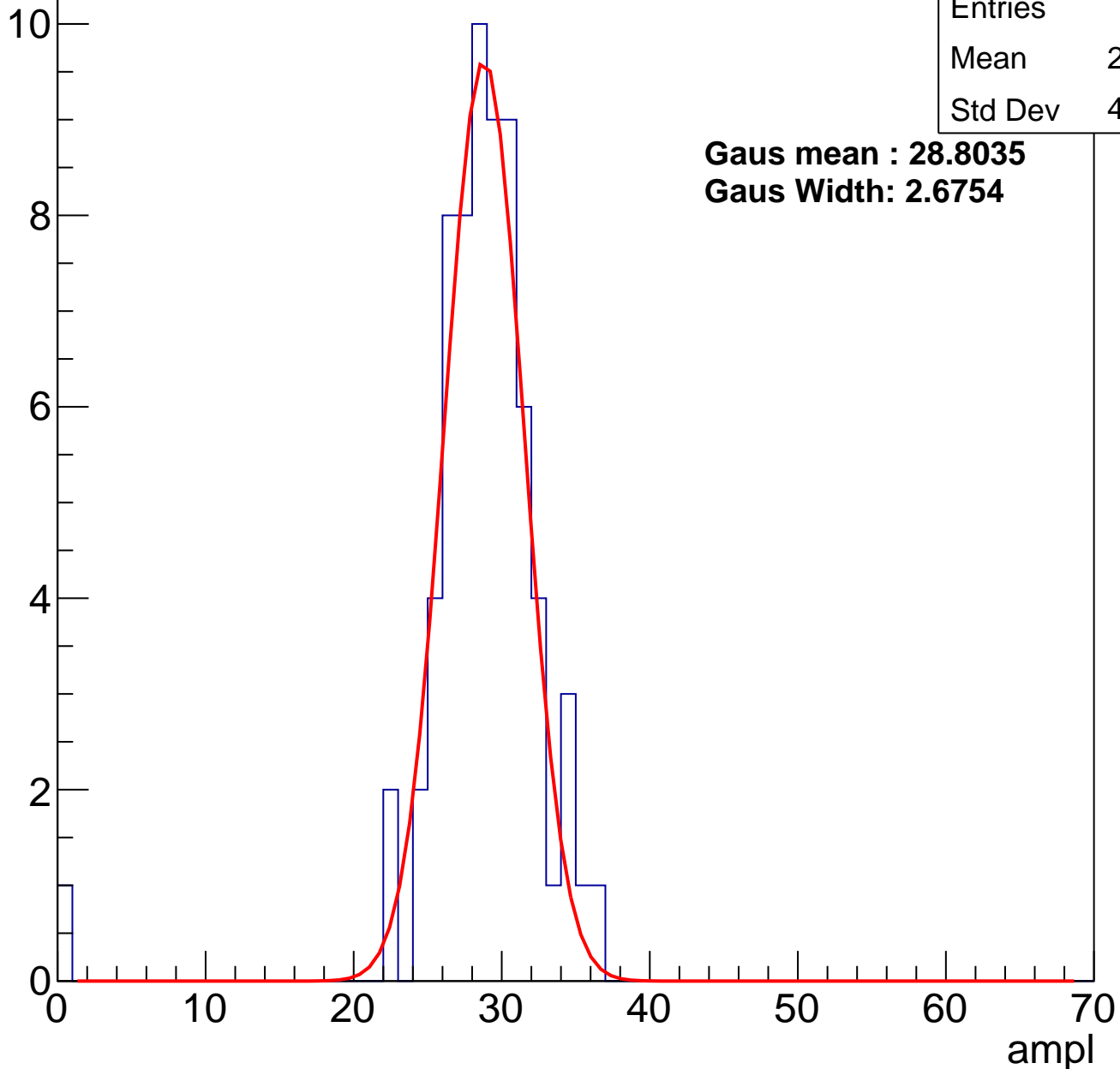
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	28.22
Std Dev	4.455

**Gaus mean : 28.8035**

**Gaus Width: 2.6754**

Entry



# B1L000S, U25-ch14, adc1

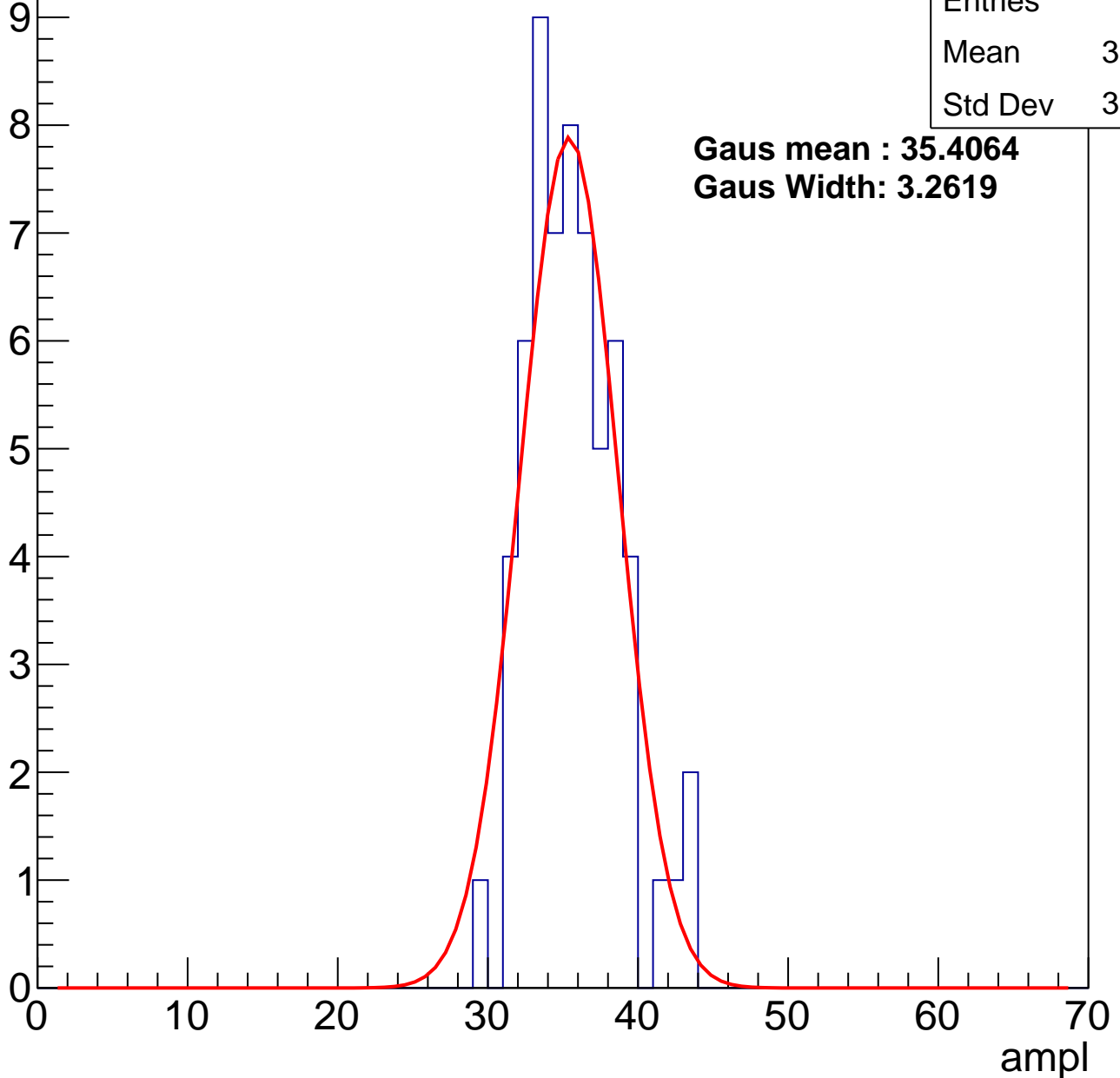
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.25
Std Dev	3.006

**Gaus mean : 35.4064**

**Gaus Width: 3.2619**



# B1L000S, U25-ch14, adc2

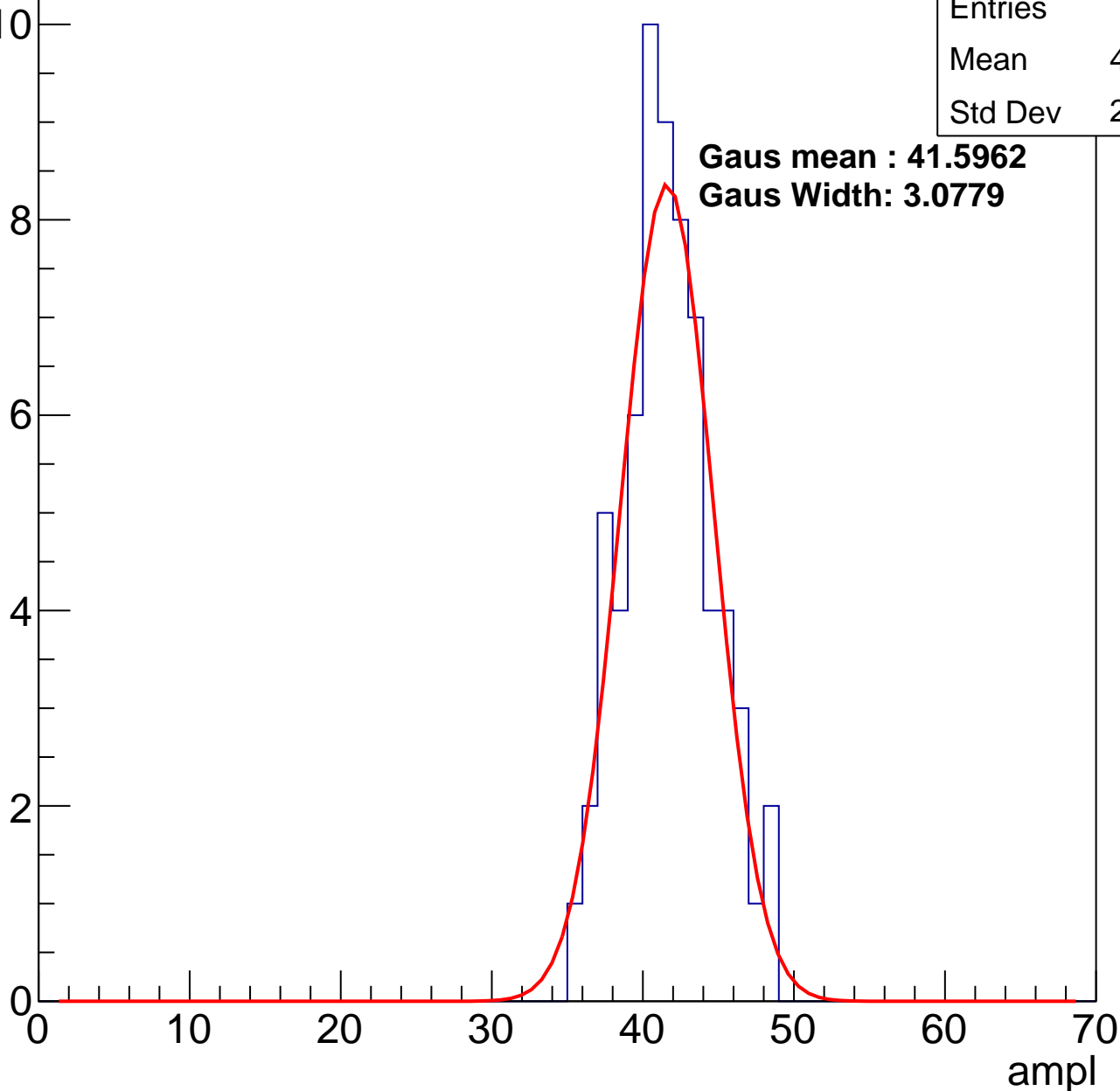
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	41.23
Std Dev	2.958

**Gaus mean : 41.5962**

**Gaus Width: 3.0779**

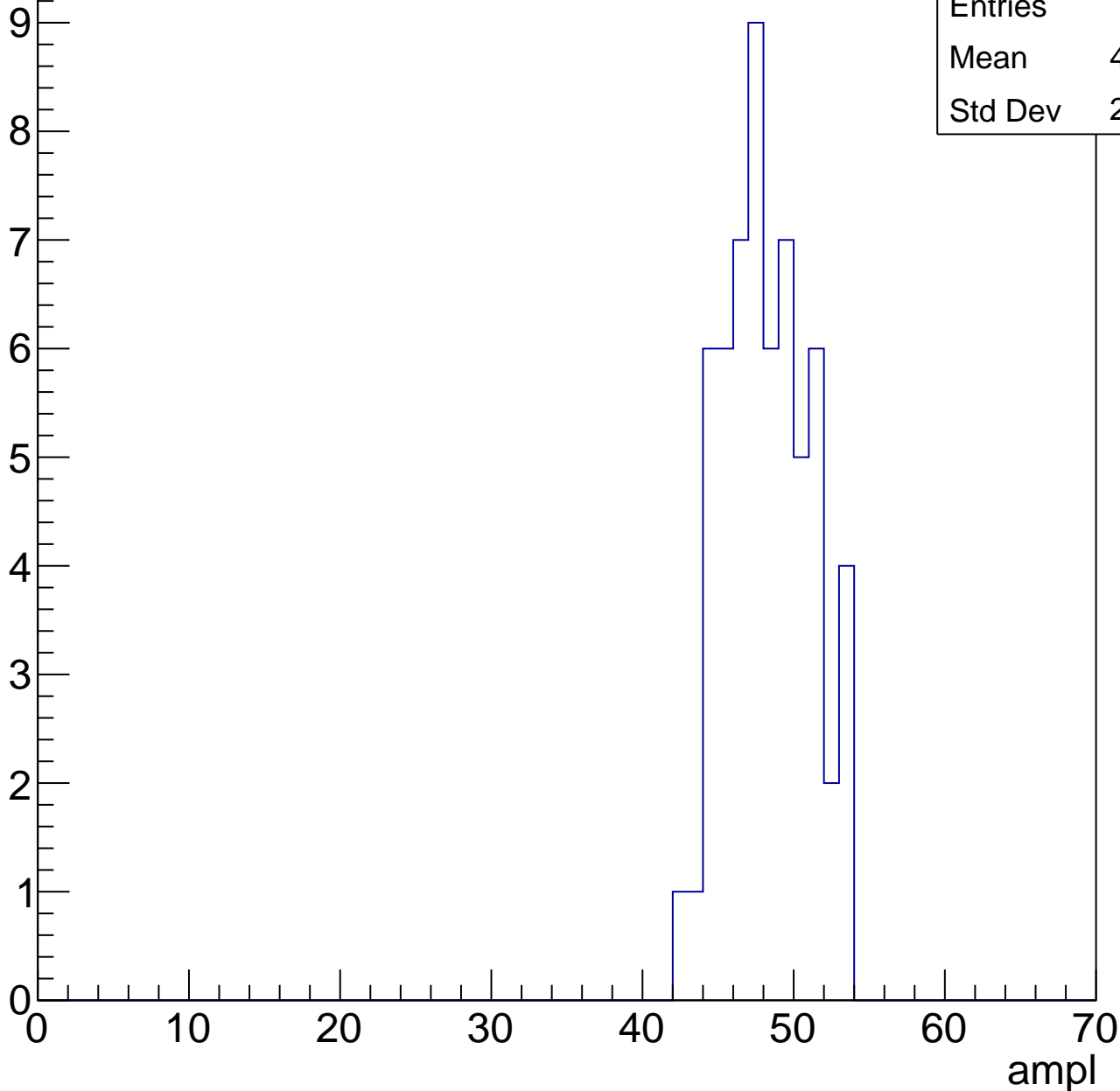


# B1L000S, U25-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

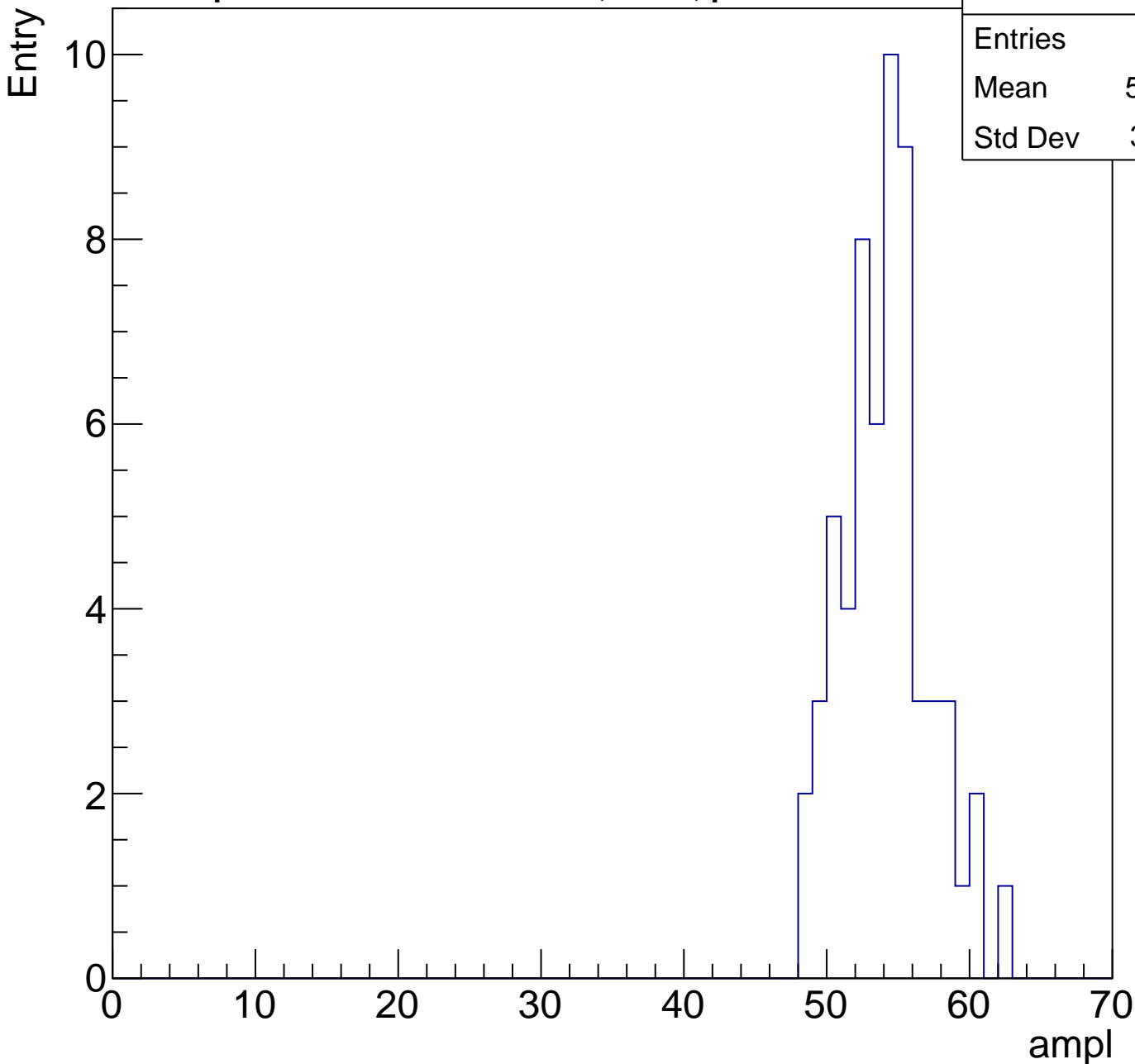
Entries	60
Mean	47.78
Std Dev	2.763



# B1L000S, U25-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	53.67
Std Dev	3.031

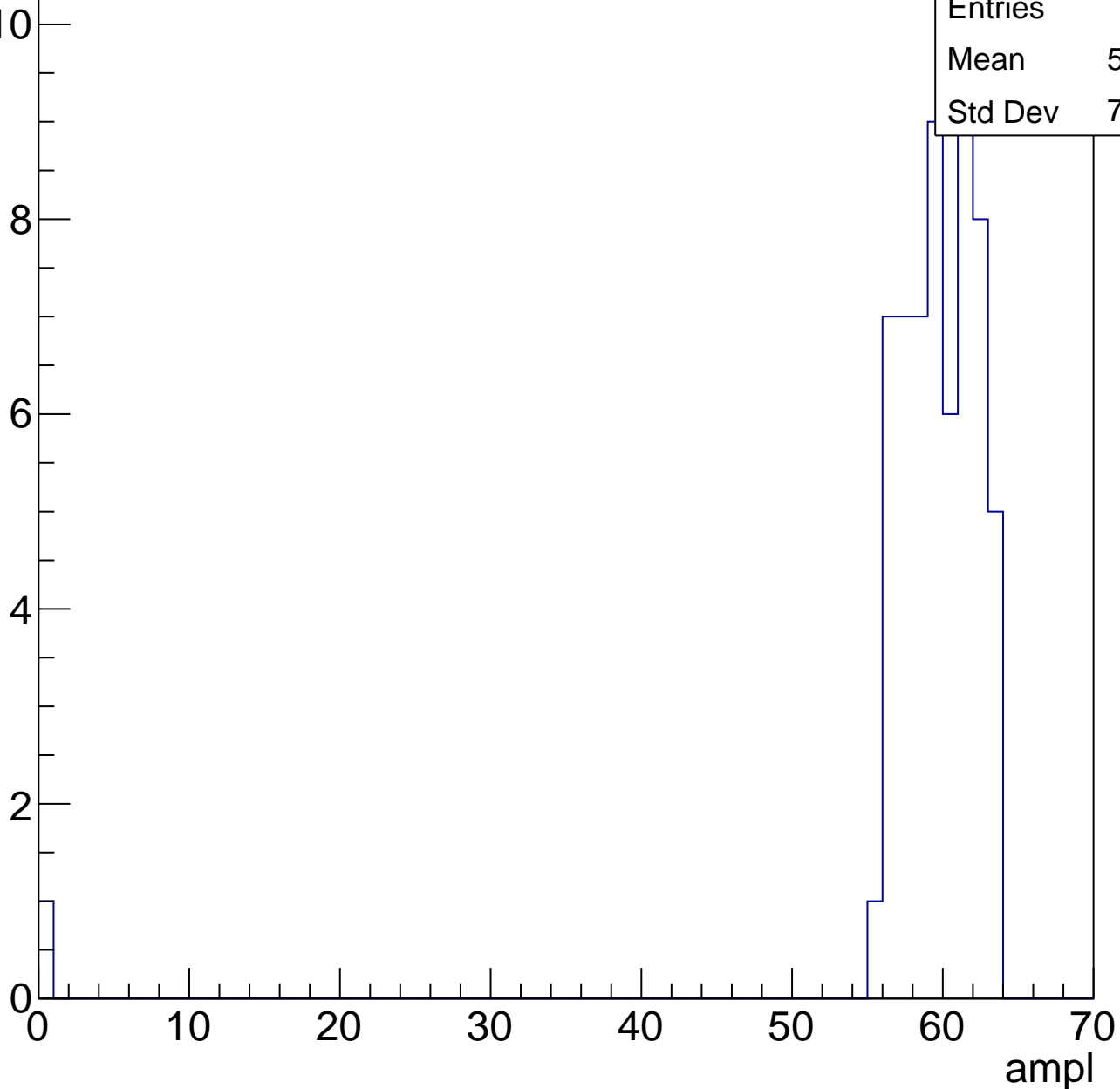


# B1L000S, U25-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.43
Std Dev	7.865

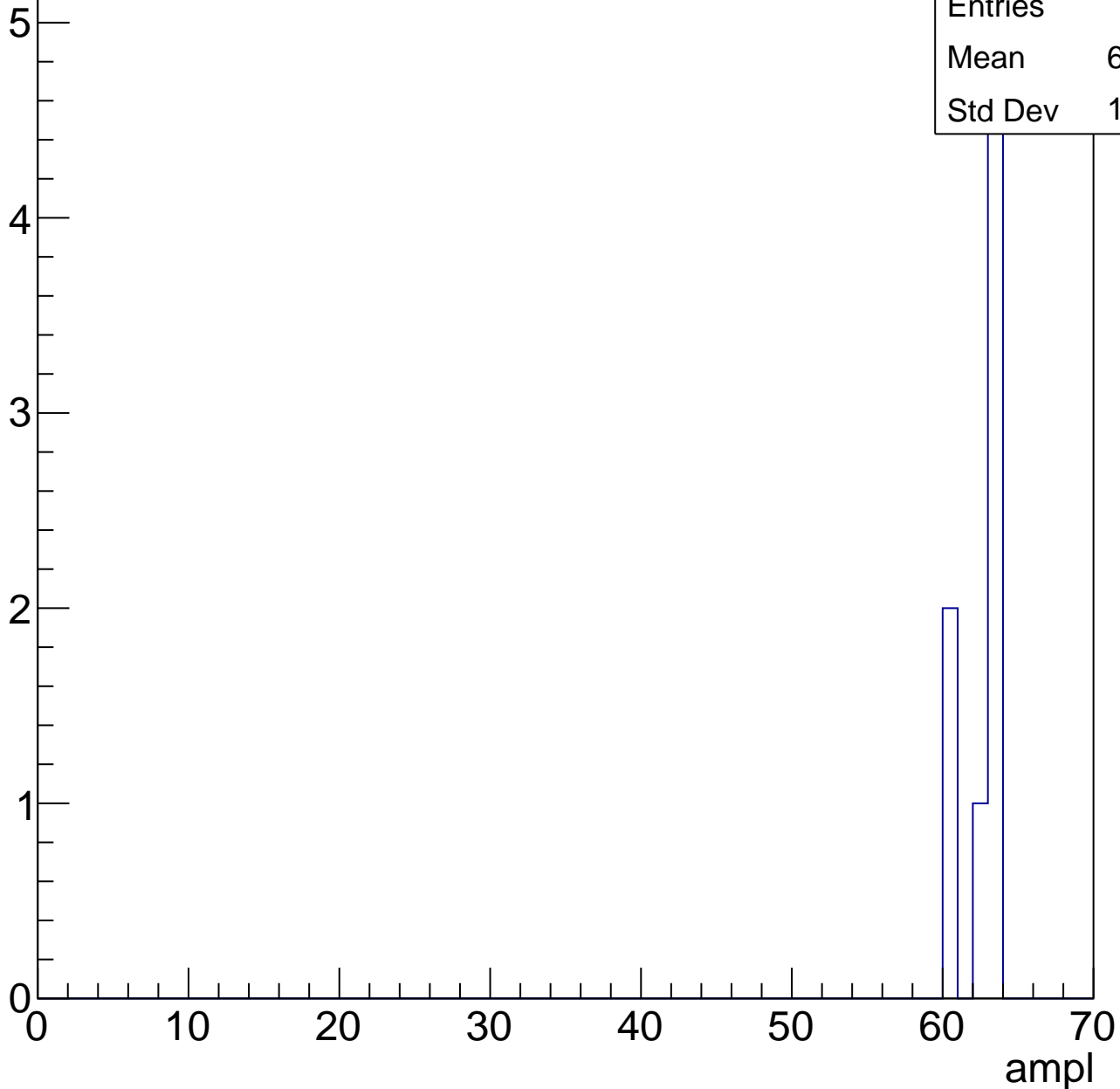


# B1L000S, U25-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.12
Std Dev	1.269





# B1L000S, U25-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U25-ch15, adc0

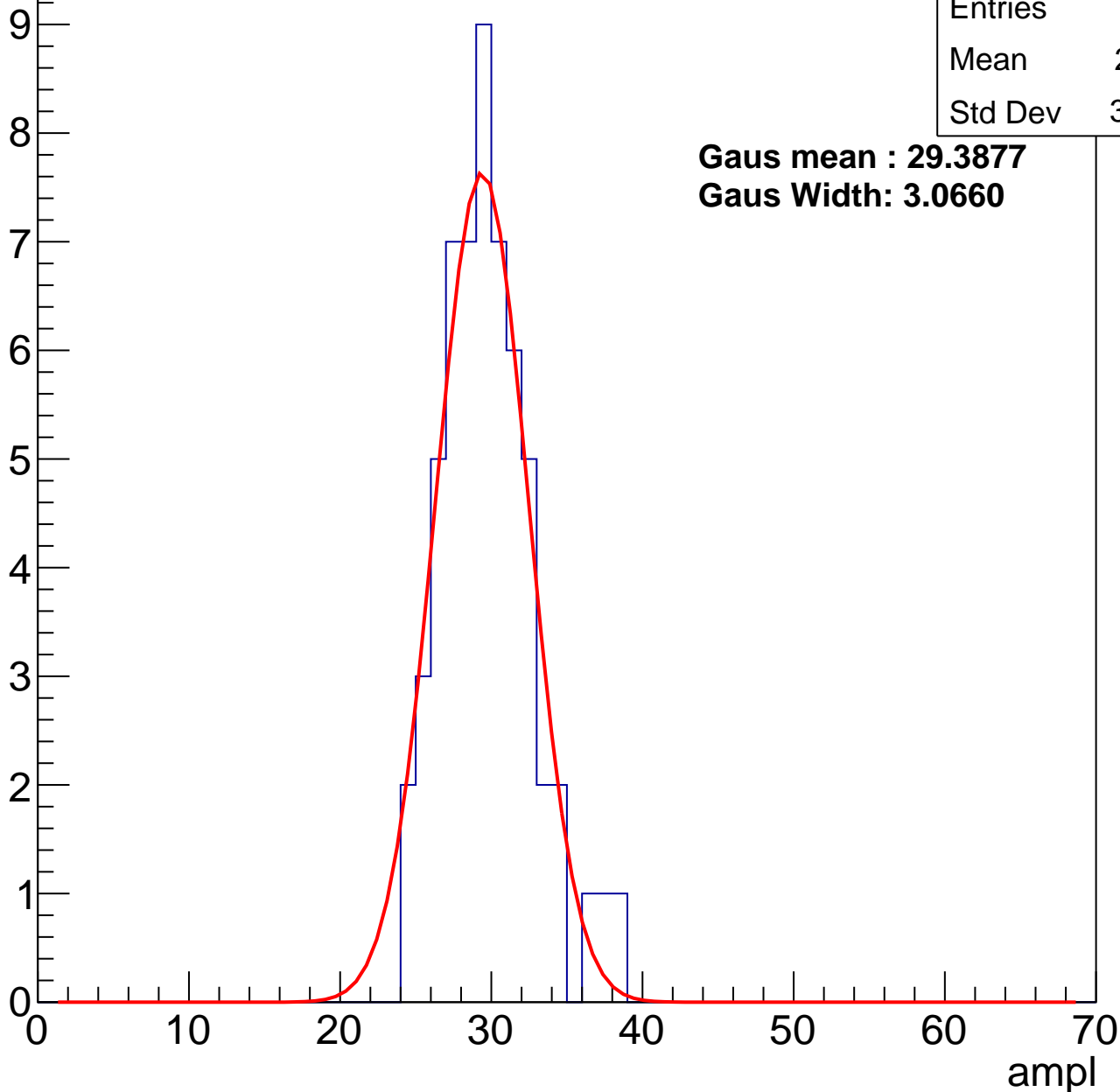
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	29.31
Std Dev	3.007

**Gaus mean : 29.3877**

**Gaus Width: 3.0660**



# B1L000S, U25-ch15, adc1

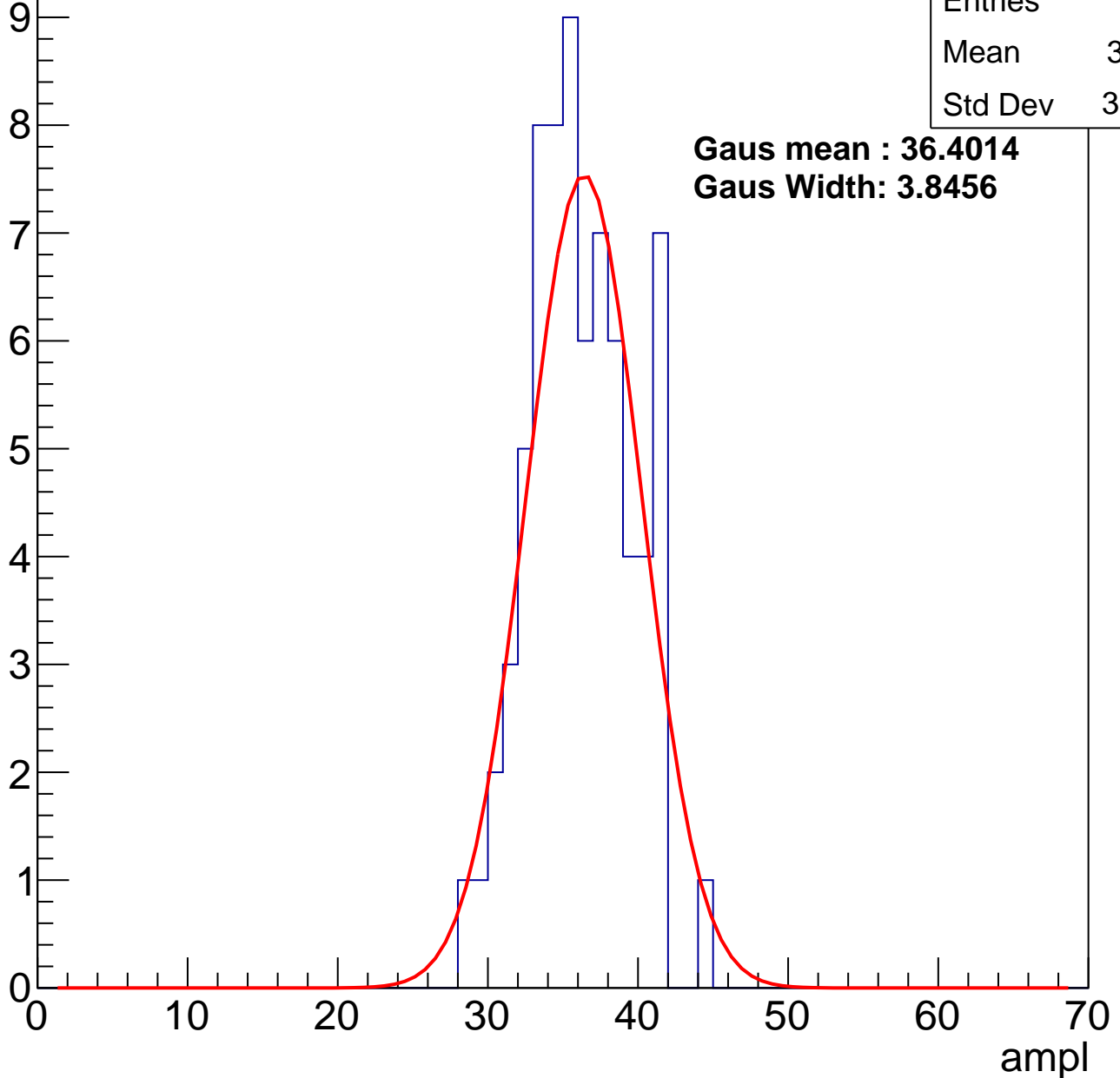
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	35.71
Std Dev	3.368

**Gaus mean : 36.4014**

**Gaus Width: 3.8456**



# B1L000S, U25-ch15, adc2

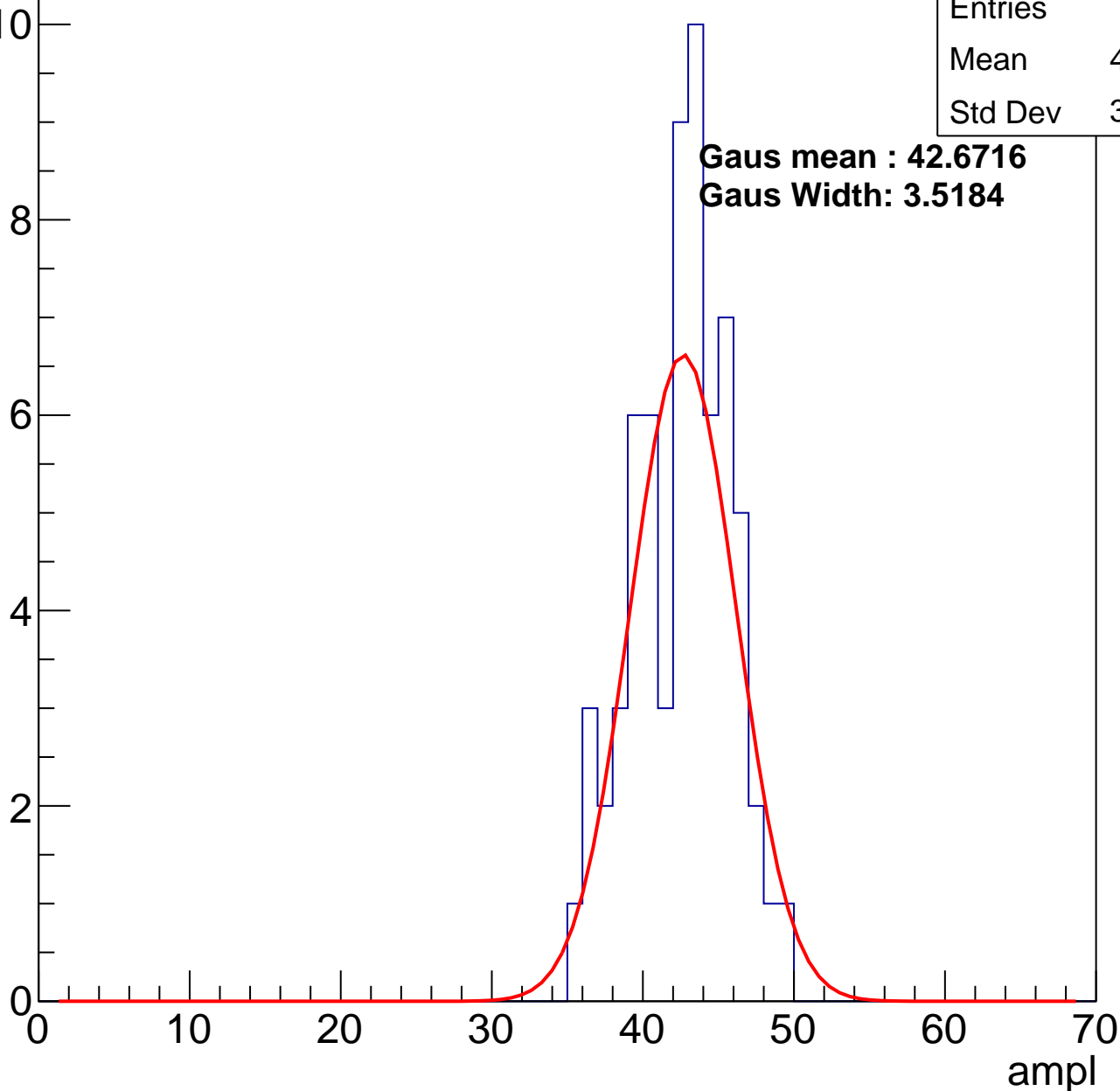
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	42.09
Std Dev	3.156

**Gaus mean : 42.6716**

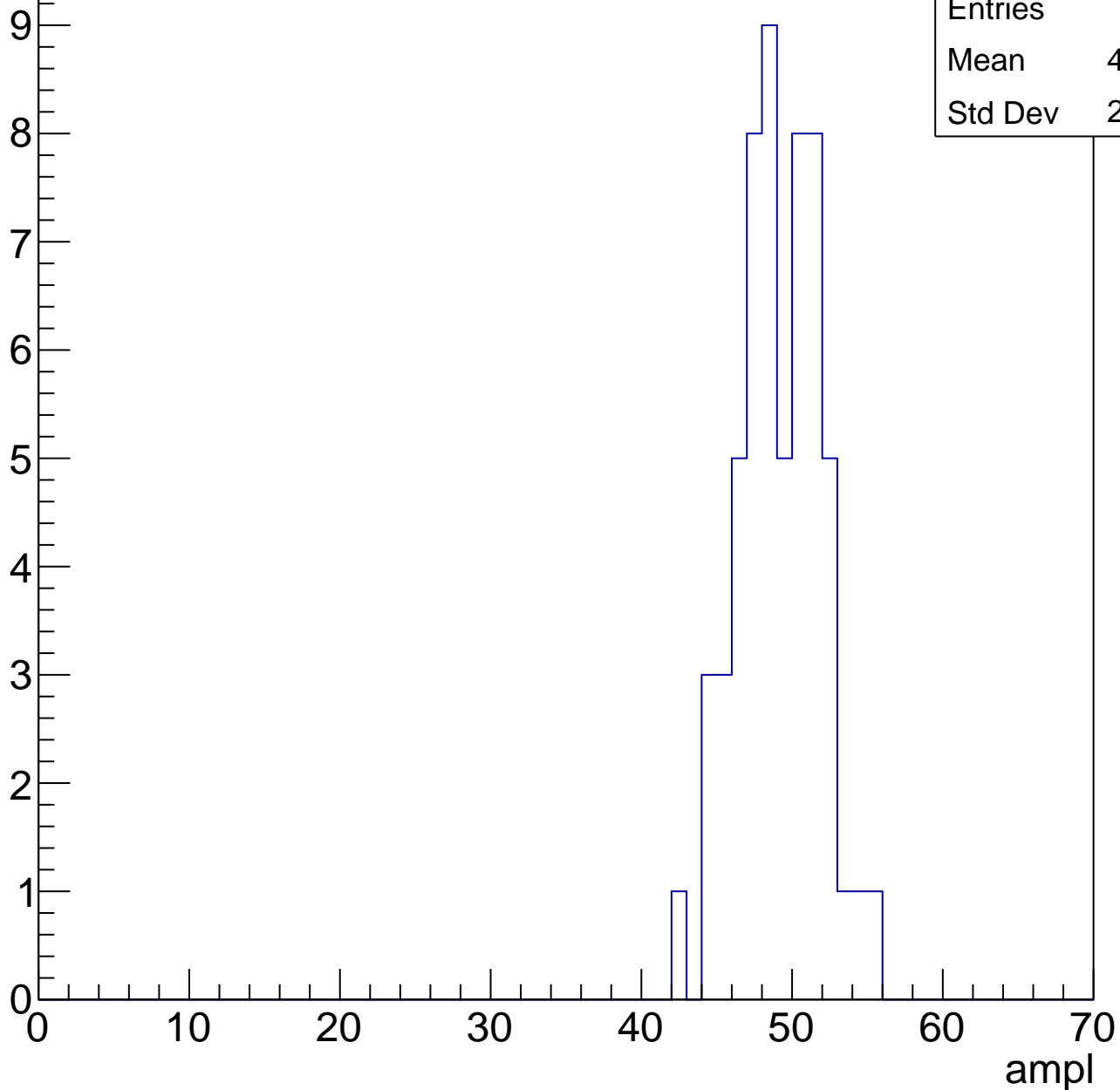
**Gaus Width: 3.5184**



# B1L000S, U25-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

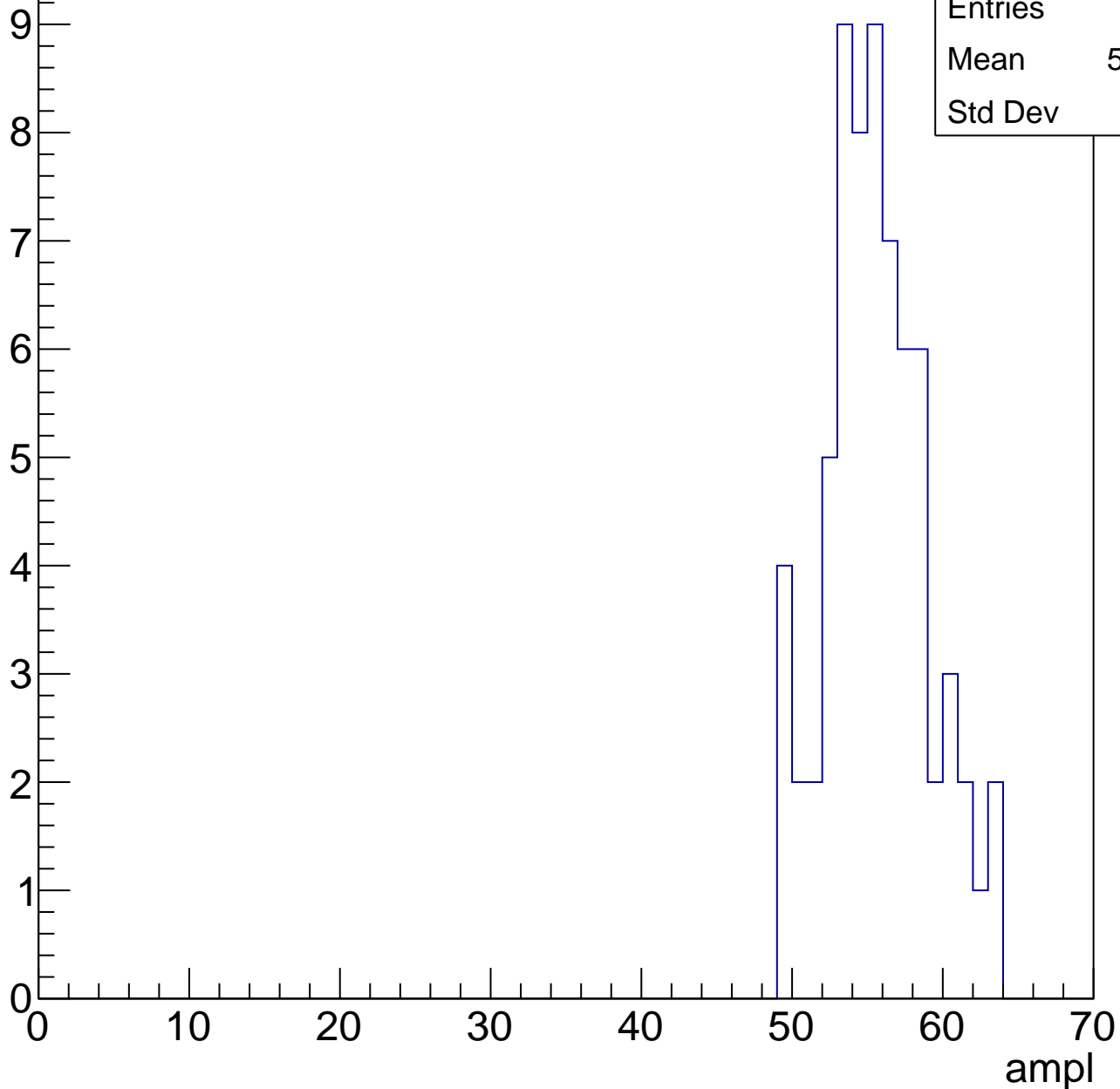
Entry



# B1L000S, U25-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

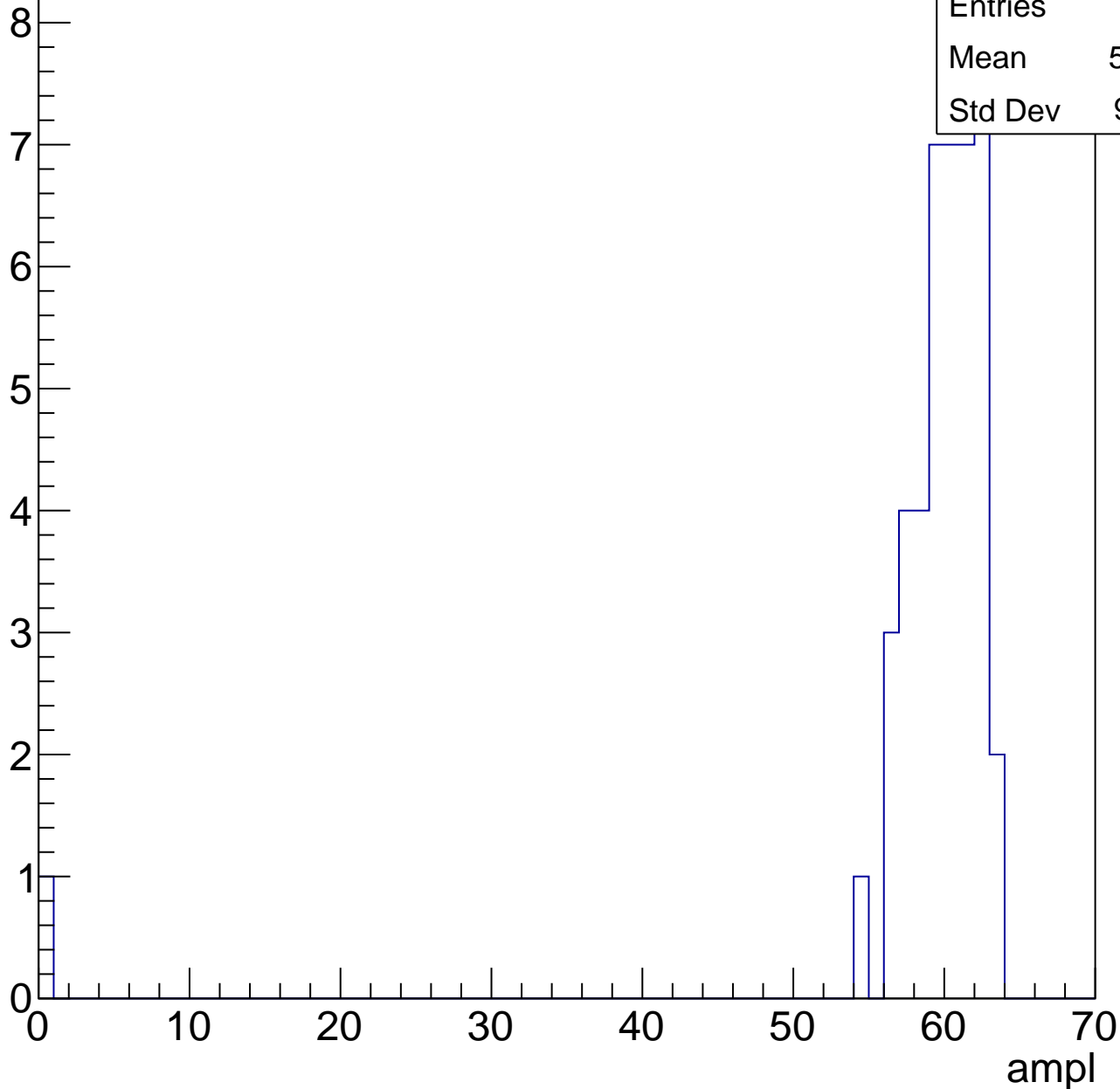


# B1L000S, U25-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

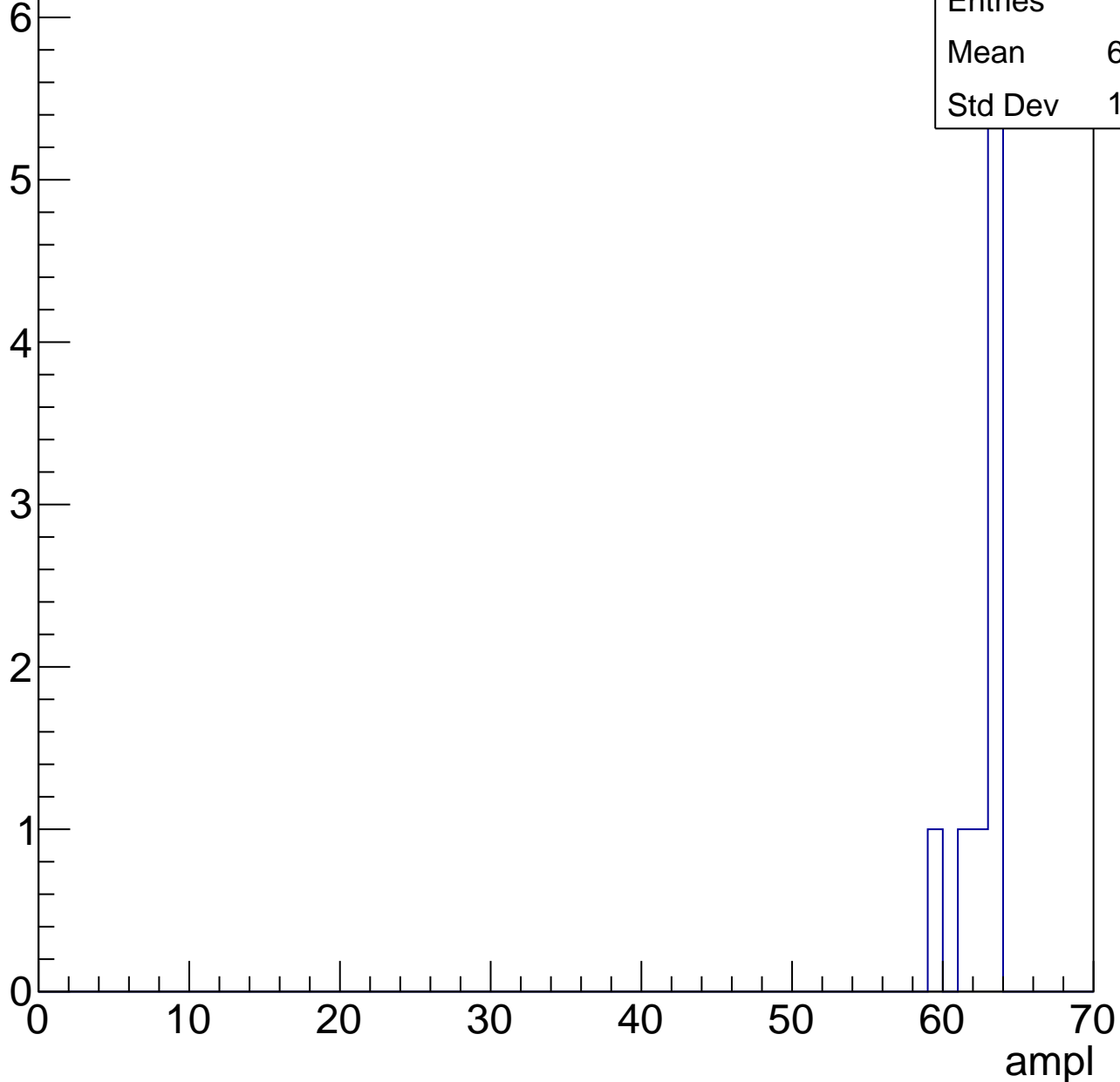
Entries	44
Mean	58.27
Std Dev	9.131



# B1L000S, U25-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	9
Mean	62.22
Std Dev	1.315



# B1L000S, U25-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L000S, U25-ch16, adc0

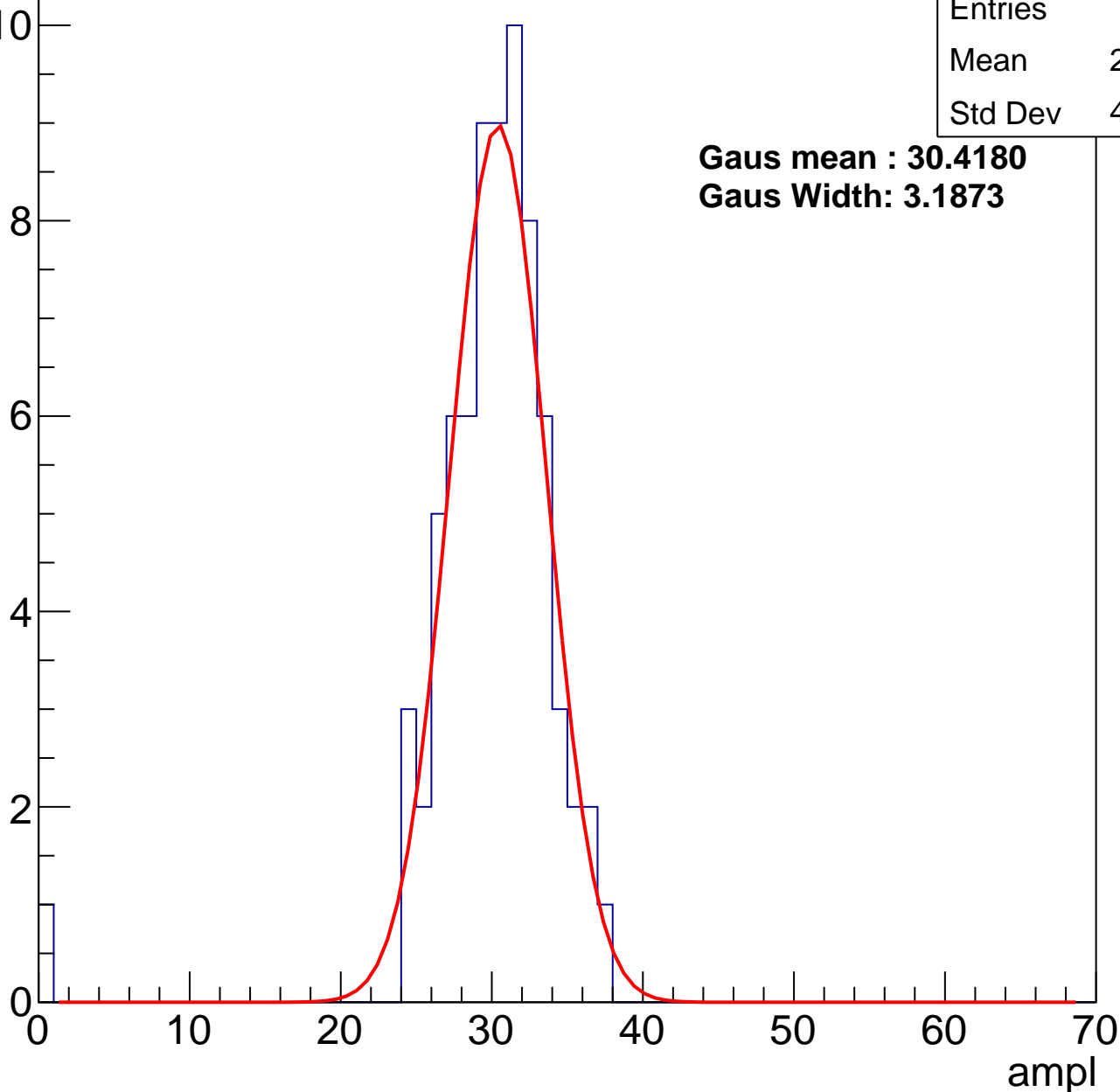
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	29.56
Std Dev	4.572

**Gaus mean : 30.4180**

**Gaus Width: 3.1873**



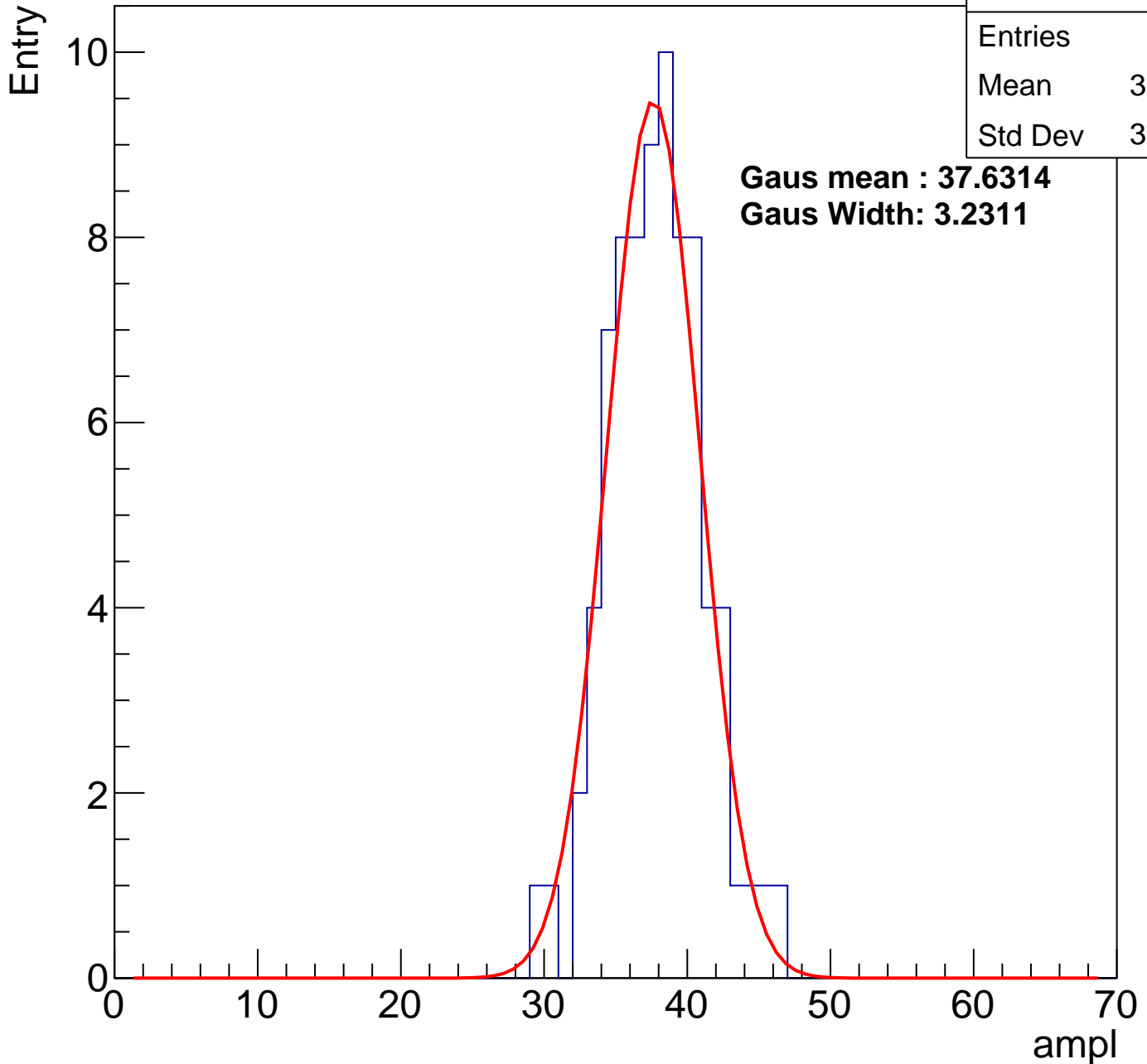
# B1L000S, U25-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	37.38
Std Dev	3.255

**Gaus mean : 37.6314**

**Gaus Width: 3.2311**



# B1L000S, U25-ch16, adc2

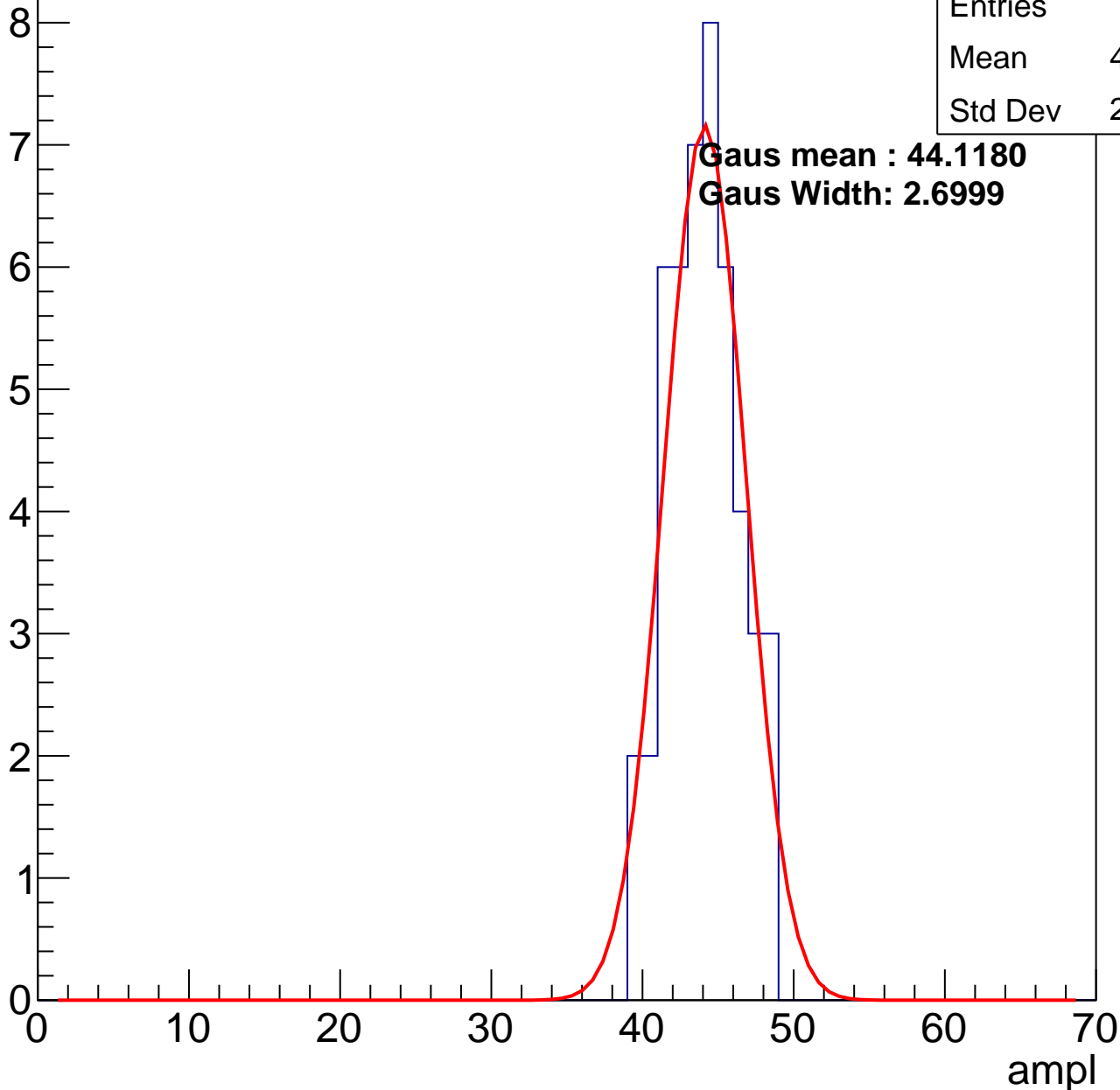
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	43.57
Std Dev	2.332

**Gaus mean : 44.1180**

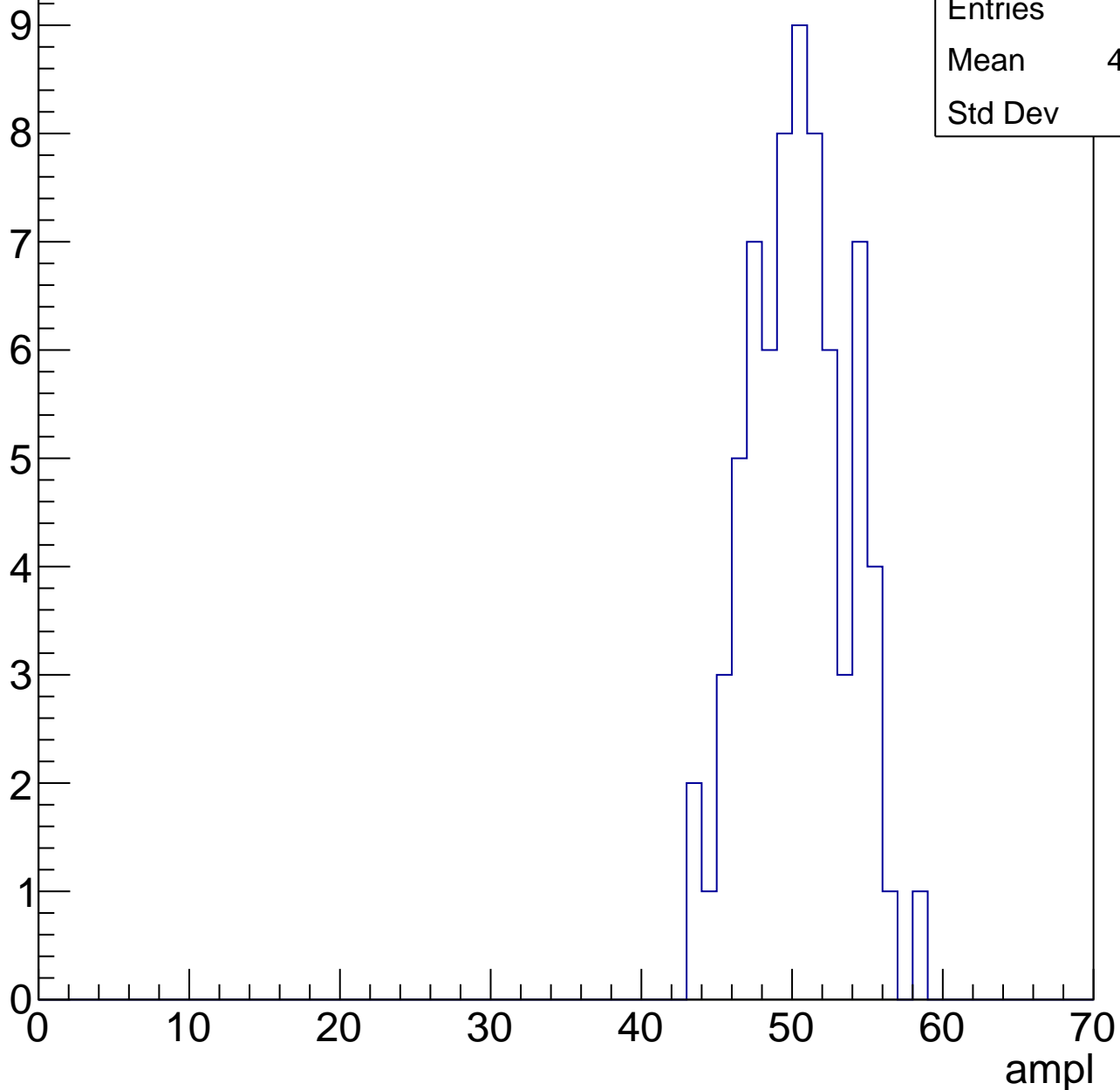
**Gaus Width: 2.6999**



# B1L000S, U25-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

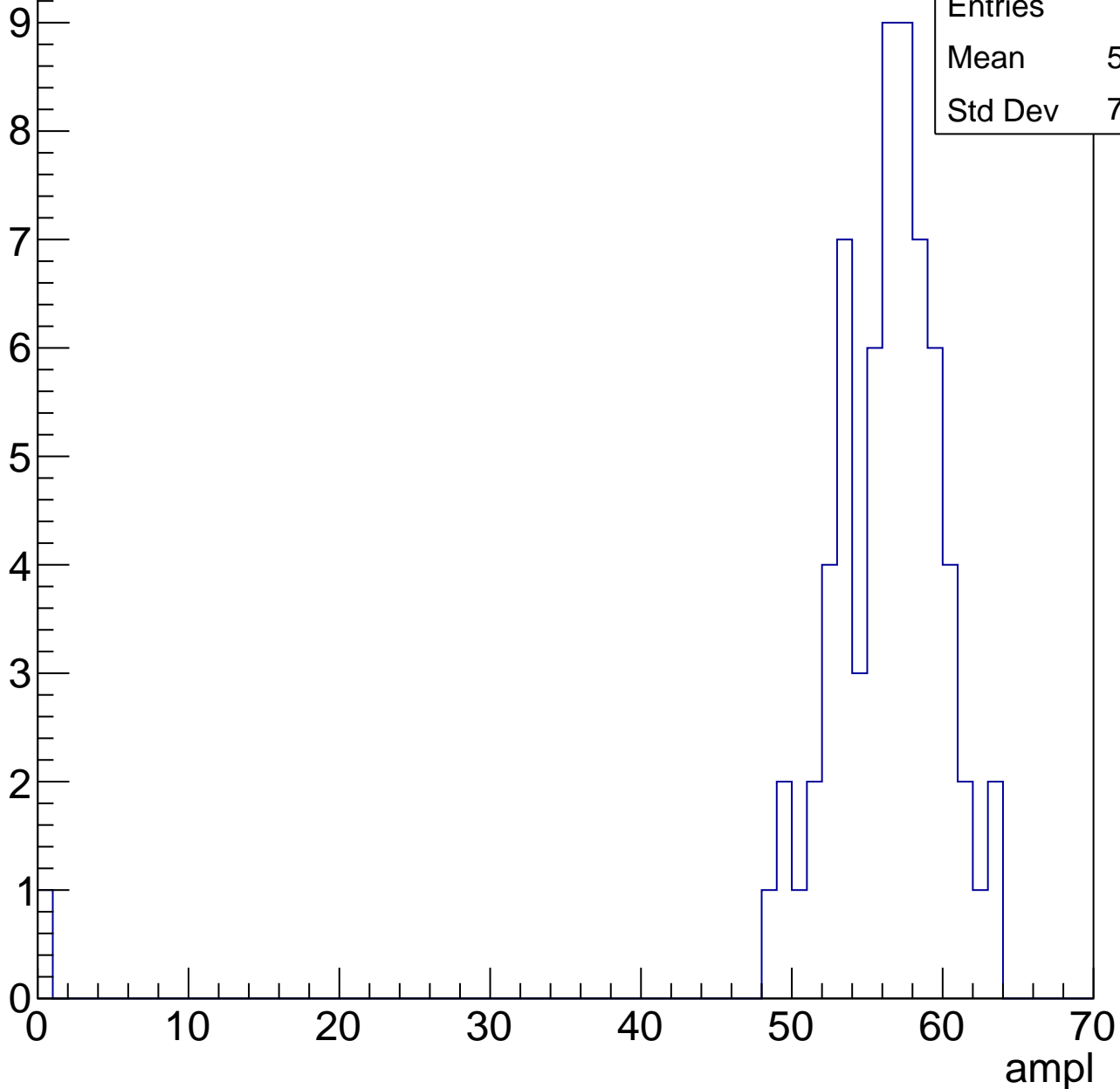


# B1L000S, U25-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	55.16
Std Dev	7.552



# B1L000S, U25-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries

31

Mean

60.81

Std Dev

1.615

0

ampl

0

10

20

30

40

50

60

70

# B1L000S, U25-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

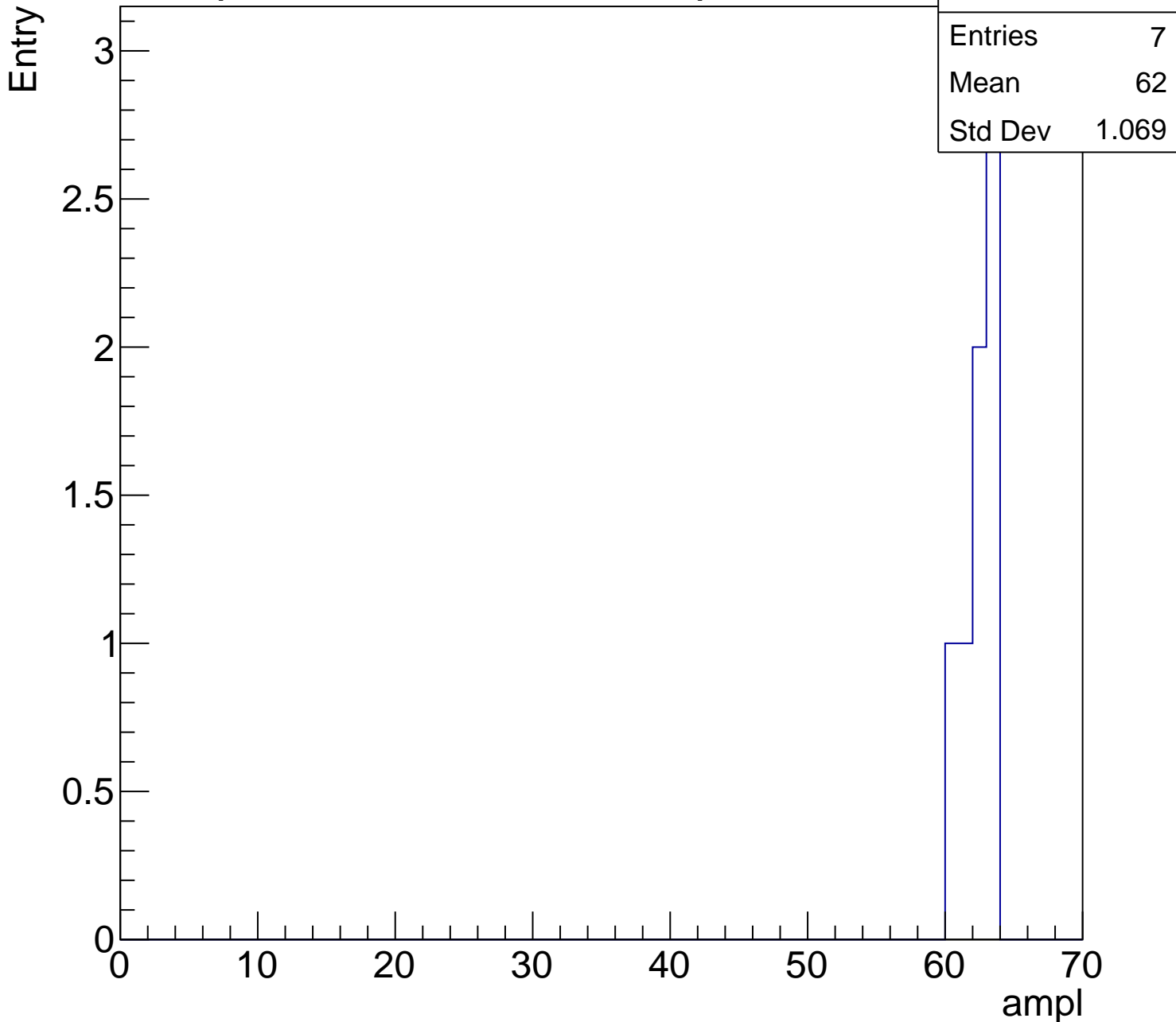
7

Mean

62

Std Dev

1.069





# B1L000S, U25-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch17, adc0

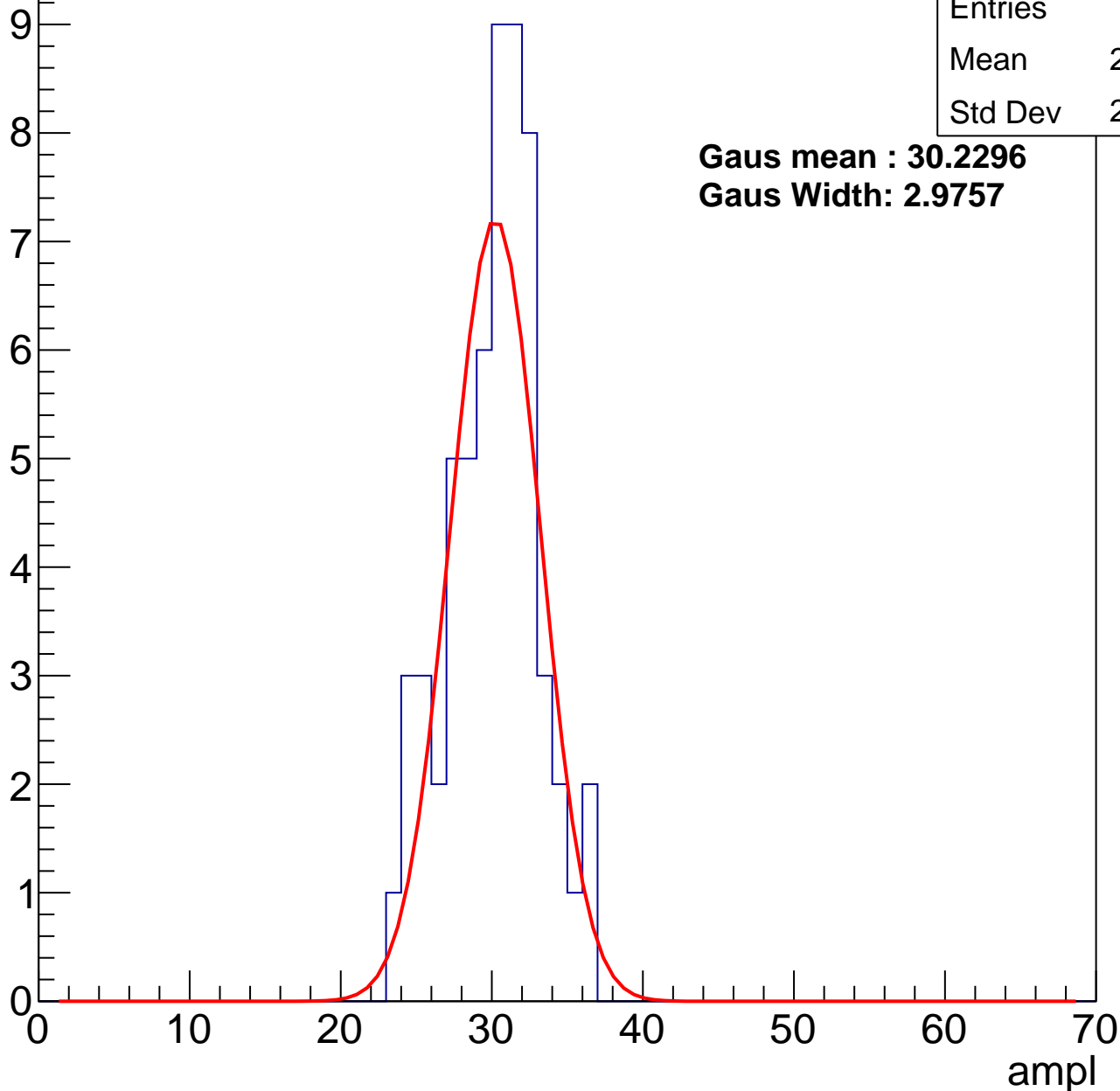
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.66
Std Dev	2.984

**Gaus mean : 30.2296**

**Gaus Width: 2.9757**



# B1L000S, U25-ch17, adc1

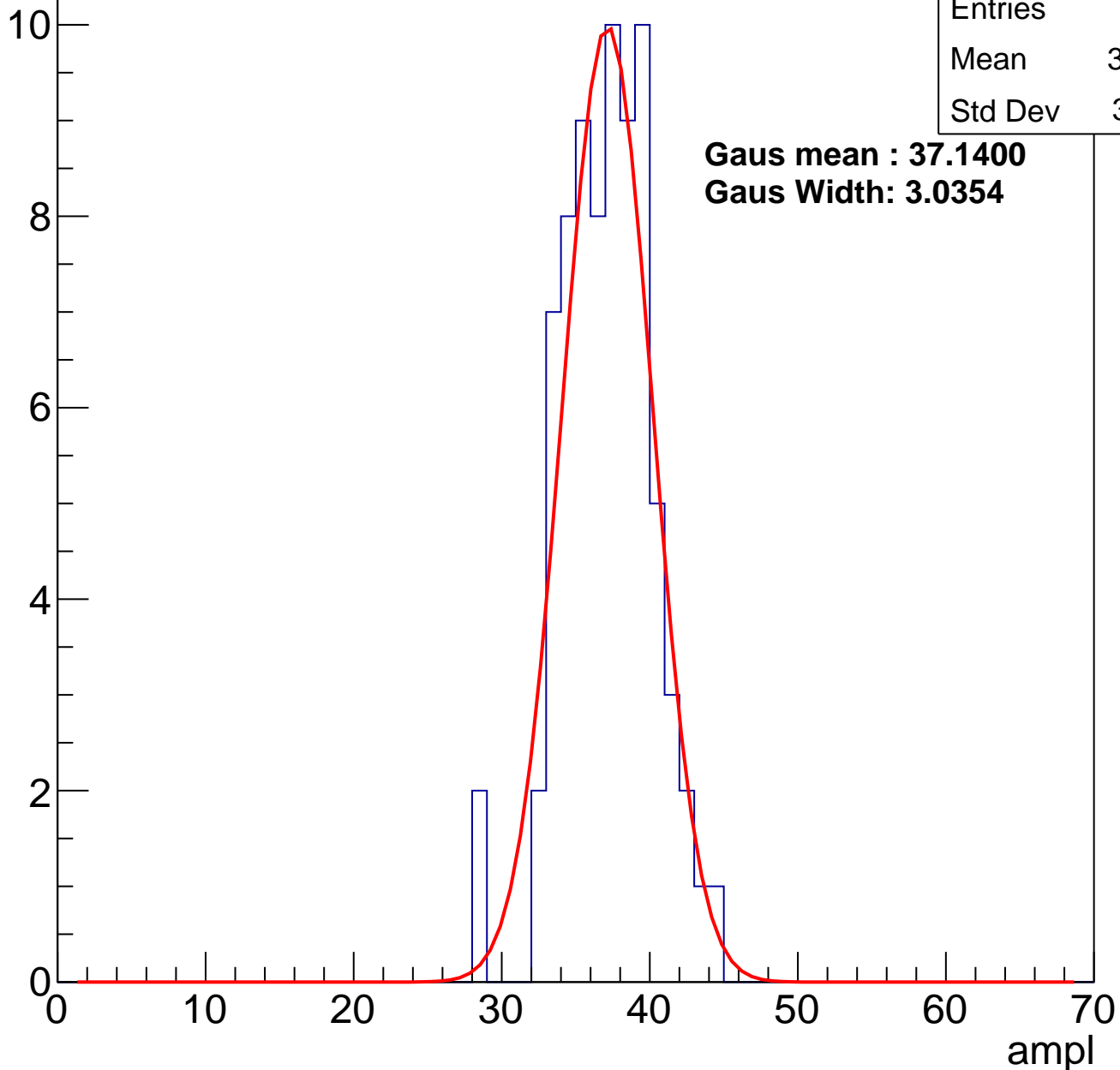
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	36.65
Std Dev	3.031

**Gaus mean : 37.1400**

**Gaus Width: 3.0354**

Entry



# B1L000S, U25-ch17, adc2

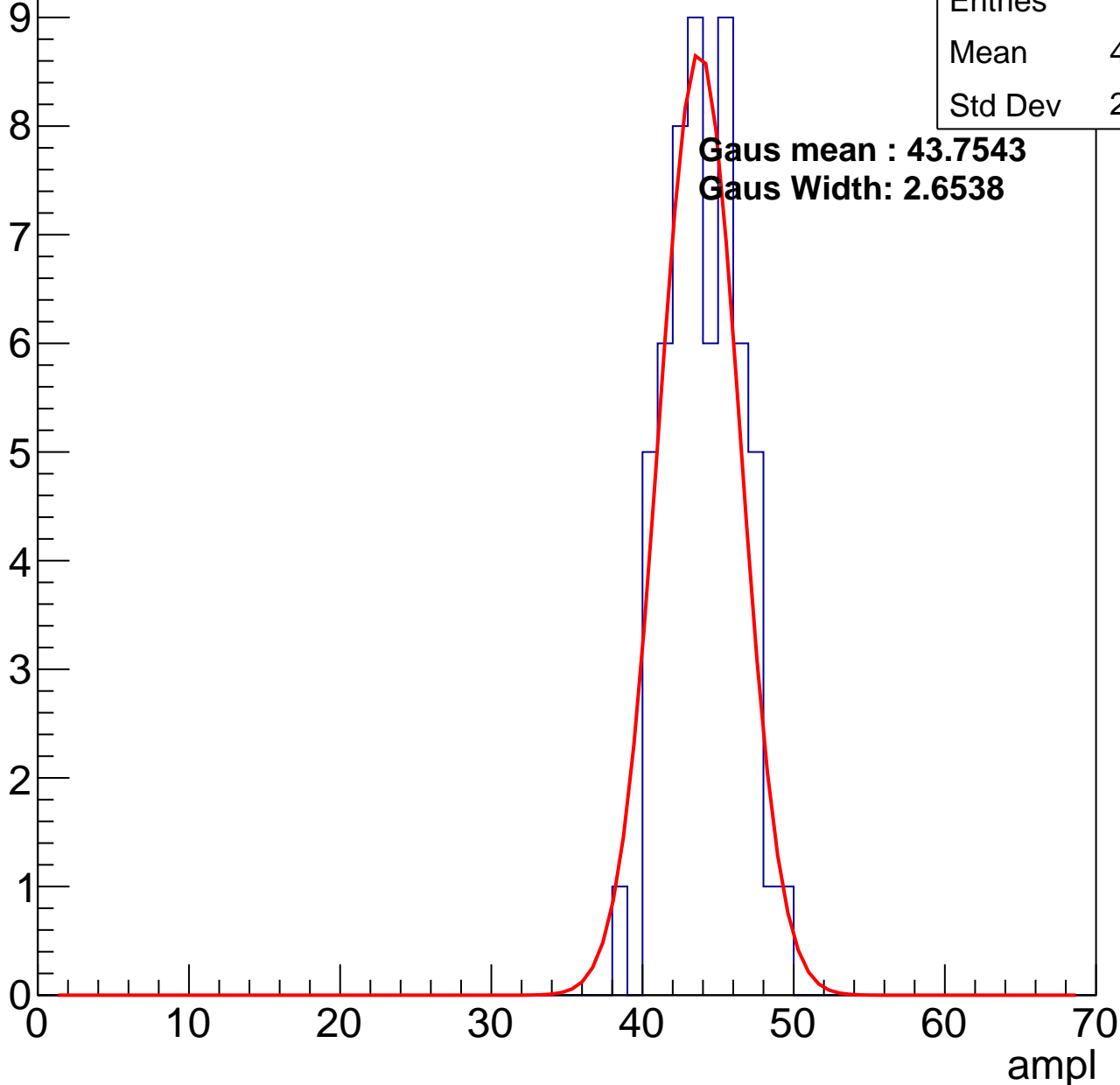
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	43.58
Std Dev	2.369

**Gaus mean : 43.7543**

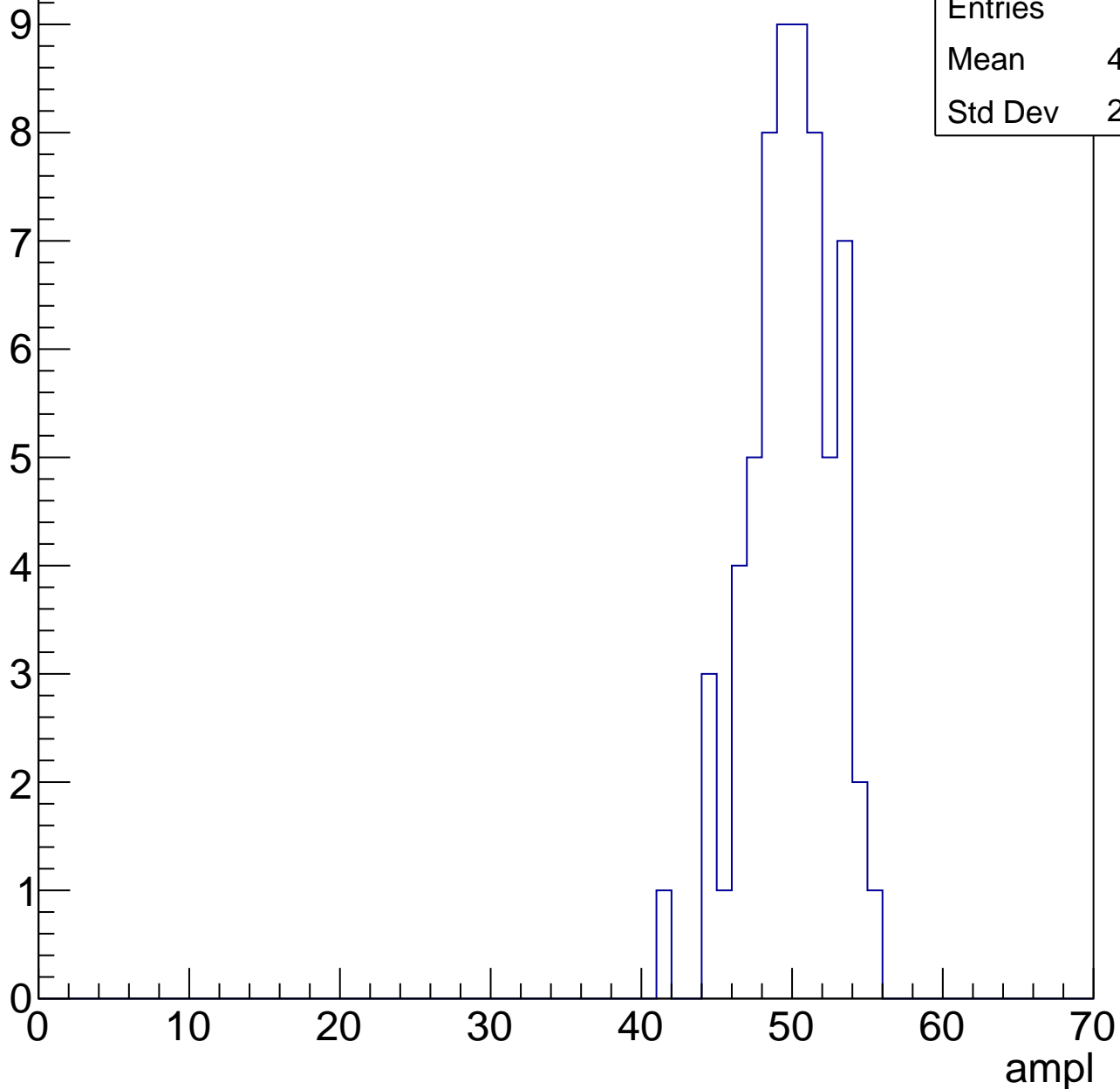
**Gaus Width: 2.6538**



# B1L000S, U25-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

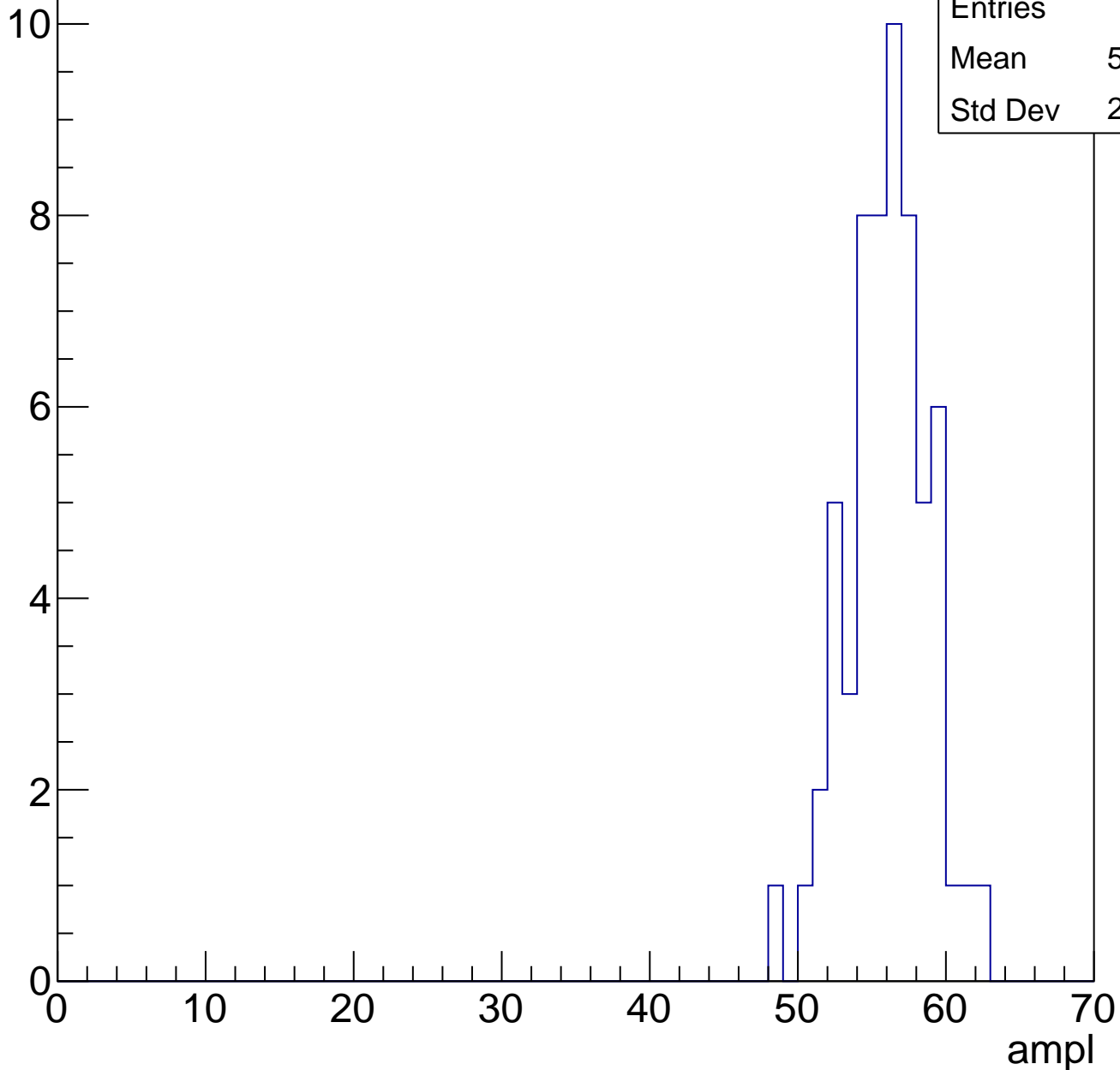


# B1L000S, U25-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	55.57
Std Dev	2.723

Entry

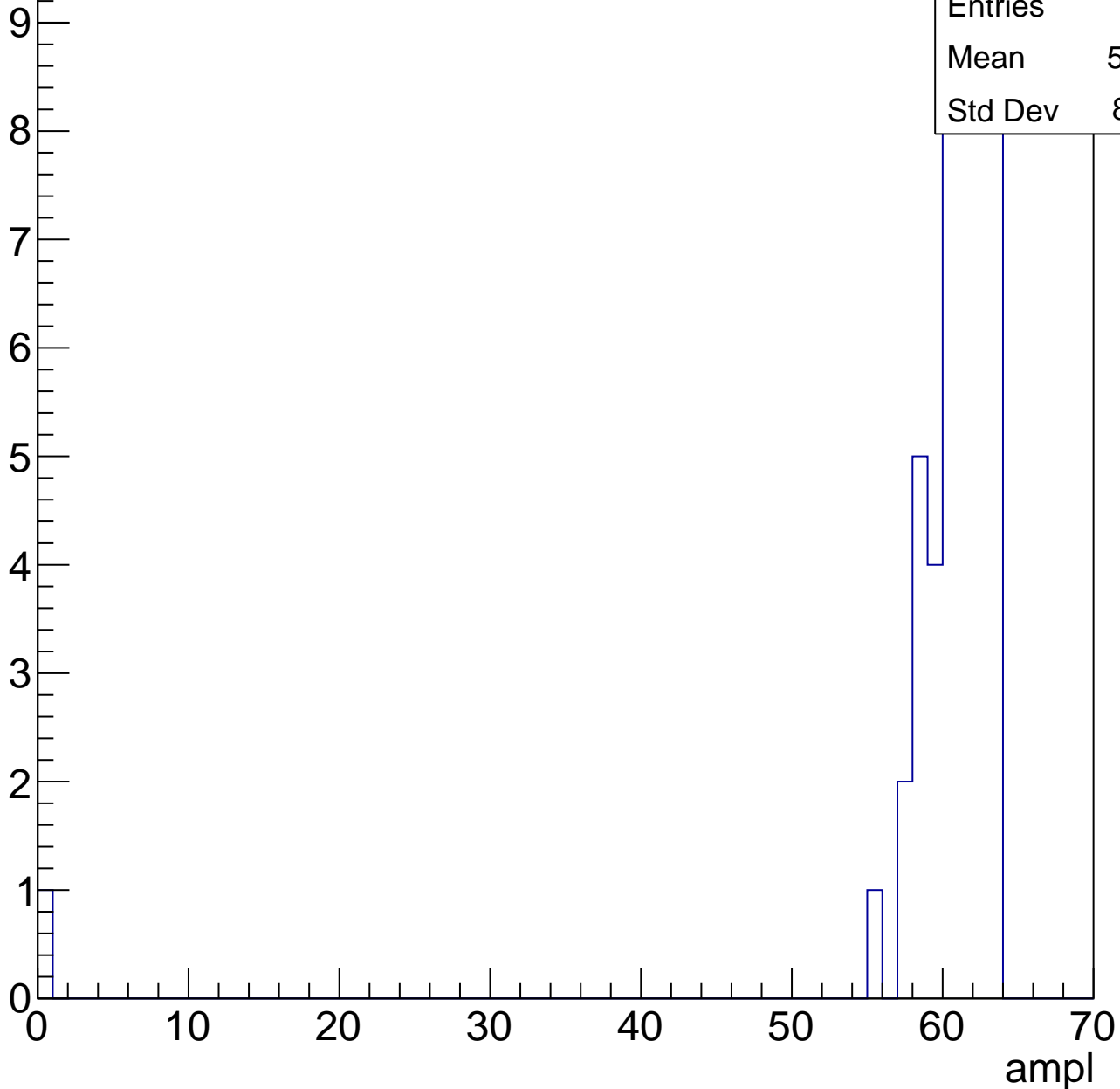


# B1L000S, U25-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	59.29
Std Dev	8.851



# B1L000S, U25-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	29.74
Std Dev	2.862

**Gaus mean : 30.5098**

**Gaus Width: 3.6846**

ampl

0

10

20

30

40

50

60

70

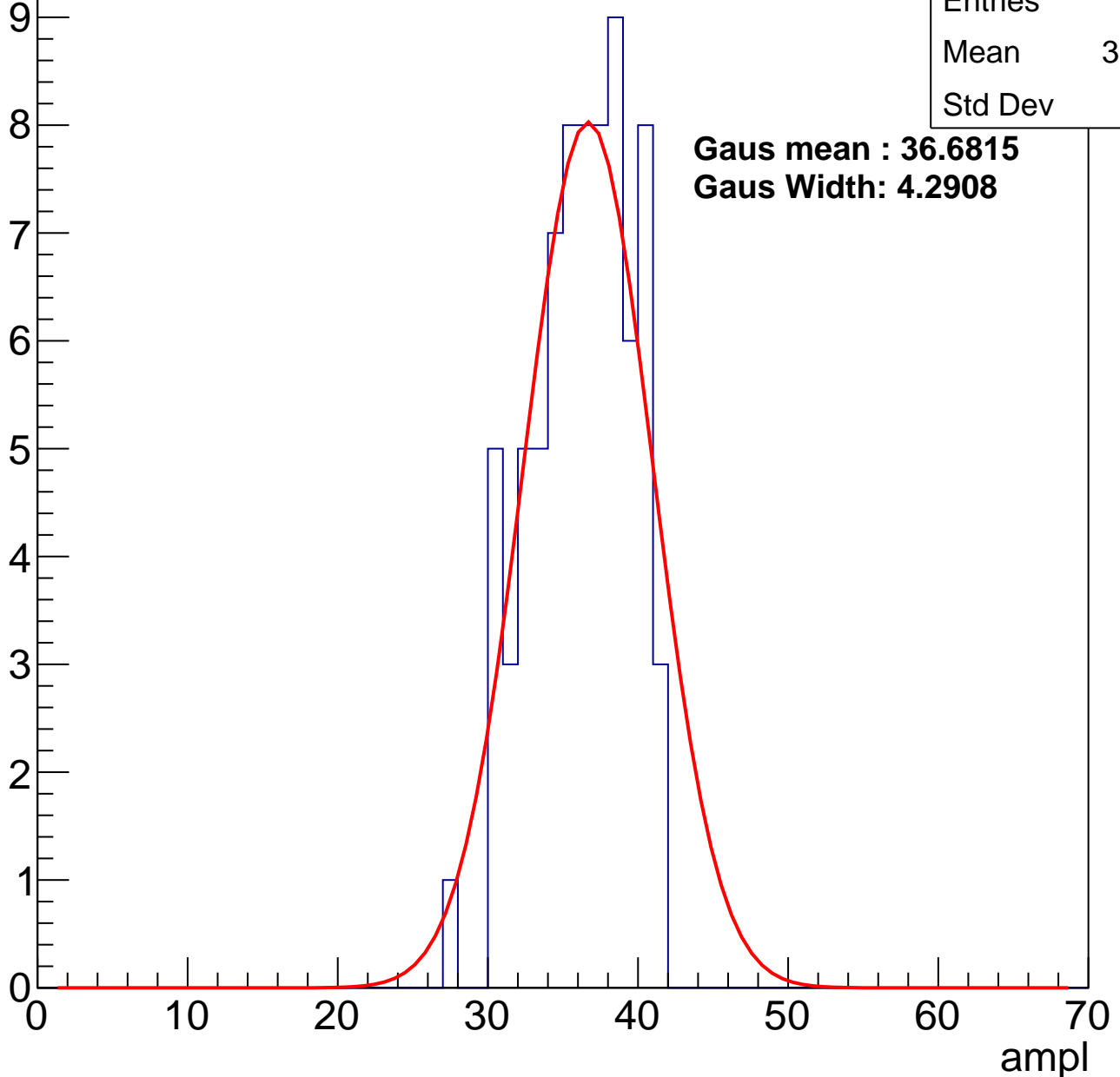
# B1L000S, U25-ch18, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	35.74
Std Dev	3.23

**Gaus mean : 36.6815**  
**Gaus Width: 4.2908**



# B1L000S, U25-ch18, adc2

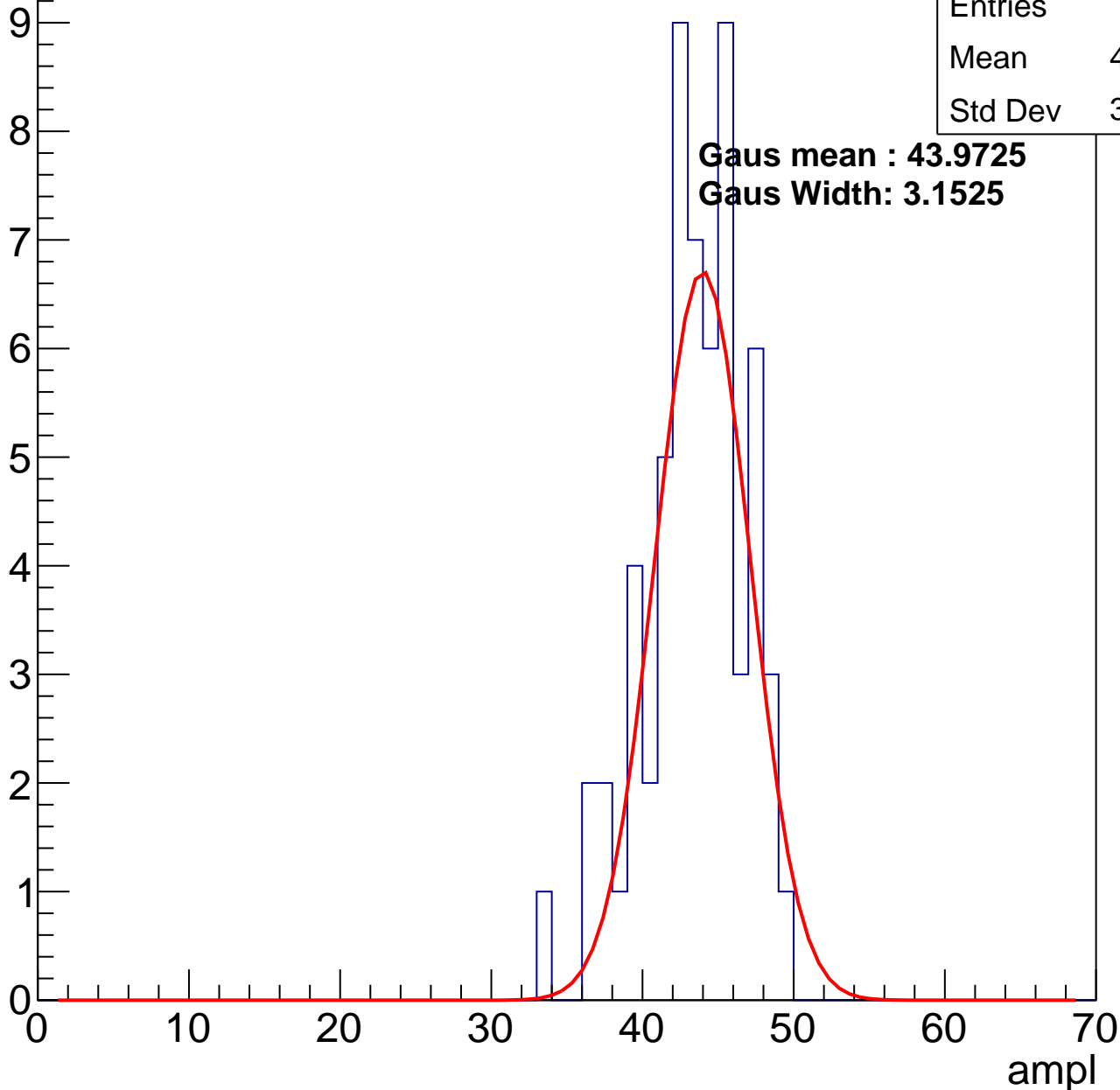
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	42.93
Std Dev	3.343

**Gaus mean : 43.9725**

**Gaus Width: 3.1525**

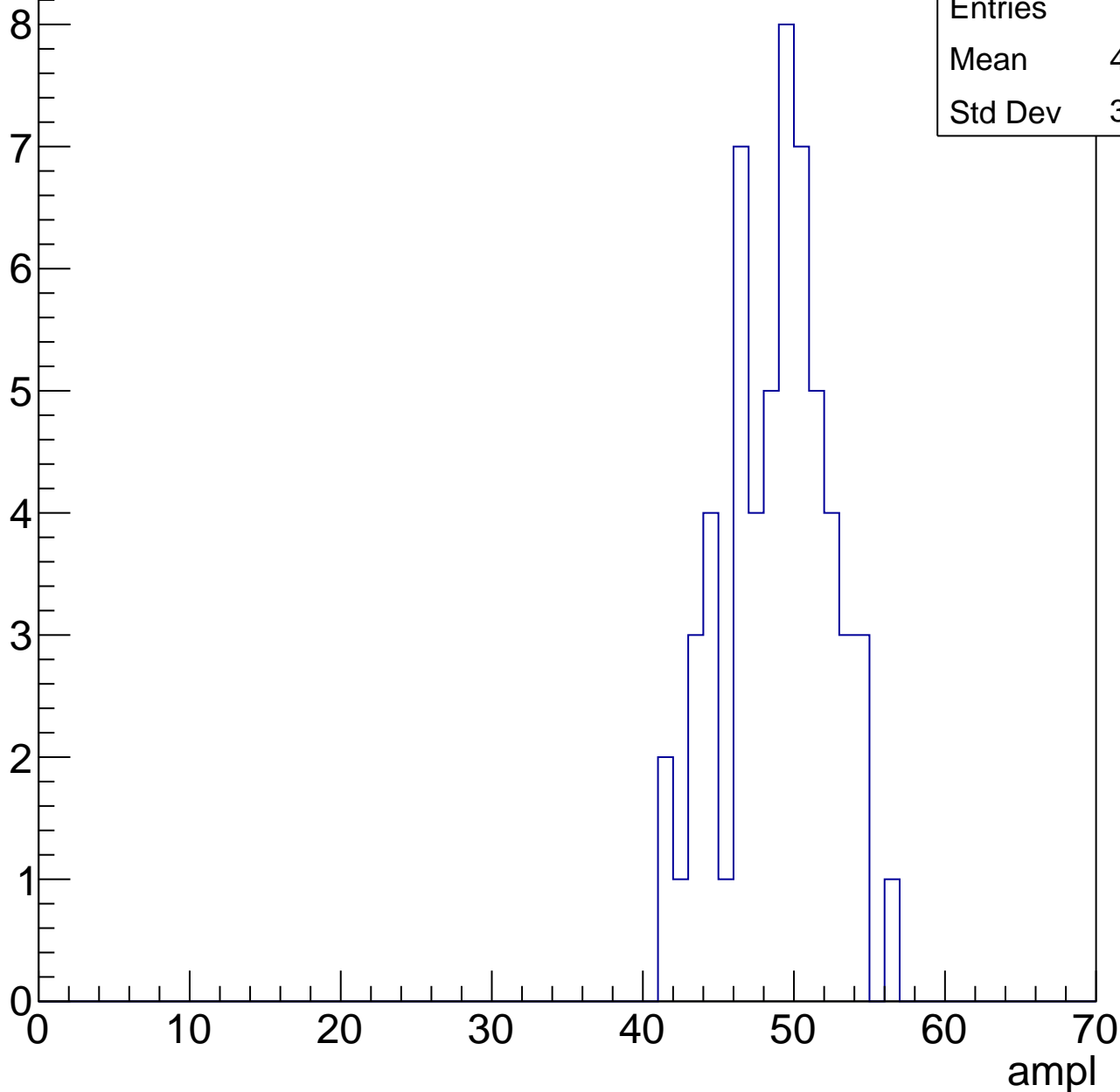


# B1L000S, U25-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

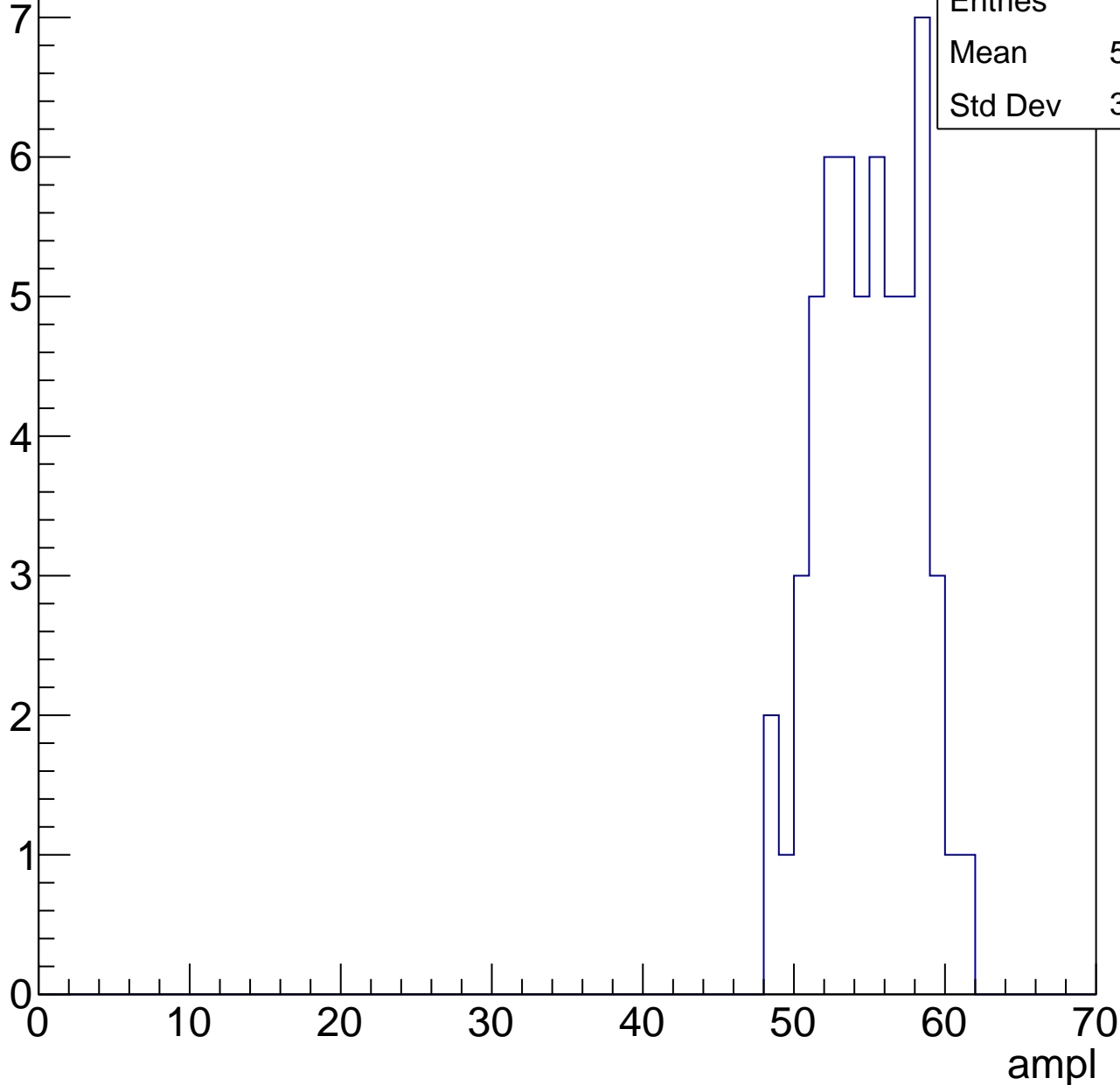
Entries	58
Mean	48.38
Std Dev	3.468



# B1L000S, U25-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch18, adc5

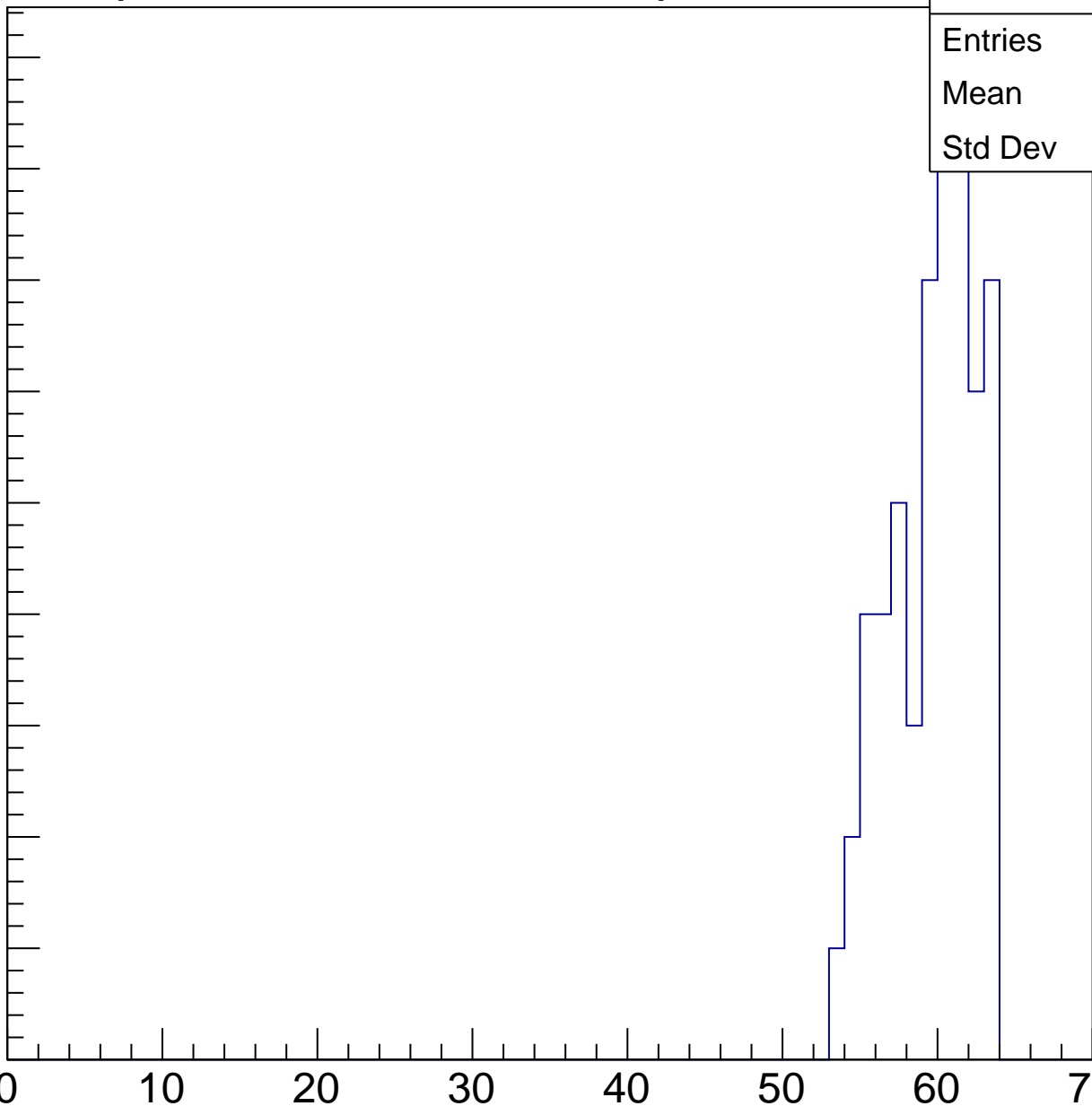
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	59.25
Std Dev	2.714

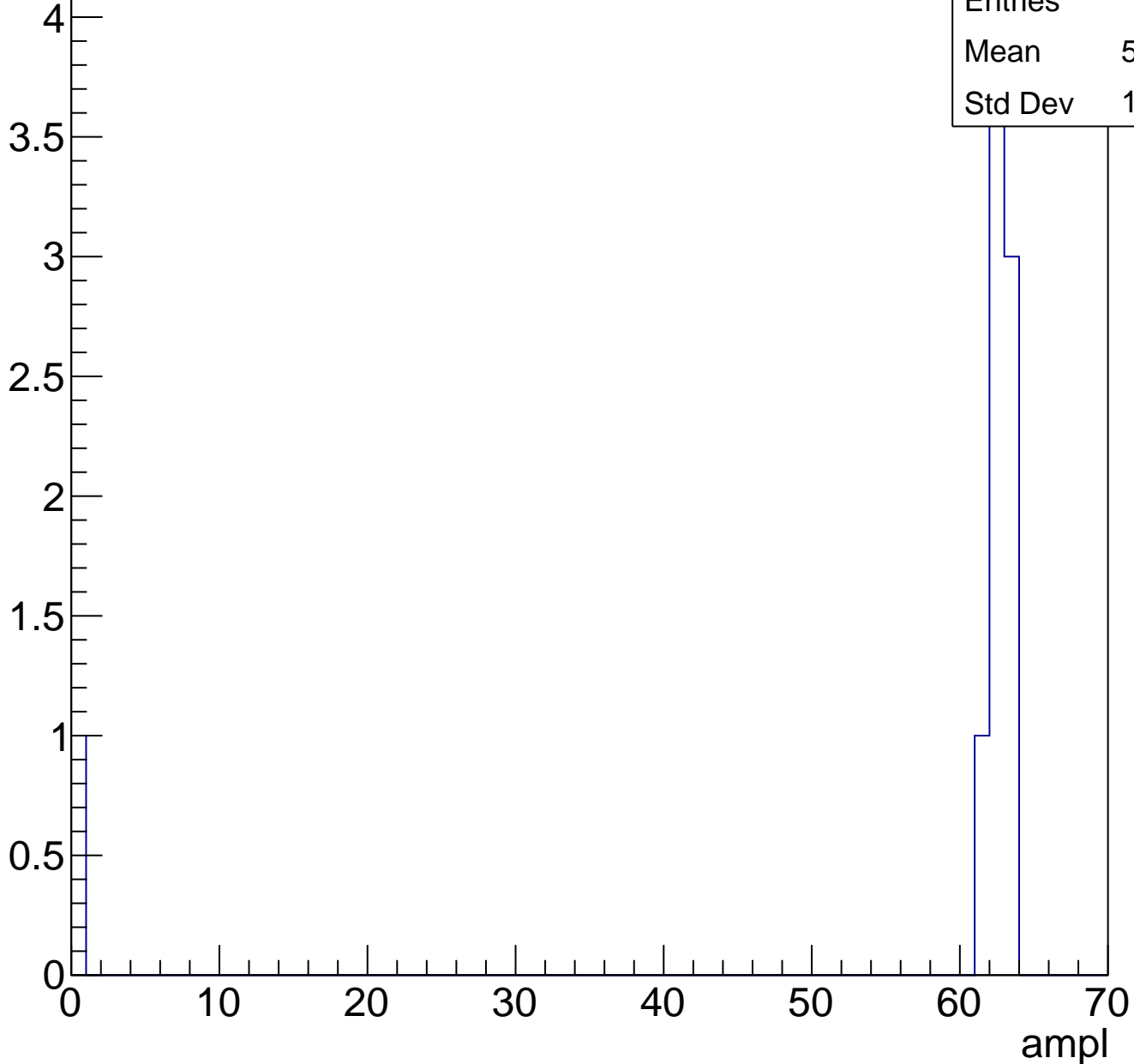
ampl



# B1L000S, U25-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch19, adc0

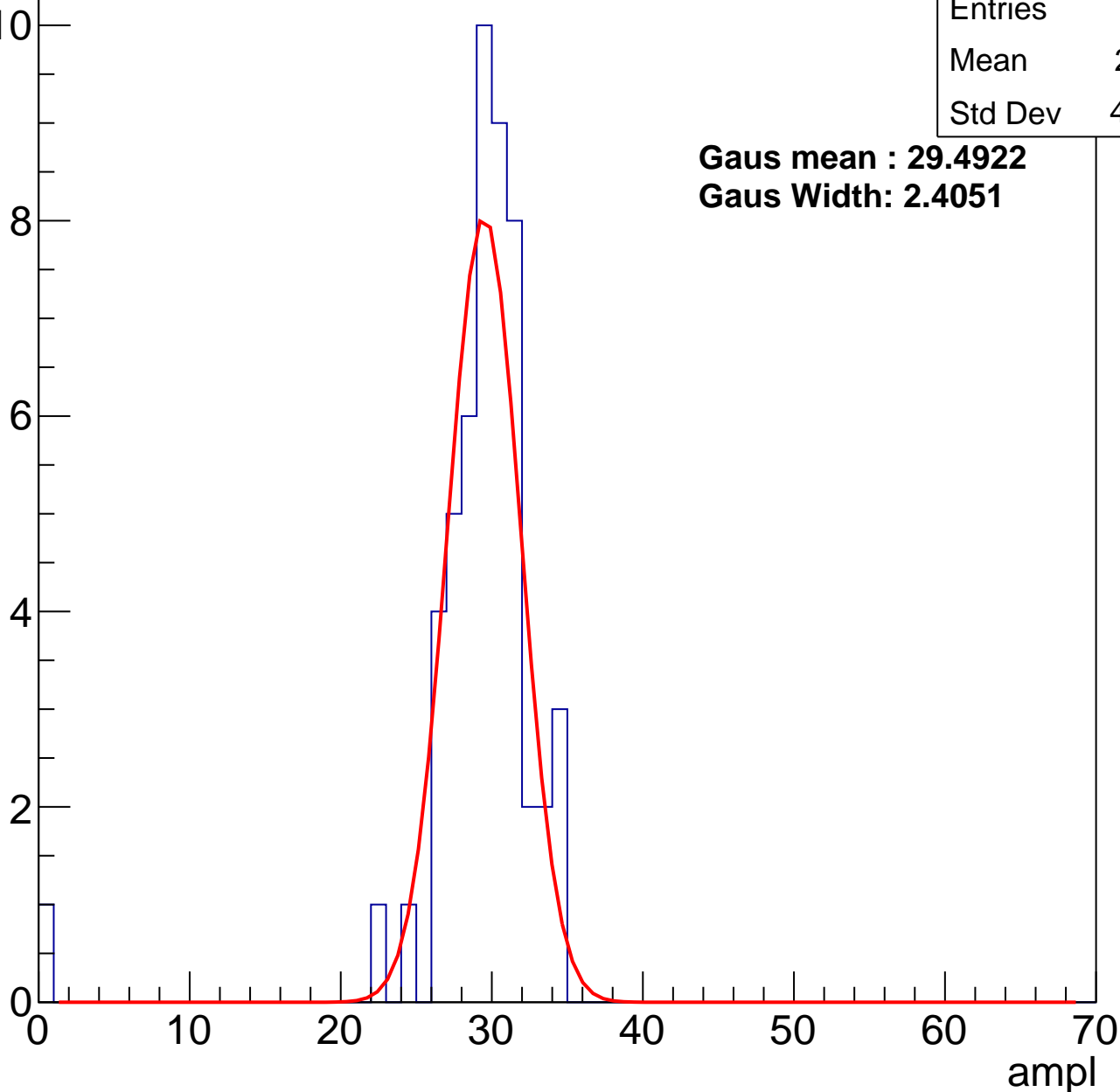
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	28.71
Std Dev	4.679

**Gaus mean : 29.4922**

**Gaus Width: 2.4051**



# B1L000S, U25-ch19, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	35.8
Std Dev	2.857

**Gaus mean : 36.0951**

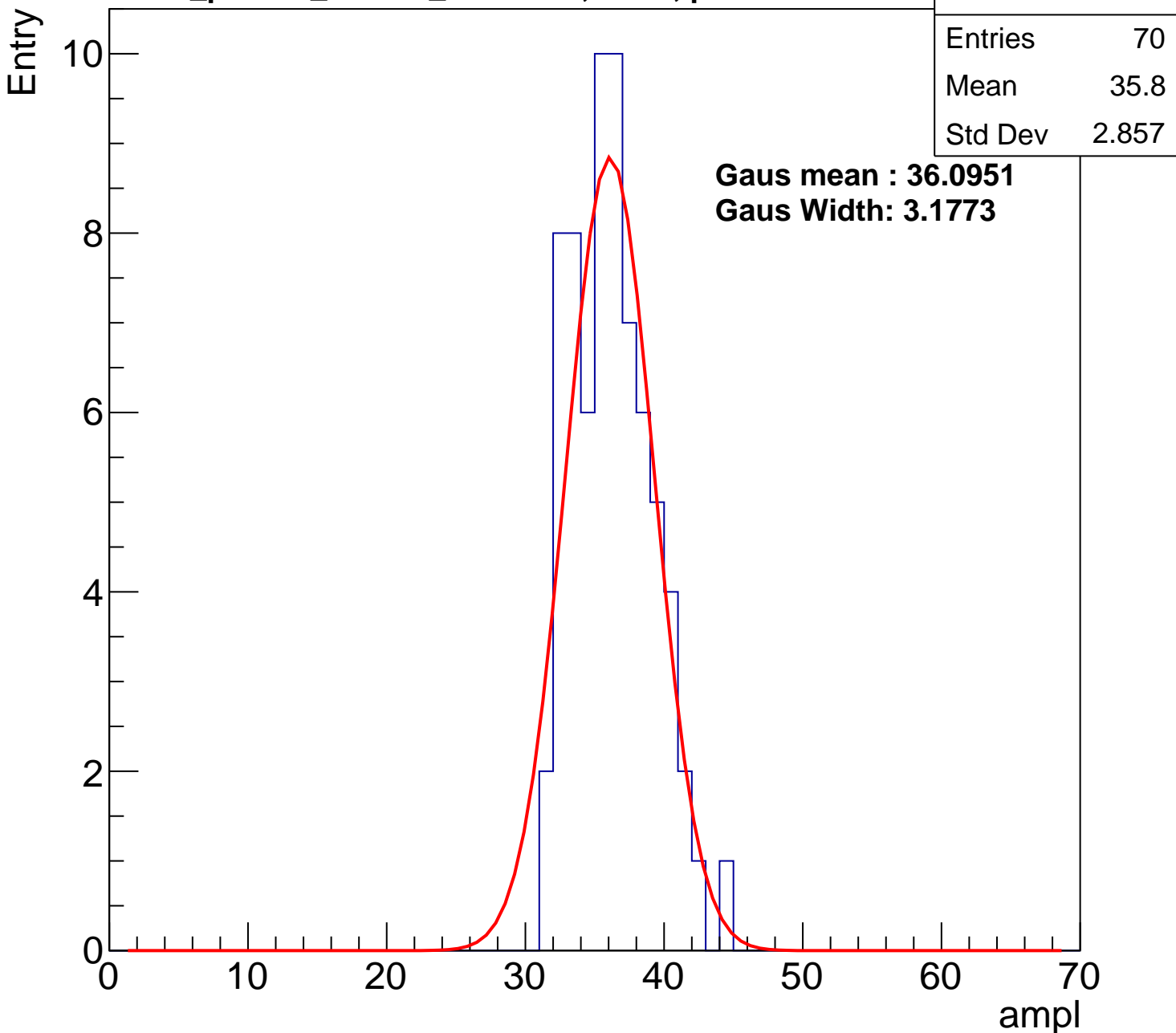
**Gaus Width: 3.1773**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch19, adc2

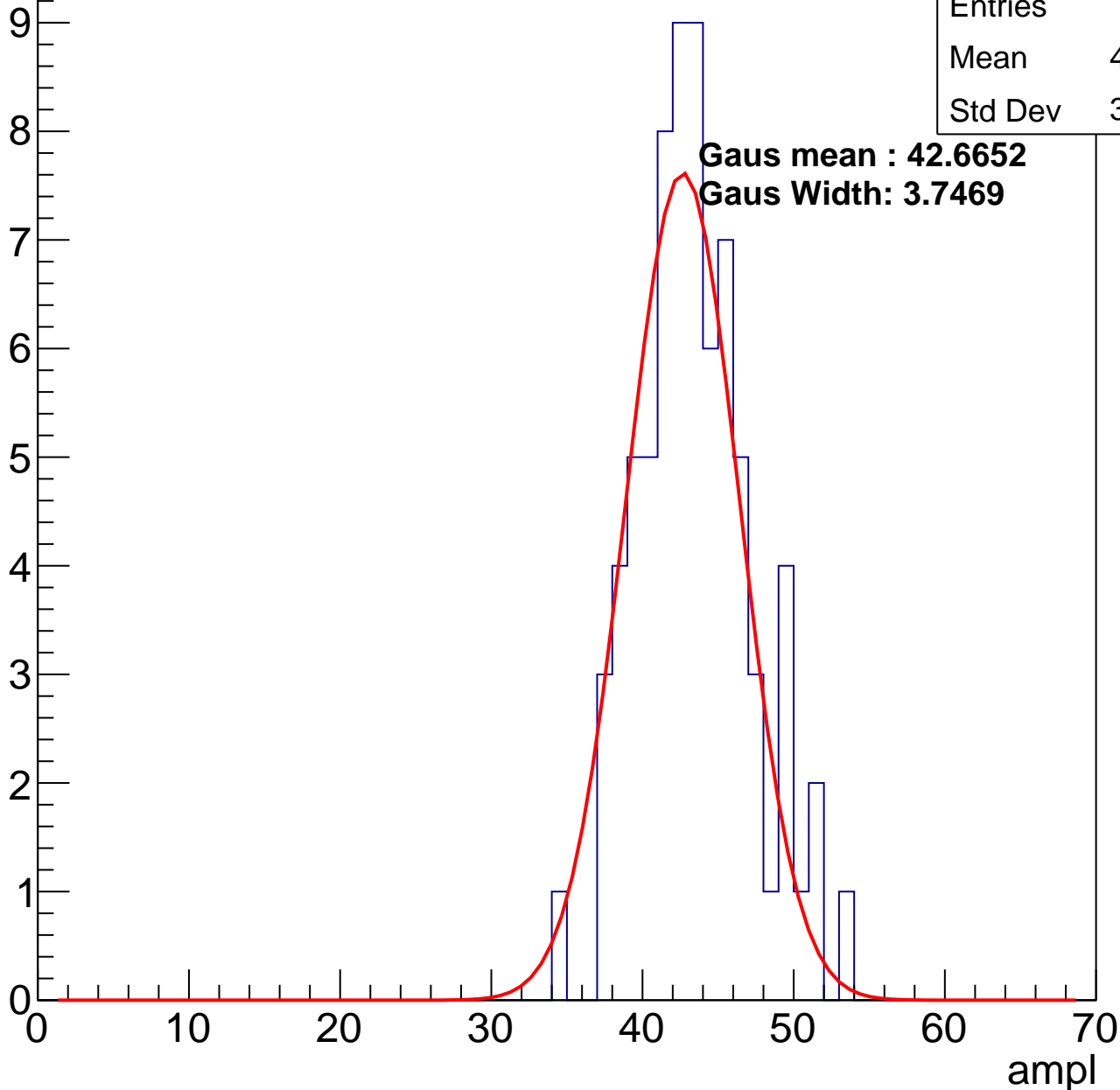
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	43.03
Std Dev	3.727

**Gaus mean : 42.6652**

**Gaus Width: 3.7469**

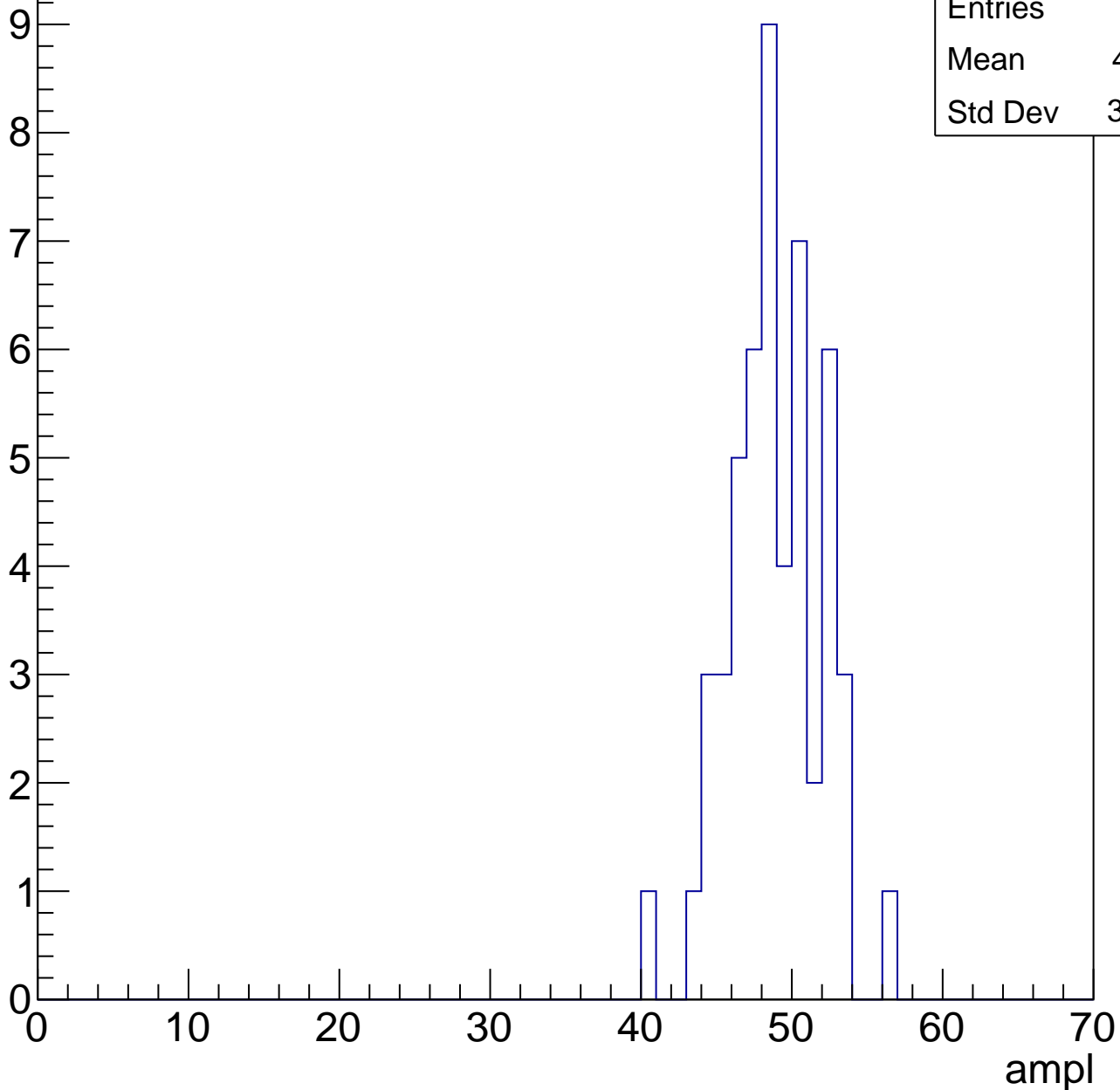


# B1L000S, U25-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	48.41
Std Dev	3.024



# B1L000S, U25-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	54.97
Std Dev	3.139

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

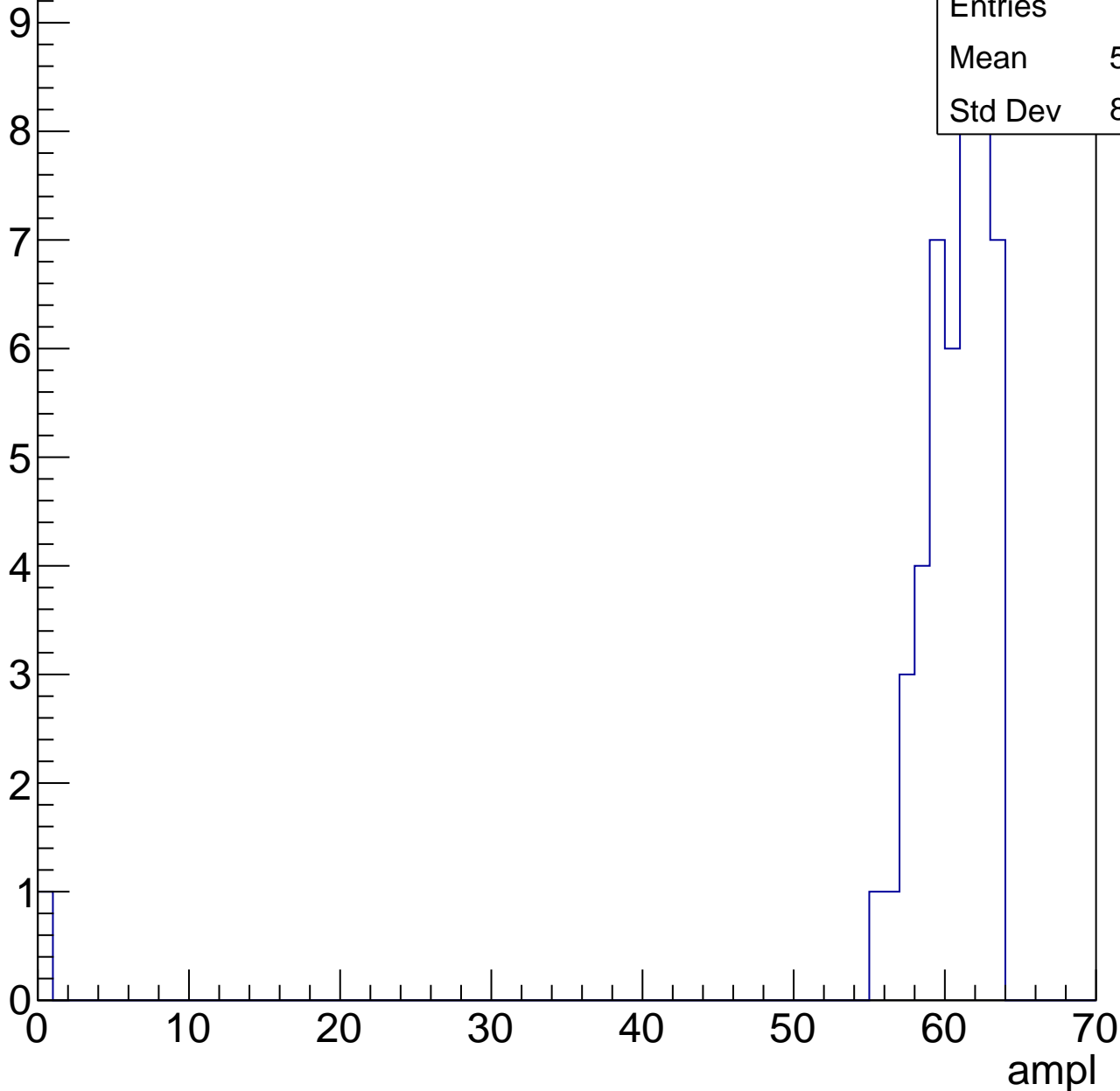
10

# B1L000S, U25-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	59.06
Std Dev	8.847



# B1L000S, U25-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L000S, U25-ch20, adc0

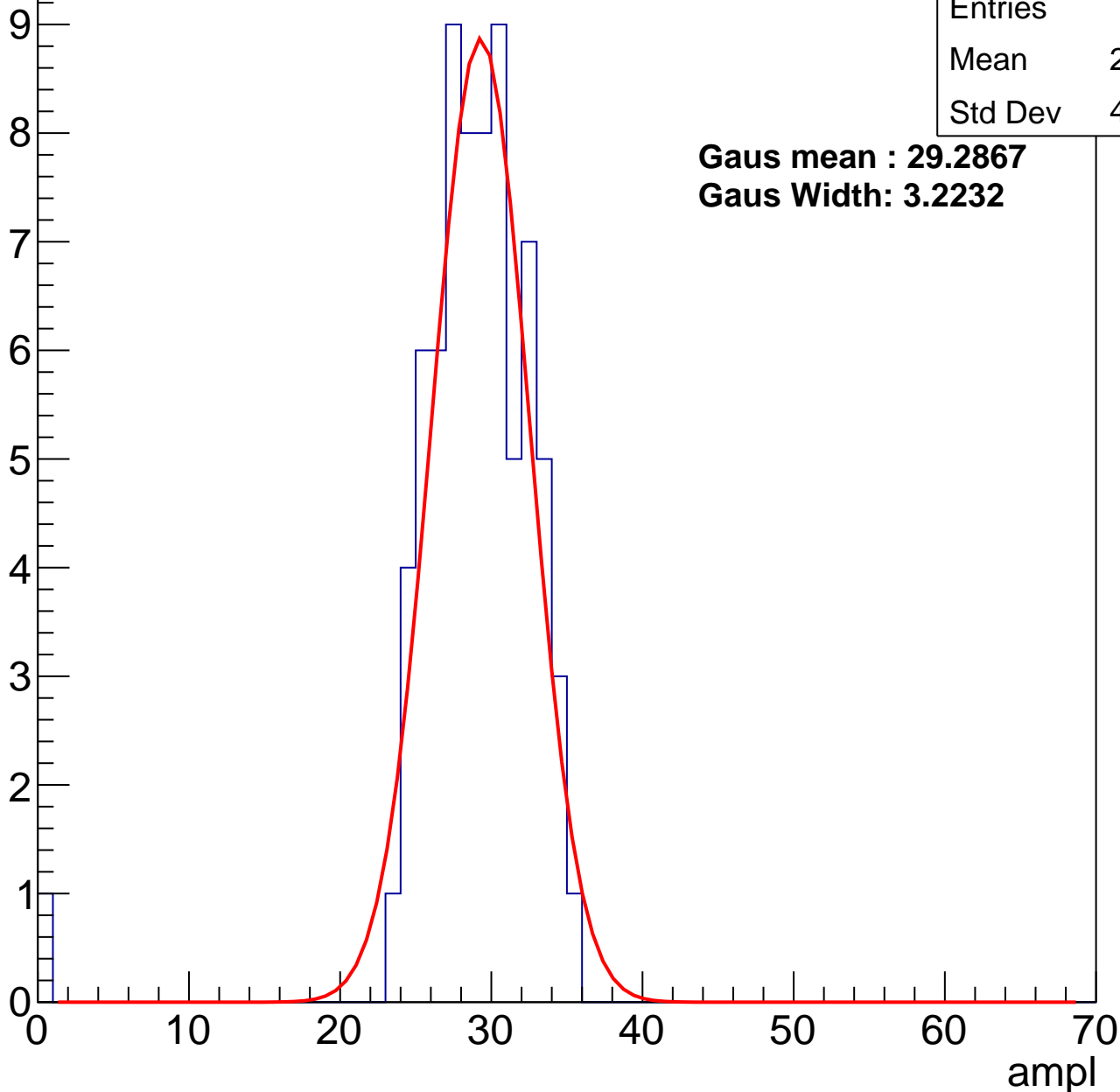
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.42
Std Dev	4.426

**Gaus mean : 29.2867**

**Gaus Width: 3.2232**



# B1L000S, U25-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	35.62
Std Dev	2.859

**Gaus mean : 36.2361**

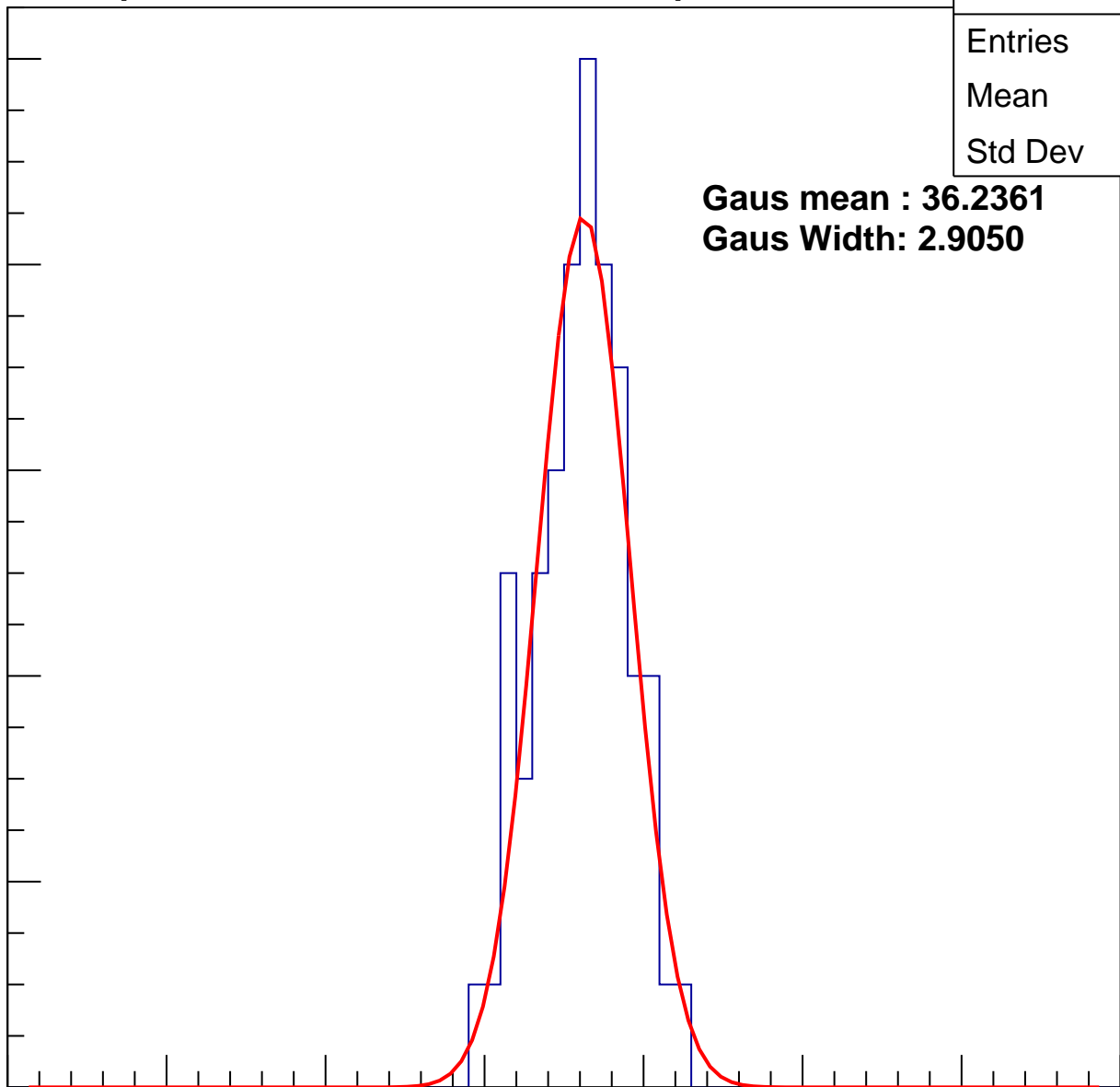
**Gaus Width: 2.9050**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch20, adc2

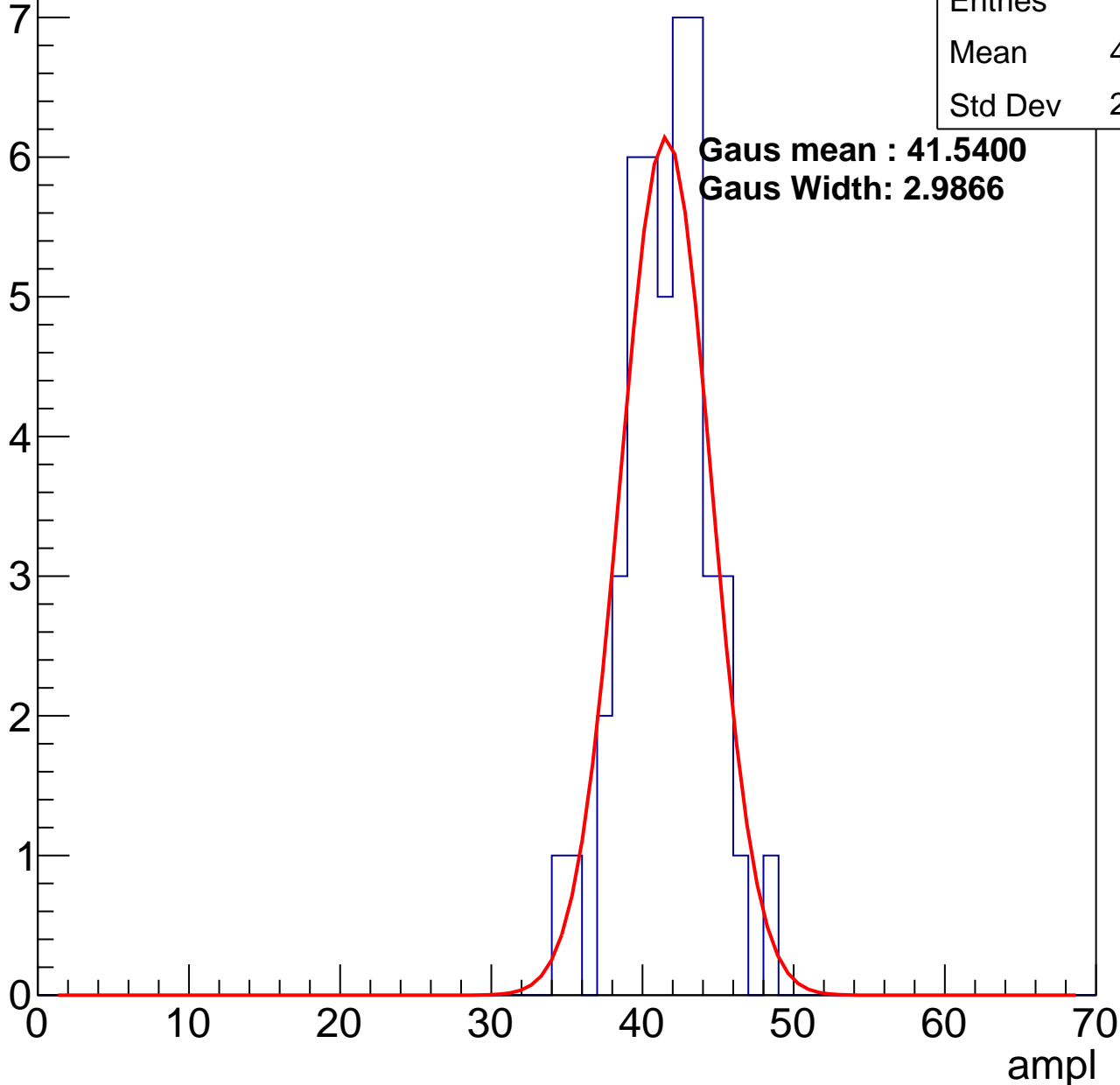
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	41.13
Std Dev	2.787

**Gaus mean : 41.5400**

**Gaus Width: 2.9866**

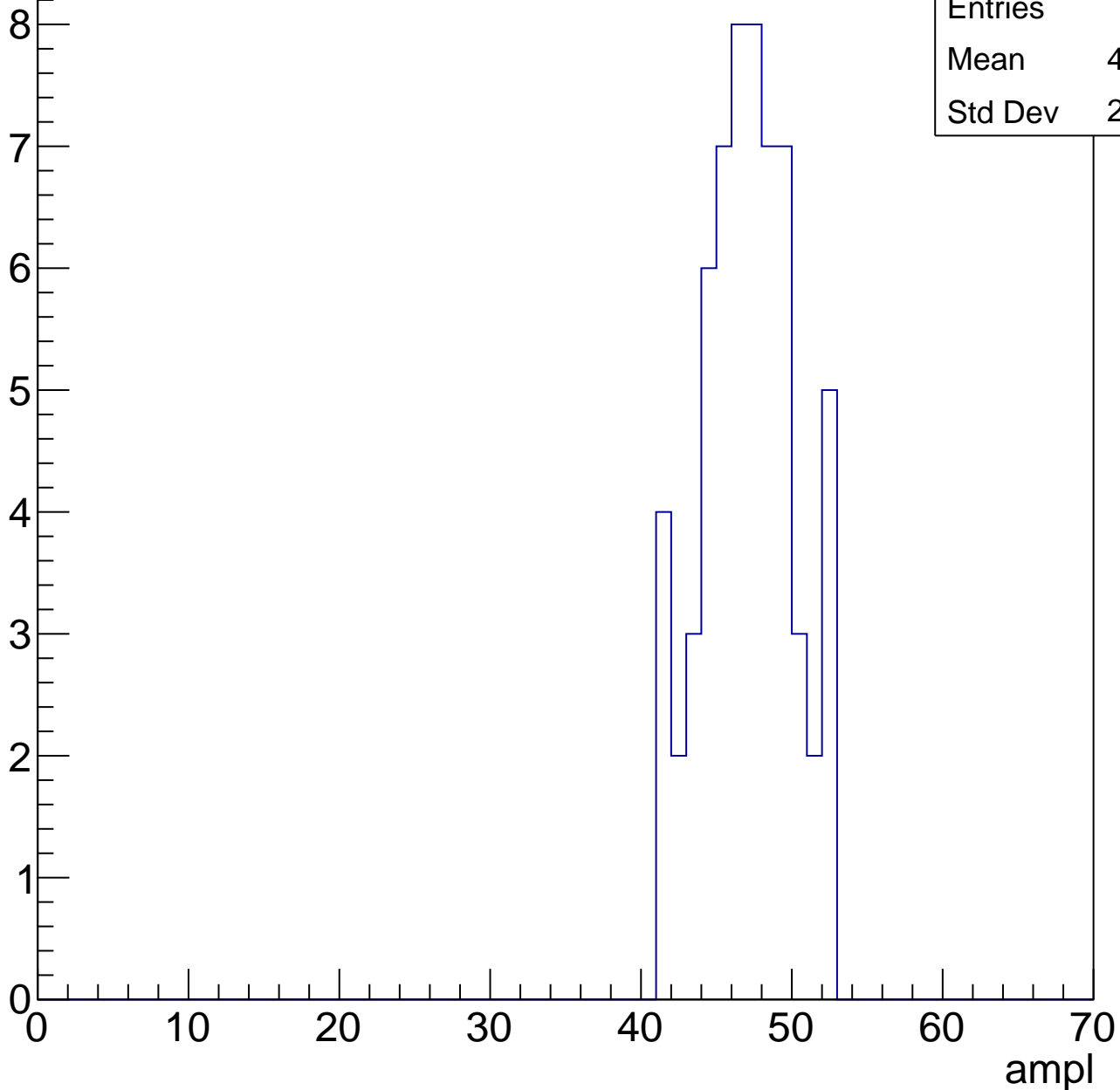


# B1L000S, U25-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	46.63
Std Dev	2.958

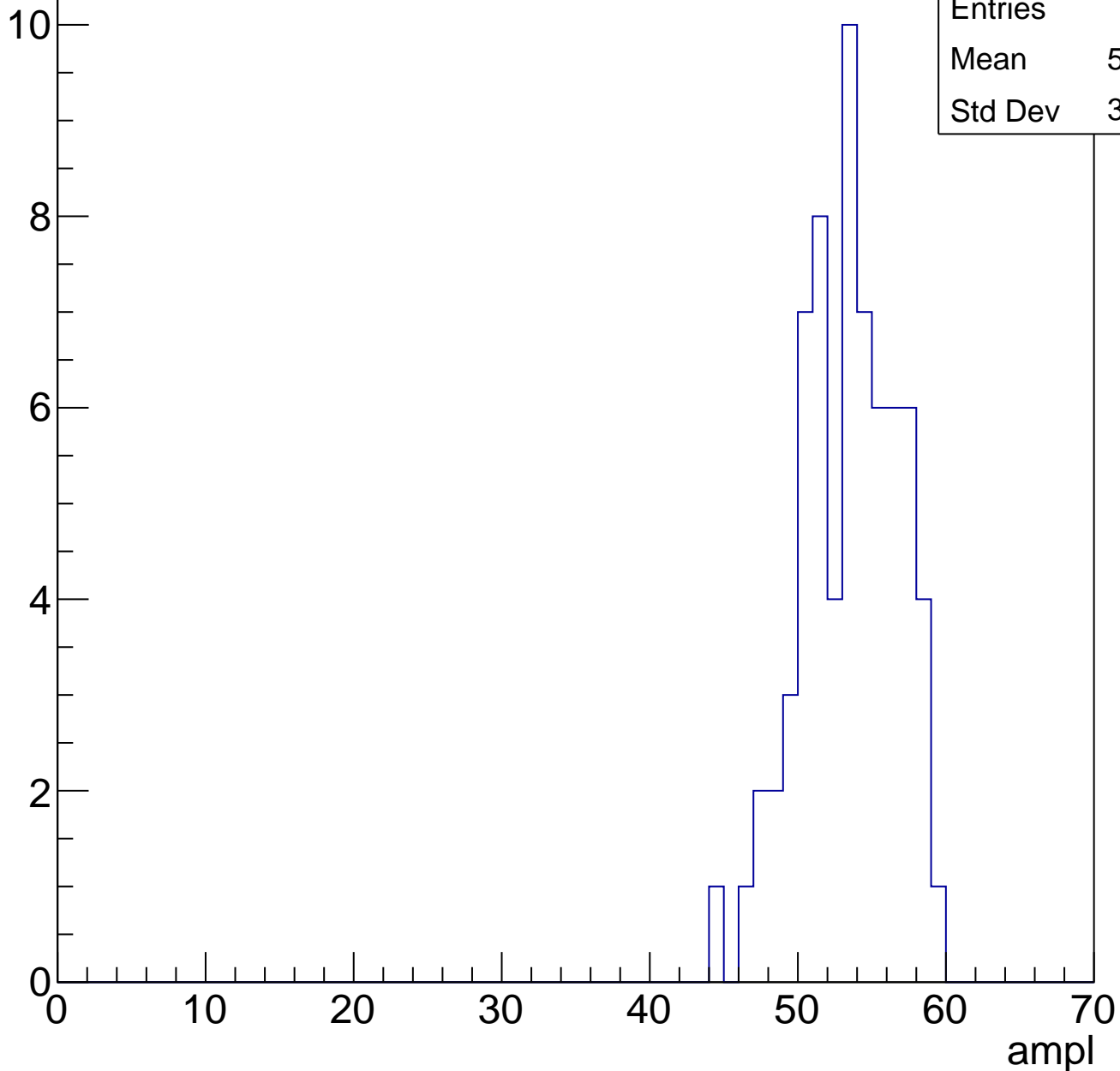


# B1L000S, U25-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	52.94
Std Dev	3.244



# B1L000S, U25-ch20, adc5

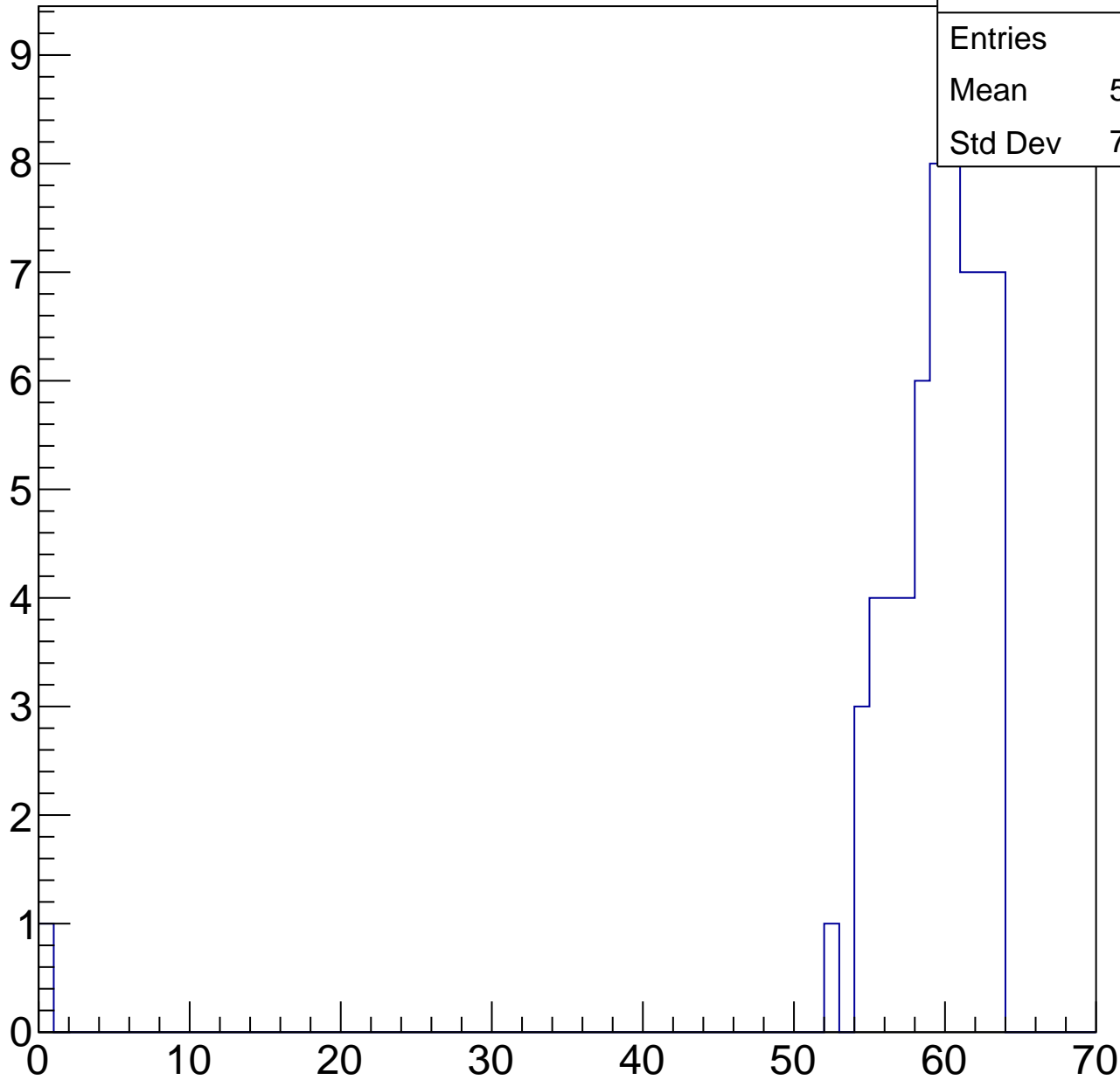
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	58.16
Std Dev	7.992

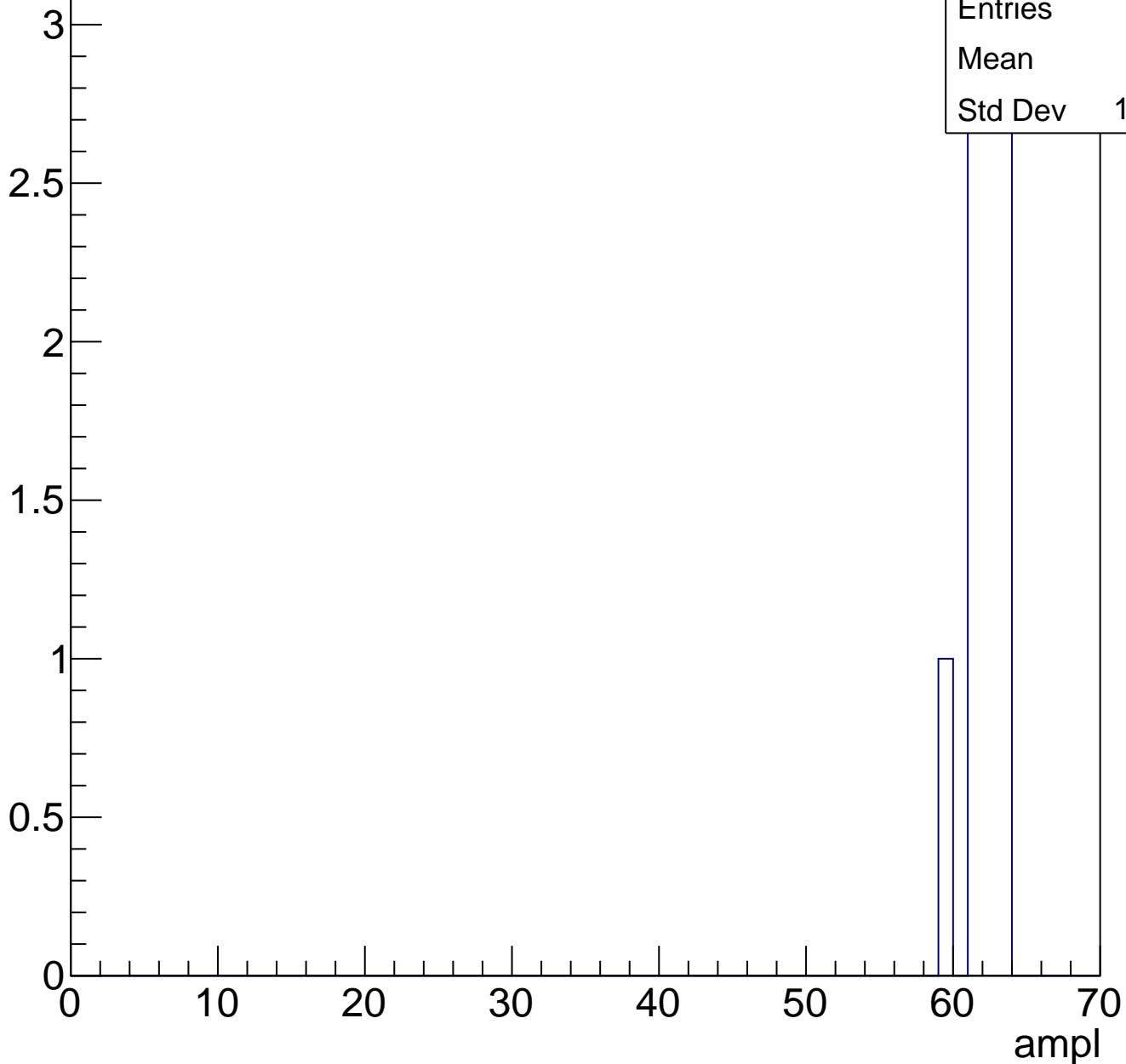
ampl



# B1L000S, U25-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	60
Std Dev	0

# B1L000S, U25-ch21, adc0

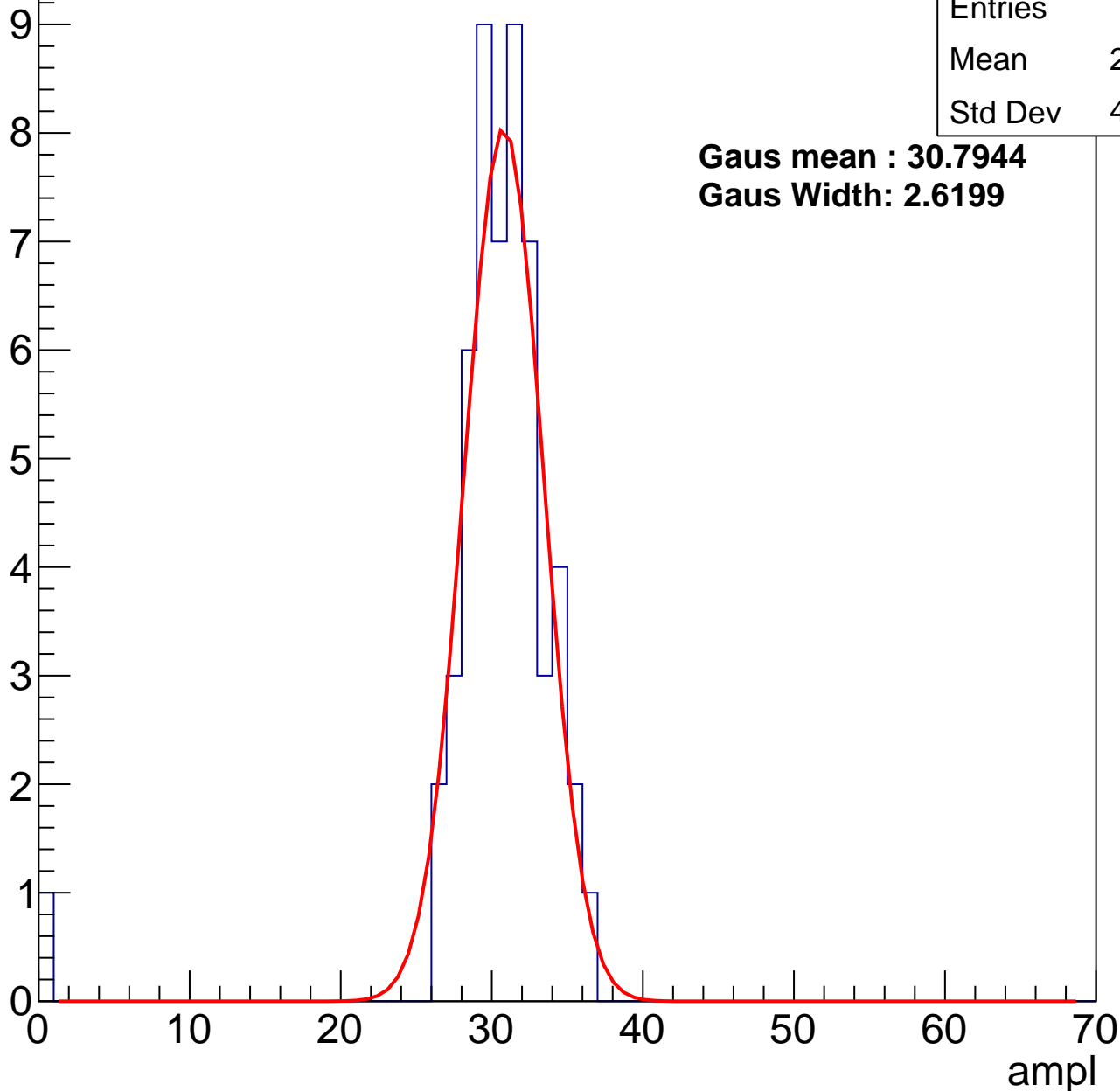
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	29.93
Std Dev	4.725

**Gaus mean : 30.7944**

**Gaus Width: 2.6199**



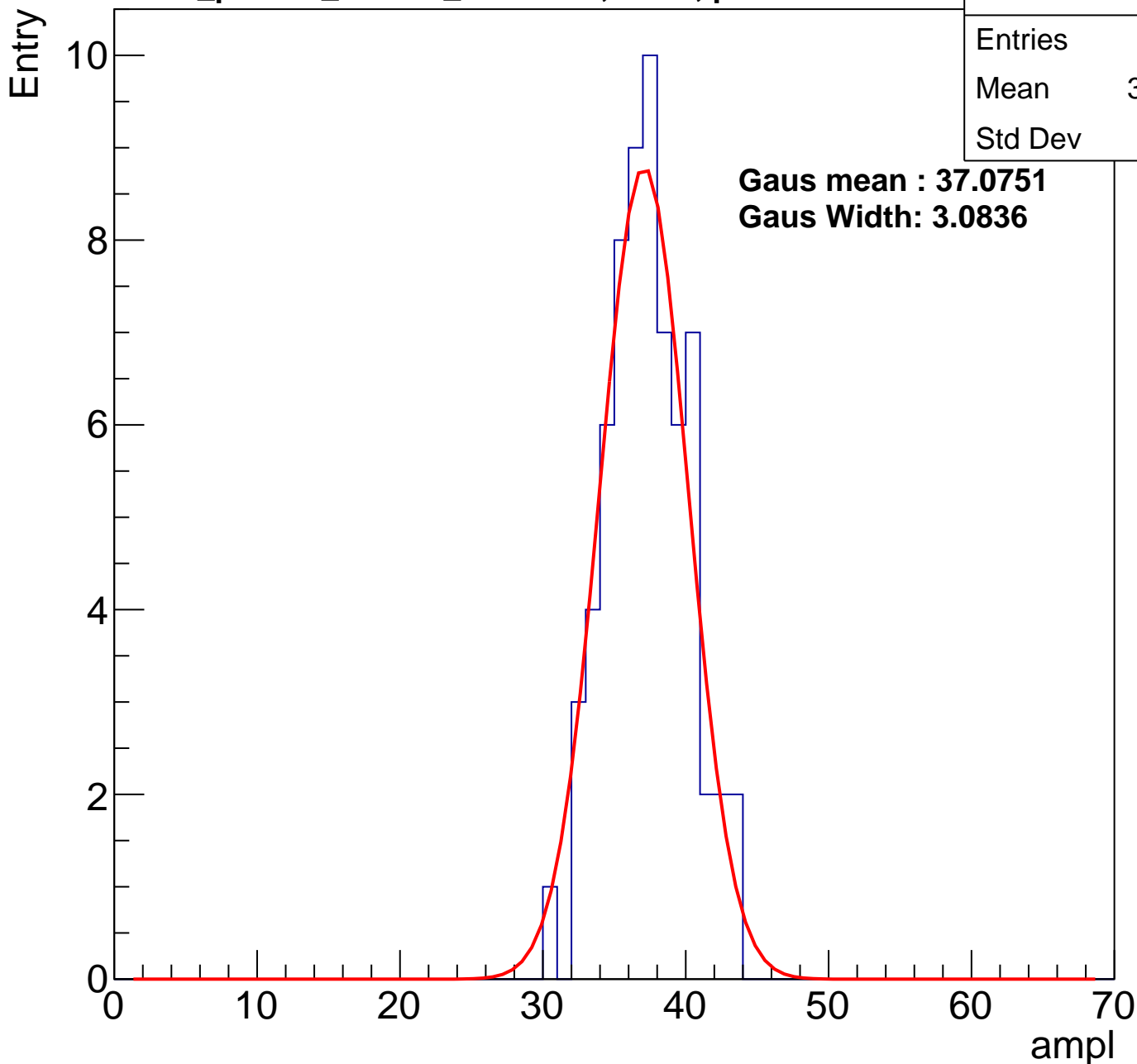
# B1L000S, U25-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	36.84
Std Dev	2.81

**Gaus mean : 37.0751**

**Gaus Width: 3.0836**



# B1L000S, U25-ch21, adc2

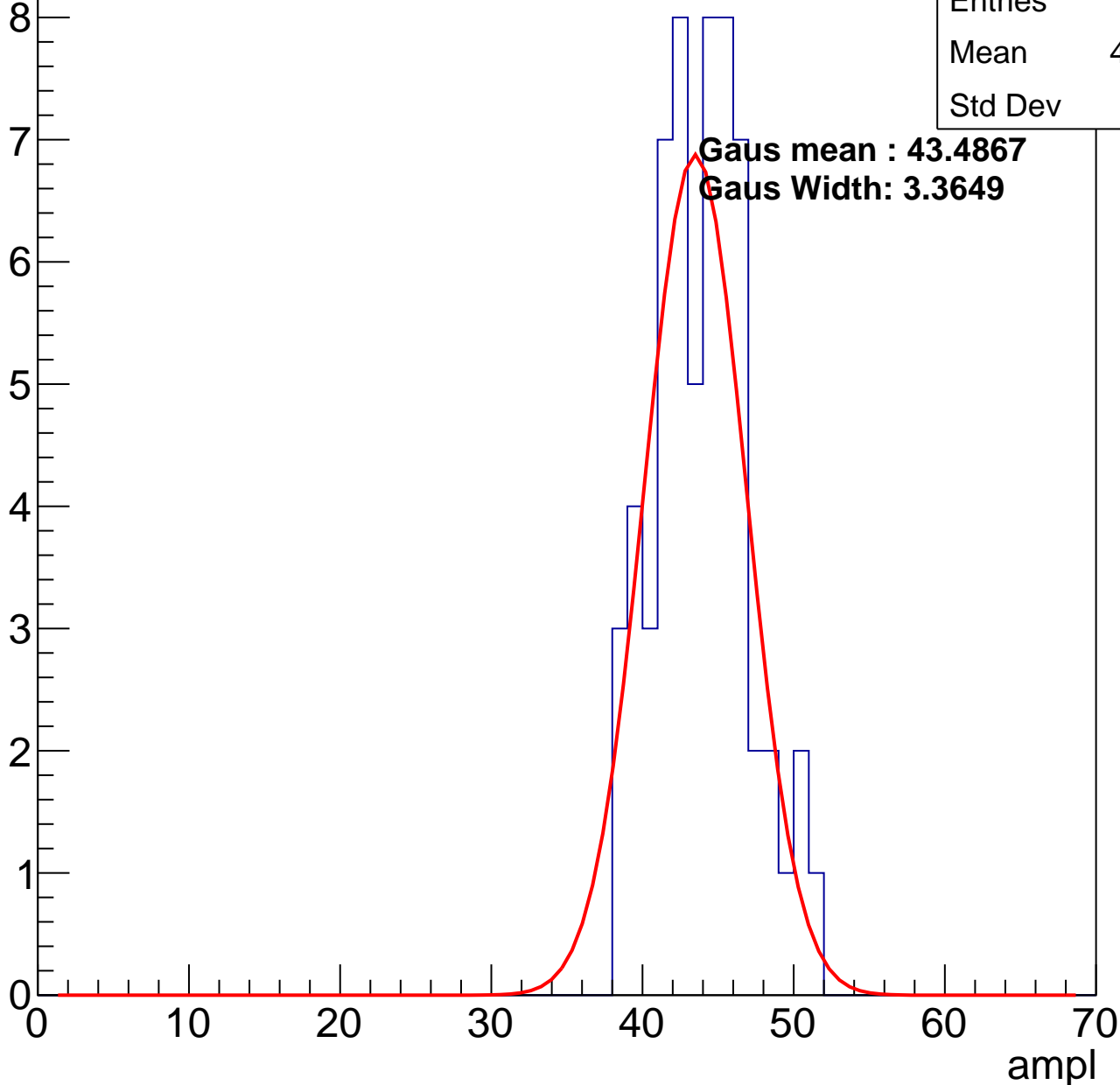
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	43.48
Std Dev	3.06

**Gaus mean : 43.4867**

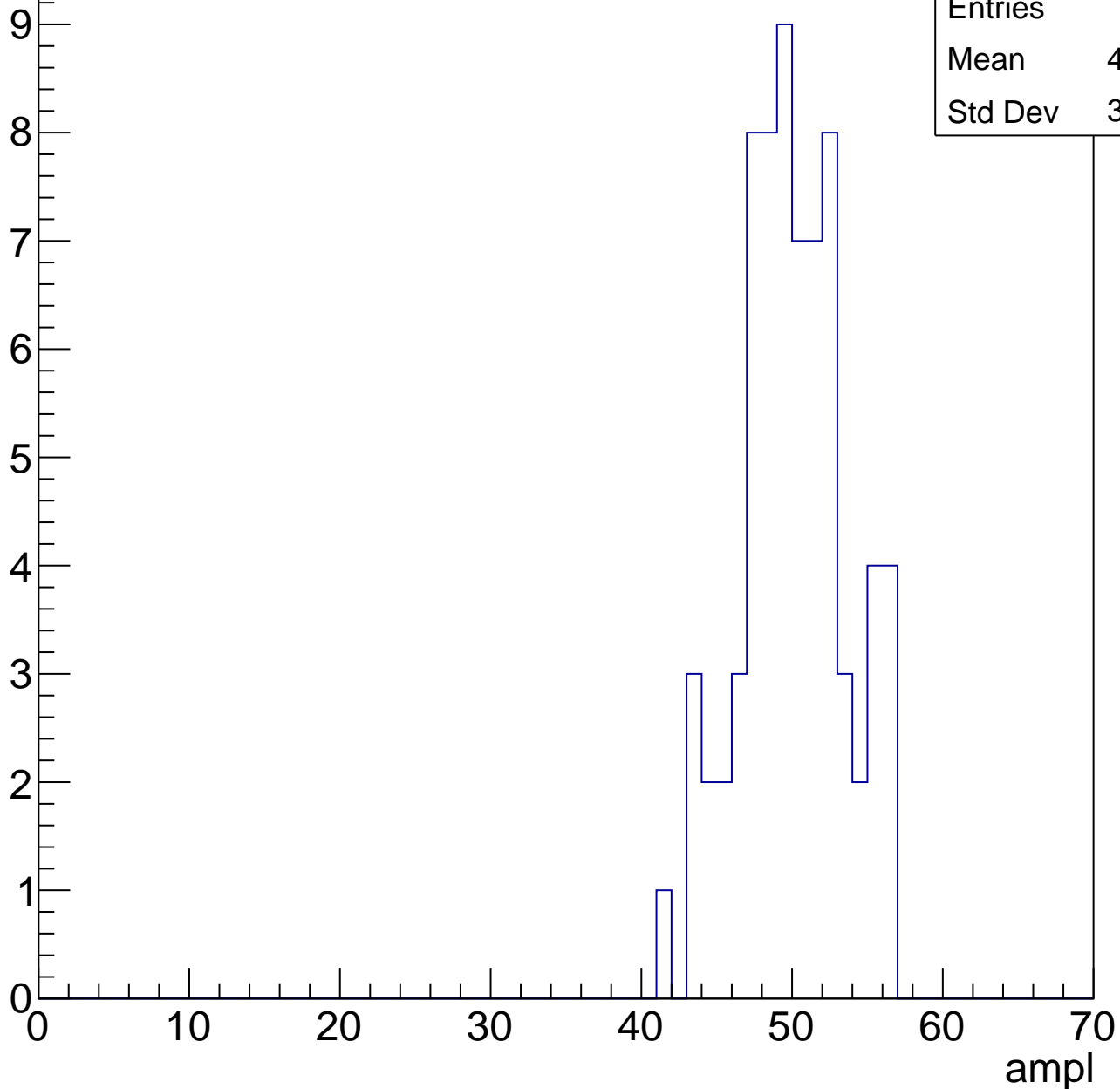
**Gaus Width: 3.3649**



# B1L000S, U25-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

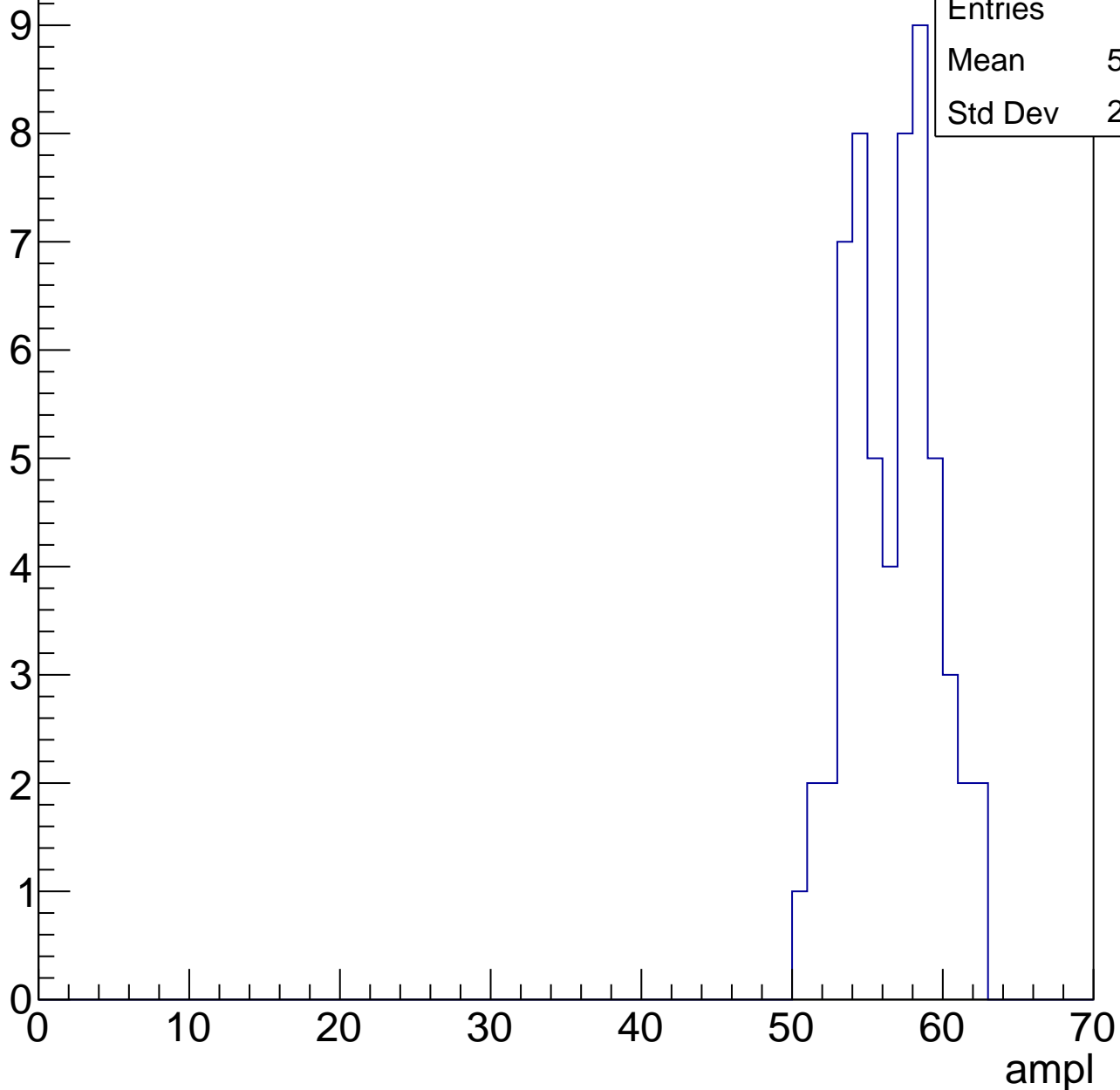


Entries	71
Mean	49.59
Std Dev	3.442

# B1L000S, U25-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch21, adc5

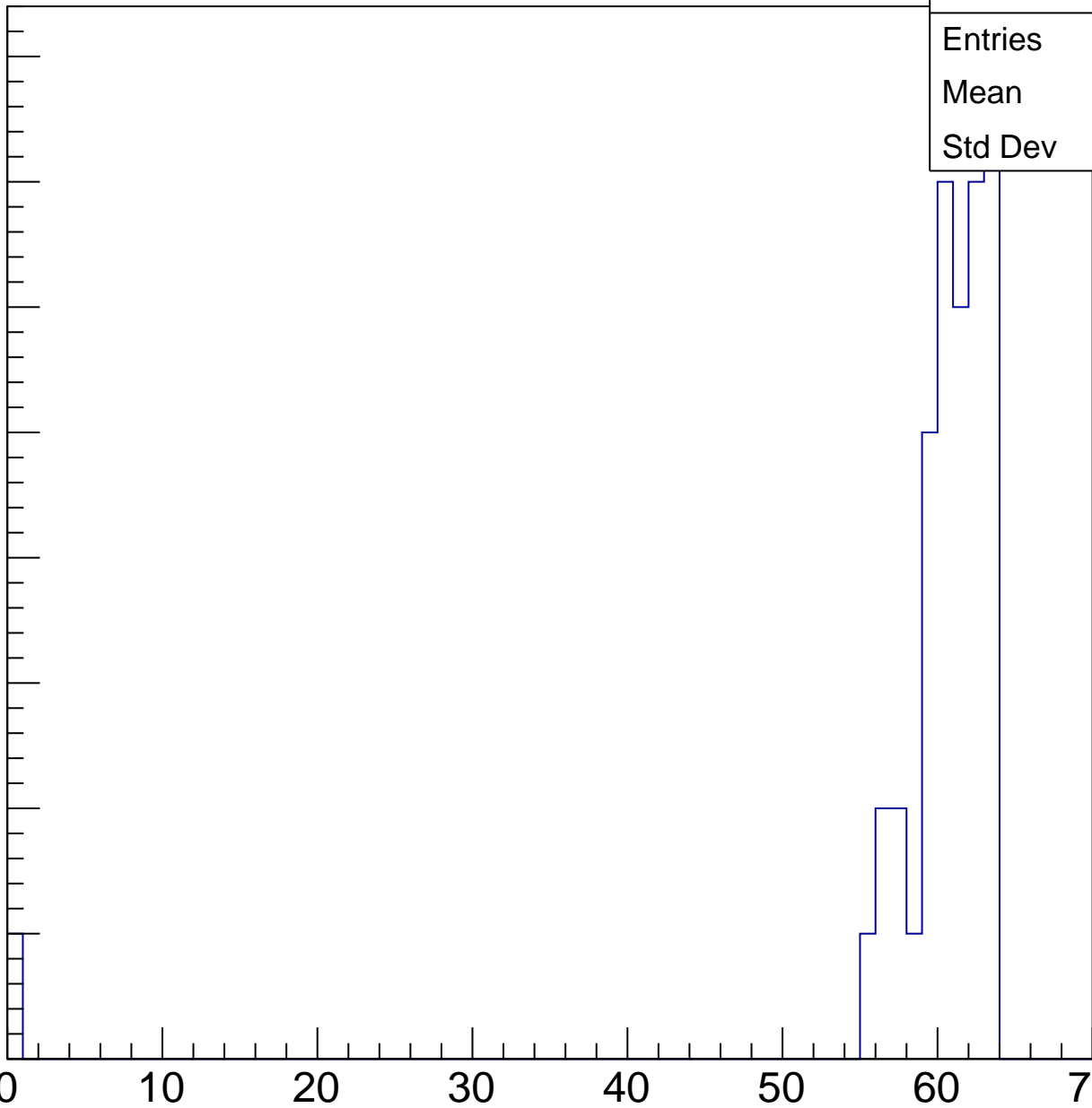
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	58.95
Std Dev	9.677

ampl



# B1L000S, U25-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch22, adc0

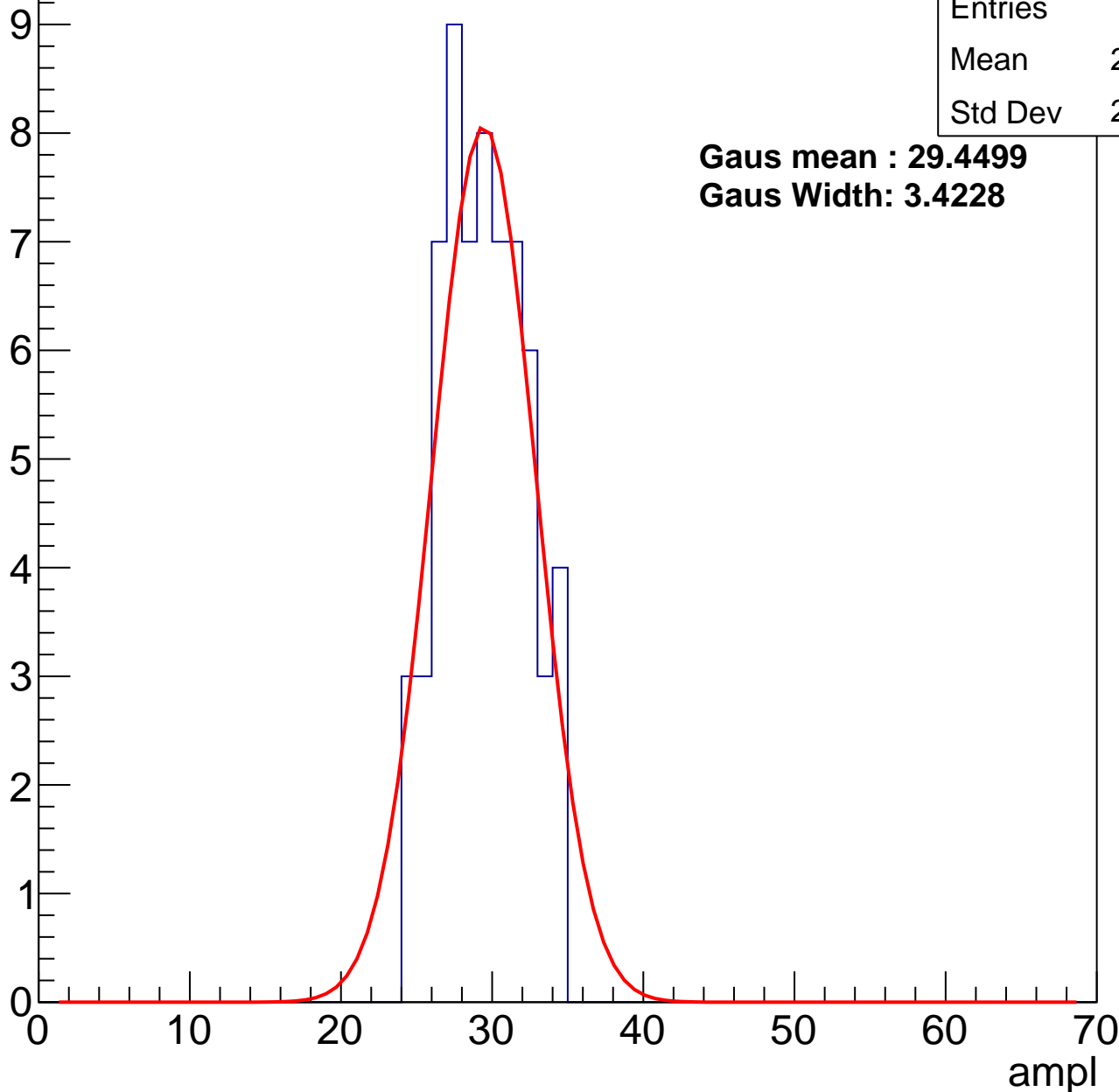
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.97
Std Dev	2.698

**Gaus mean : 29.4499**

**Gaus Width: 3.4228**



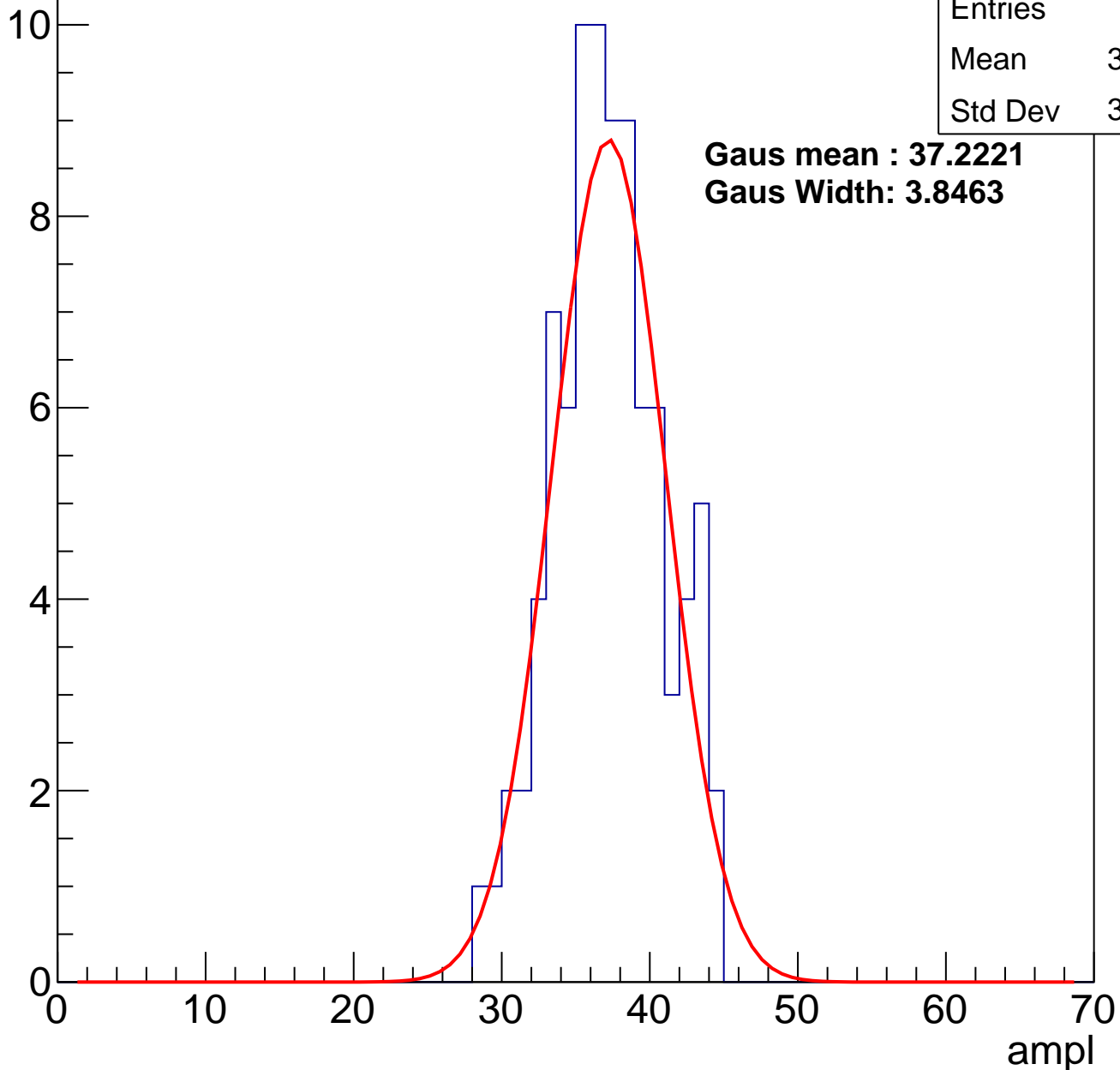
# B1L000S, U25-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	36.72
Std Dev	3.616

**Gaus mean : 37.2221**  
**Gaus Width: 3.8463**

Entry



# B1L000S, U25-ch22, adc2

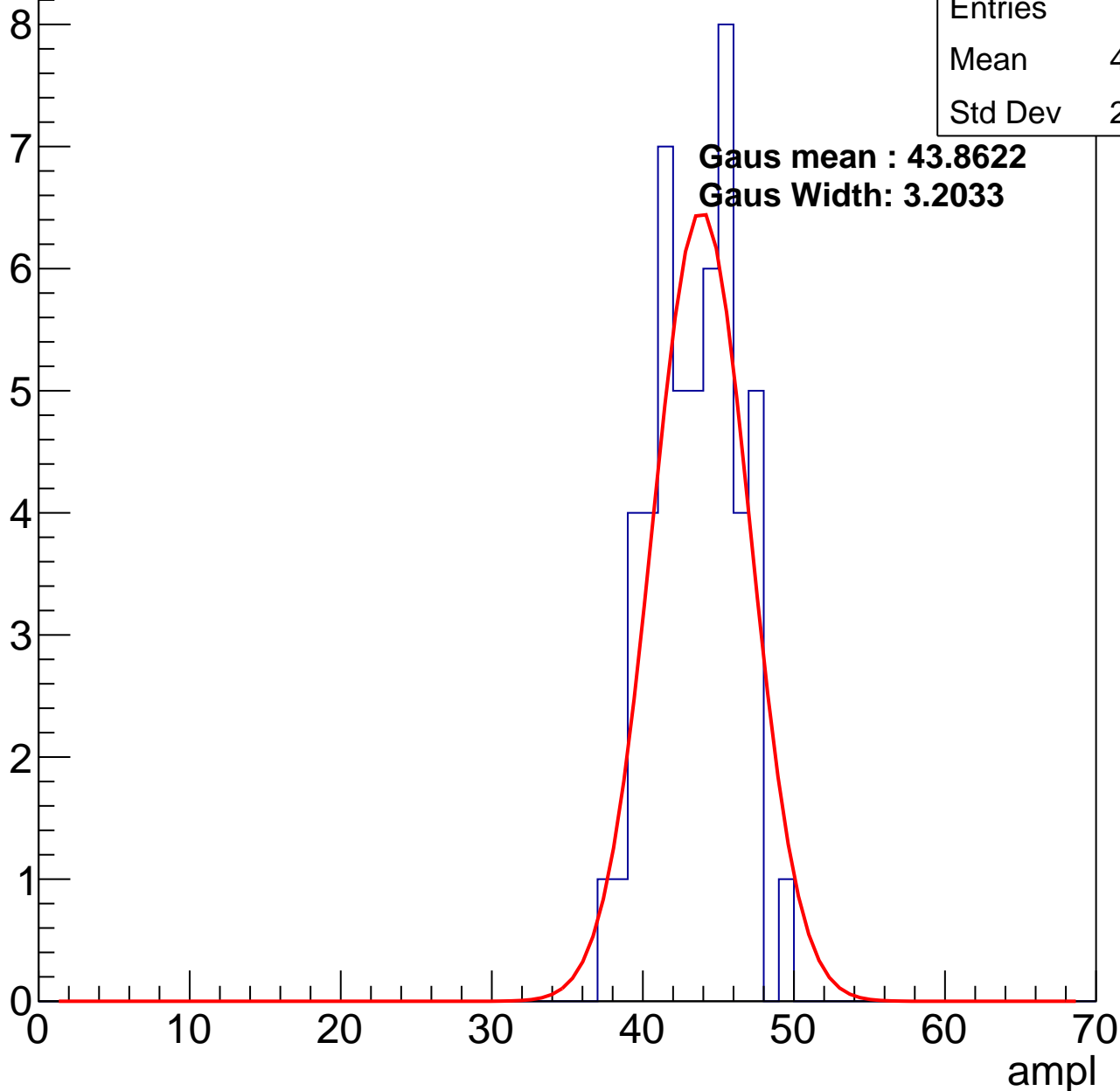
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	43.04
Std Dev	2.744

**Gaus mean : 43.8622**

**Gaus Width: 3.2033**

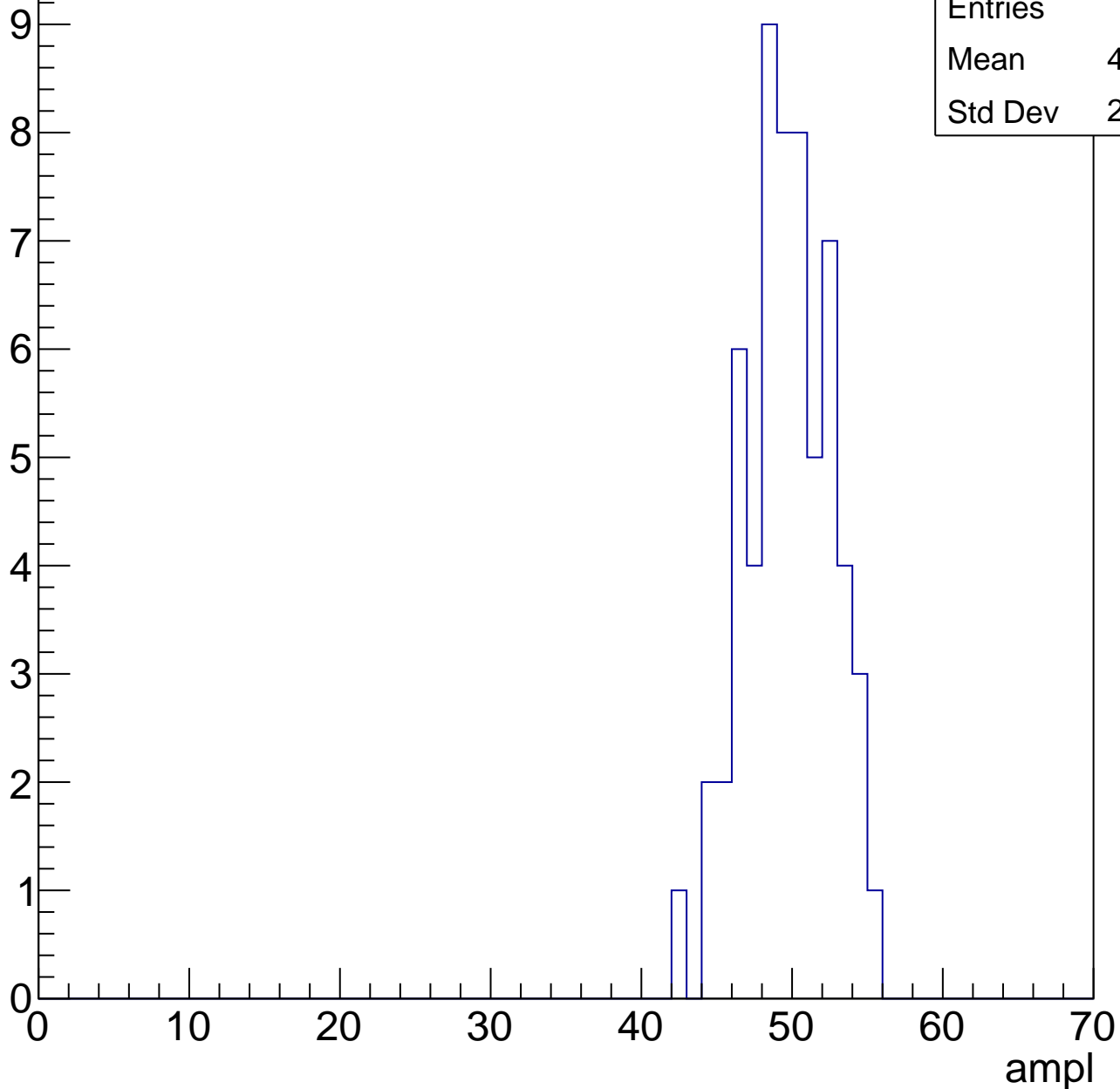


# B1L000S, U25-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

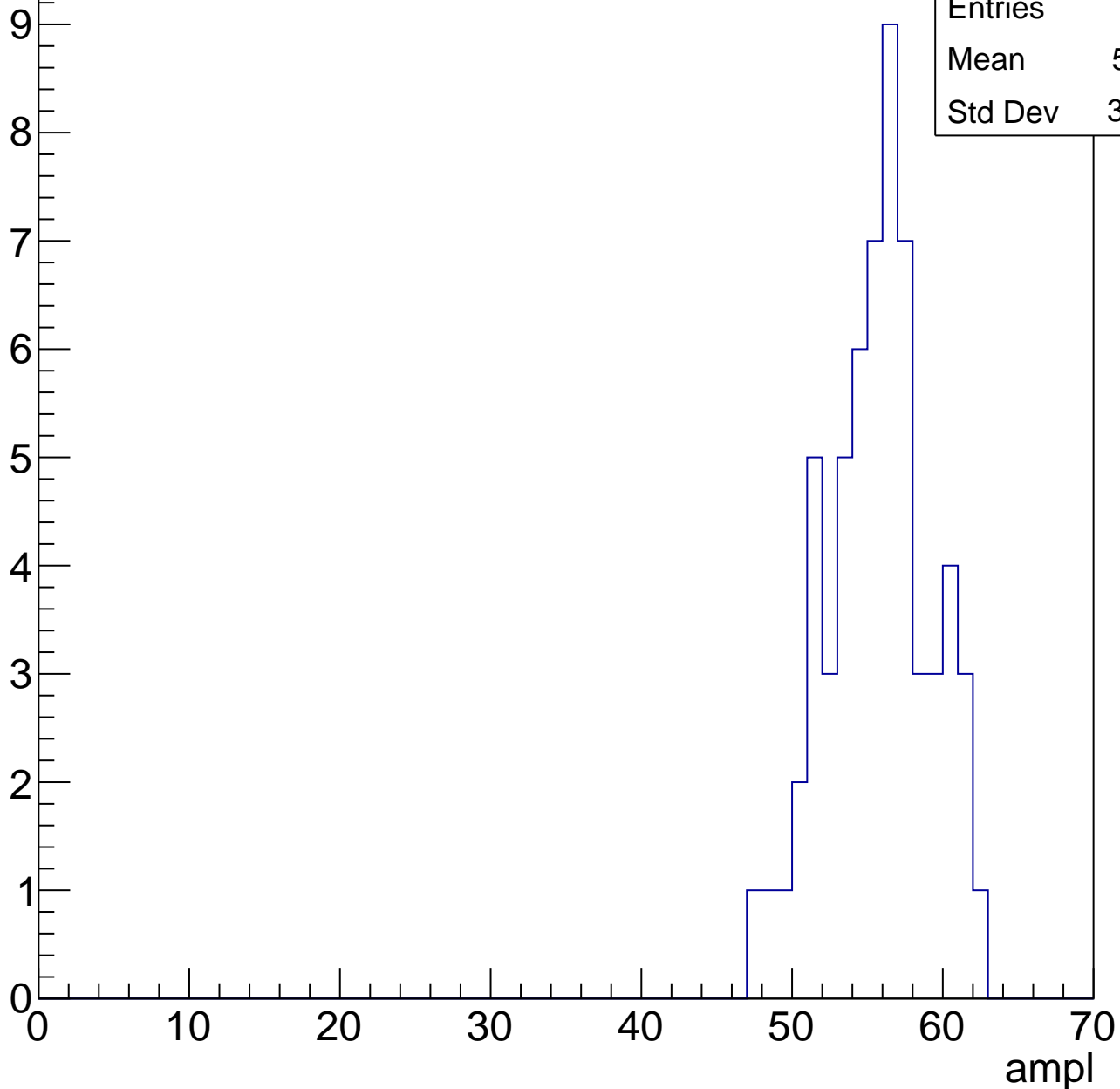
Entries	60
Mean	49.27
Std Dev	2.804



# B1L000S, U25-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

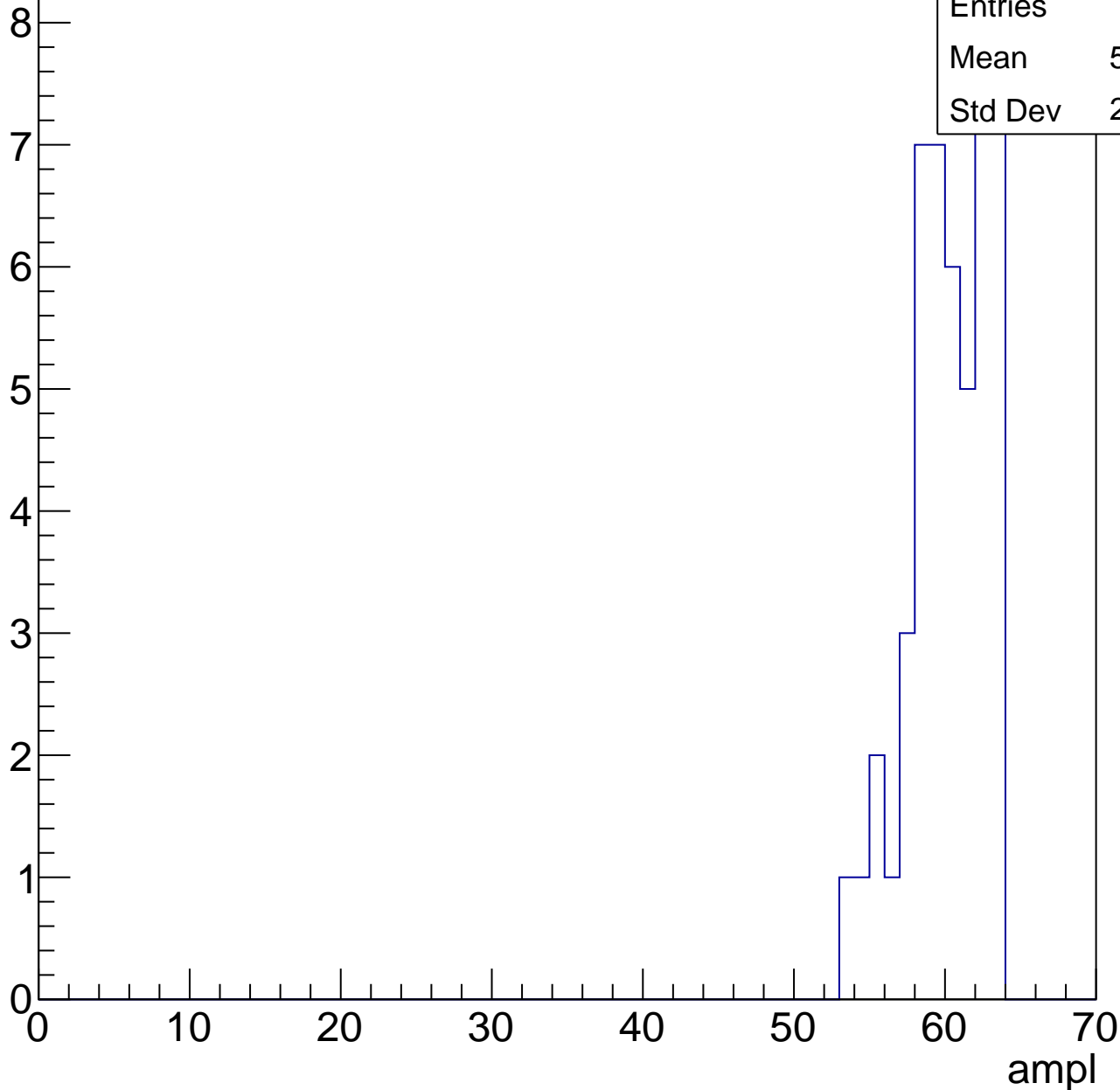


# B1L000S, U25-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

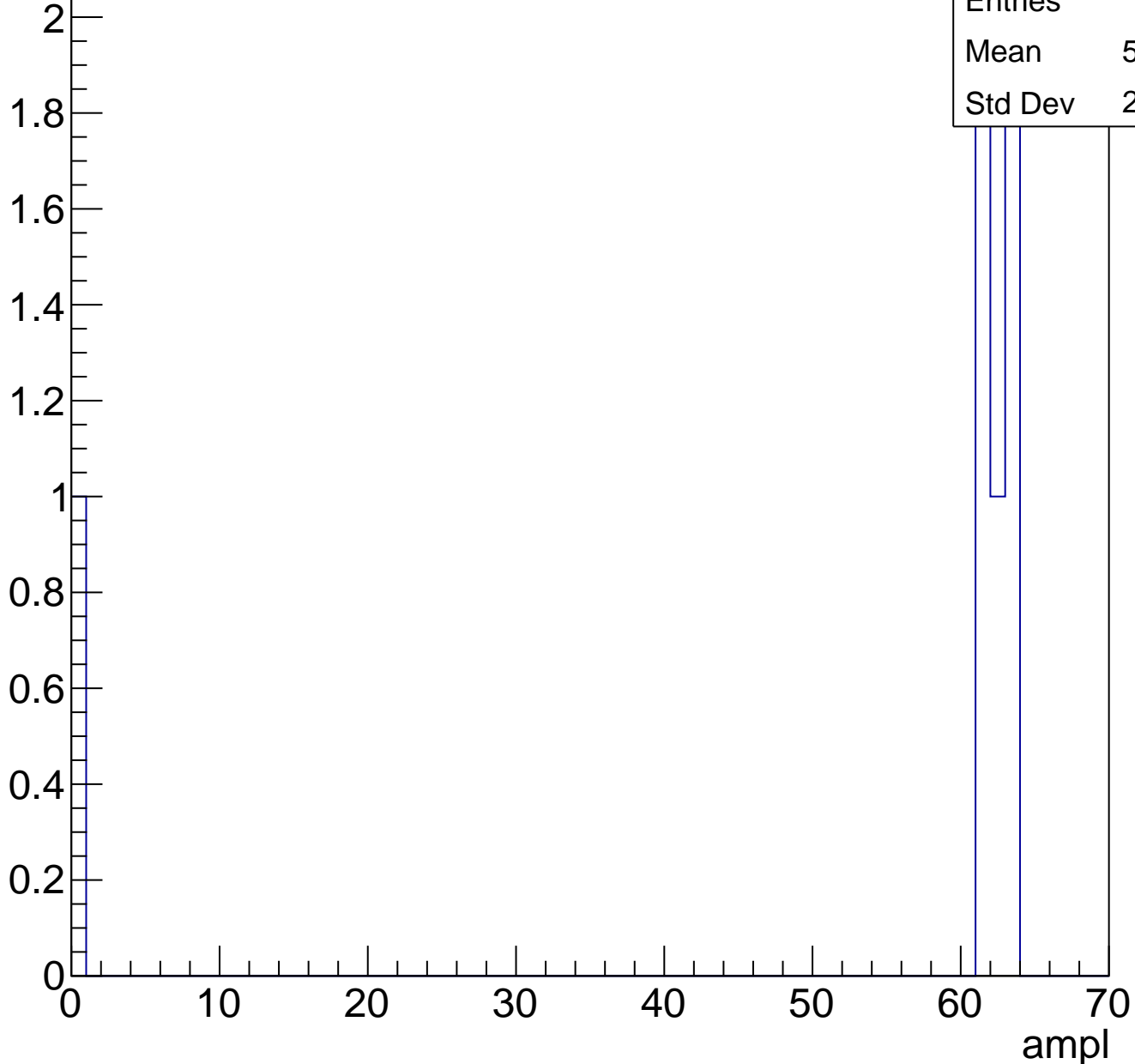
Entries	49
Mean	59.76
Std Dev	2.552



# B1L000S, U25-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch23, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	29.85
Std Dev	2.867

**Gaus mean : 29.7628**

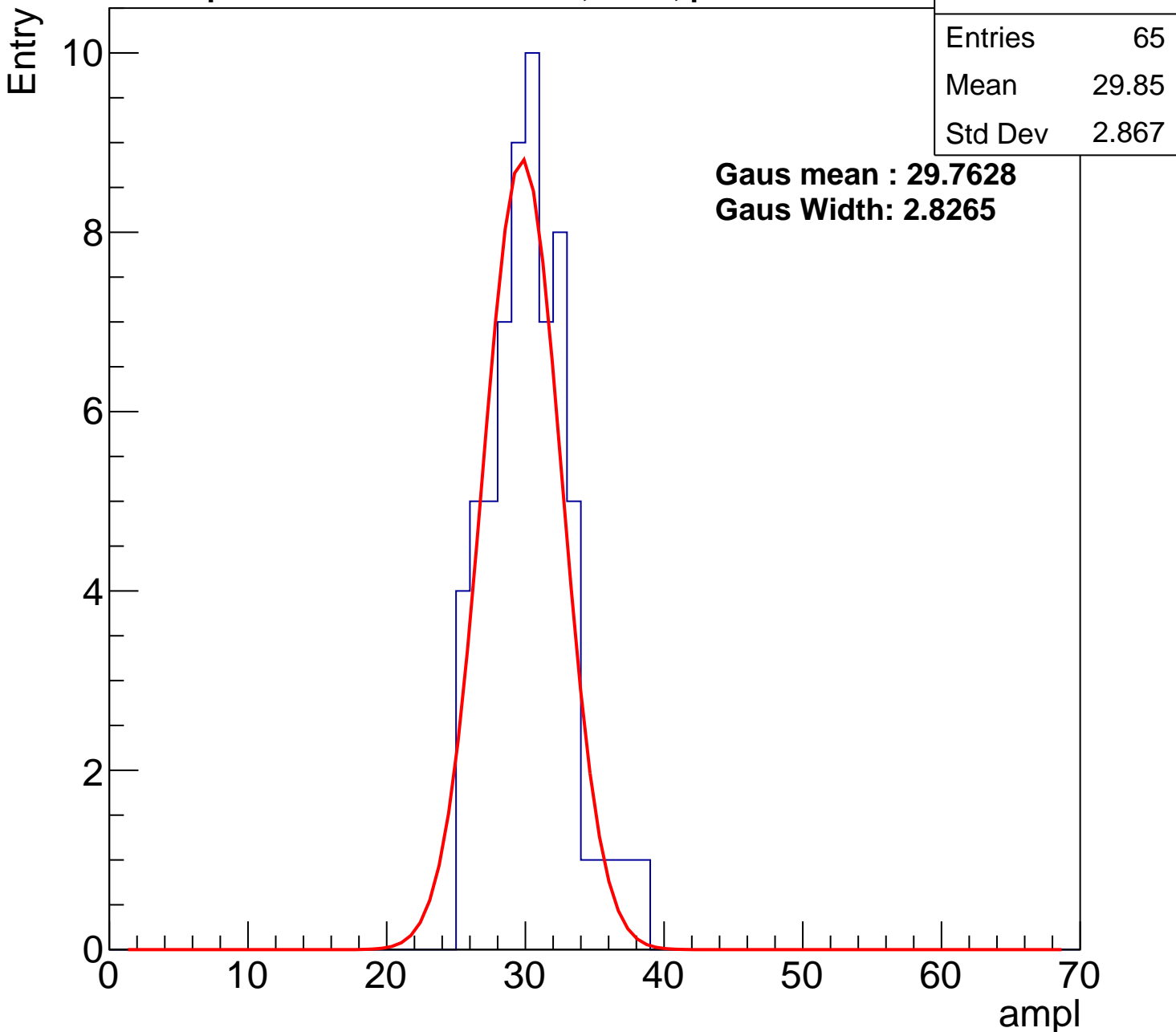
**Gaus Width: 2.8265**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch23, adc1

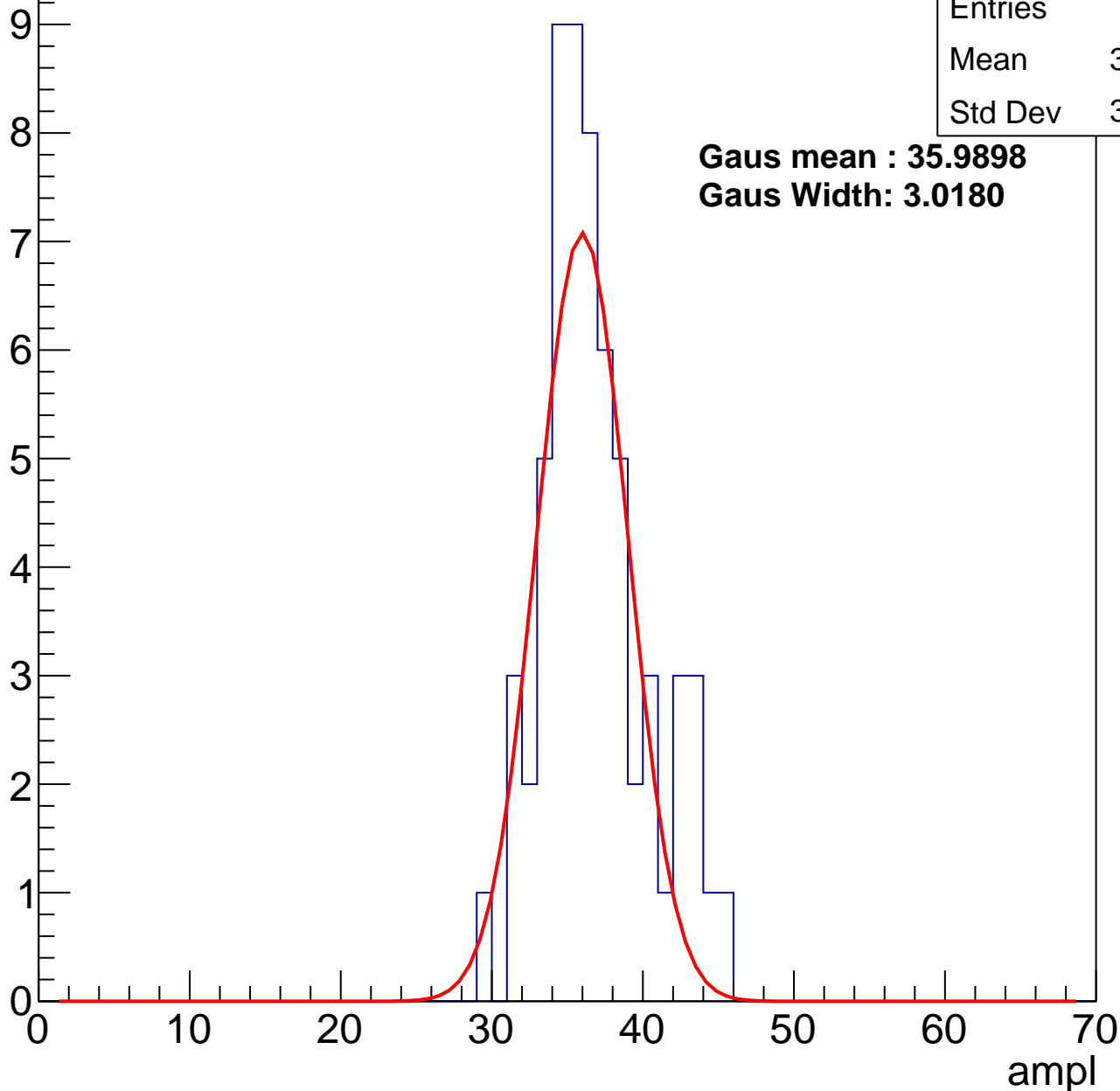
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.37
Std Dev	3.488

**Gaus mean : 35.9898**

**Gaus Width: 3.0180**



# B1L000S, U25-ch23, adc2

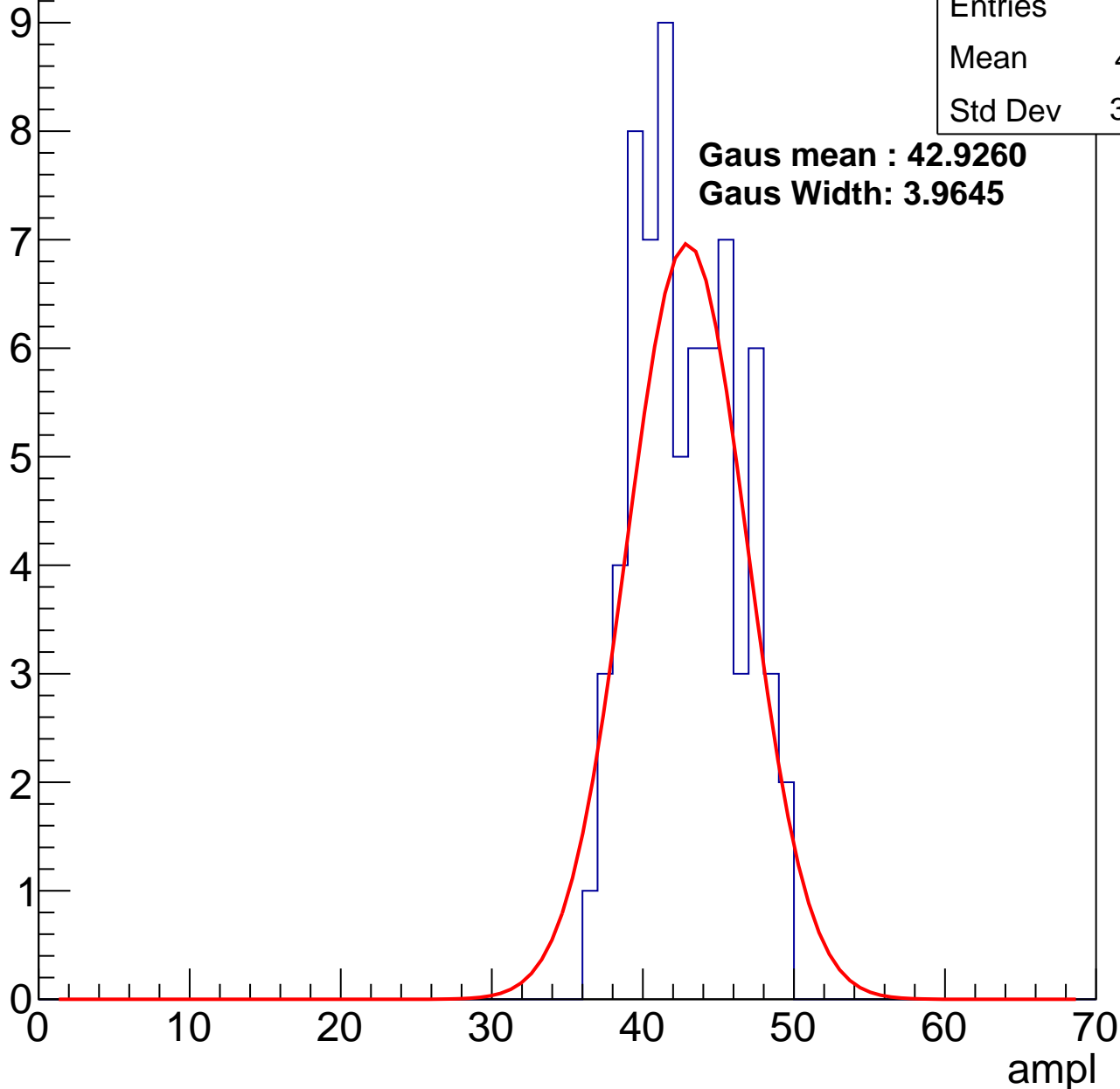
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.41
Std Dev	3.314

**Gaus mean : 42.9260**

**Gaus Width: 3.9645**

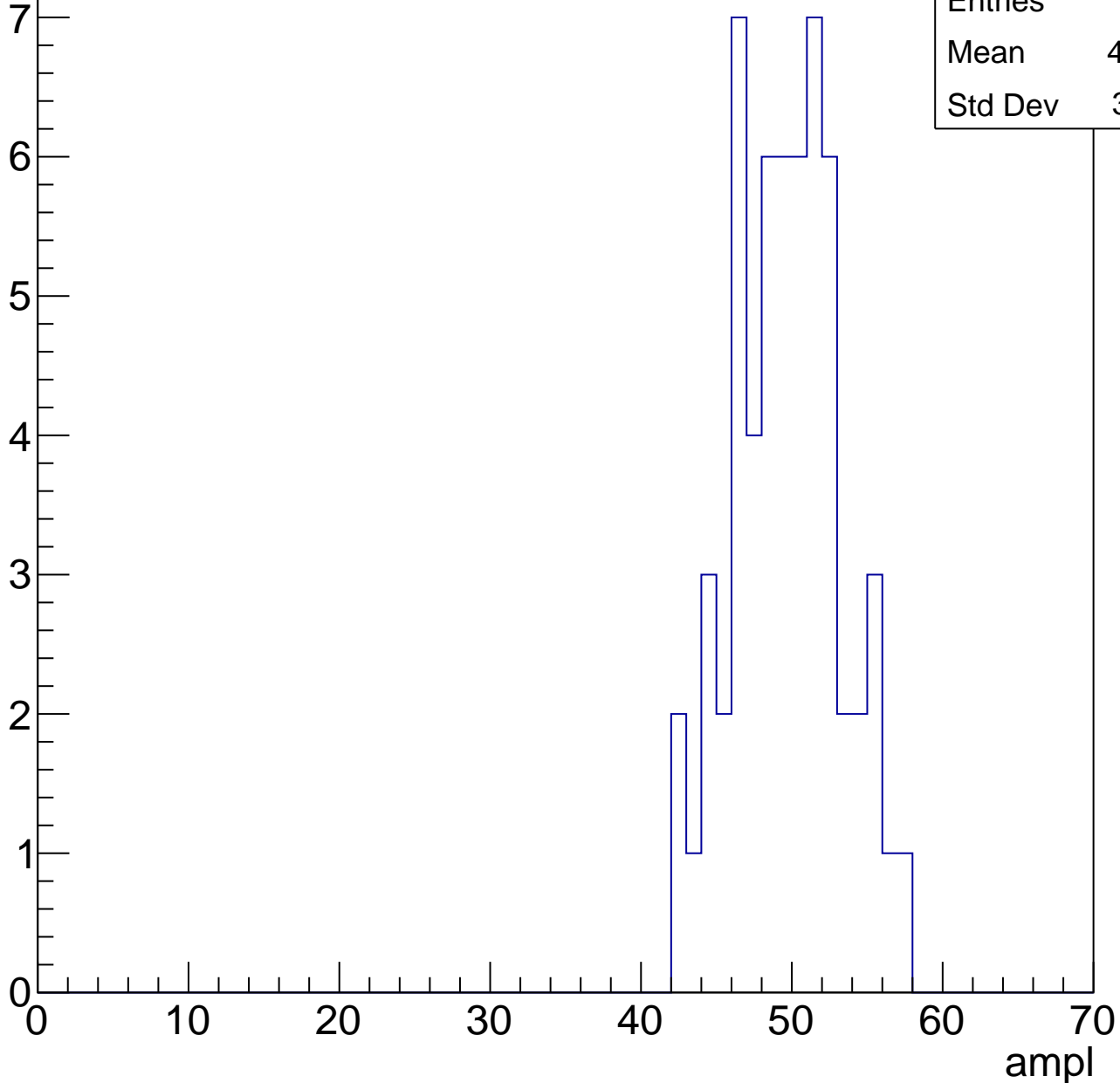


# B1L000S, U25-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	49.19
Std Dev	3.481

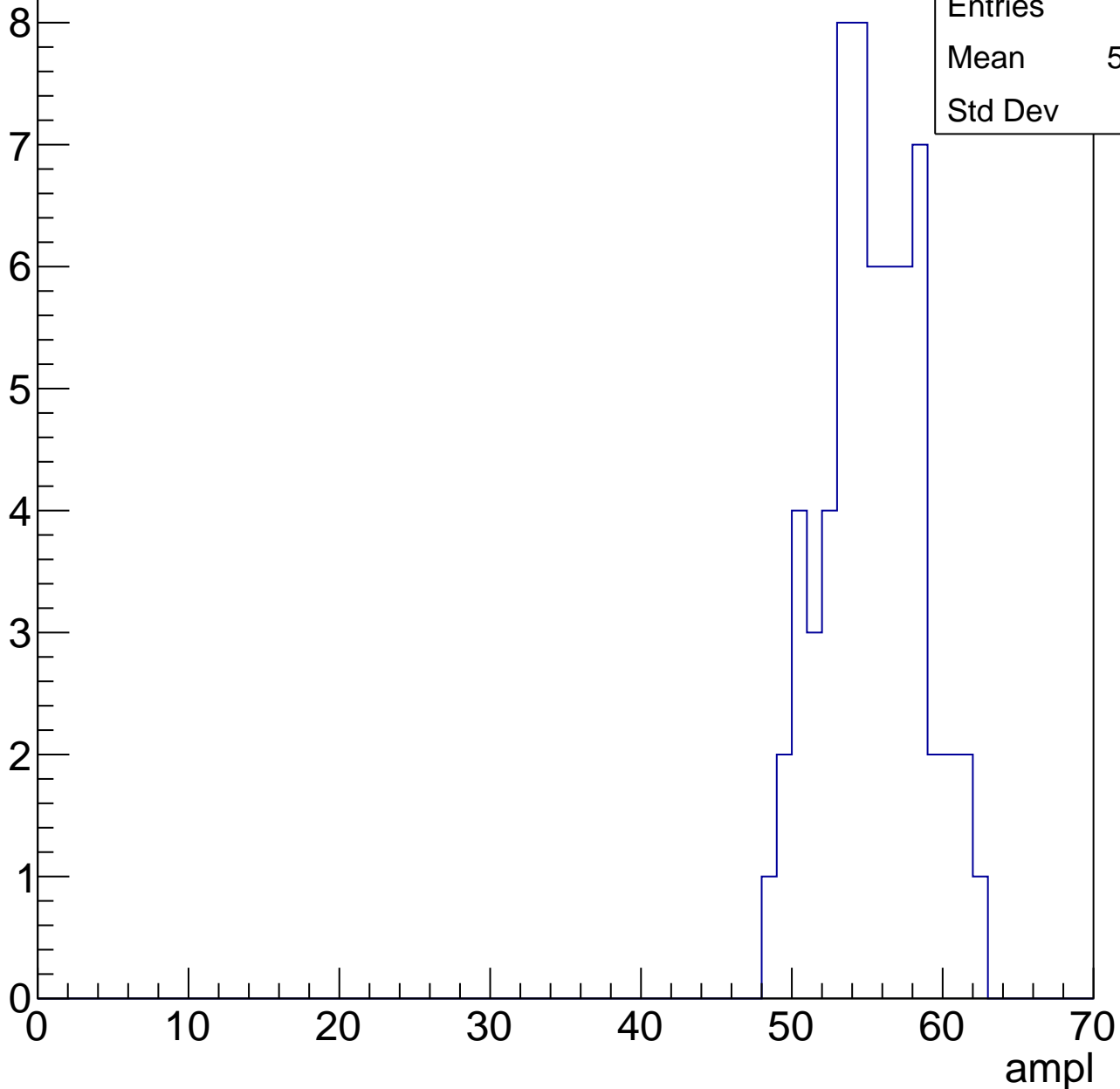


# B1L000S, U25-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	54.82
Std Dev	3.21

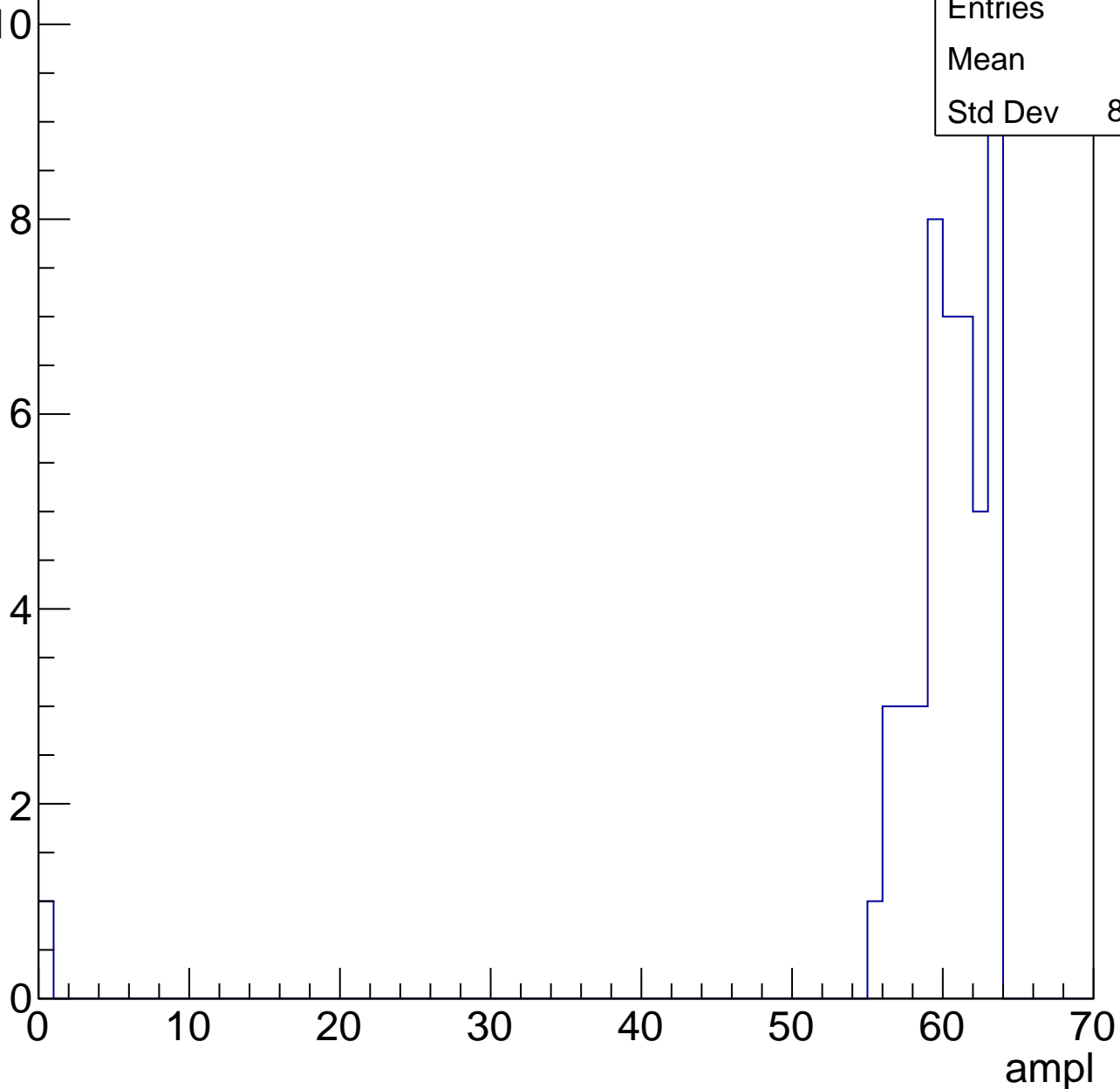


# B1L000S, U25-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

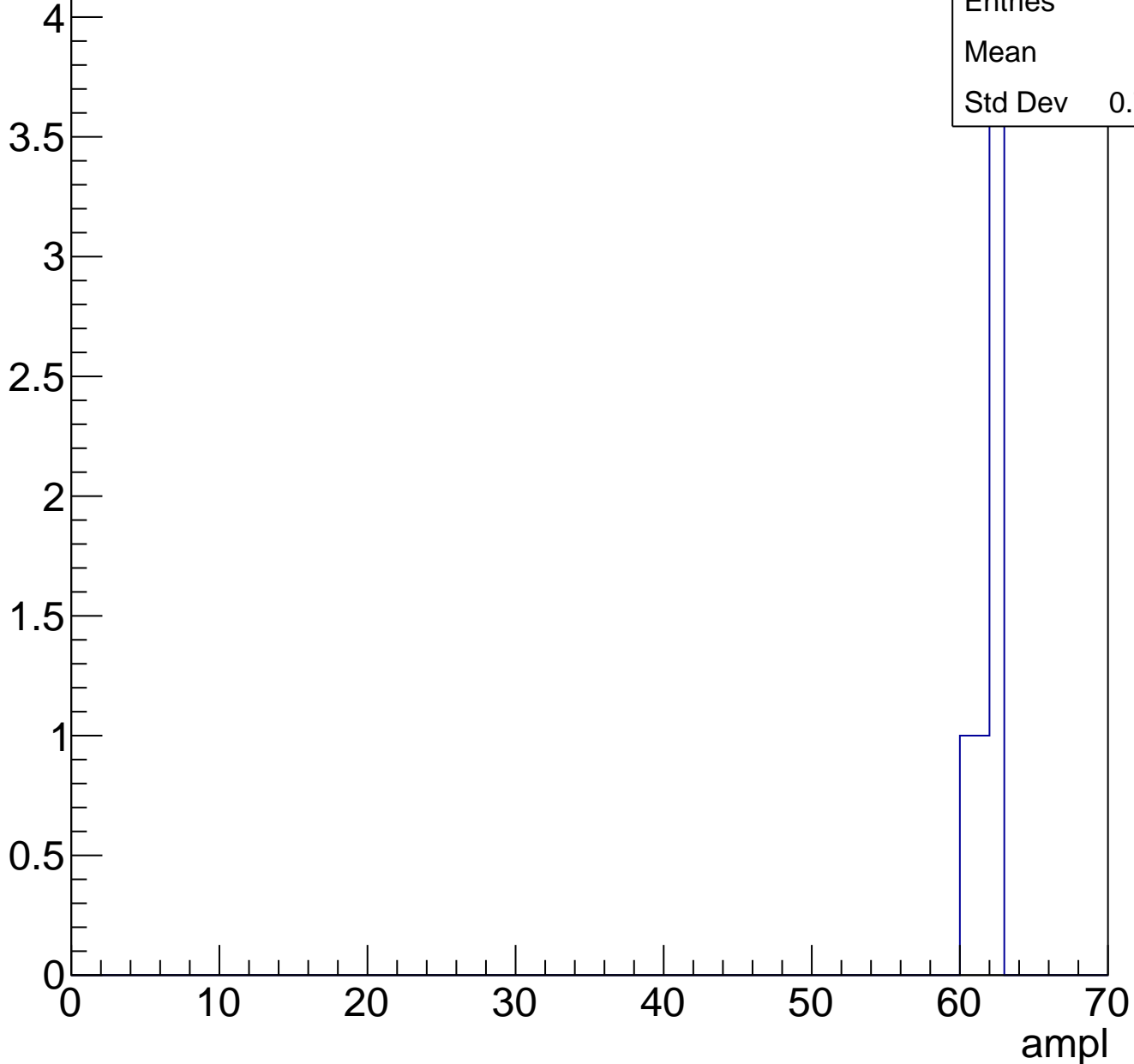
Entries	48
Mean	58.9
Std Dev	8.872



# B1L000S, U25-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	8.333
Std Dev	11.79

# B1L000S, U25-ch24, adc0

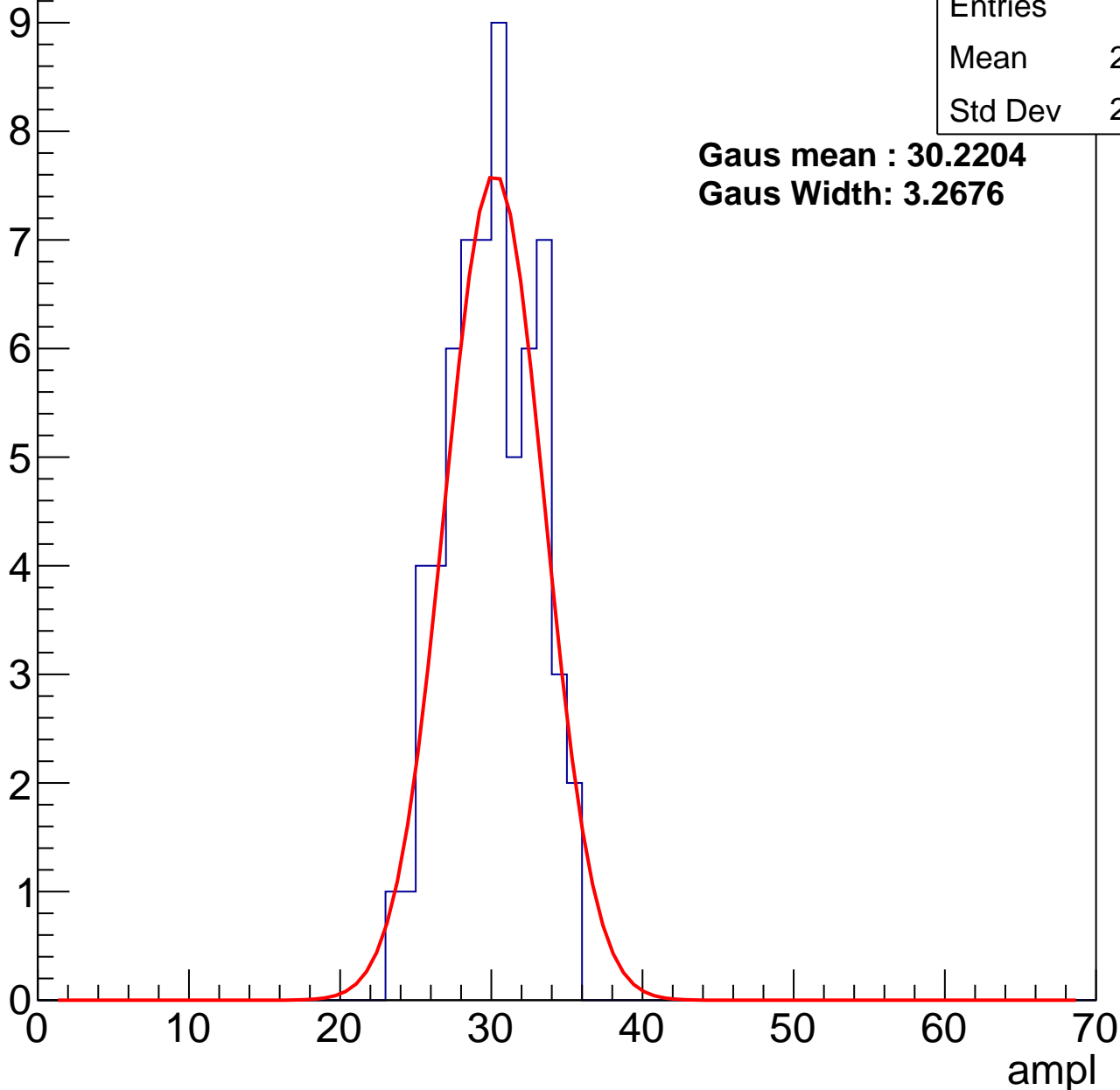
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	29.55
Std Dev	2.883

**Gaus mean : 30.2204**

**Gaus Width: 3.2676**



# B1L000S, U25-ch24, adc1

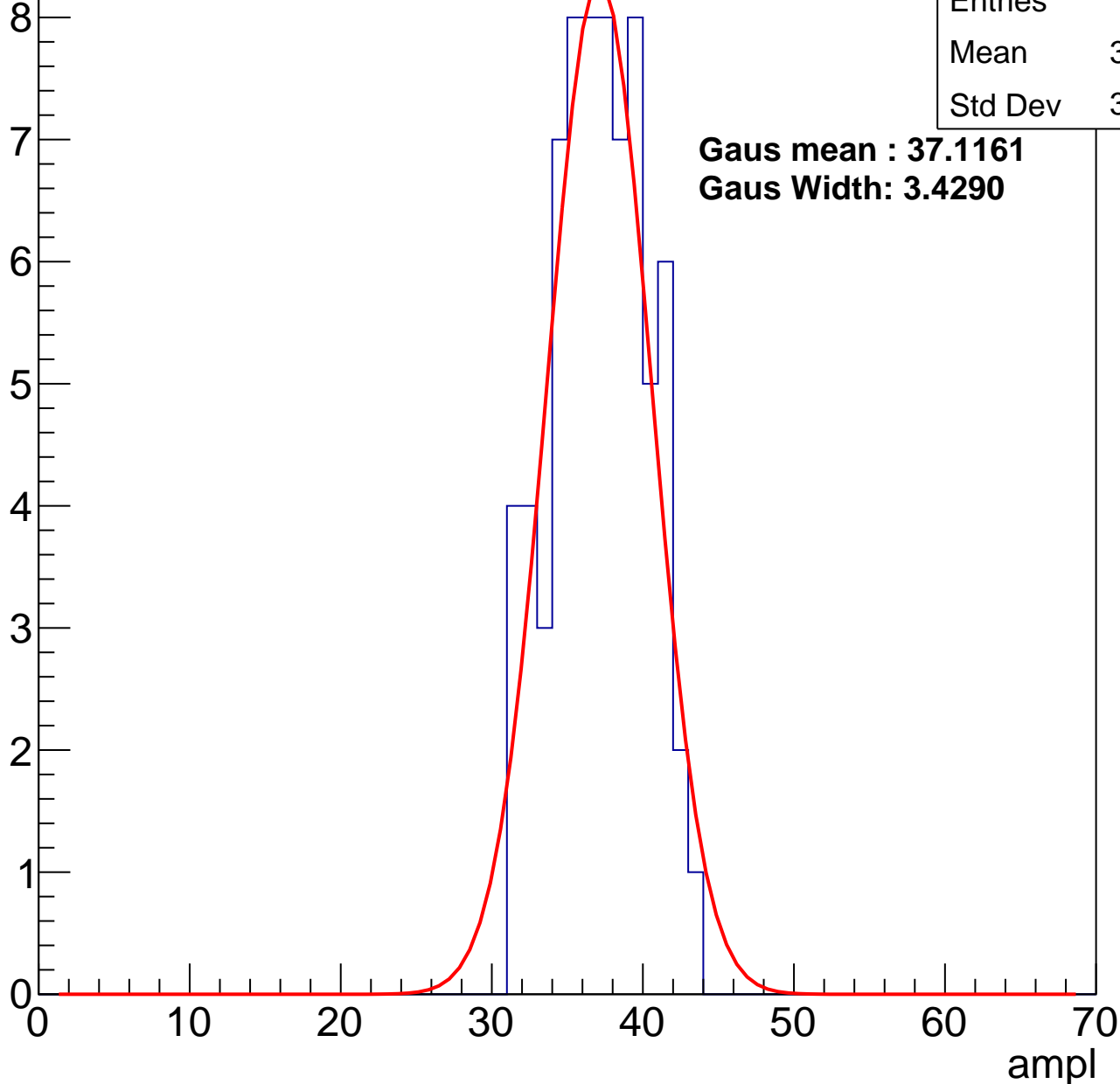
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	36.68
Std Dev	3.034

**Gaus mean : 37.1161**

**Gaus Width: 3.4290**



# B1L000S, U25-ch24, adc2

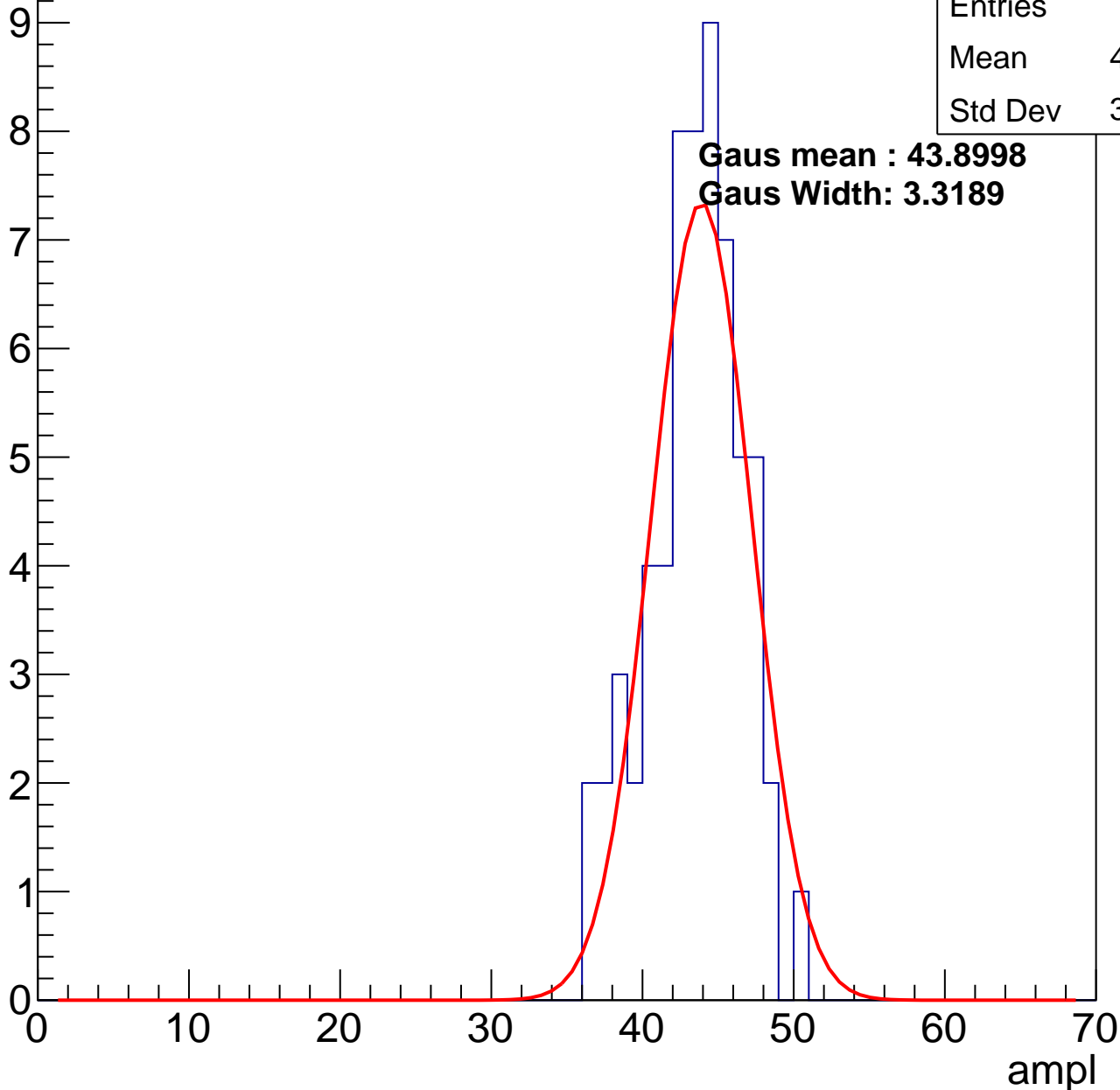
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.97
Std Dev	3.105

**Gaus mean : 43.8998**

**Gaus Width: 3.3189**

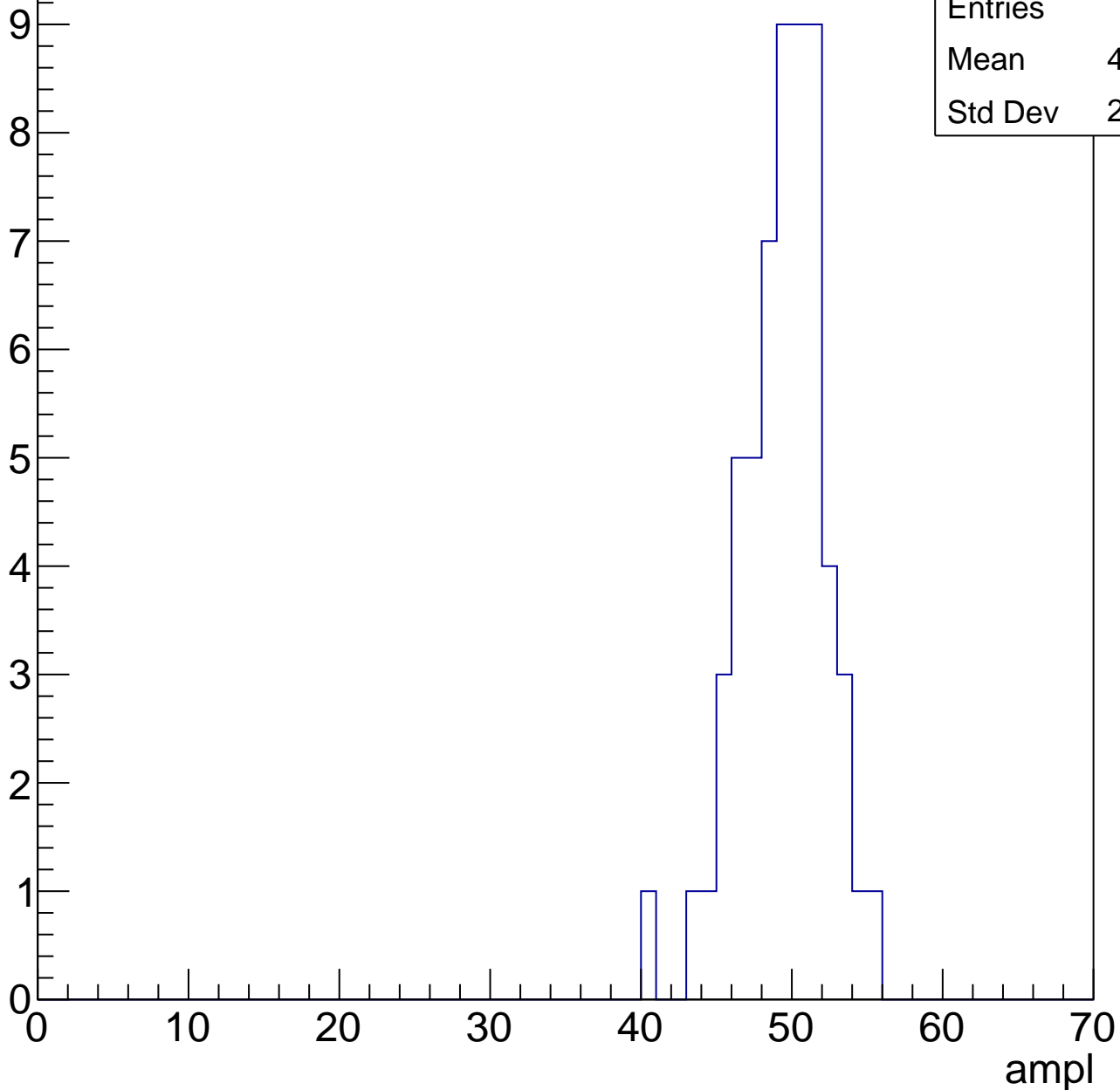


# B1L000S, U25-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

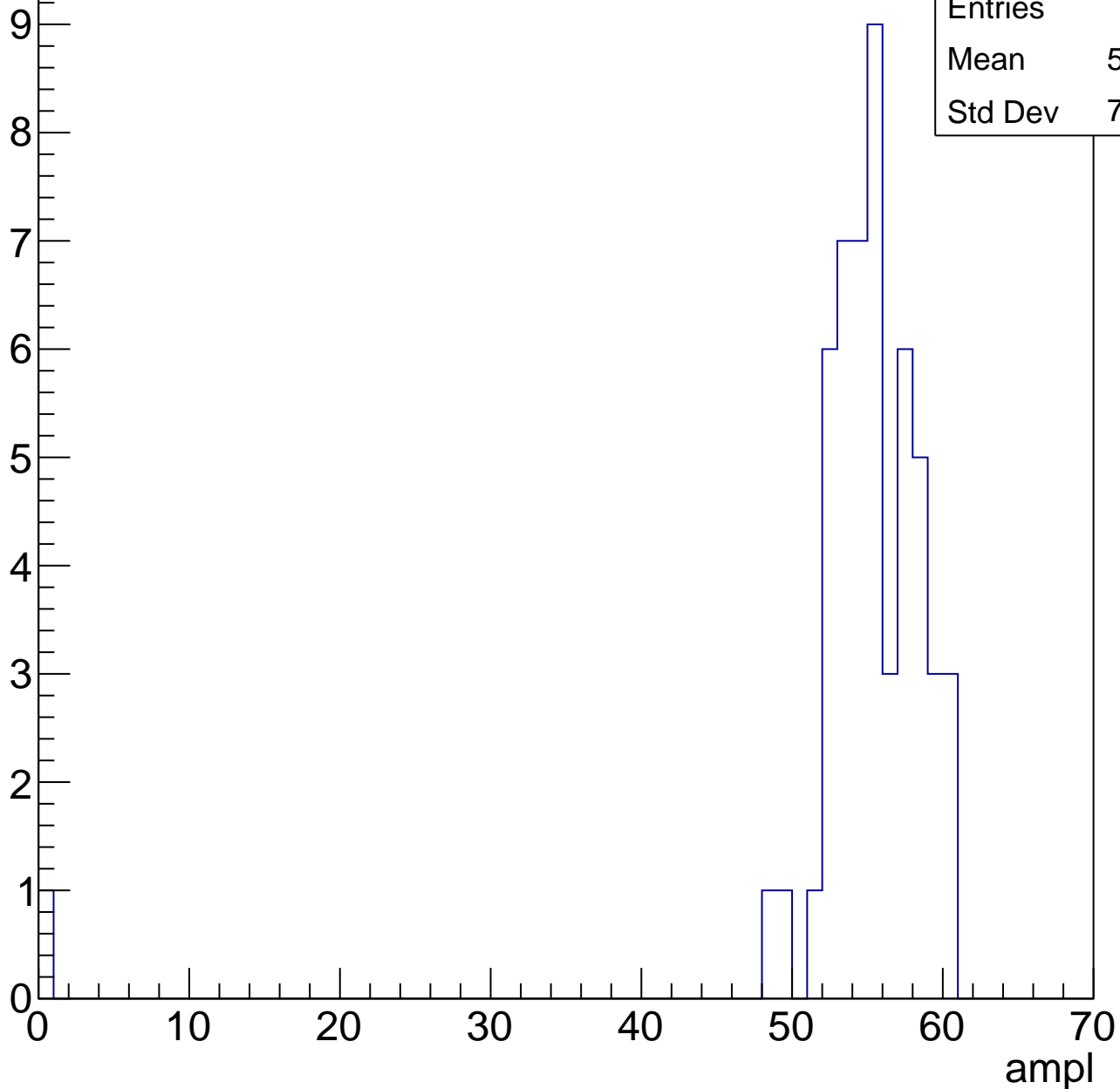
Entries	59
Mean	48.97
Std Dev	2.768



# B1L000S, U25-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

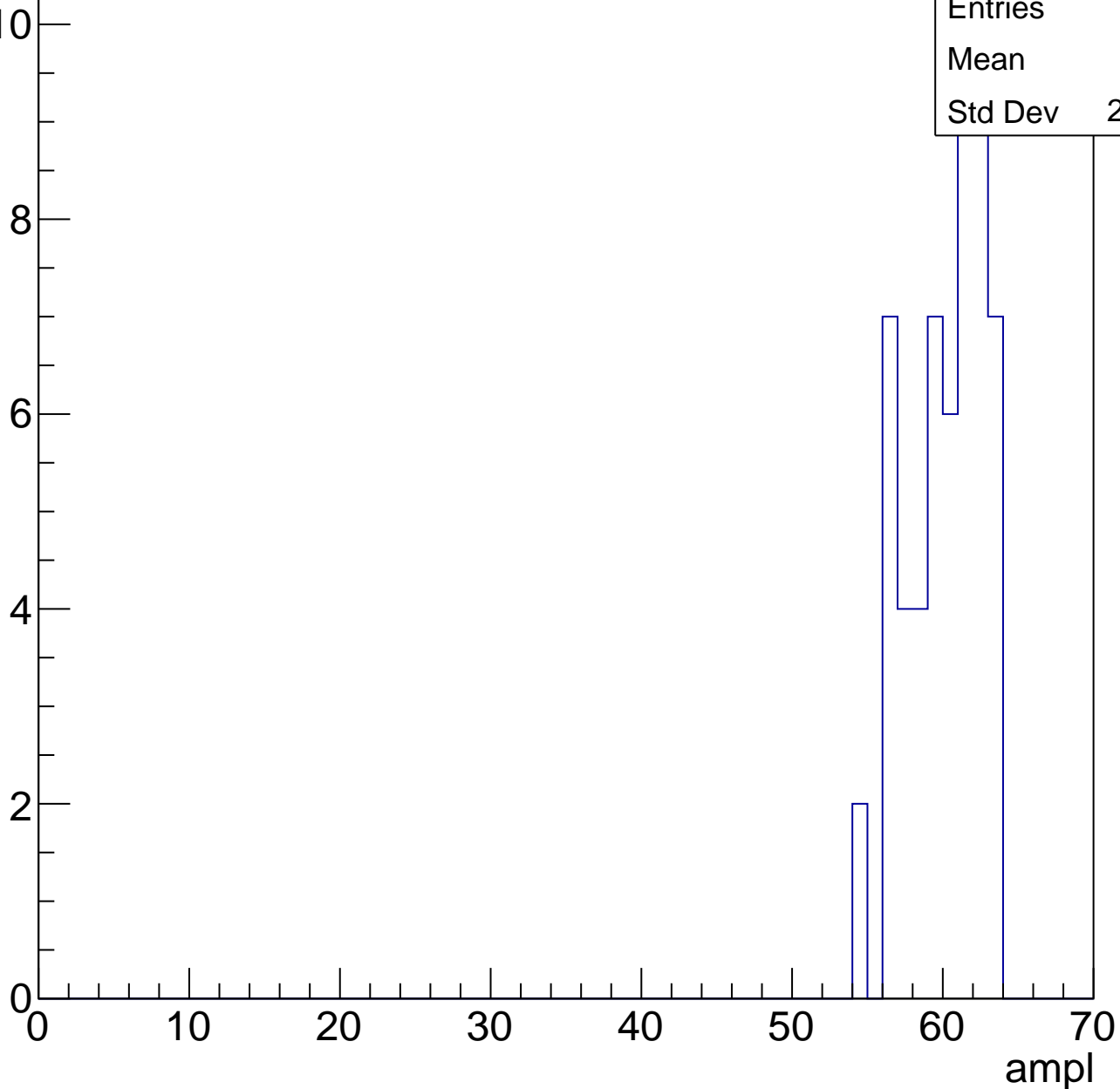


# B1L000S, U25-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

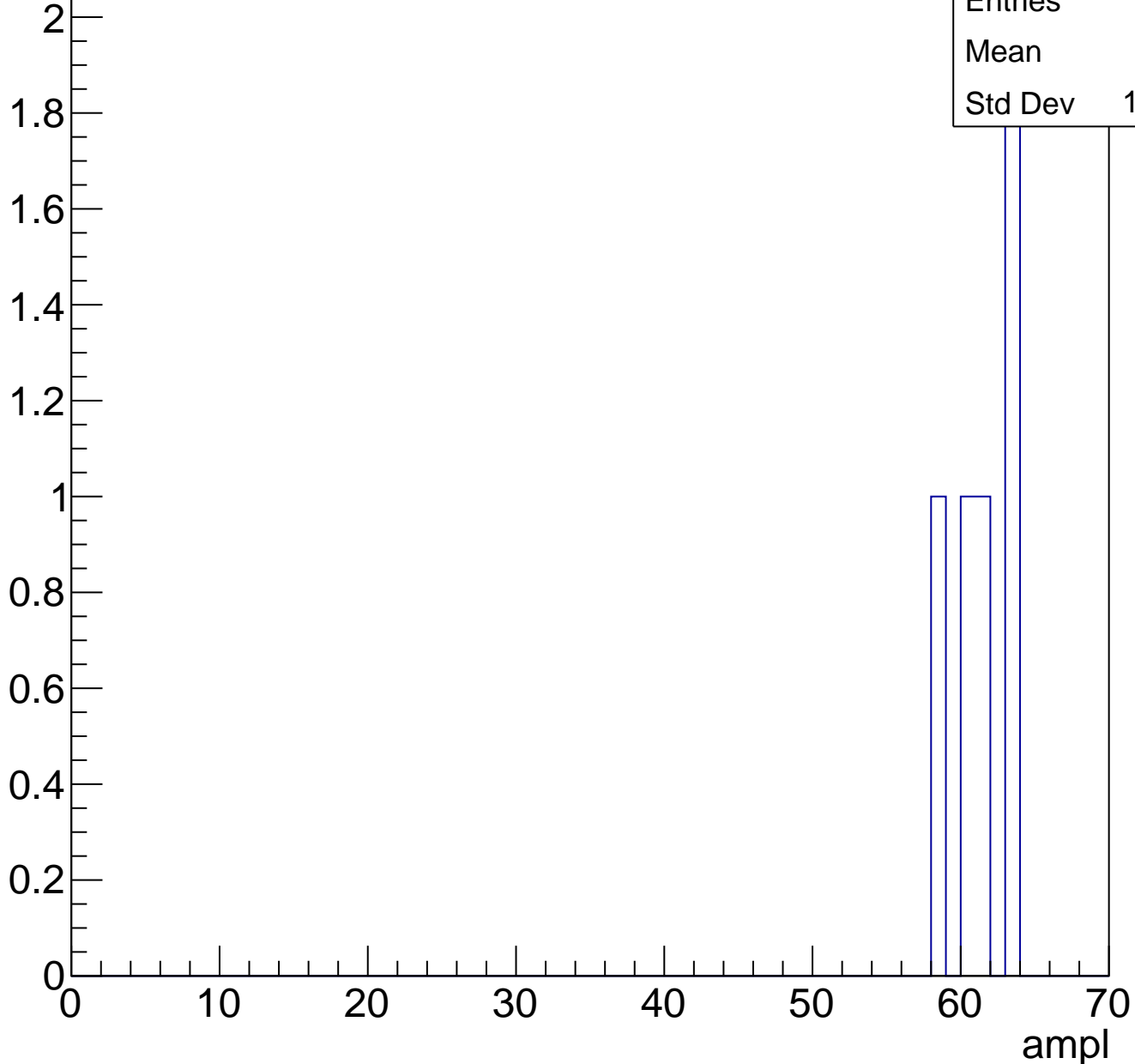
Entries	56
Mean	59.7
Std Dev	2.499



# B1L000S, U25-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L000S, U25-ch25, adc0

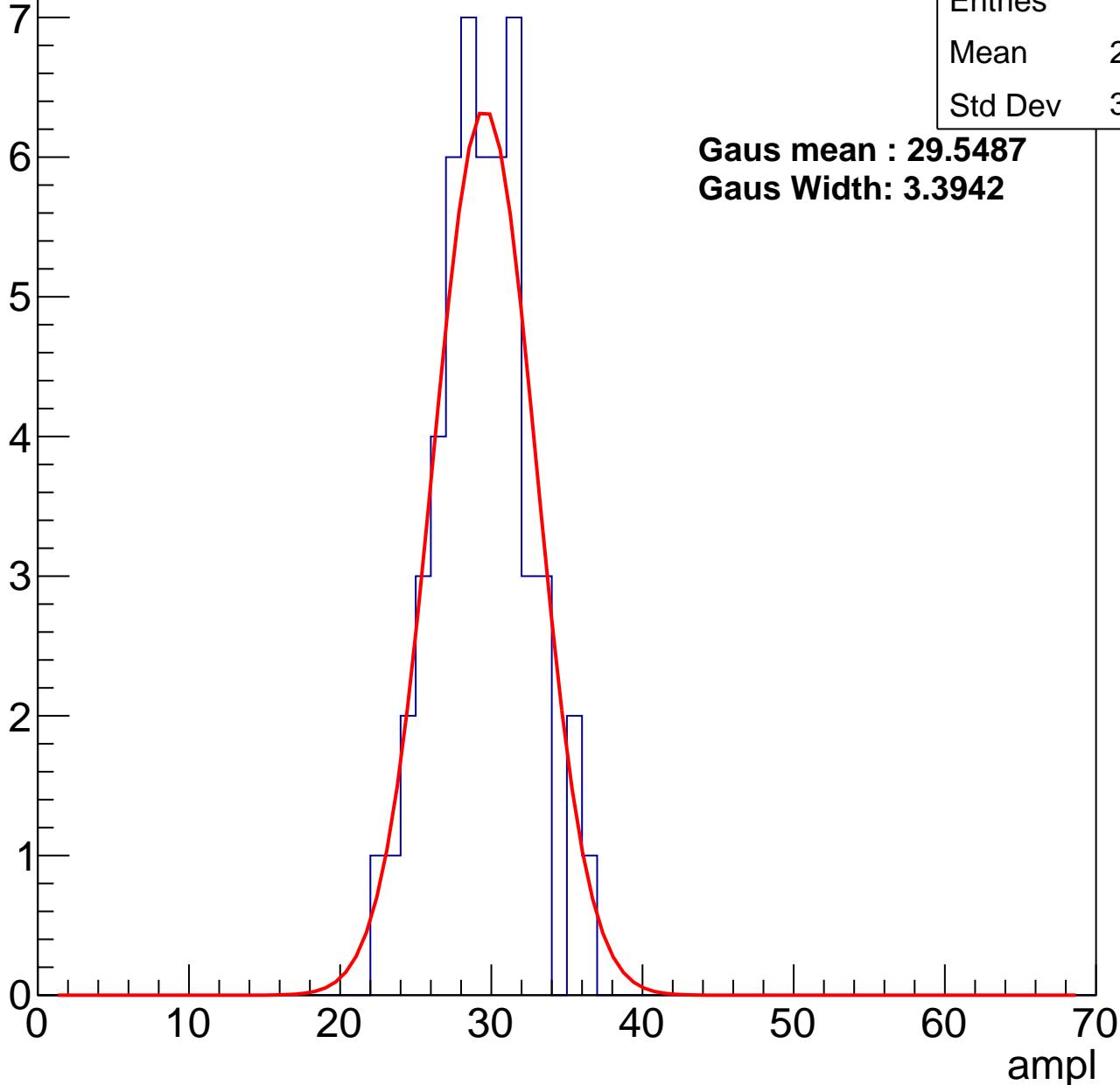
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	28.88
Std Dev	3.036

**Gaus mean : 29.5487**

**Gaus Width: 3.3942**



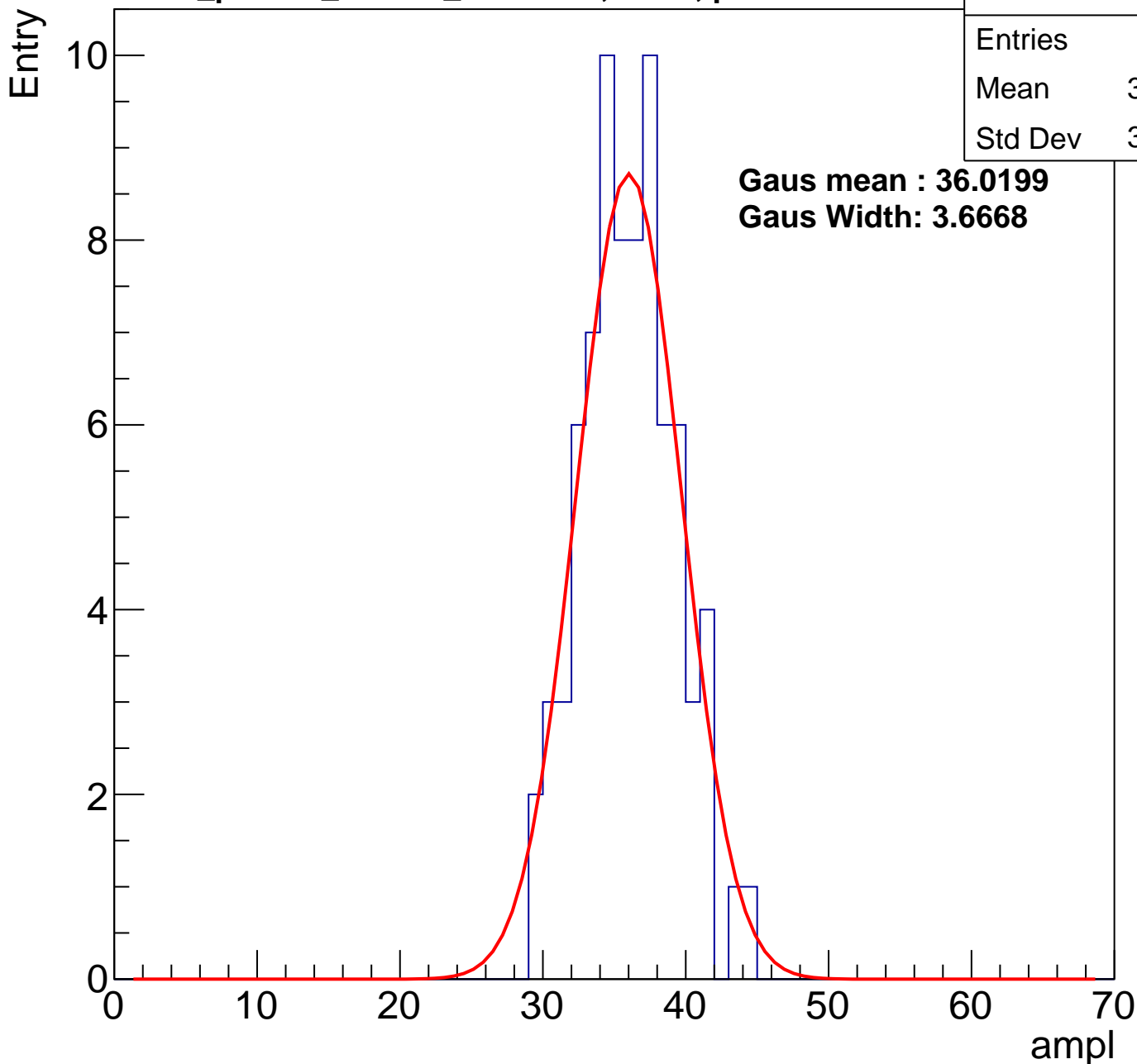
# B1L000S, U25-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	35.58
Std Dev	3.248

**Gaus mean : 36.0199**

**Gaus Width: 3.6668**



# B1L000S, U25-ch25, adc2

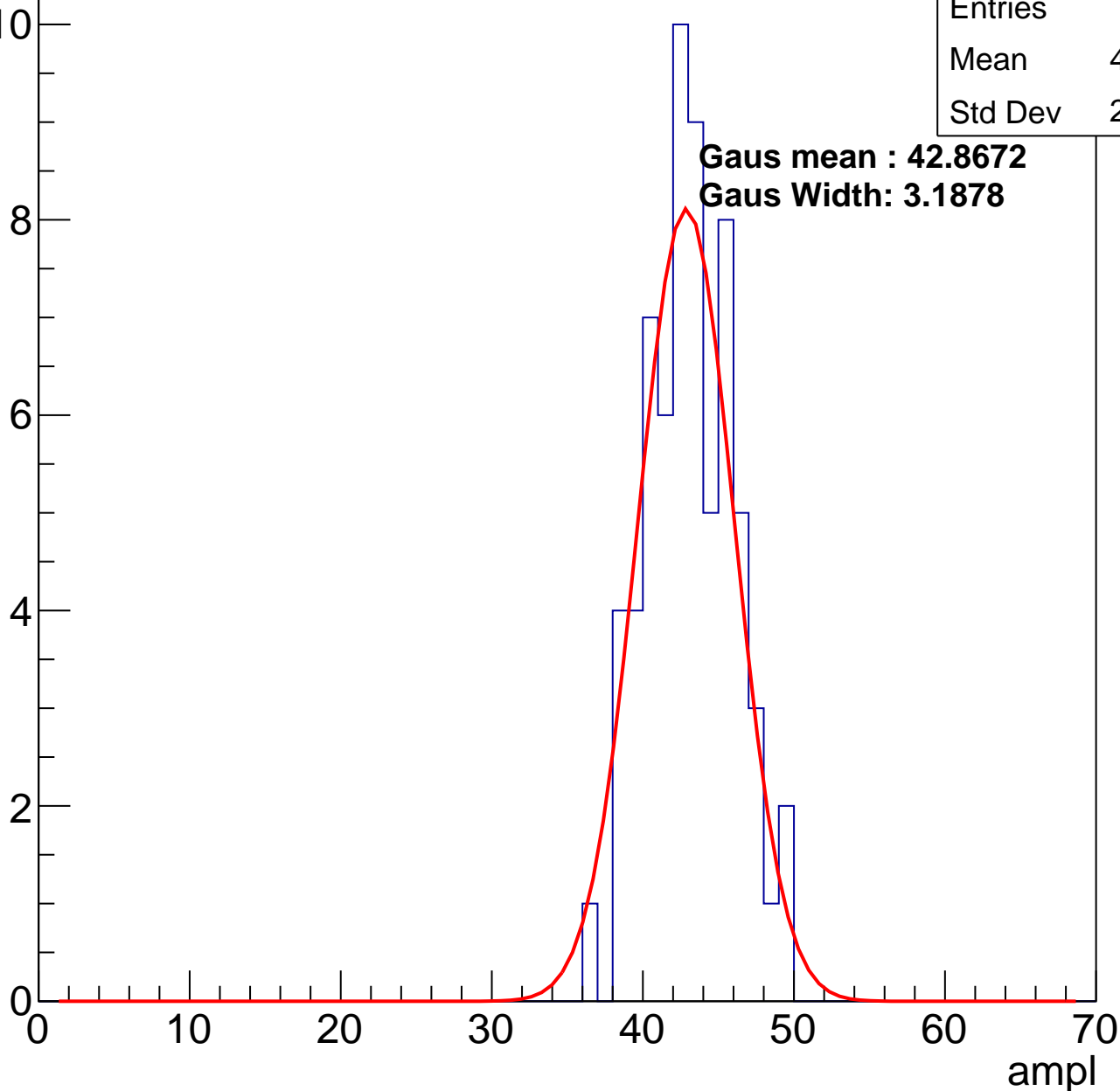
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	42.68
Std Dev	2.856

**Gaus mean : 42.8672**

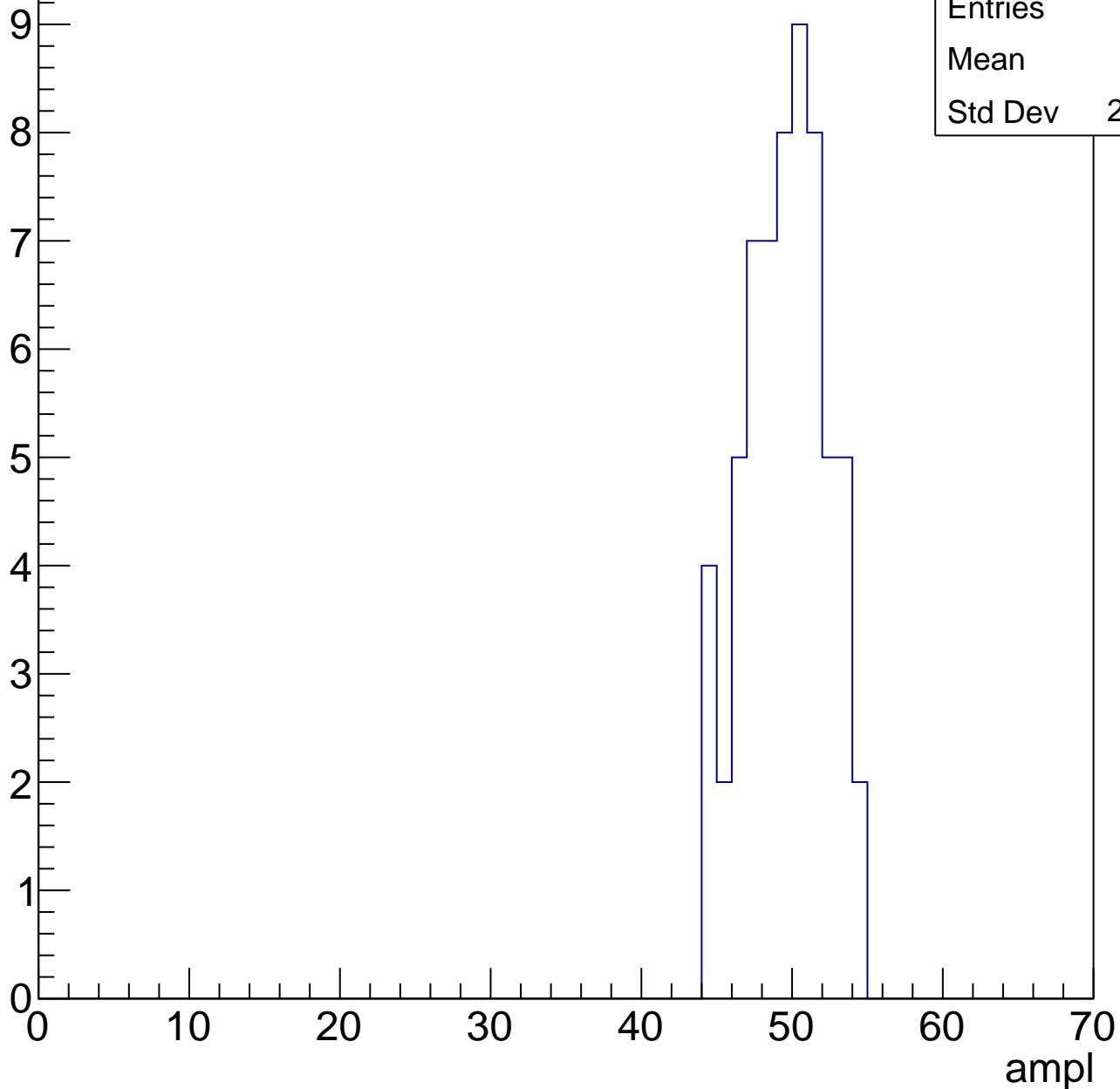
**Gaus Width: 3.1878**



# B1L000S, U25-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

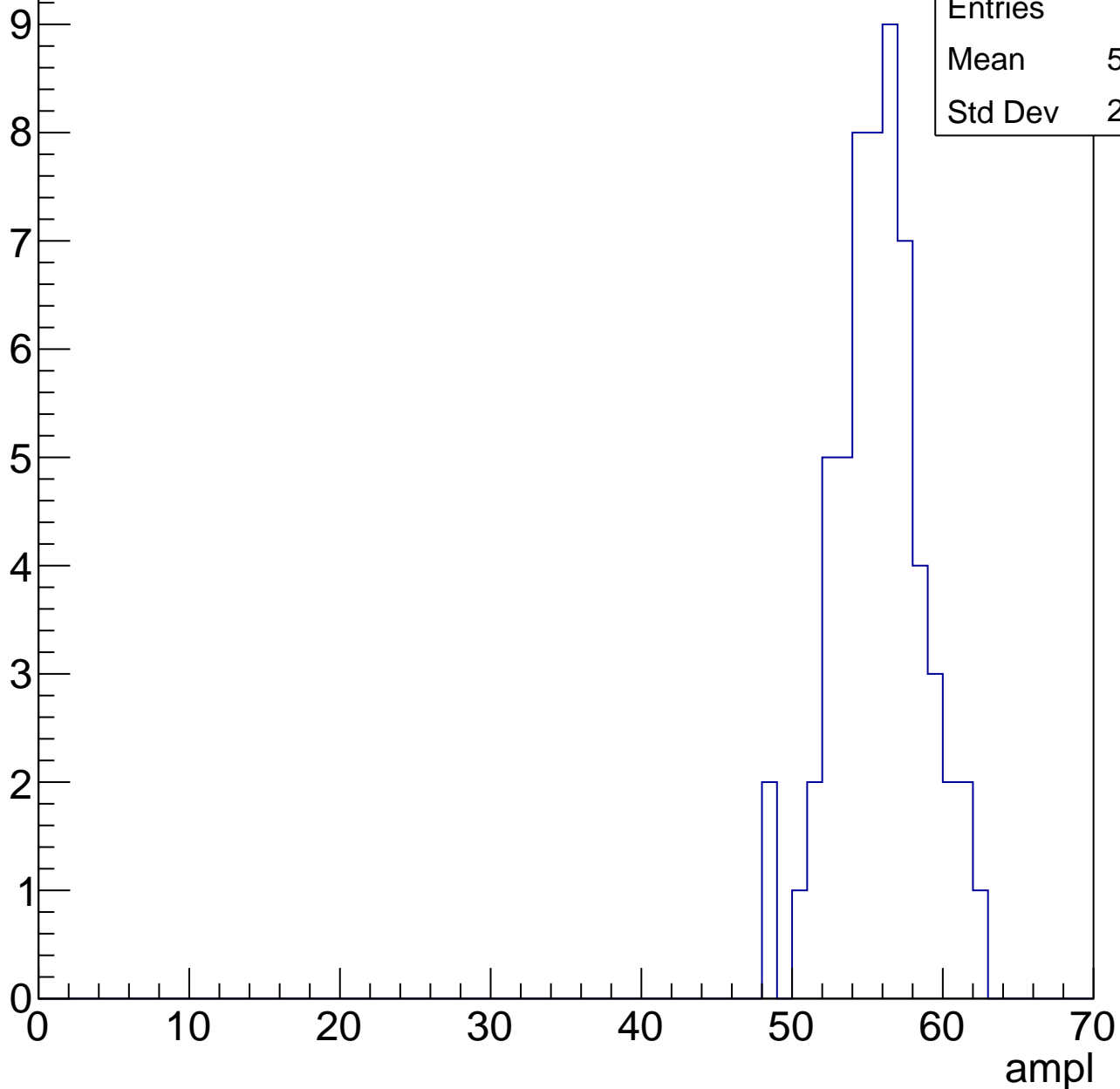


Entries	62
Mean	49.1
Std Dev	2.626

# B1L000S, U25-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

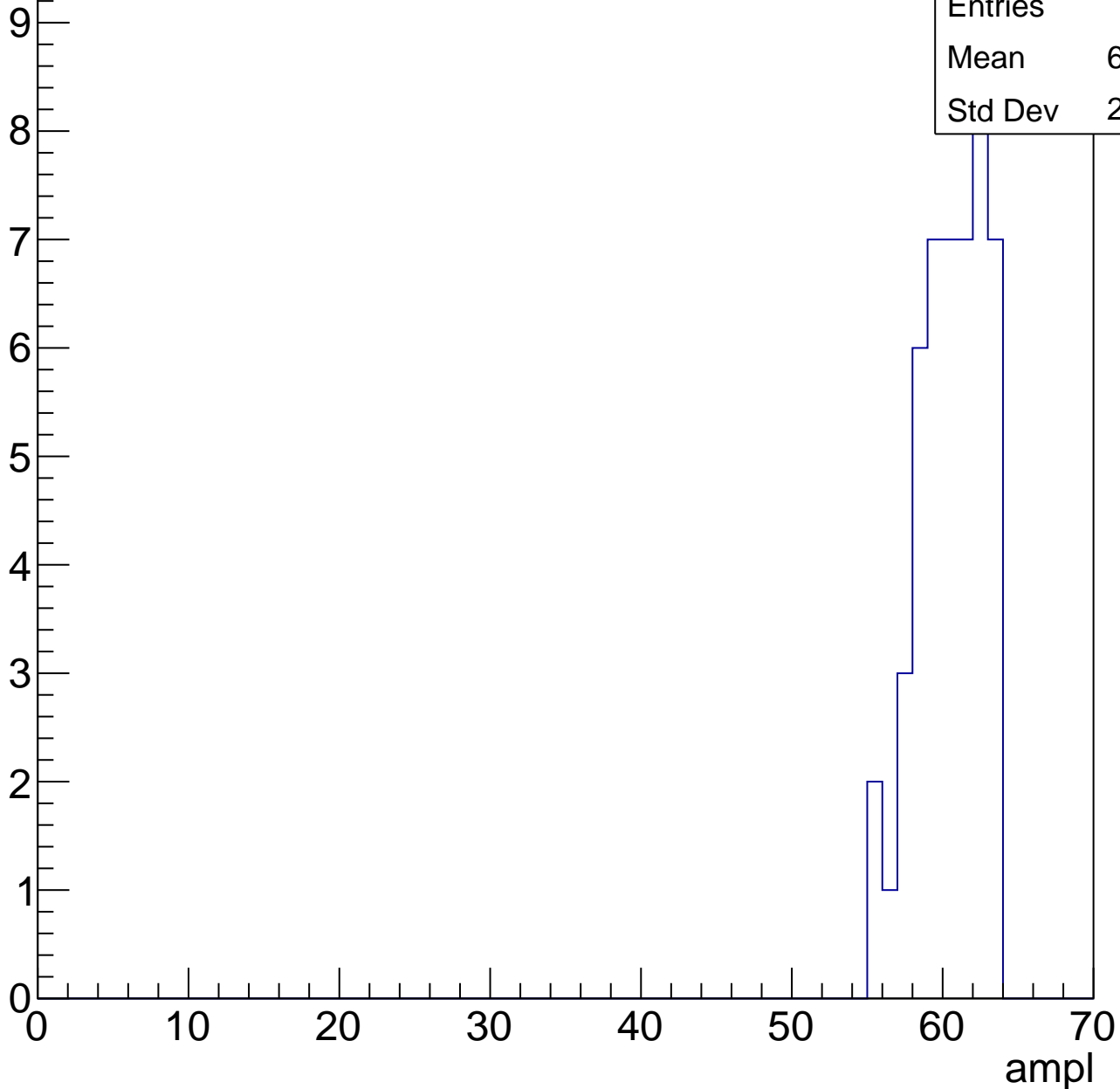


# B1L000S, U25-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

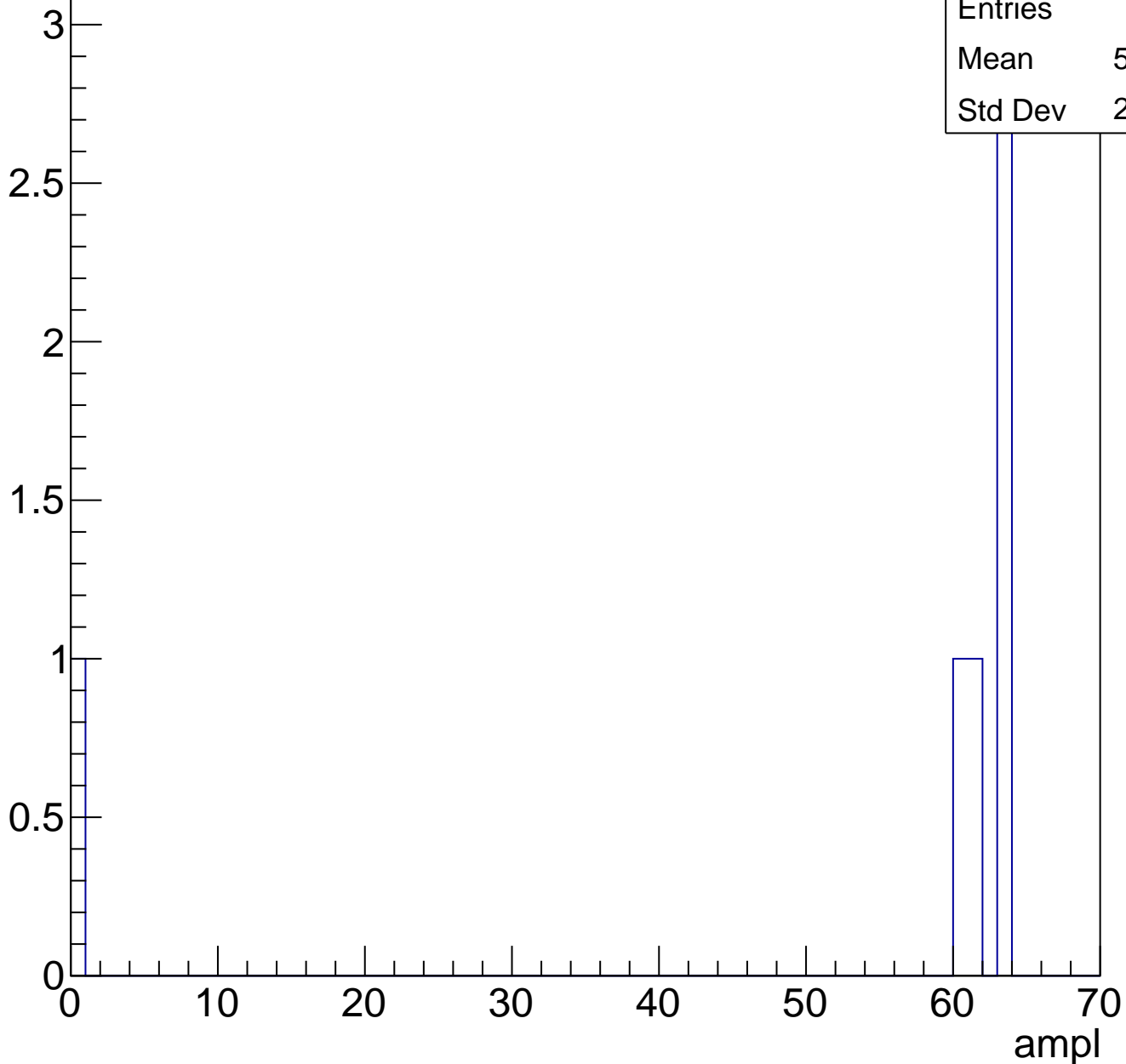
Entries	49
Mean	60.08
Std Dev	2.165



# B1L000S, U25-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

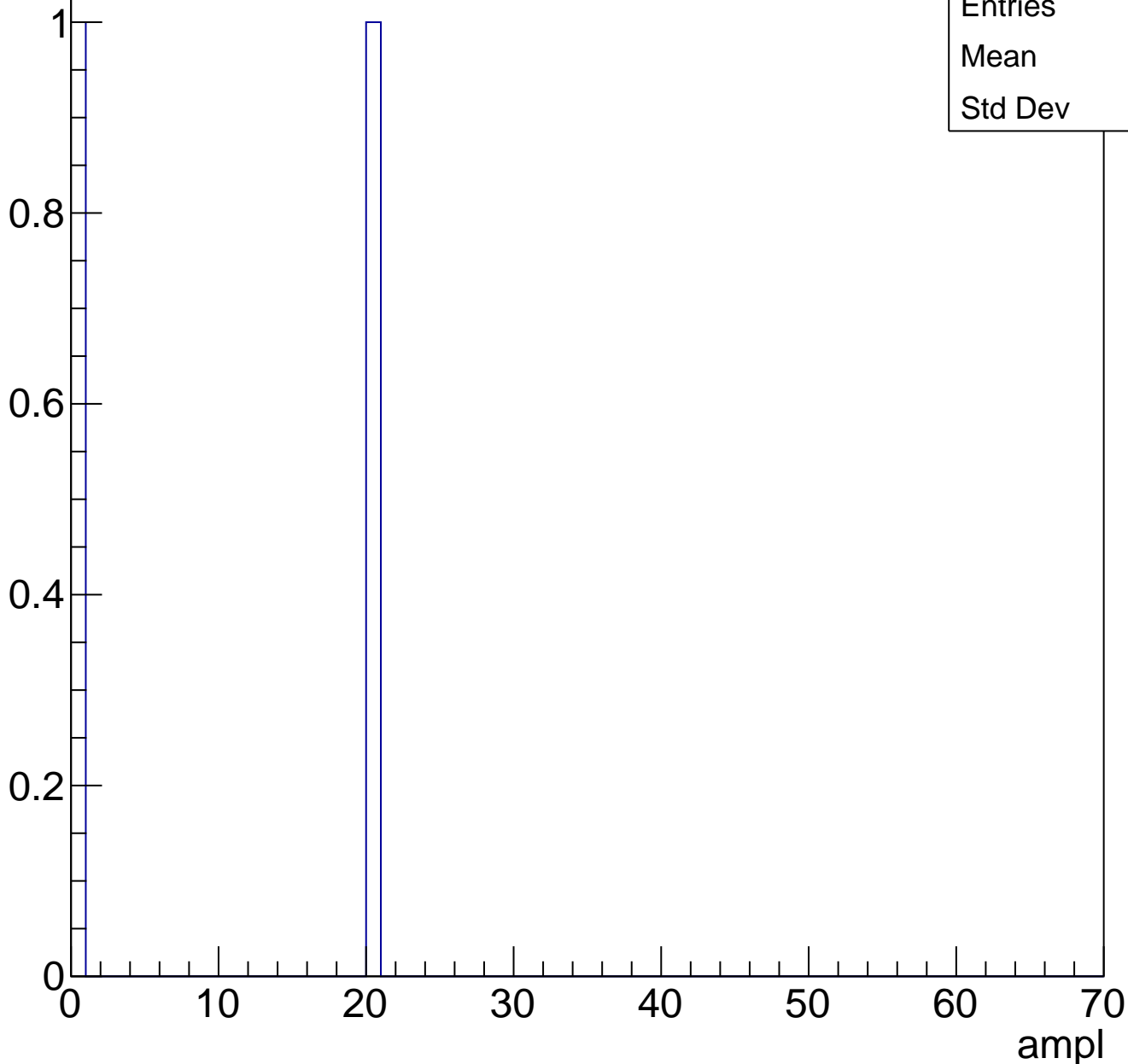




# B1L000S, U25-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch26, adc0

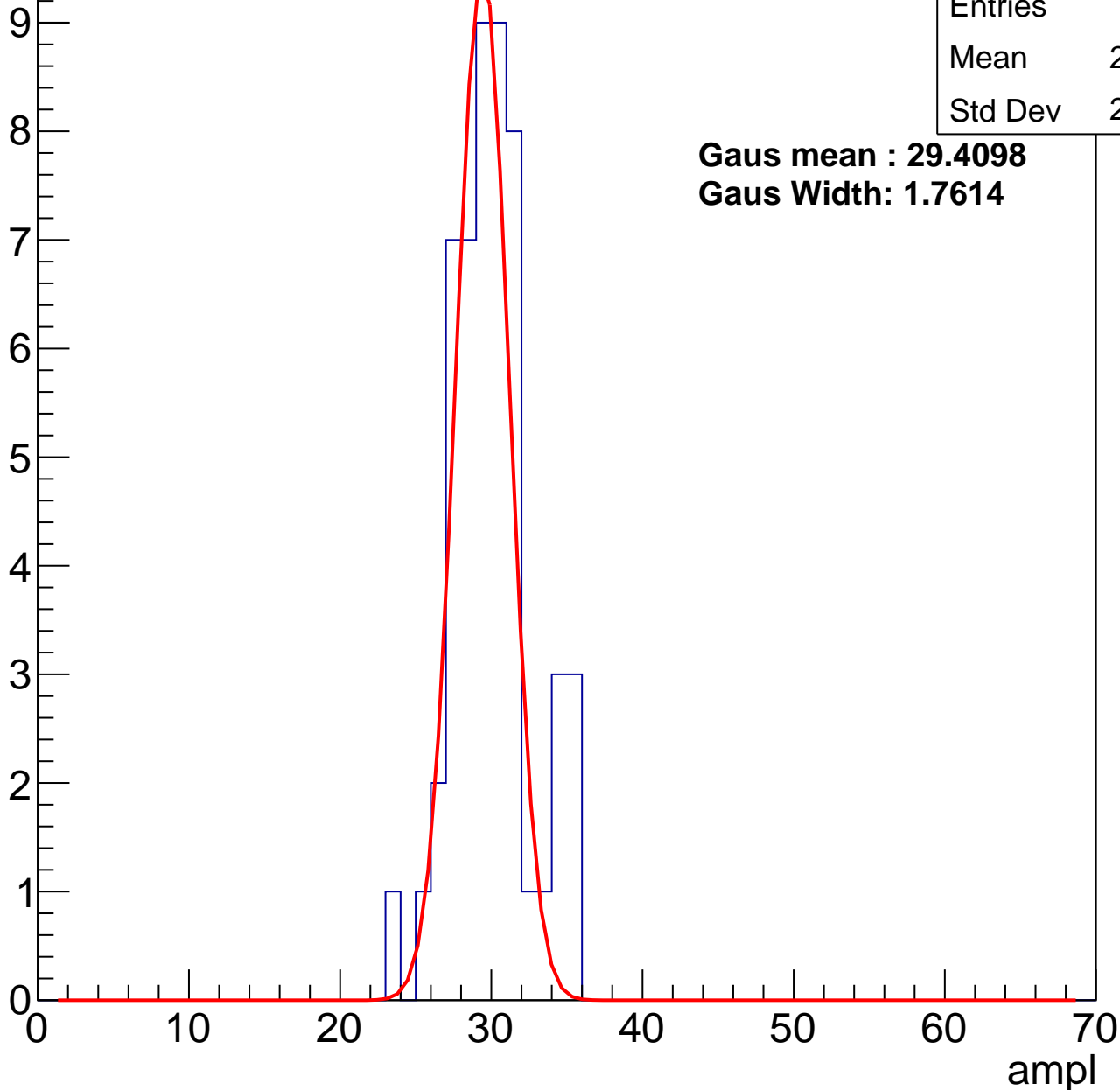
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	29.54
Std Dev	2.553

**Gaus mean : 29.4098**

**Gaus Width: 1.7614**



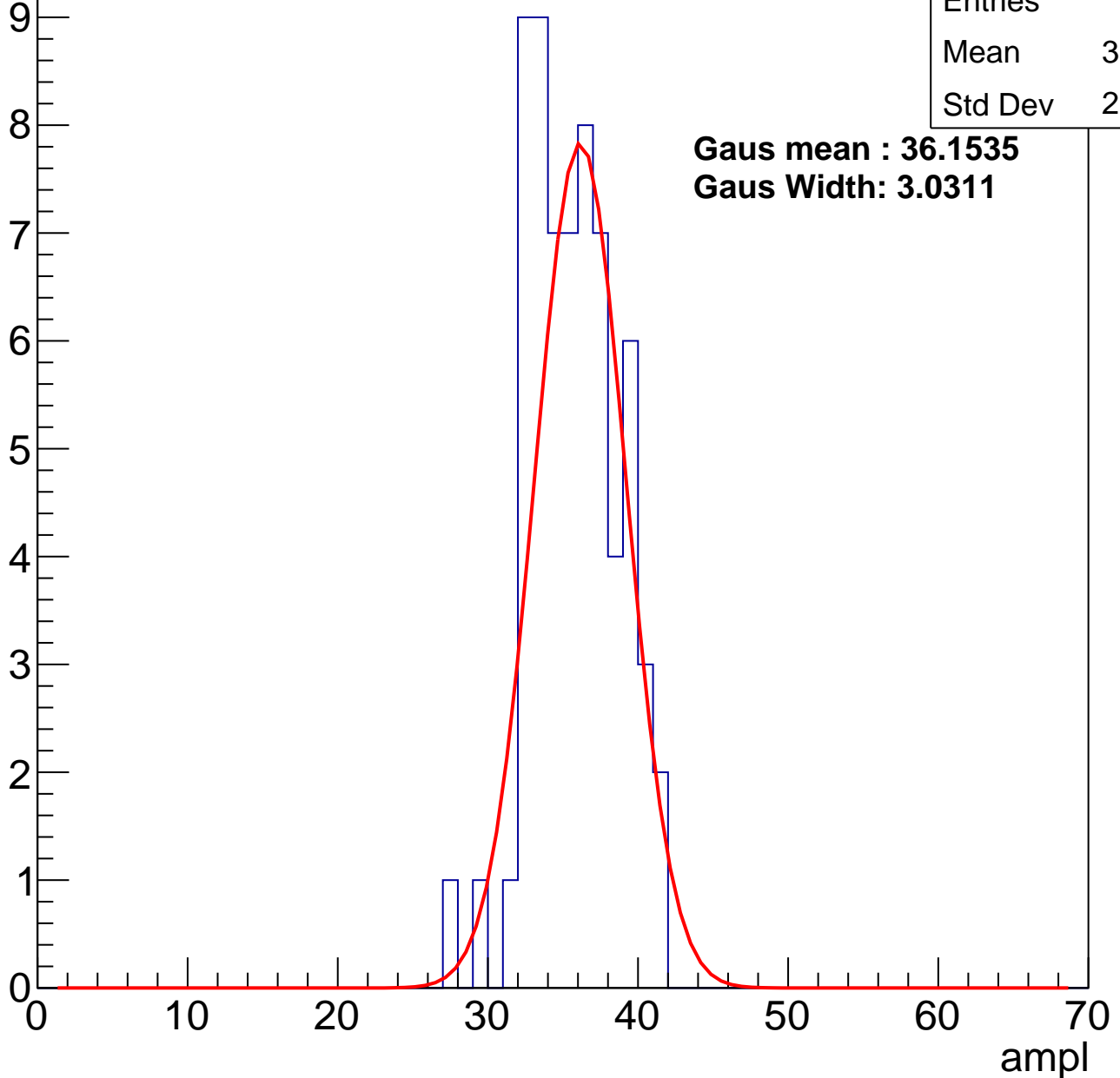
# B1L000S, U25-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.23
Std Dev	2.913

**Gaus mean : 36.1535**  
**Gaus Width: 3.0311**



# B1L000S, U25-ch26, adc2

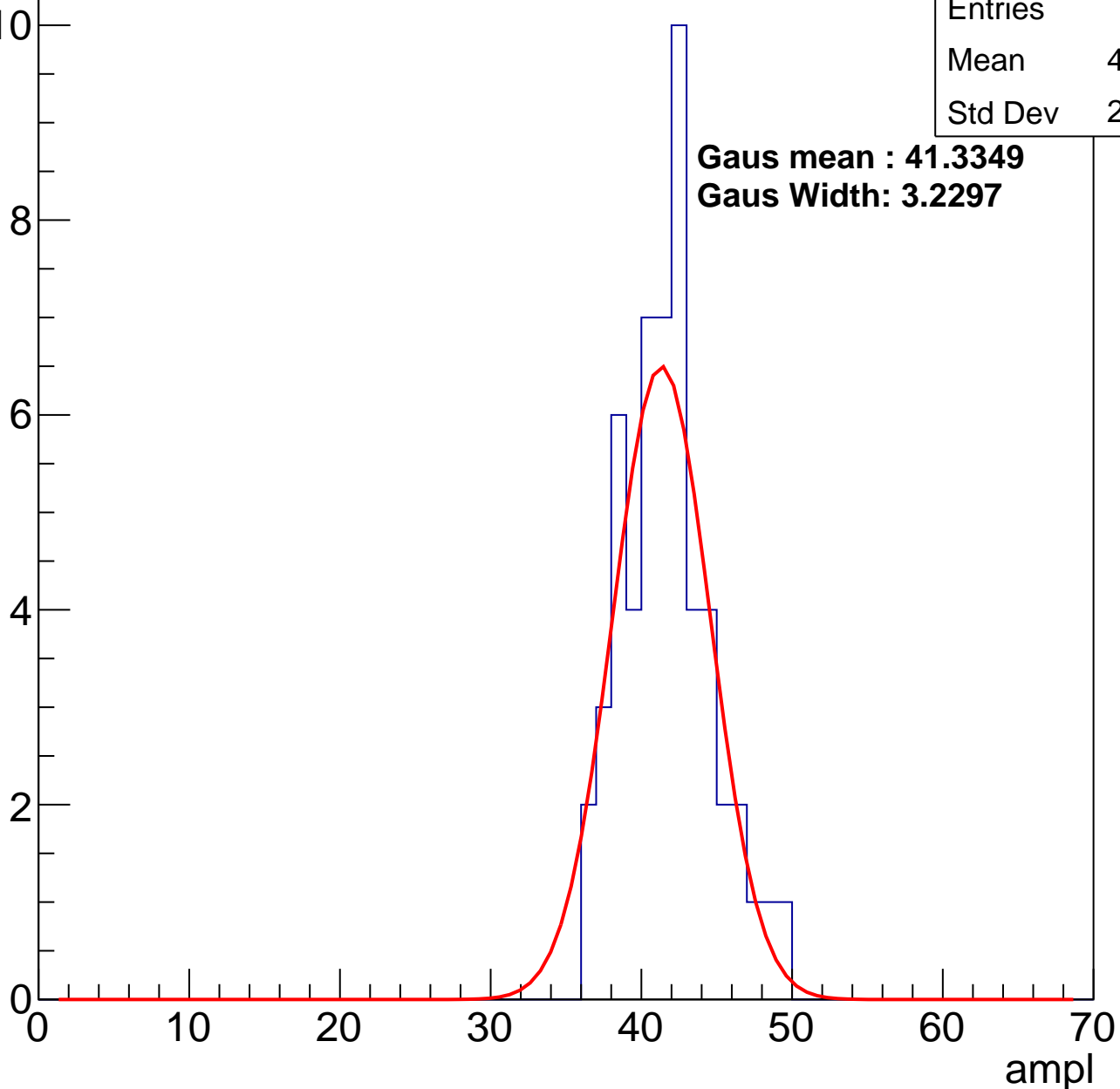
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.26
Std Dev	2.932

**Gaus mean : 41.3349**

**Gaus Width: 3.2297**

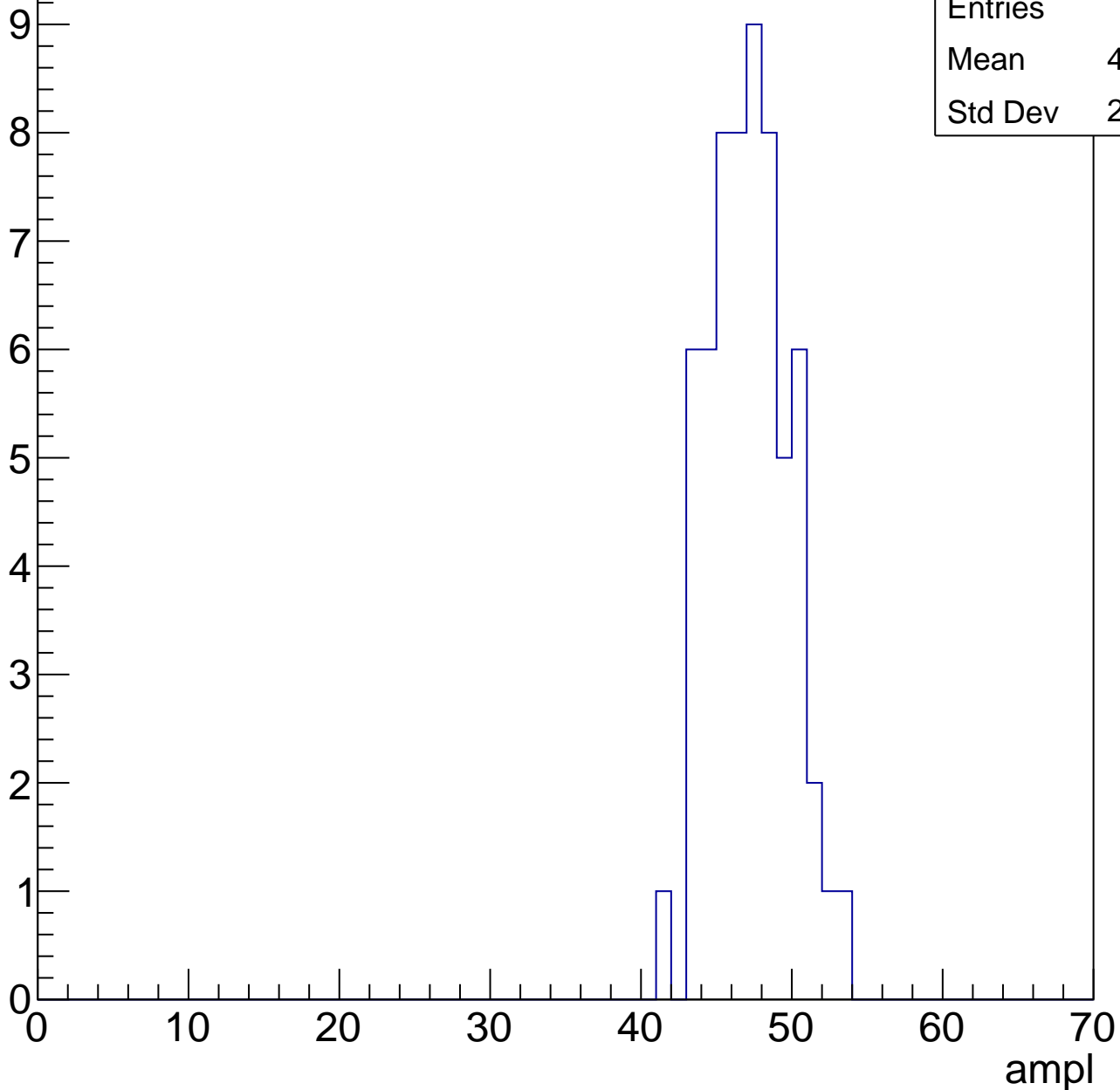


# B1L000S, U25-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

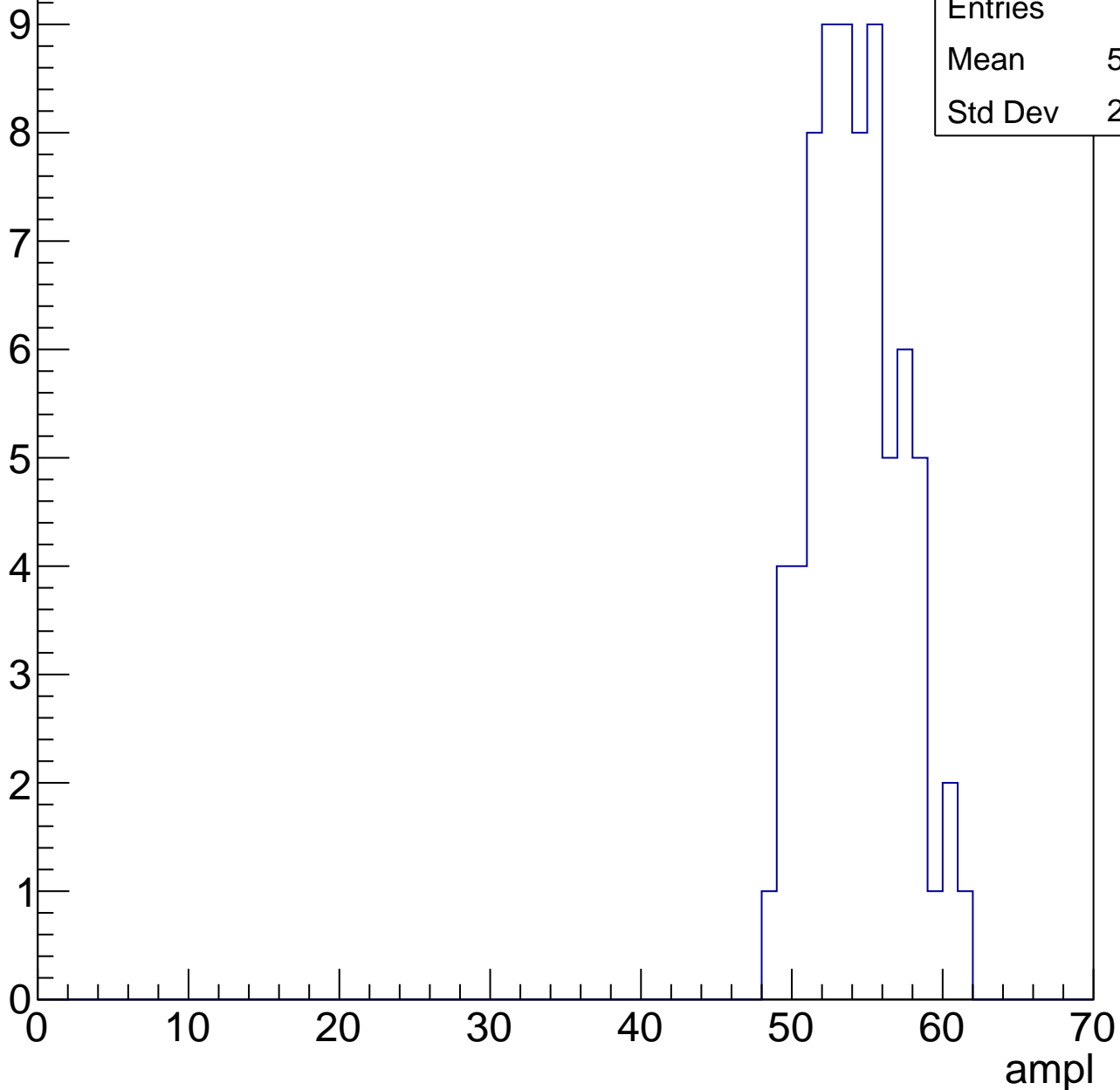
Entries	61
Mean	46.72
Std Dev	2.549



# B1L000S, U25-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

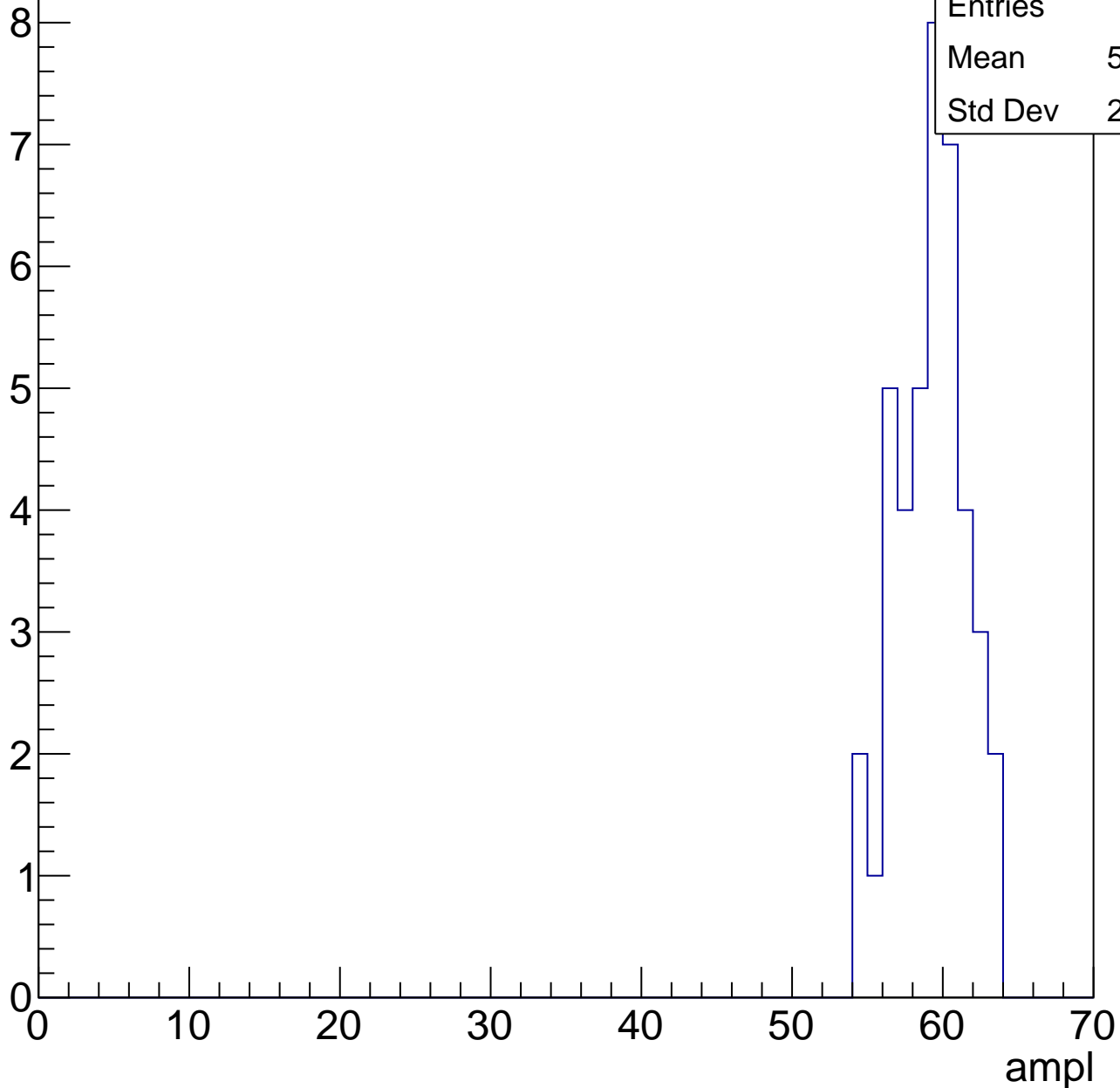


Entries	72
Mean	53.83
Std Dev	2.944

# B1L000S, U25-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

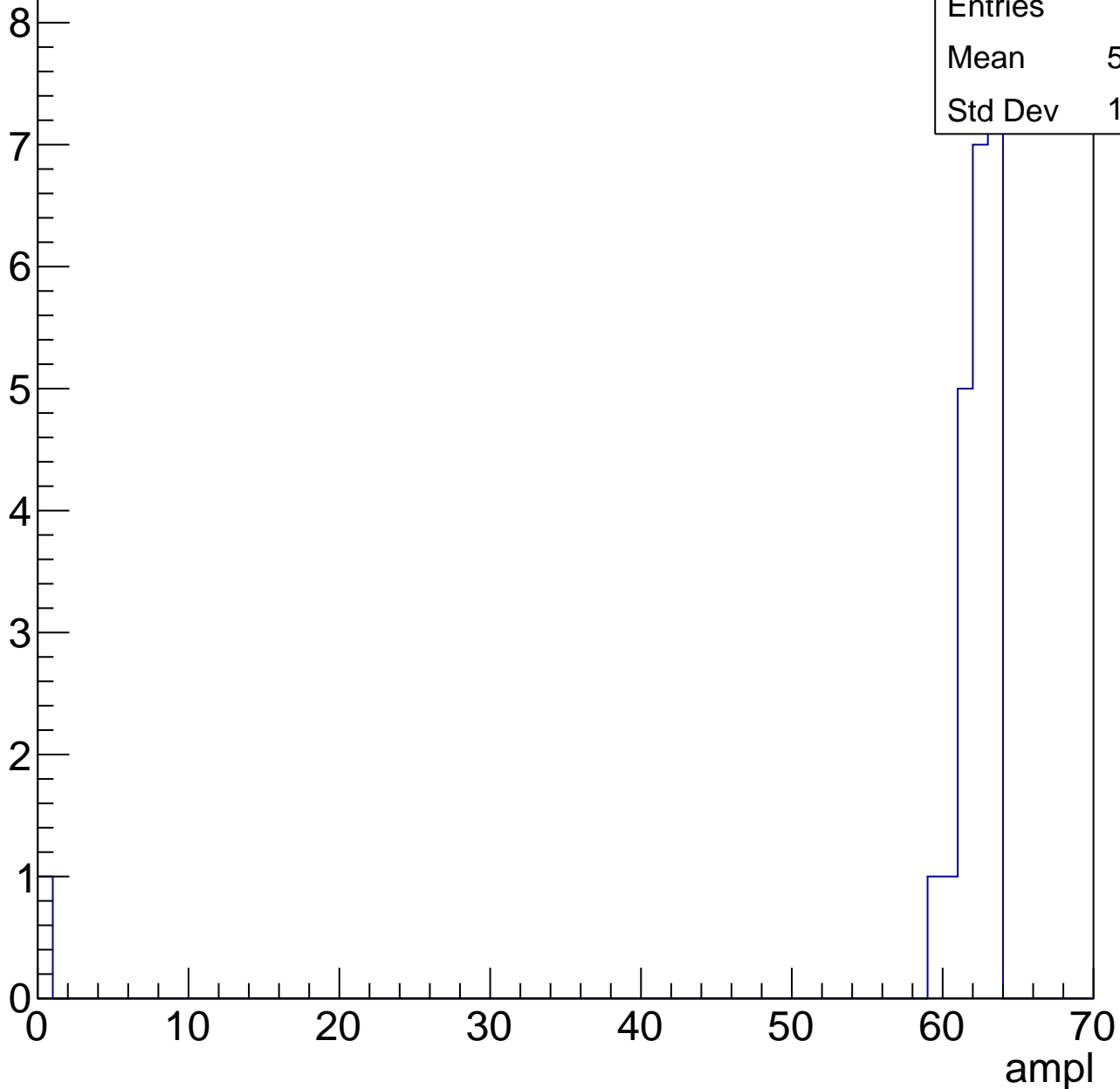


# B1L000S, U25-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	23
Mean	59.22
Std Dev	12.67





# B1L000S, U25-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch27, adc0

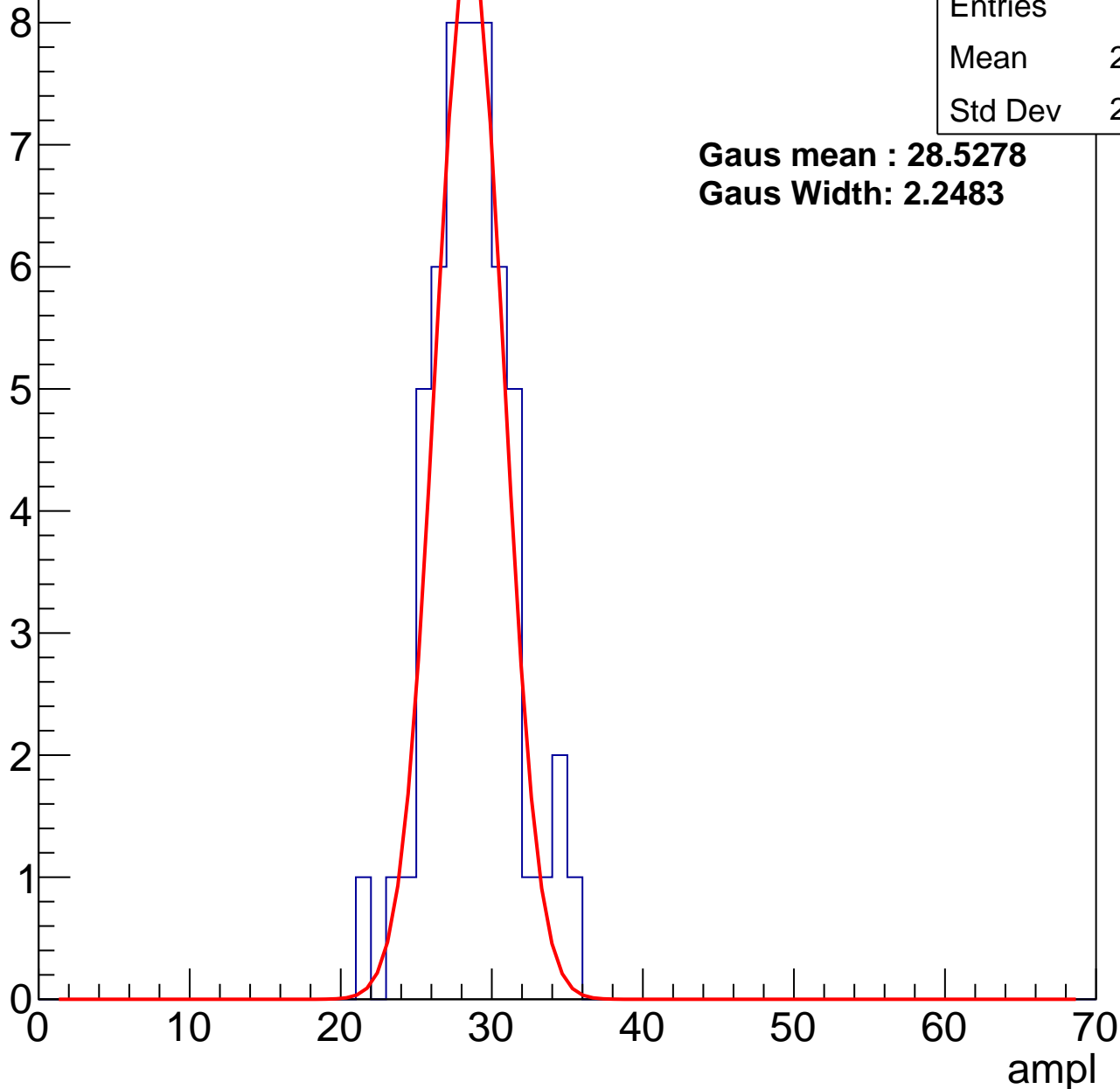
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	28.22
Std Dev	2.733

**Gaus mean : 28.5278**

**Gaus Width: 2.2483**



# B1L000S, U25-ch27, adc1

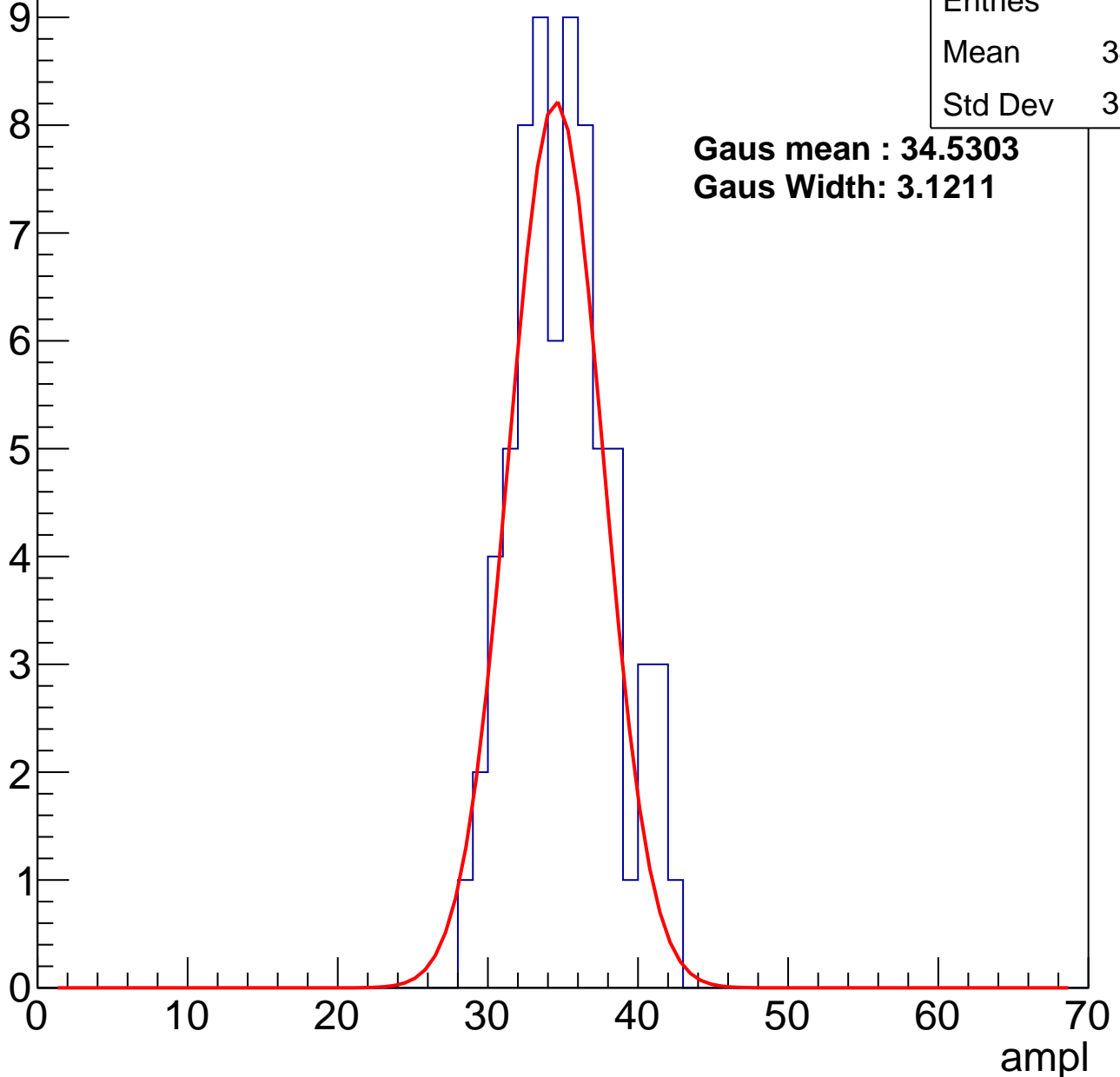
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	34.57
Std Dev	3.214

**Gaus mean : 34.5303**

**Gaus Width: 3.1211**



# B1L000S, U25-ch27, adc2

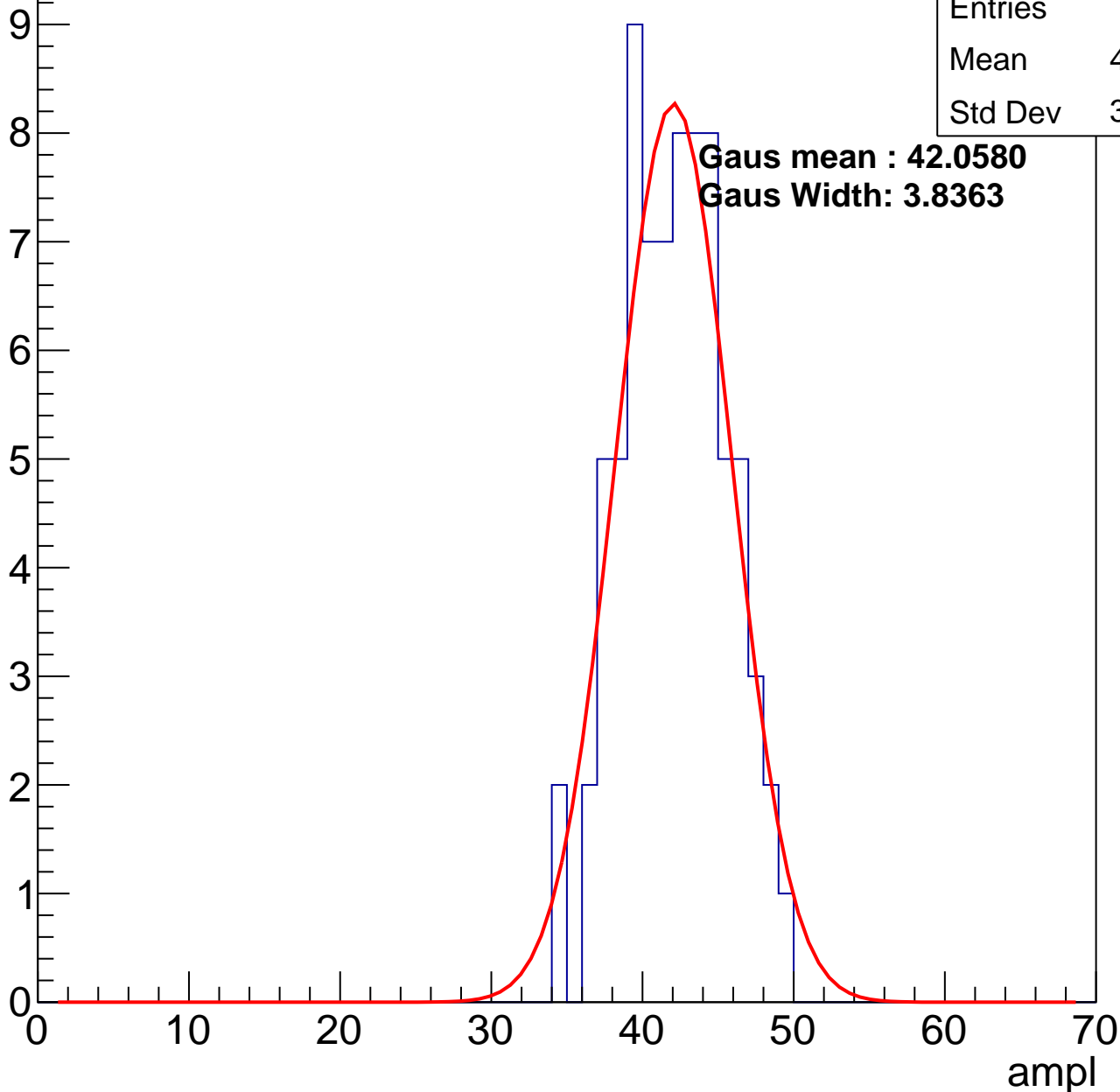
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	41.64
Std Dev	3.365

**Gaus mean : 42.0580**

**Gaus Width: 3.8363**

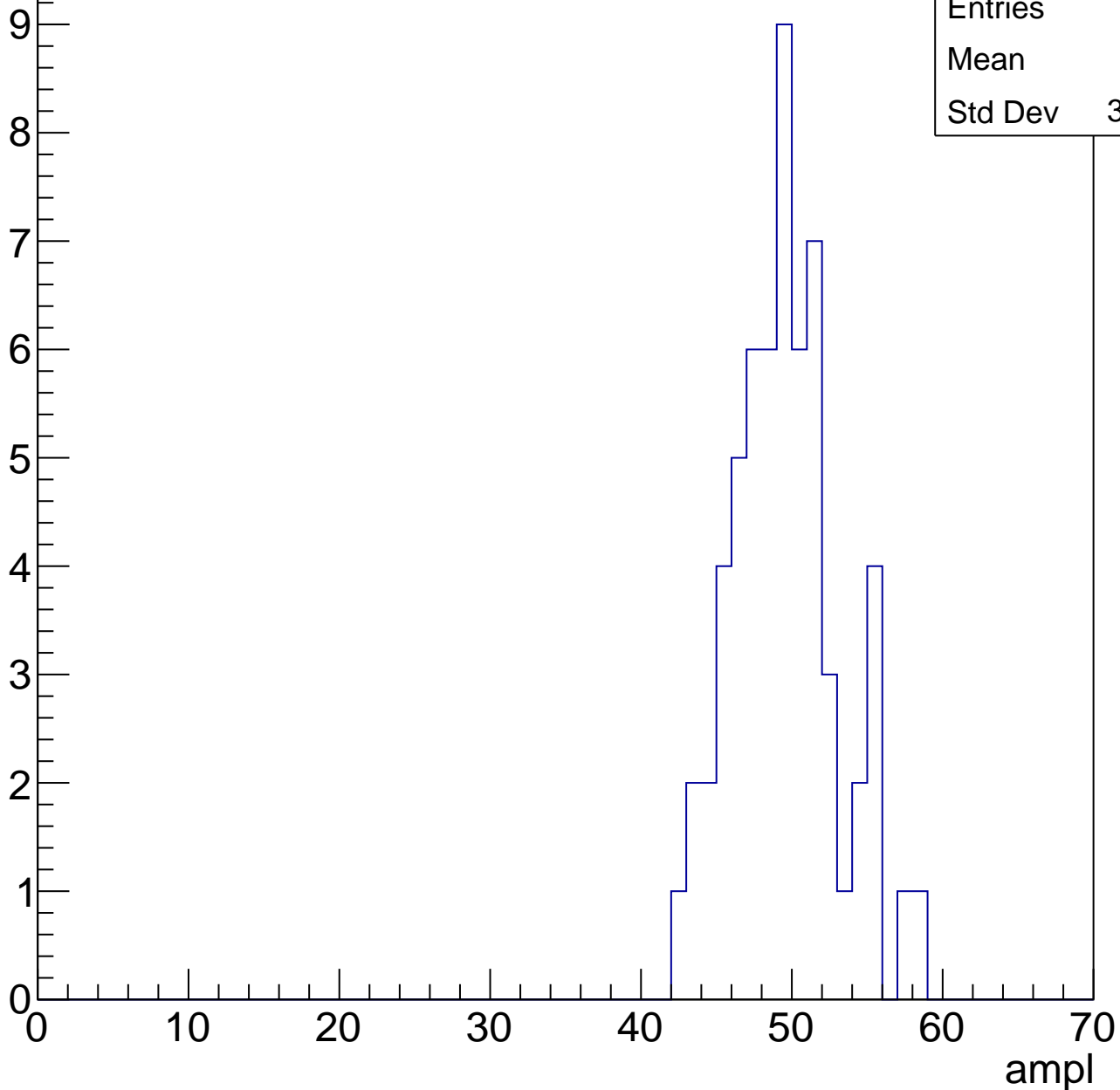


# B1L000S, U25-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	49.1
Std Dev	3.477

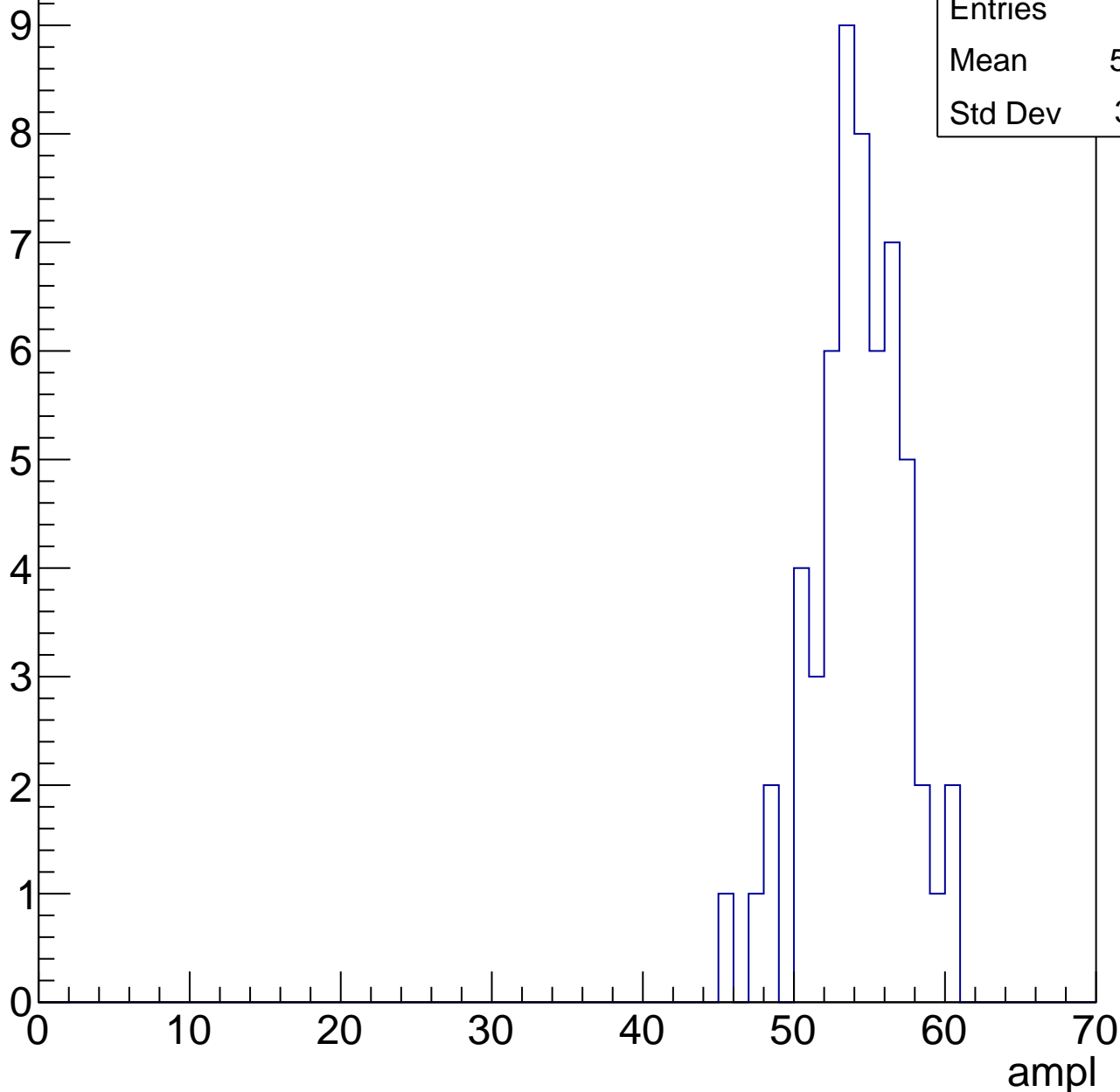


# B1L000S, U25-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.75
Std Dev	3.051

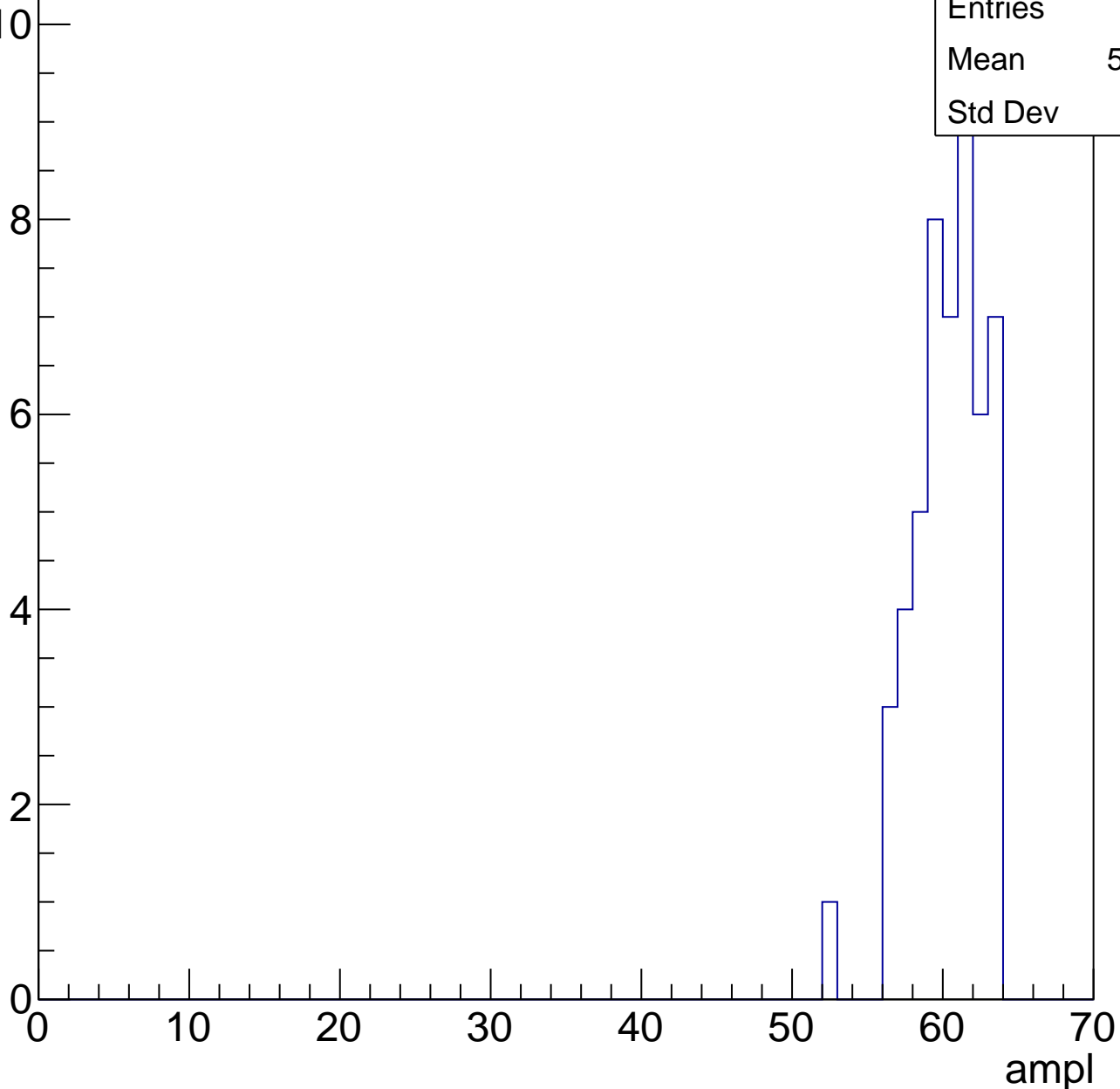


# B1L000S, U25-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

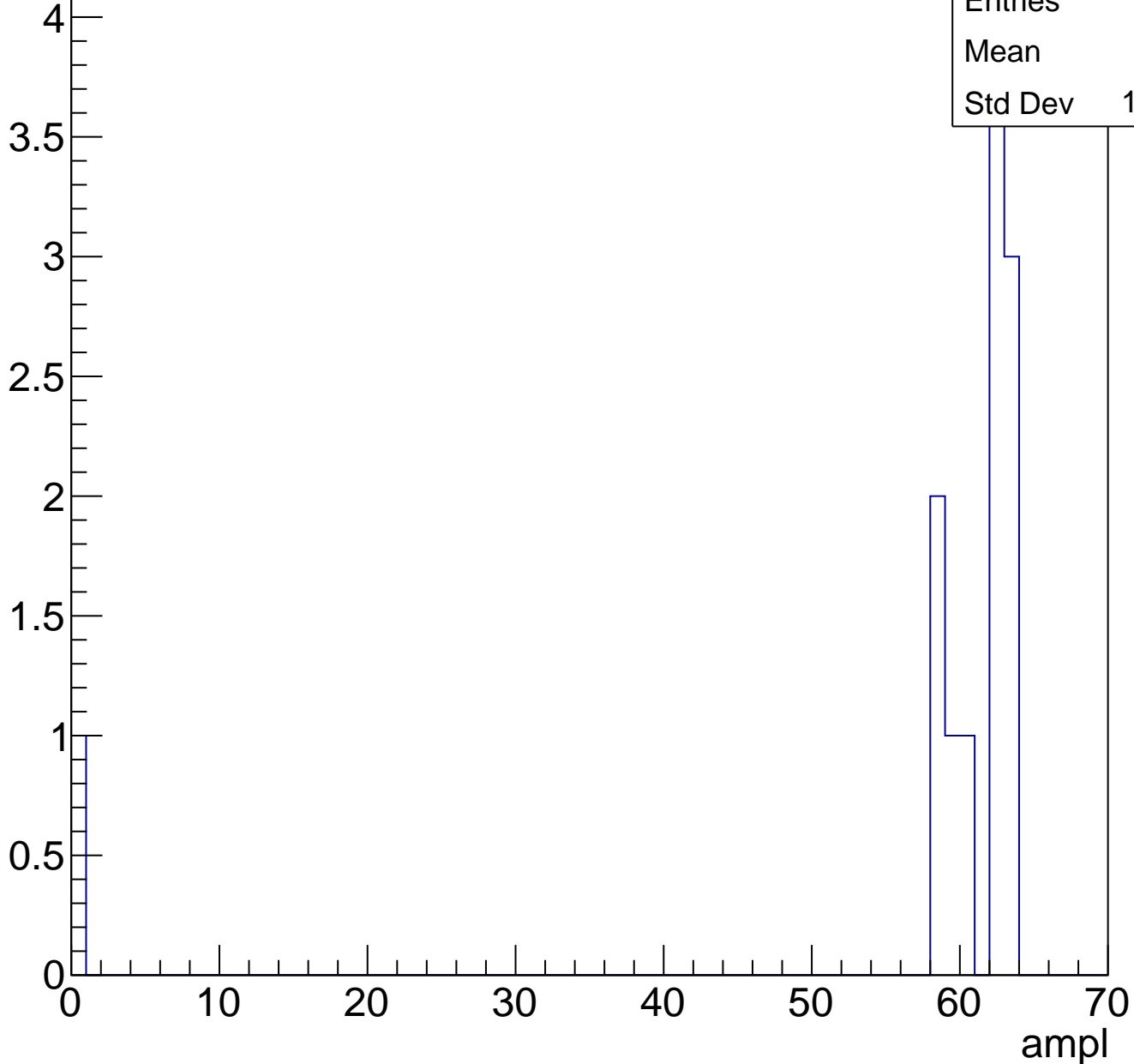
Entries	51
Mean	59.86
Std Dev	2.31



# B1L000S, U25-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch28, adc0

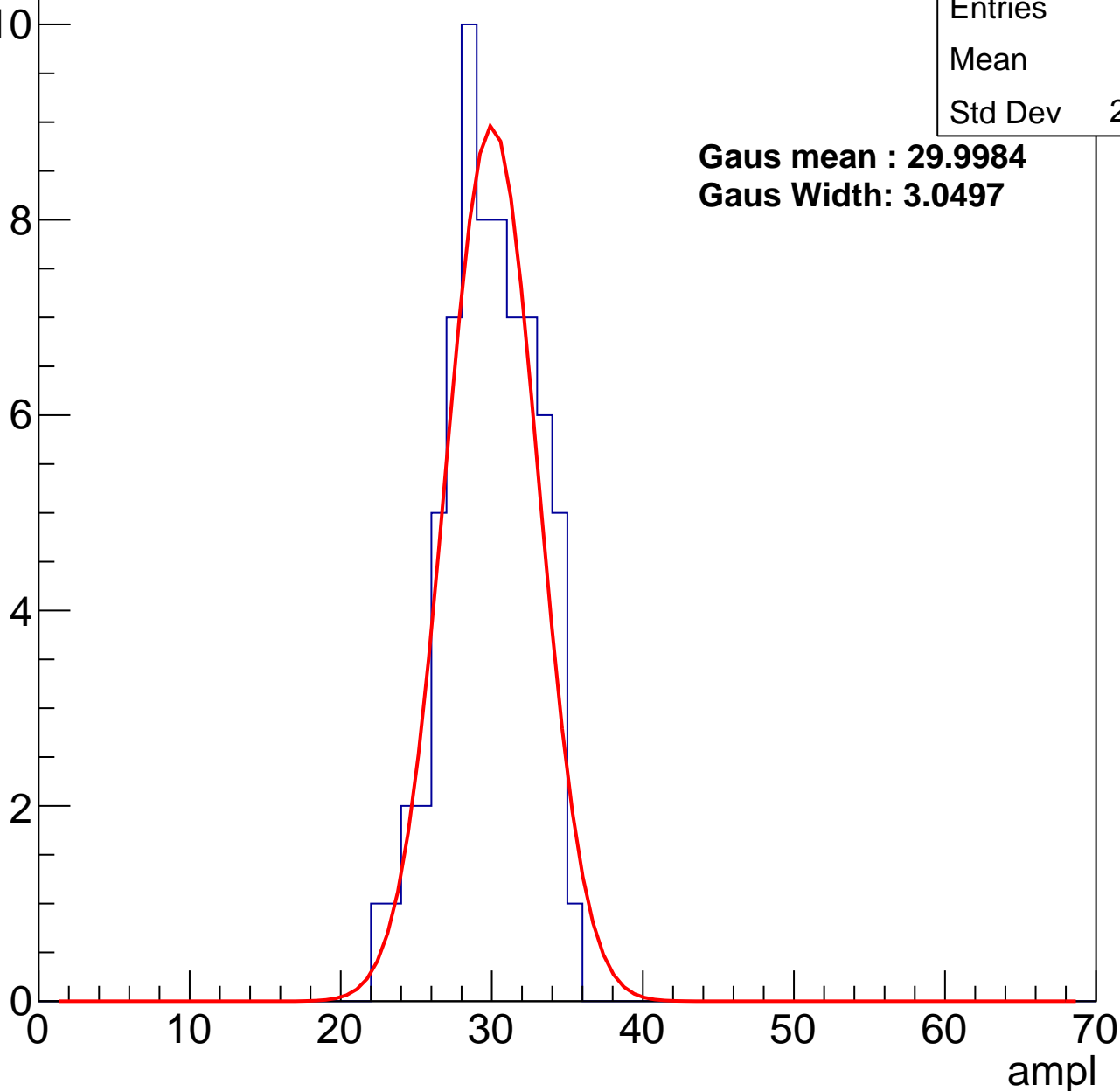
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	29.4
Std Dev	2.915

**Gaus mean : 29.9984**

**Gaus Width: 3.0497**



# B1L000S, U25-ch28, adc1

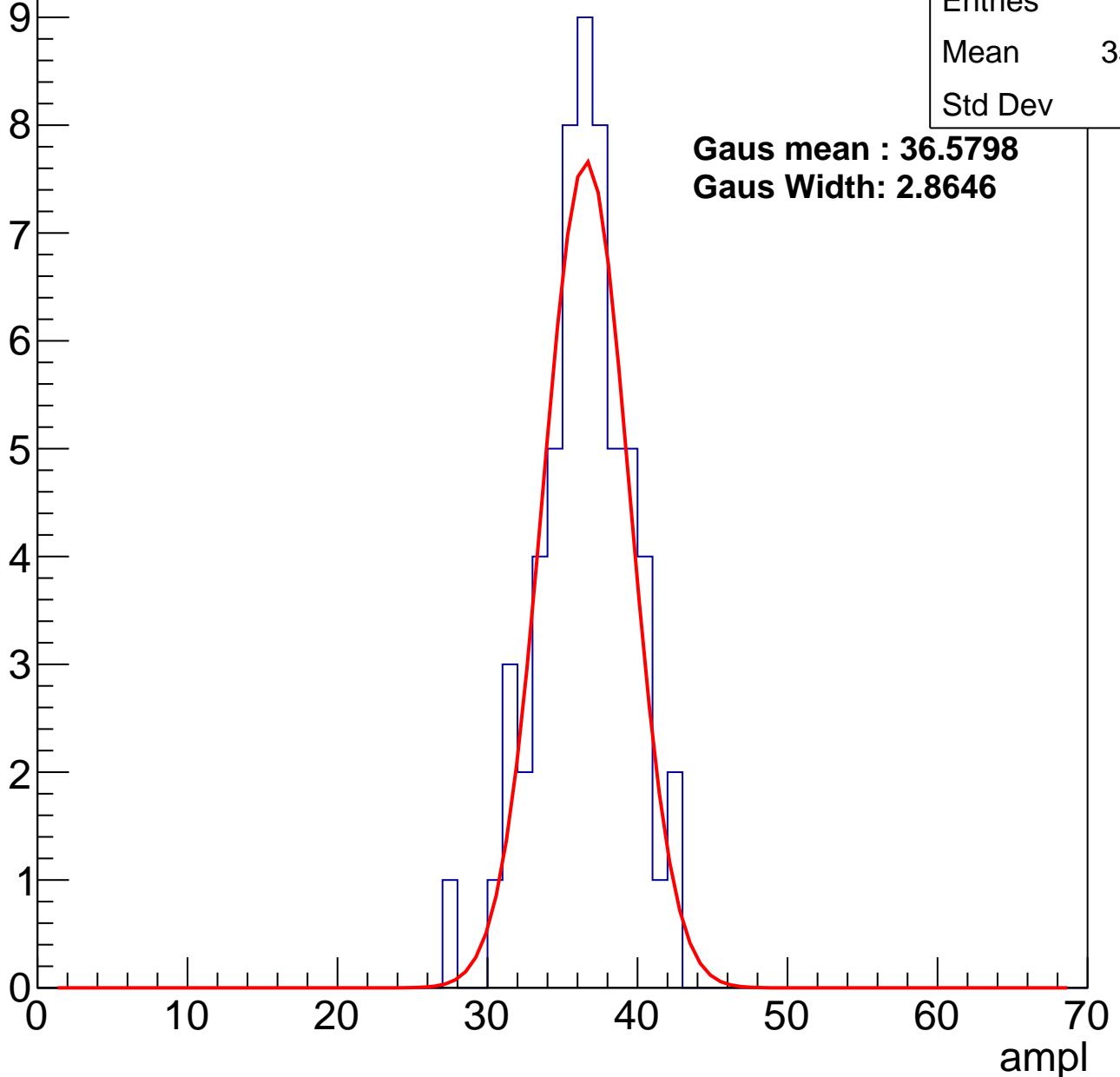
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.97
Std Dev	3

**Gaus mean : 36.5798**

**Gaus Width: 2.8646**



# B1L000S, U25-ch28, adc2

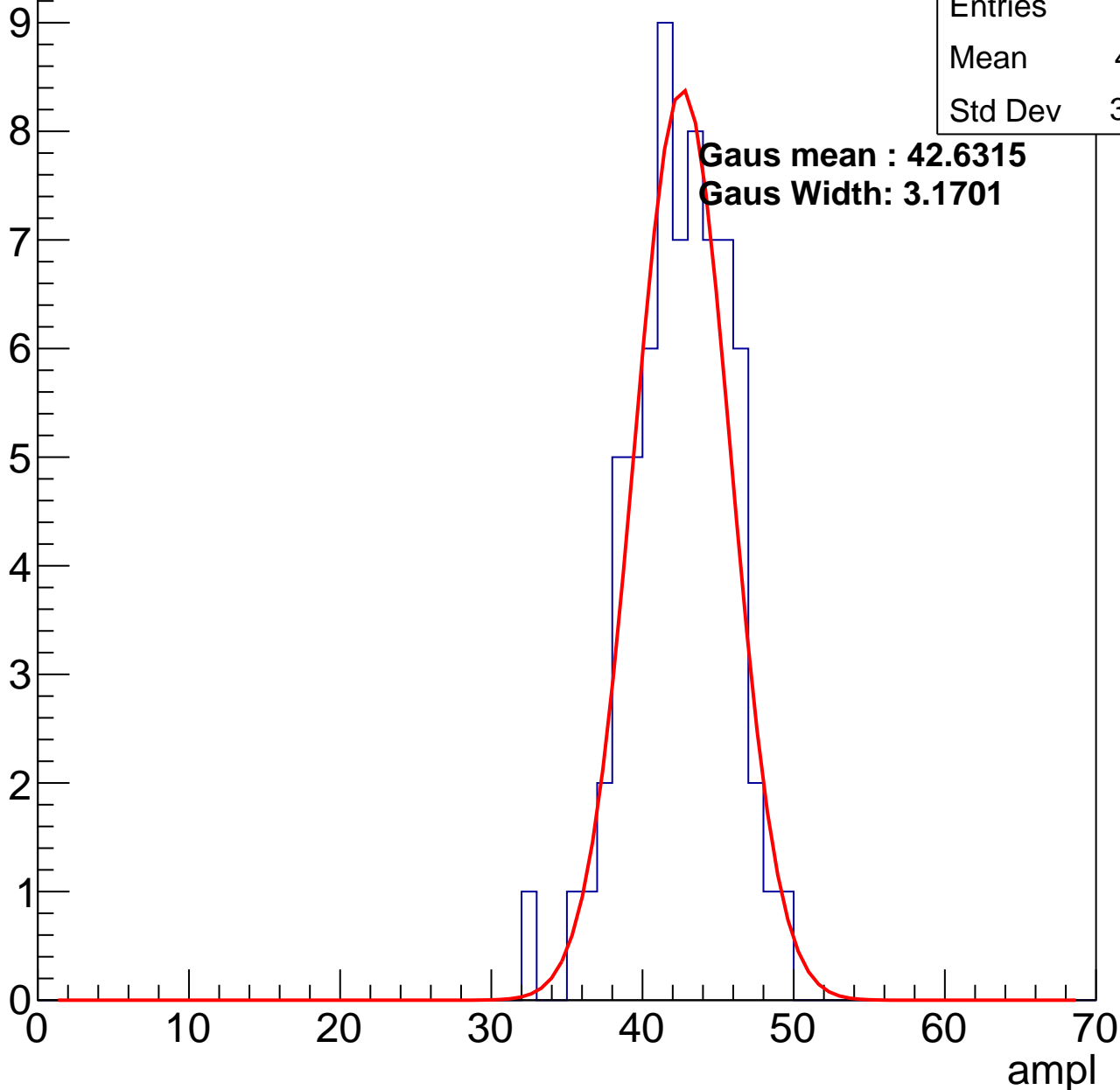
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	42.01
Std Dev	3.237

**Gaus mean : 42.6315**

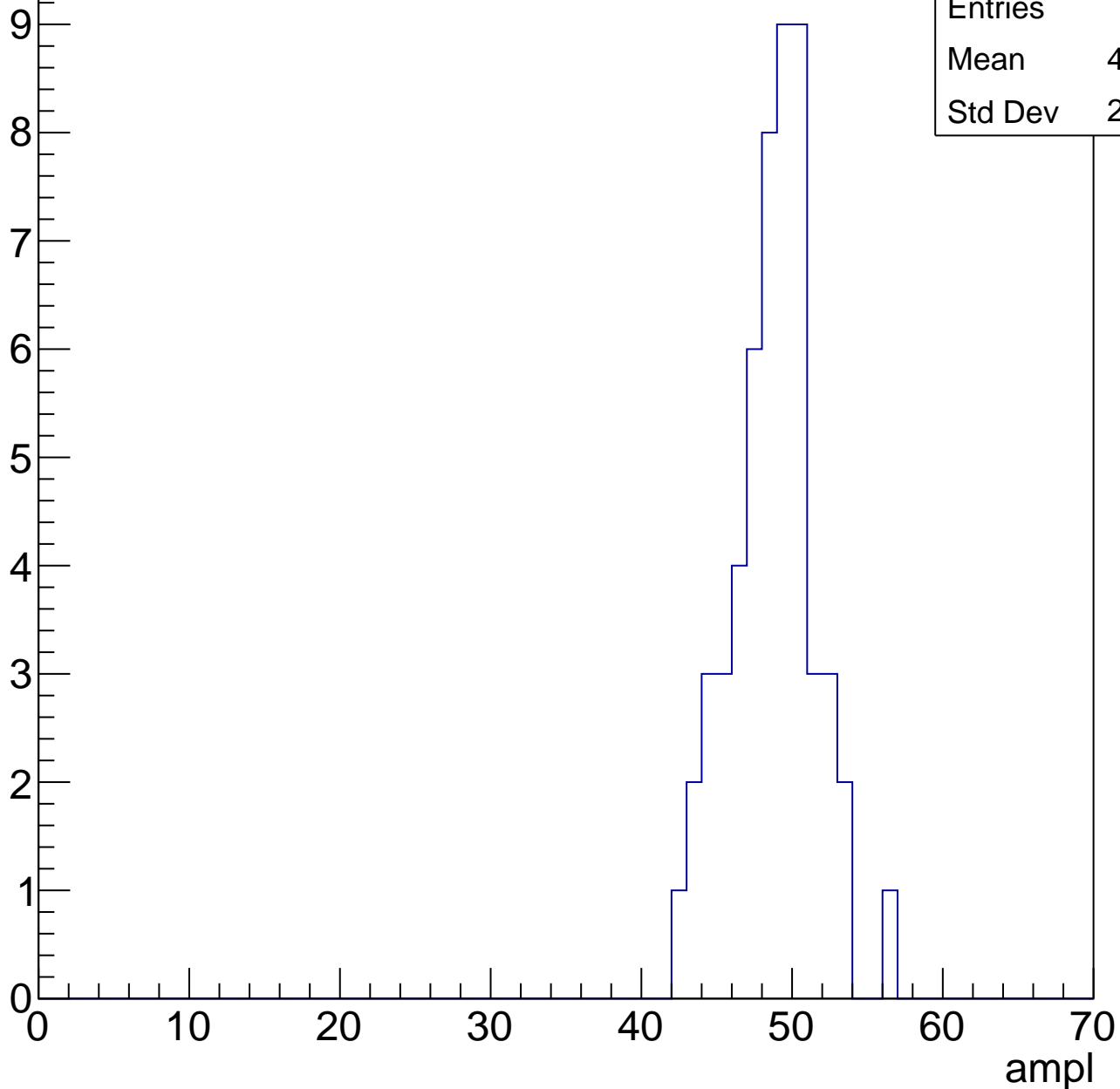
**Gaus Width: 3.1701**



# B1L000S, U25-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



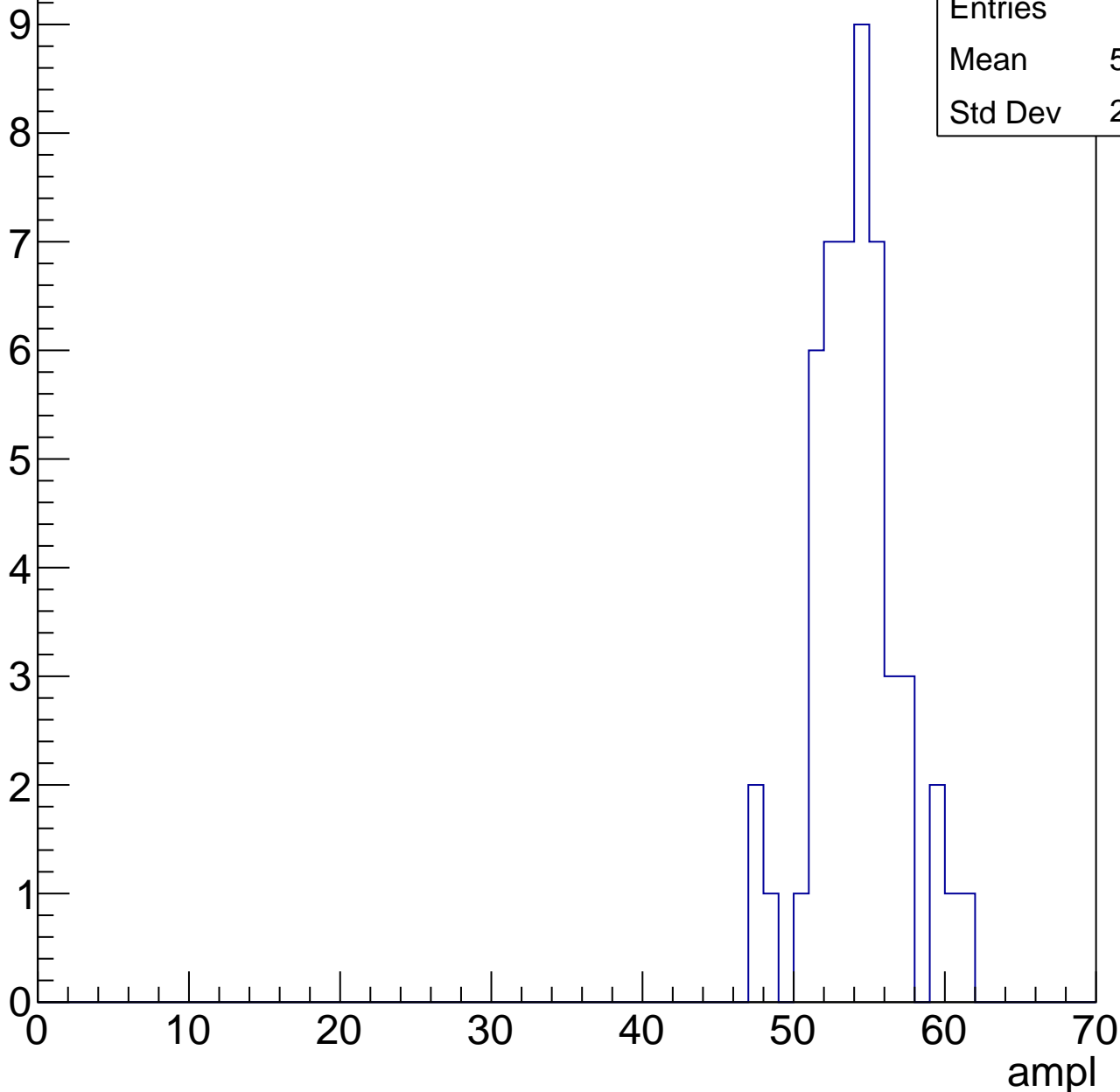
Entries	54
Mean	48.28
Std Dev	2.765

# B1L000S, U25-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	53.64
Std Dev	2.876

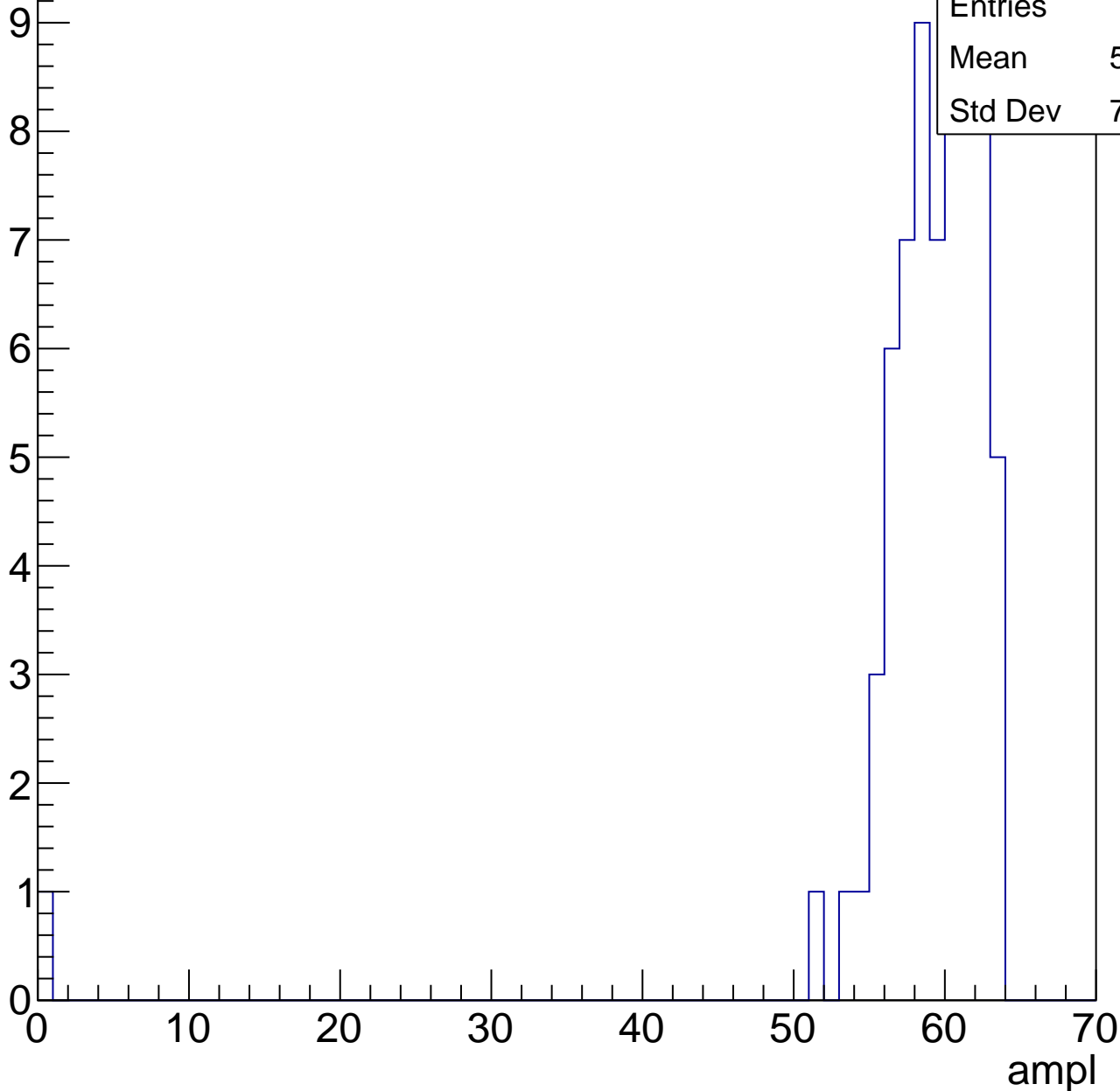


# B1L000S, U25-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	58.03
Std Dev	7.722

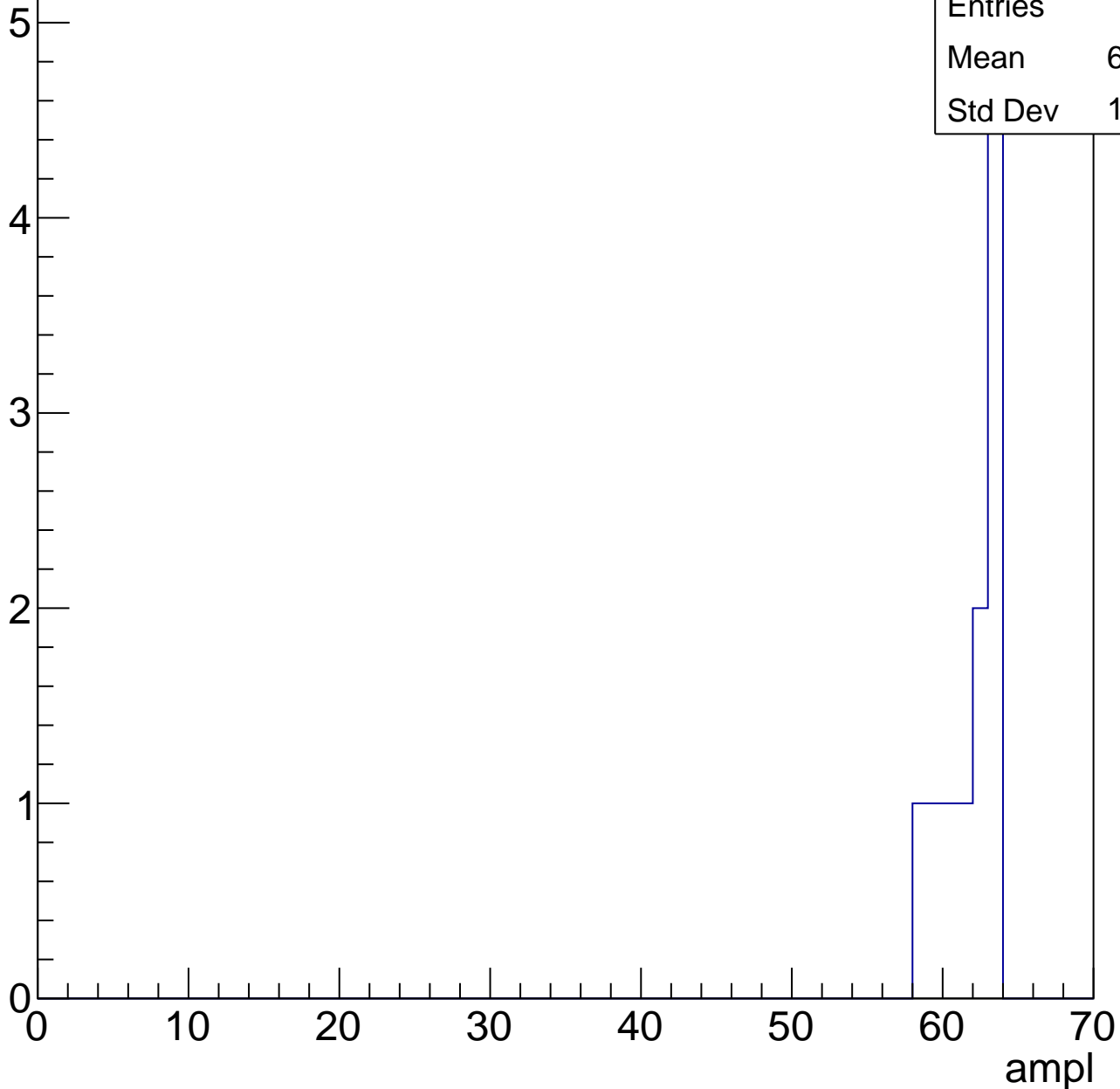


# B1L000S, U25-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	61.55
Std Dev	1.725

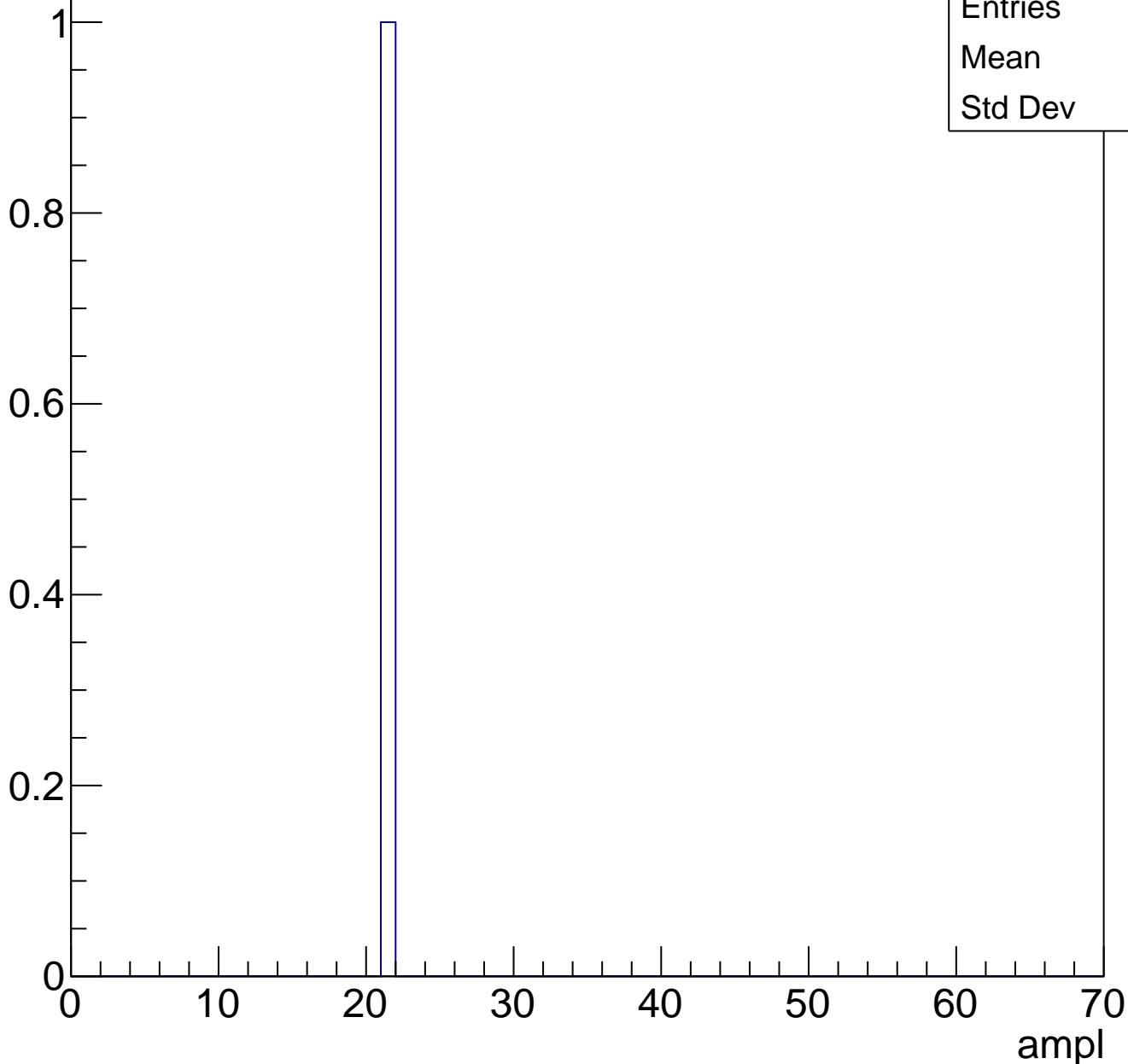




# B1L000S, U25-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	28.84
Std Dev	2.956

**Gaus mean : 29.2982**

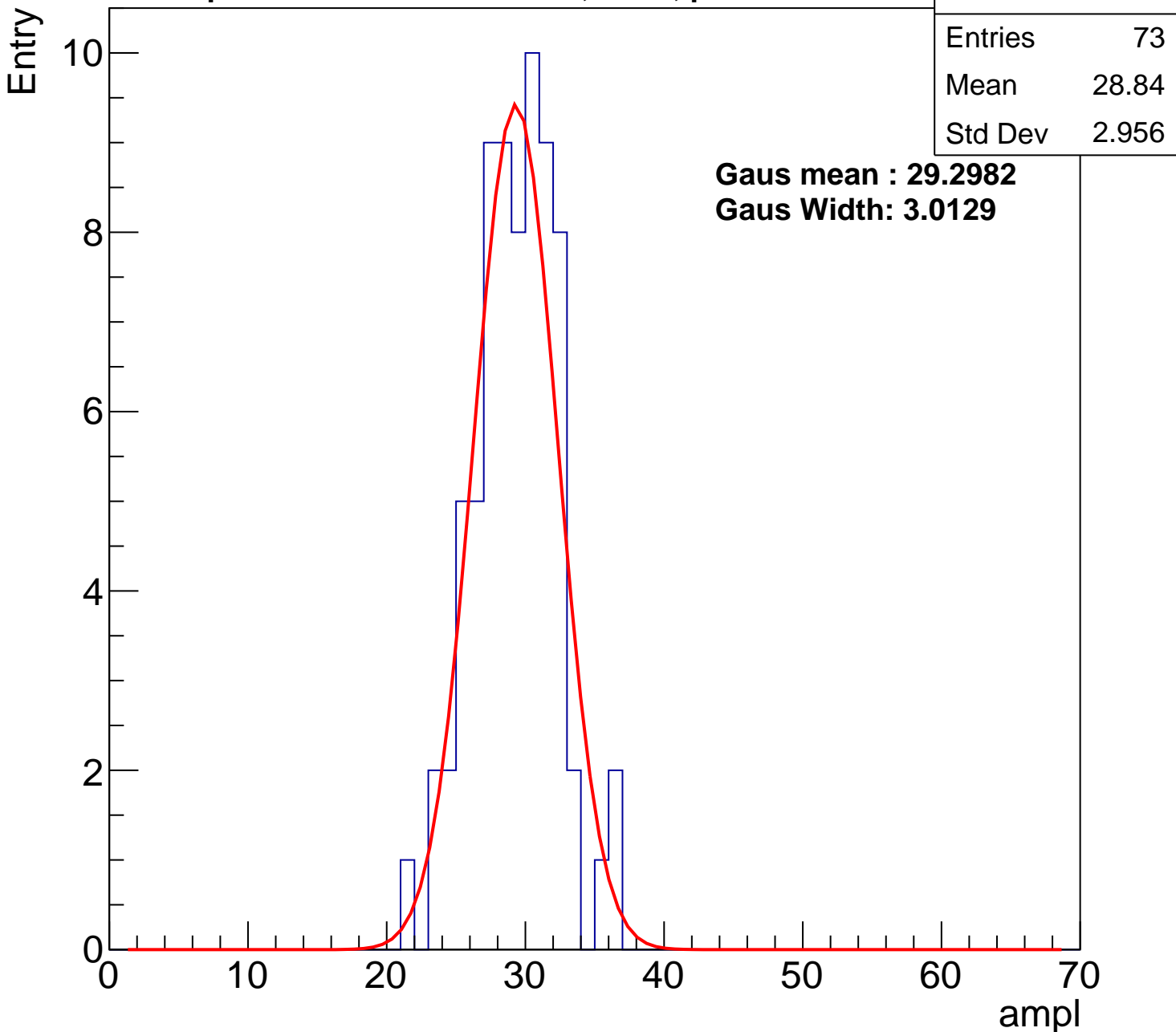
**Gaus Width: 3.0129**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



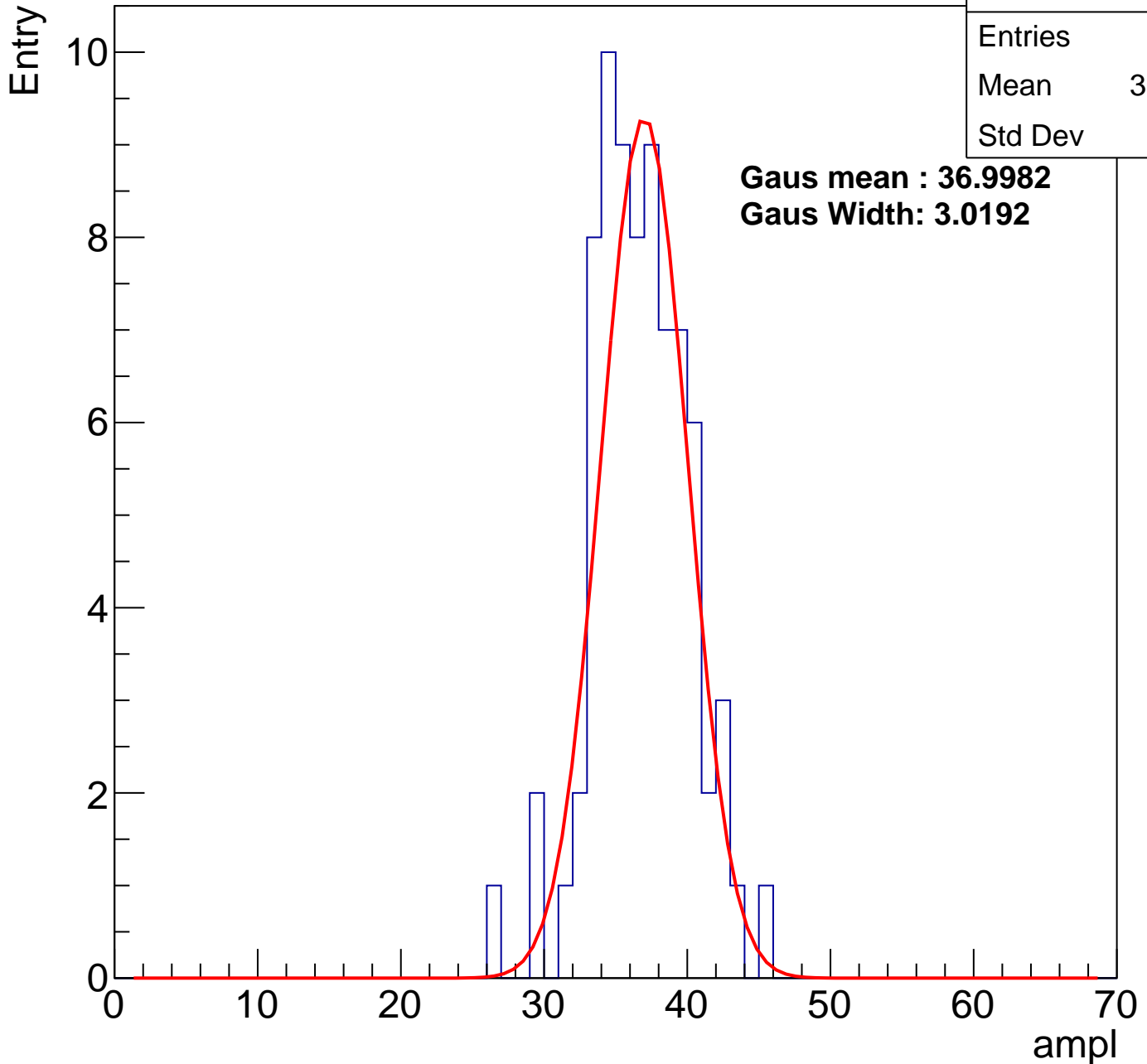
# B1L000S, U25-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	36.29
Std Dev	3.33

**Gaus mean : 36.9982**

**Gaus Width: 3.0192**



# B1L000S, U25-ch29, adc2

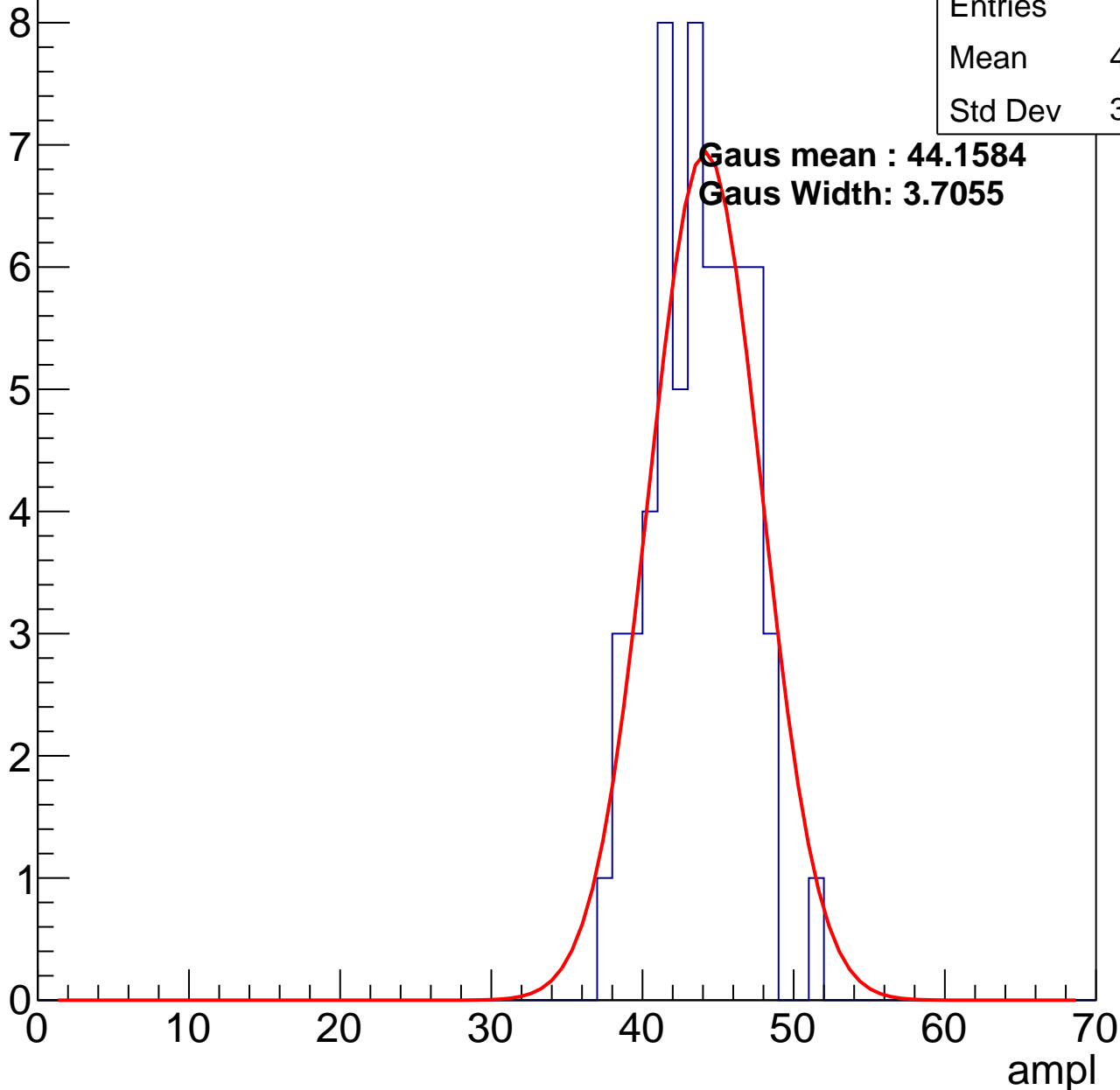
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	43.28
Std Dev	3.017

**Gaus mean : 44.1584**

**Gaus Width: 3.7055**

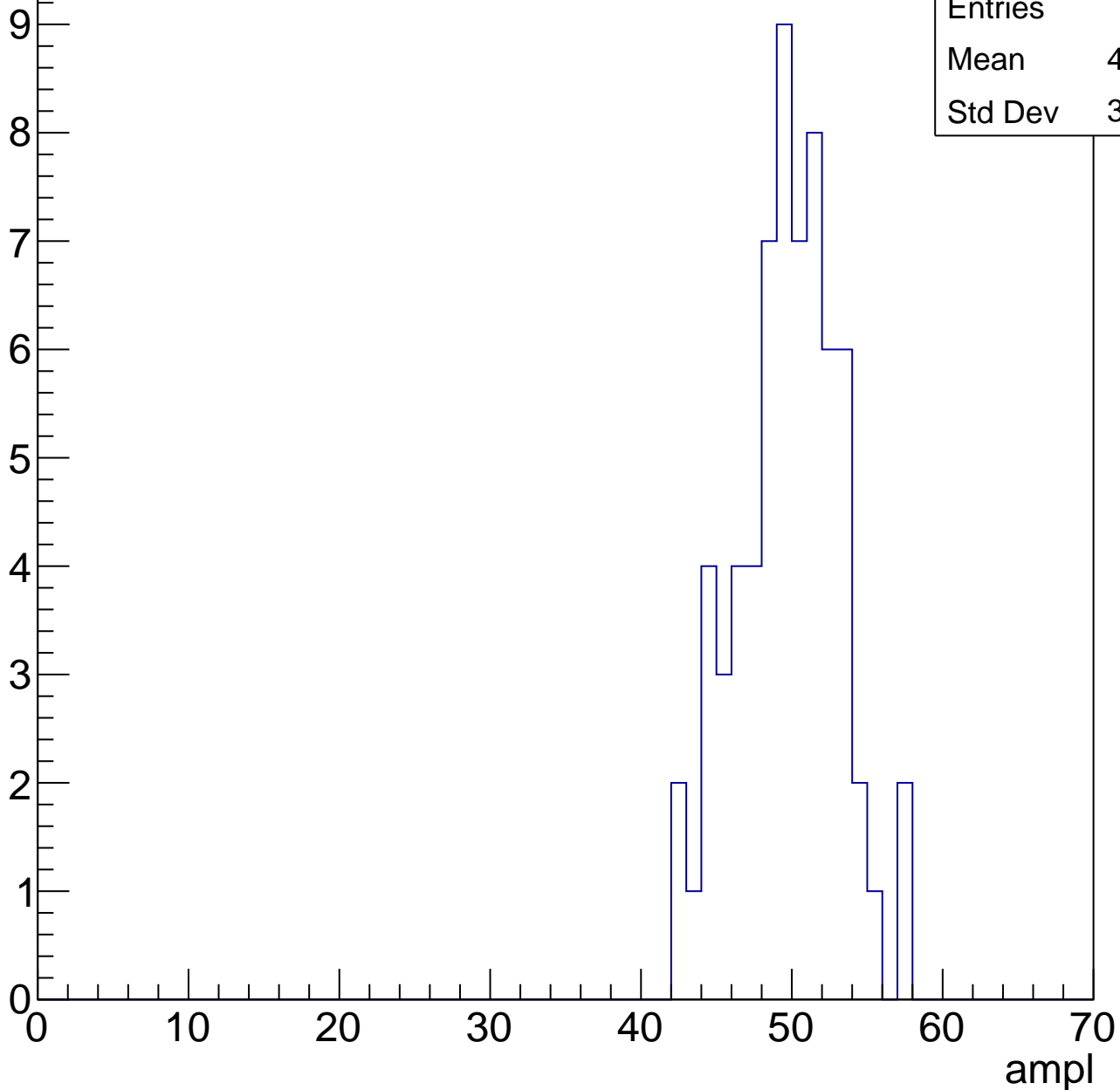


# B1L000S, U25-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

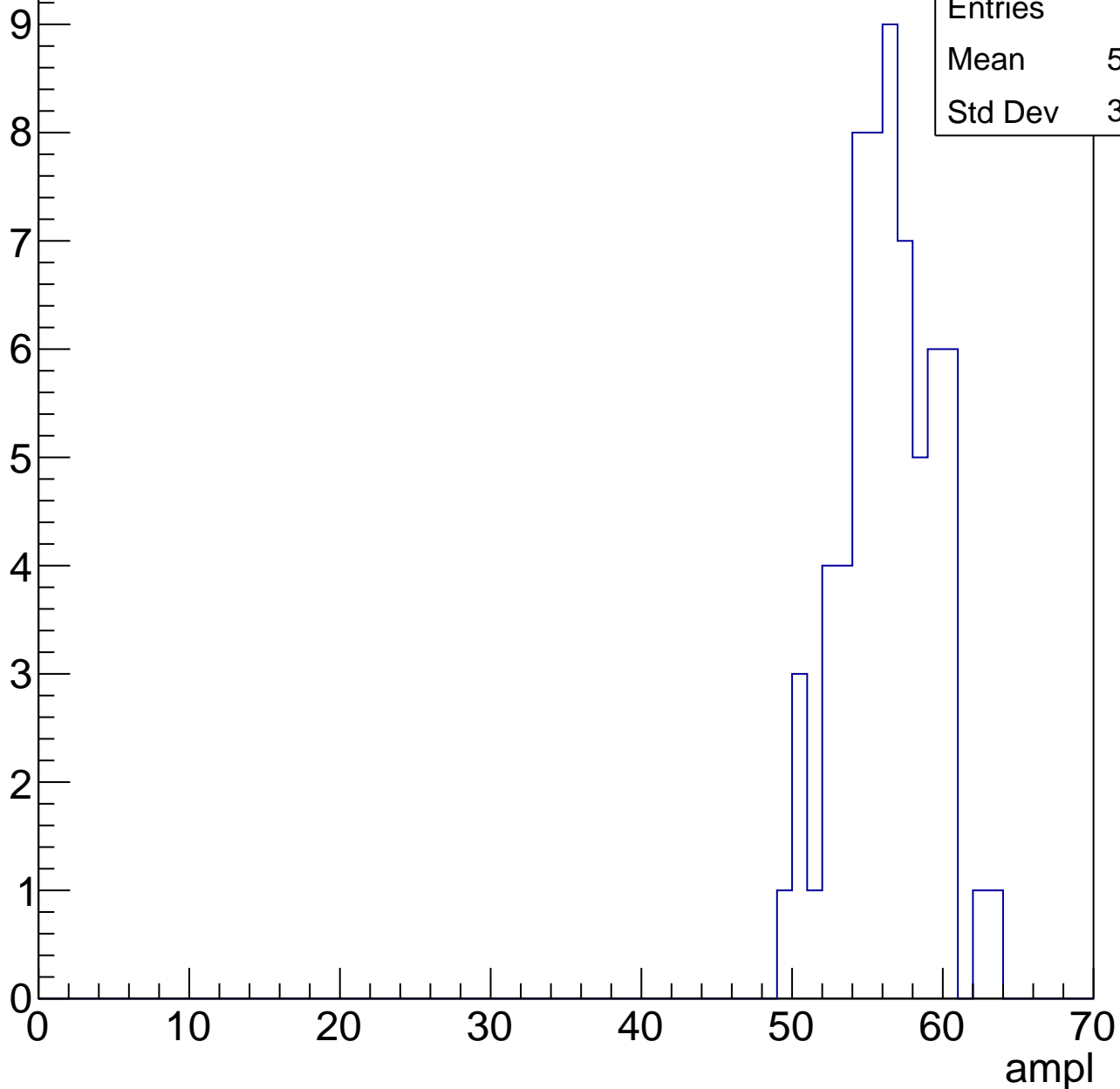
Entries	66
Mean	49.27
Std Dev	3.346



# B1L000S, U25-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

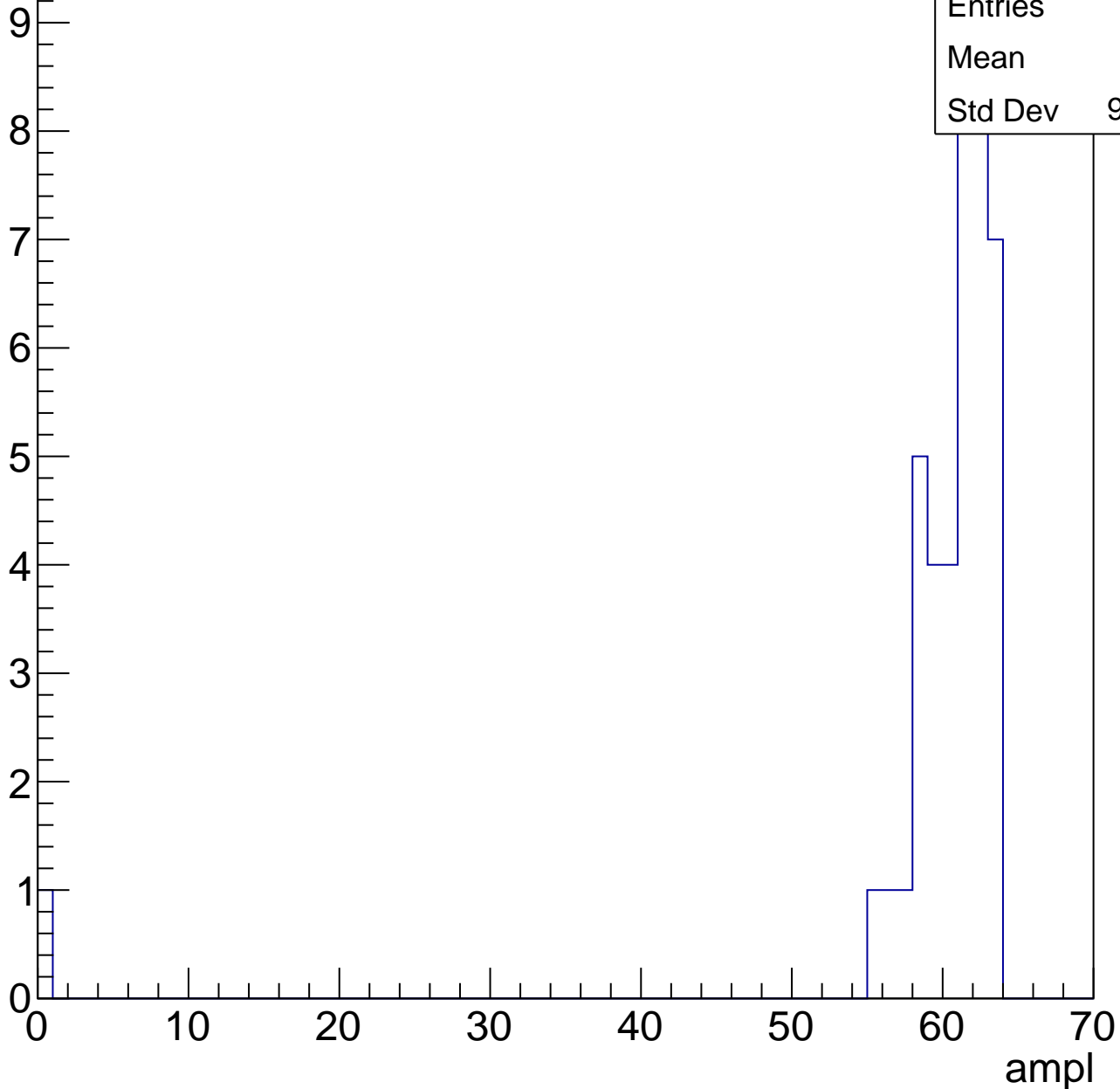


# B1L000S, U25-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	59.1
Std Dev	9.446



# B1L000S, U25-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U25-ch30, adc0

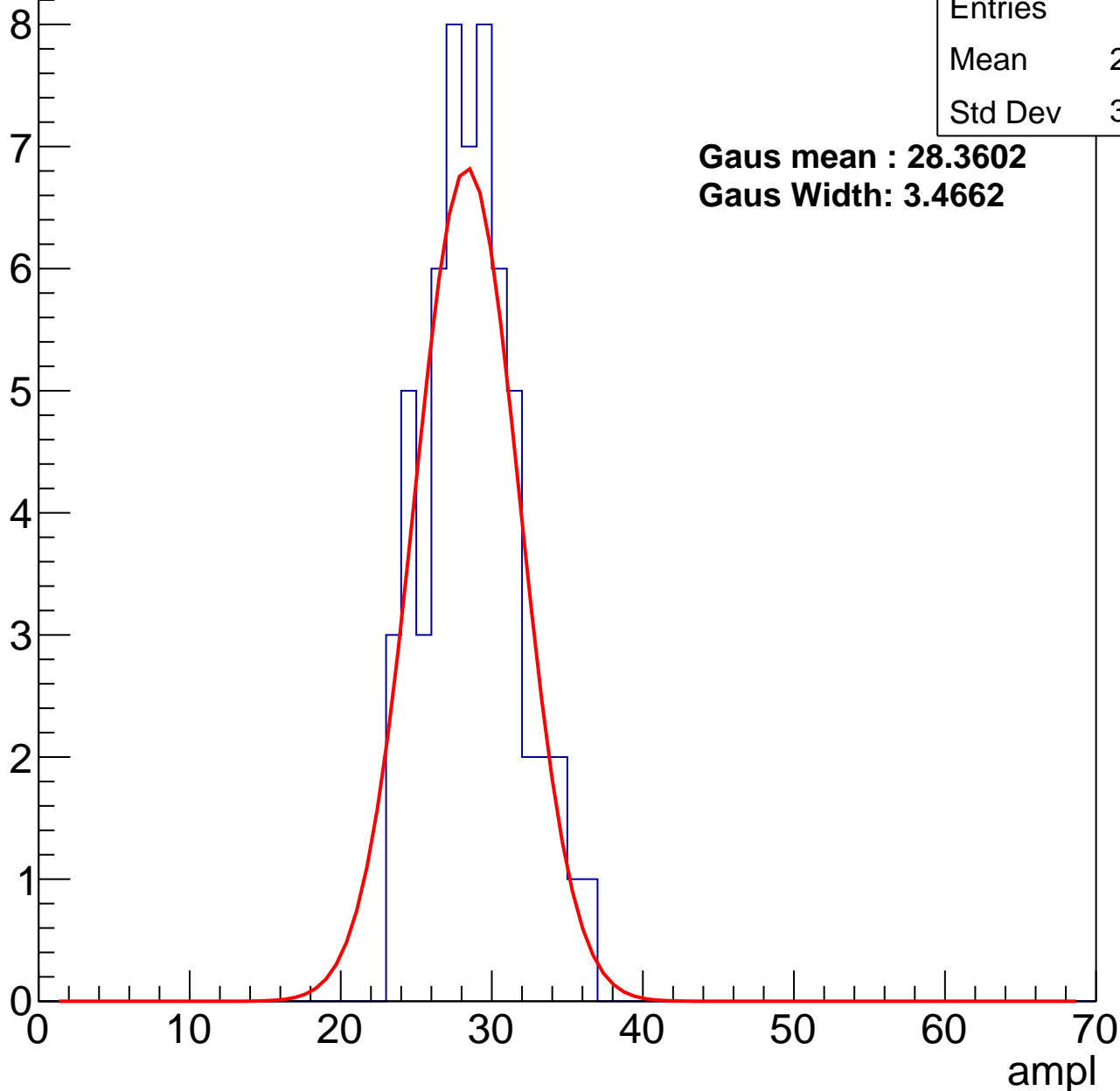
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	28.27
Std Dev	3.063

**Gaus mean : 28.3602**

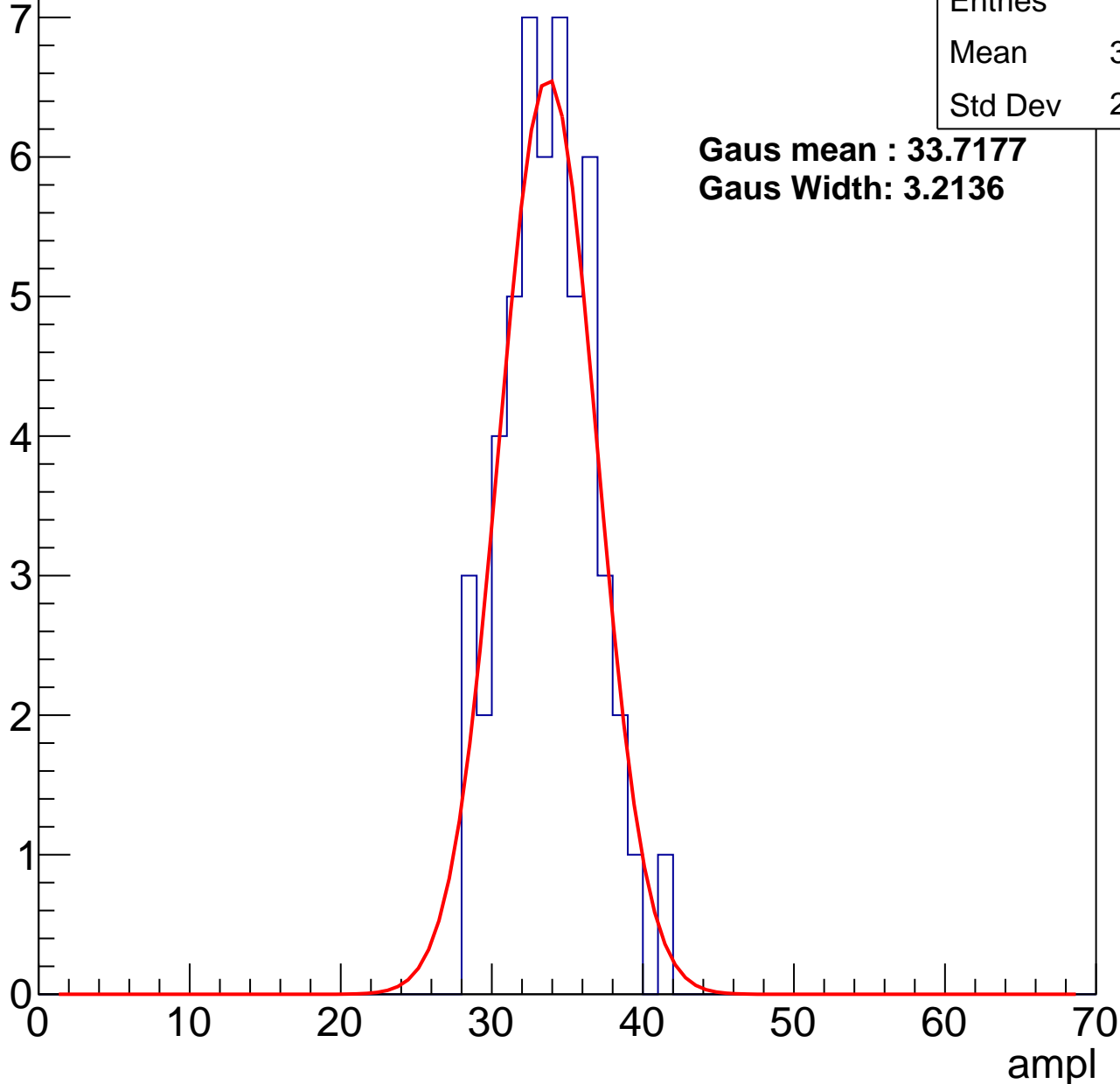
**Gaus Width: 3.4662**



# B1L000S, U25-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch30, adc2

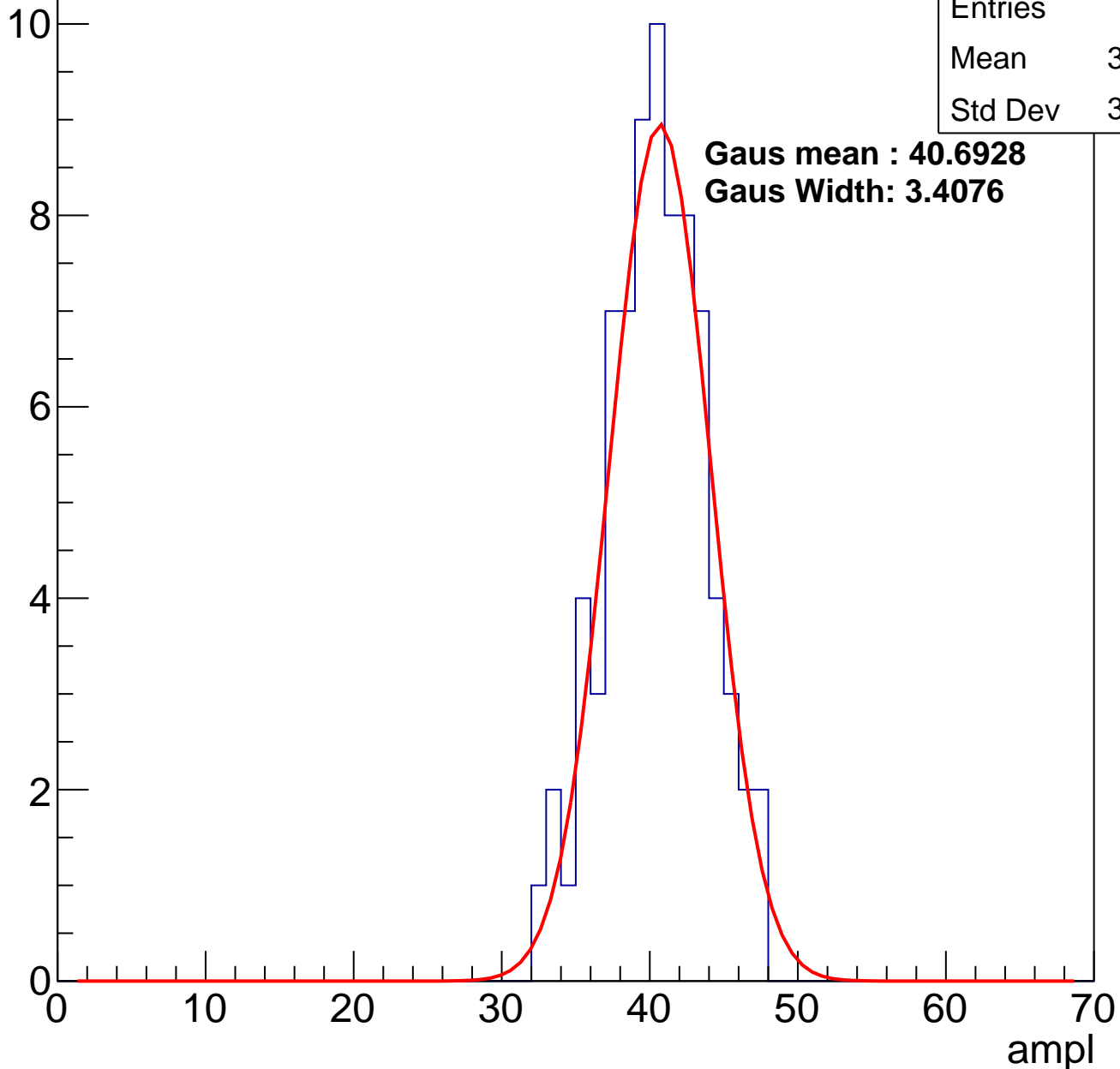
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	78
Mean	39.97
Std Dev	3.317

**Gaus mean : 40.6928**

**Gaus Width: 3.4076**

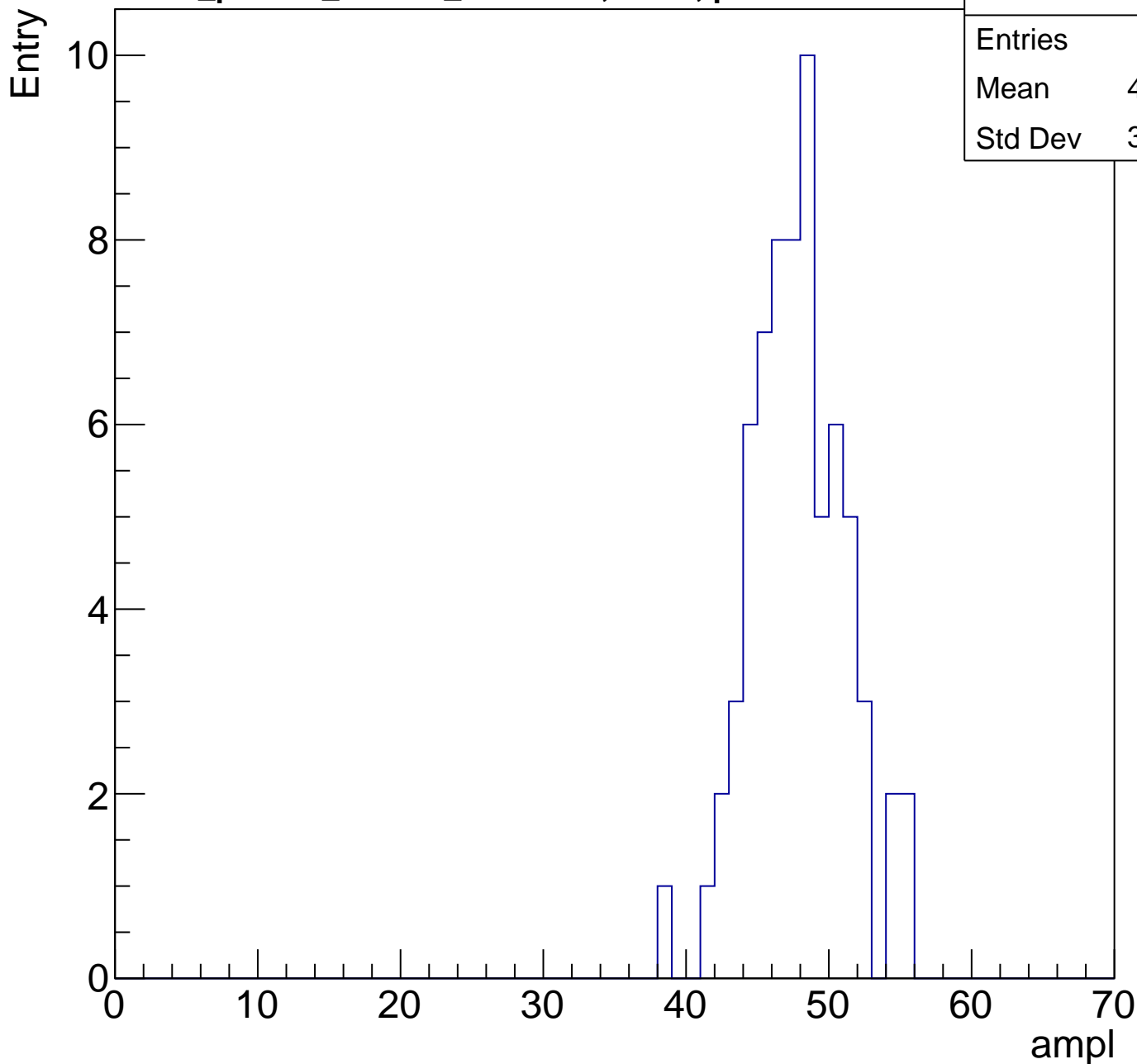
Entry



# B1L000S, U25-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	47.38
Std Dev	3.319



# B1L000S, U25-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

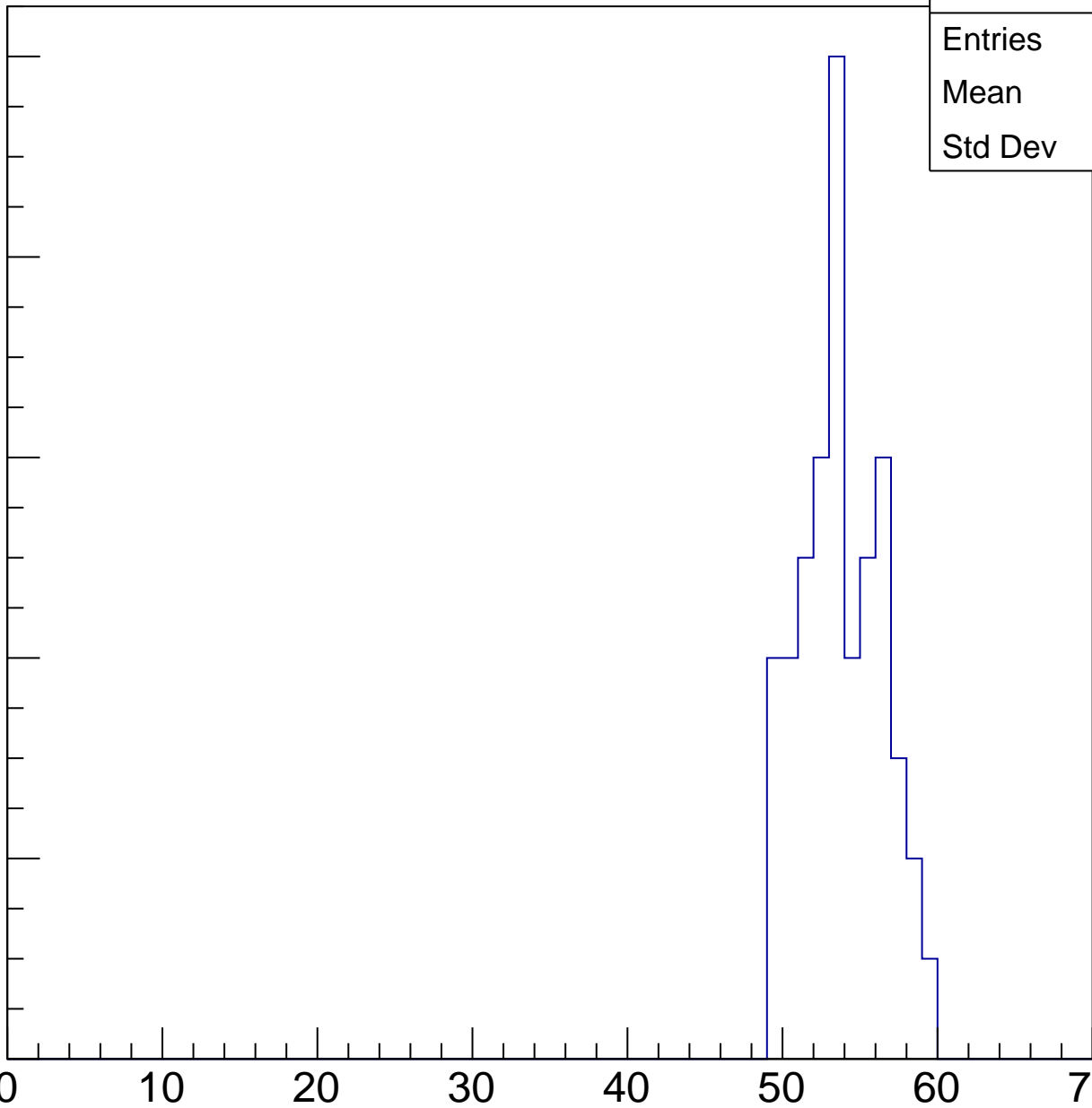
Entries	50
Mean	53.32
Std Dev	2.58

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

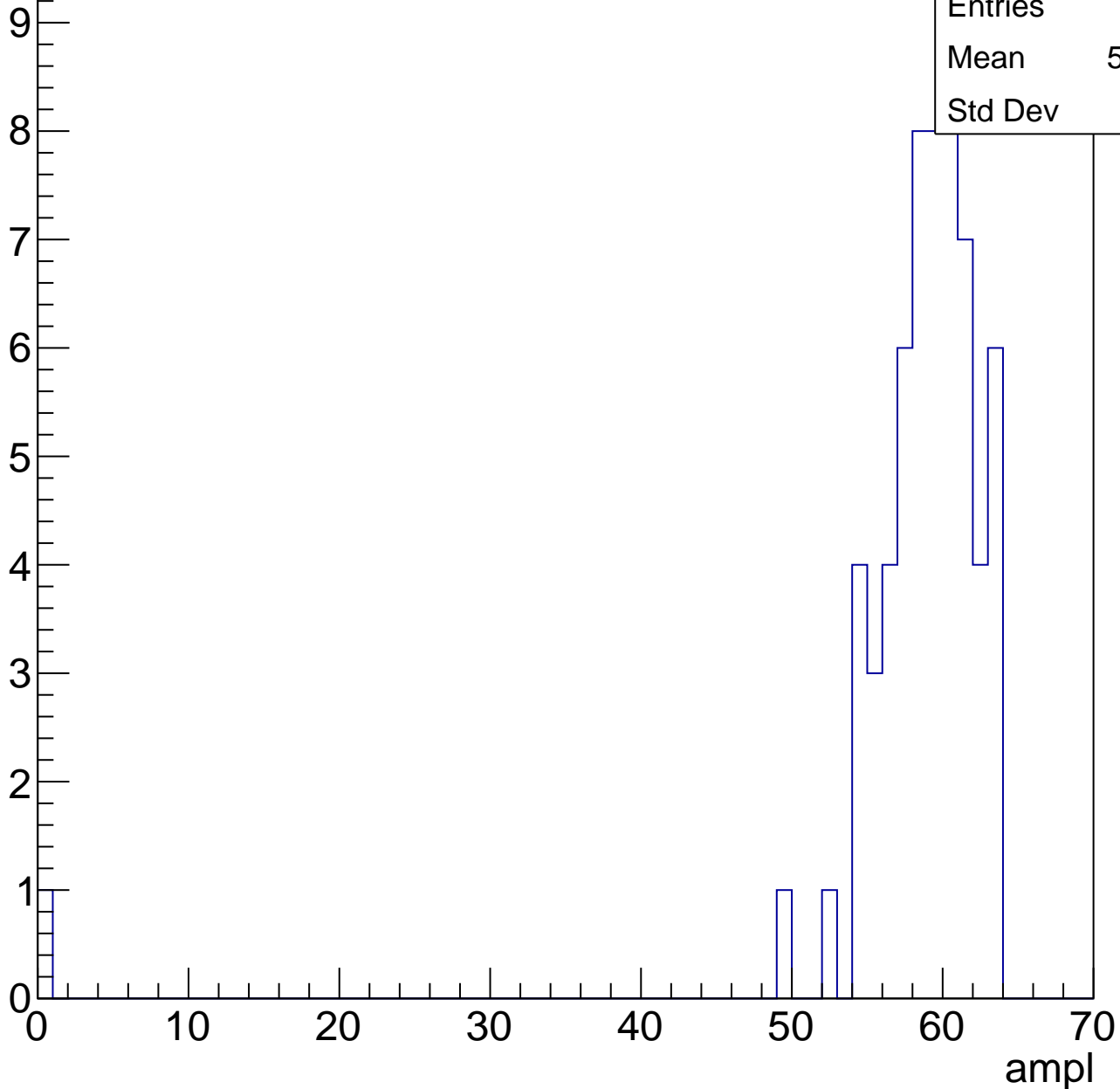


# B1L000S, U25-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	57.69
Std Dev	7.94

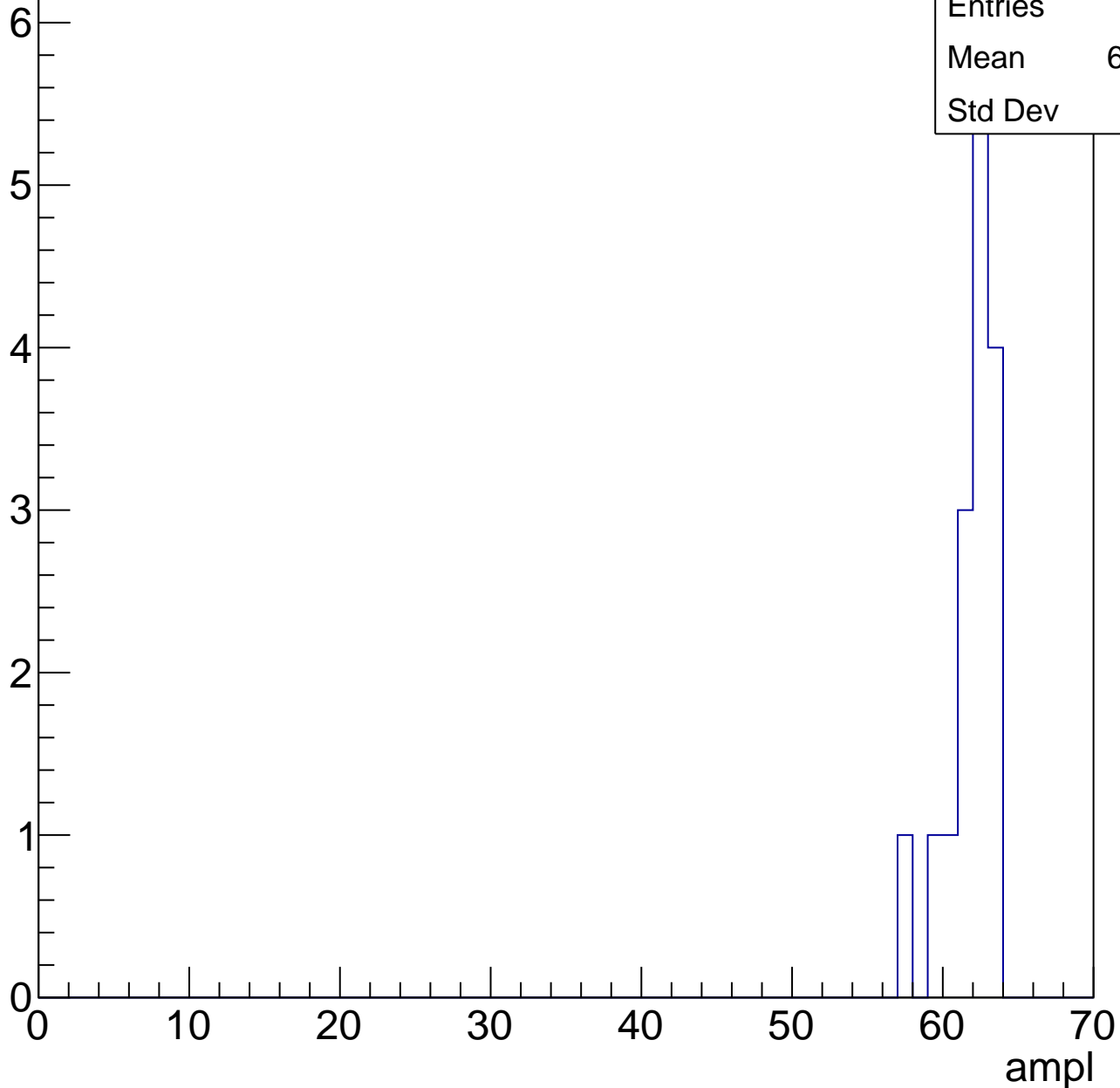


# B1L000S, U25-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.44
Std Dev	1.58





# B1L000S, U25-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L000S, U25-ch31, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	28.2
Std Dev	4.459

**Gaus mean : 29.2240**

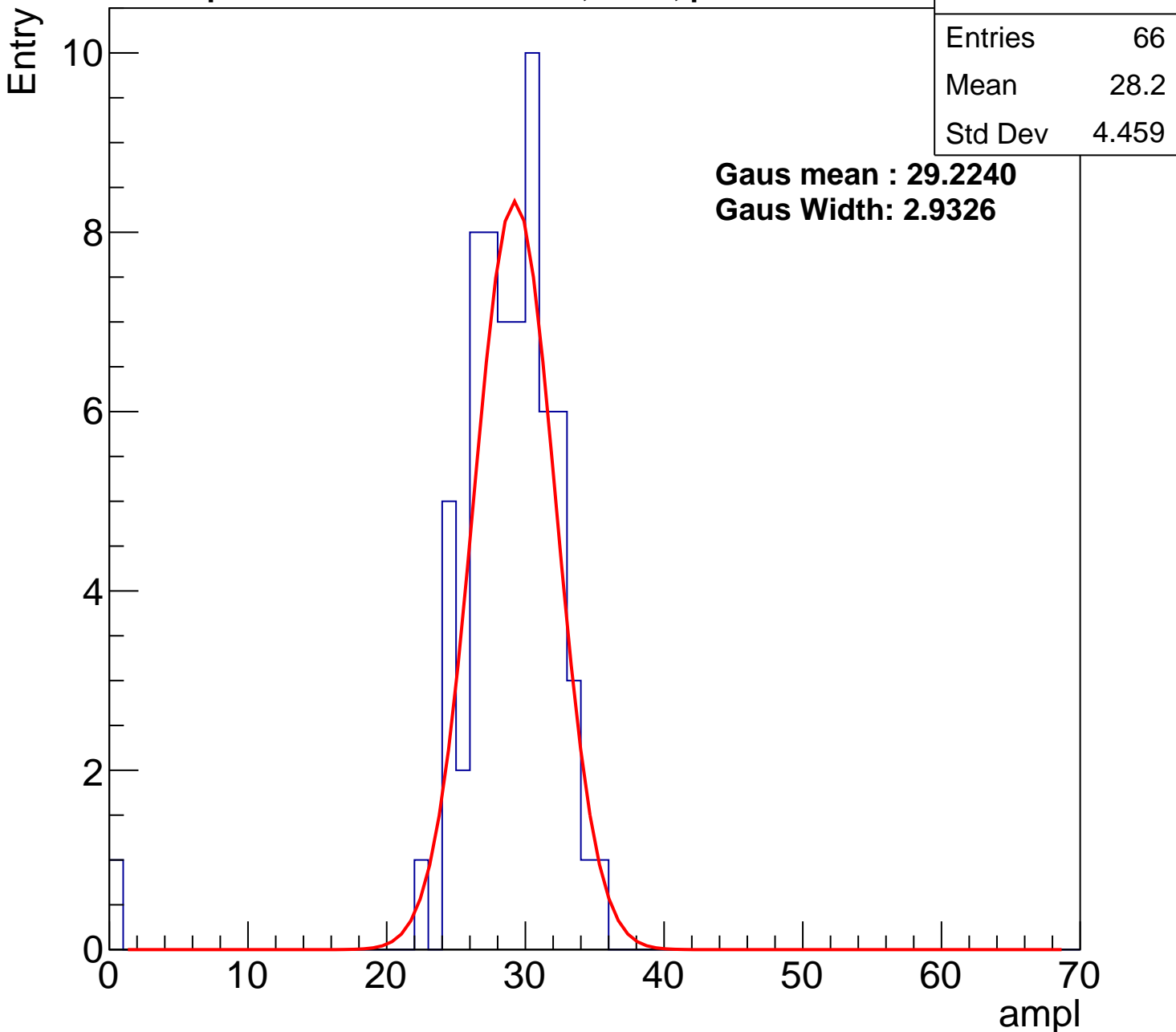
**Gaus Width: 2.9326**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



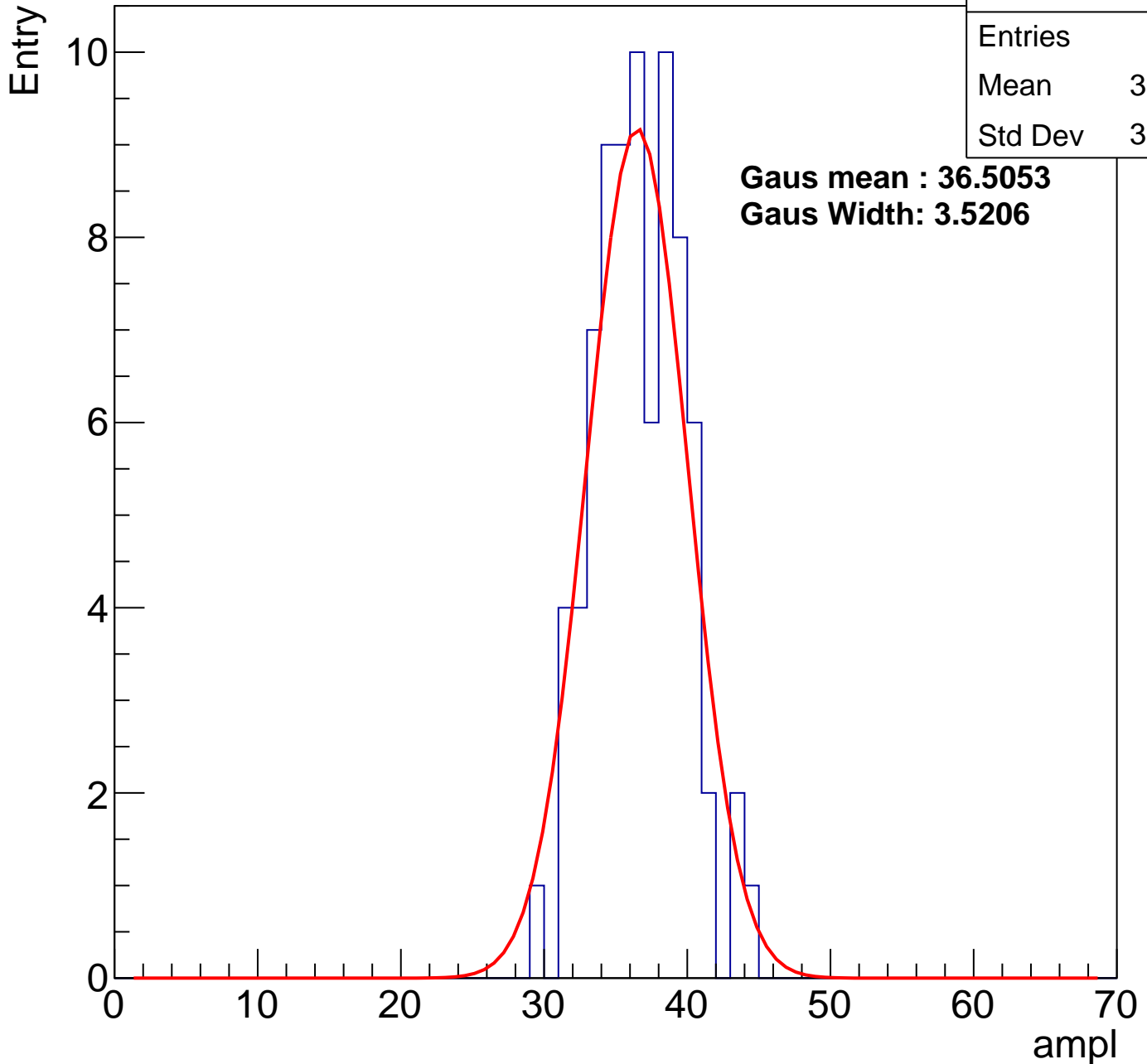
# B1L000S, U25-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	36.19
Std Dev	3.069

**Gaus mean : 36.5053**

**Gaus Width: 3.5206**

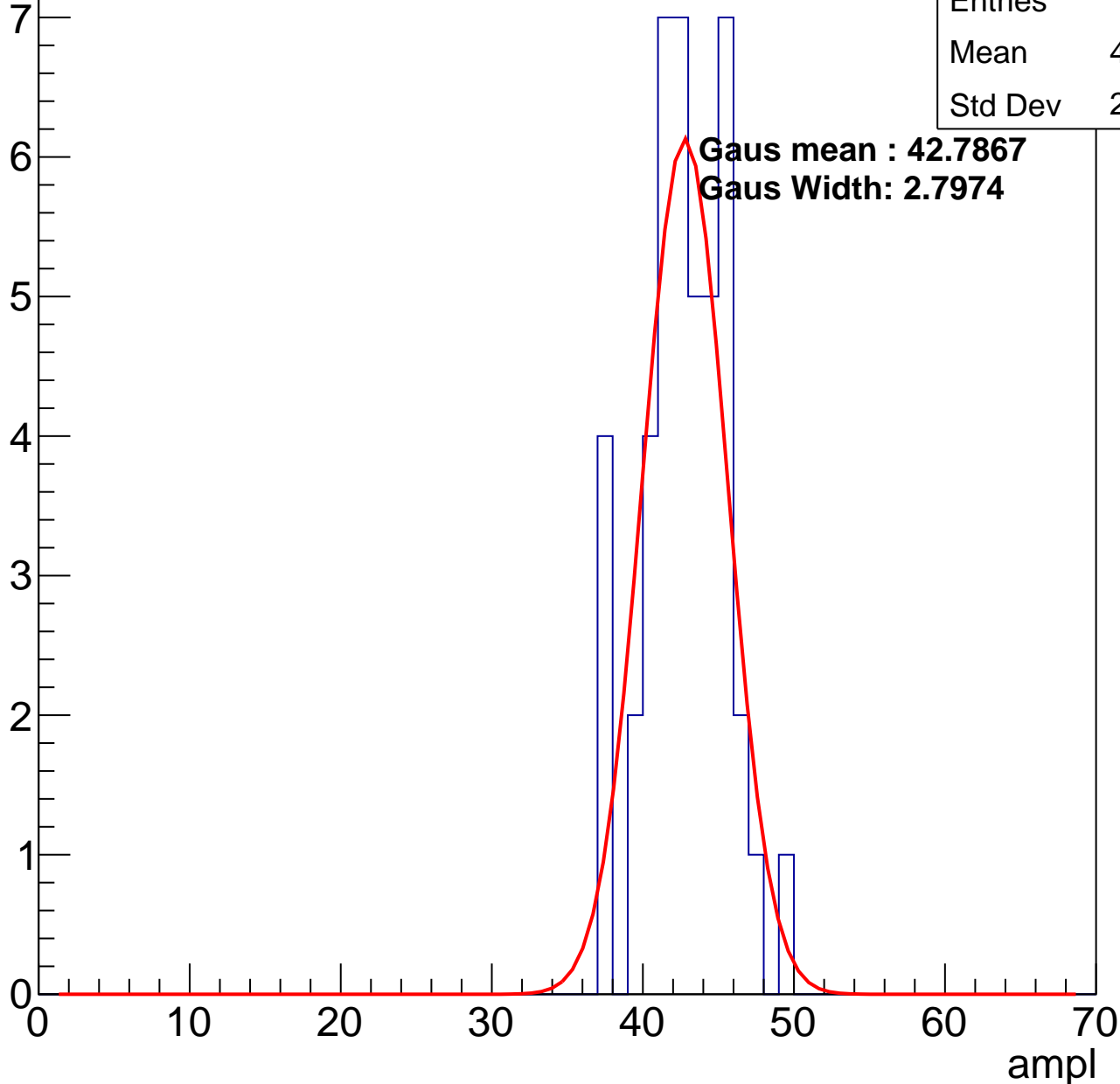


# B1L000S, U25-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	42.33
Std Dev	2.708



# B1L000S, U25-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

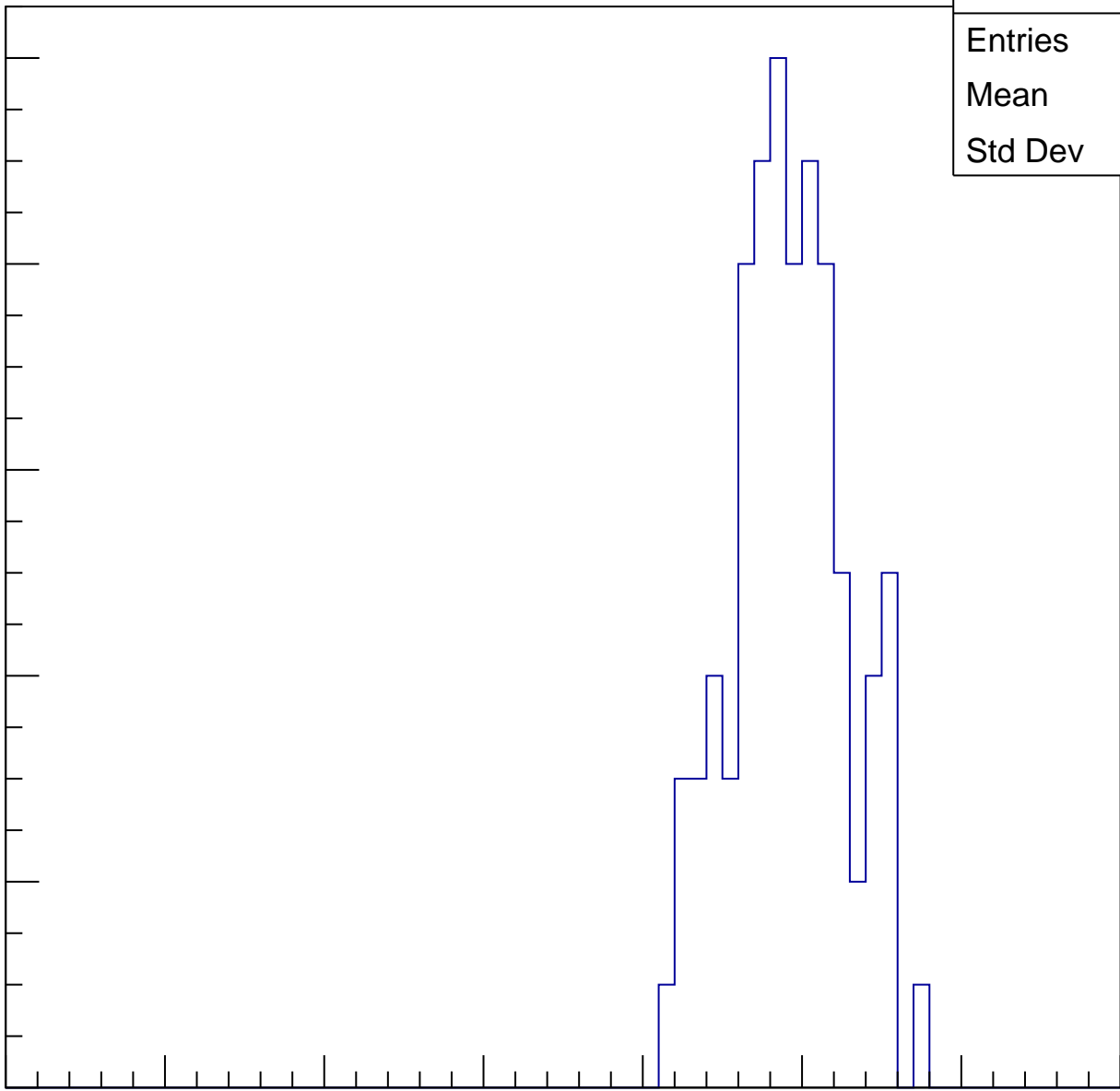
Entries	83
Mean	48.7
Std Dev	3.539

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

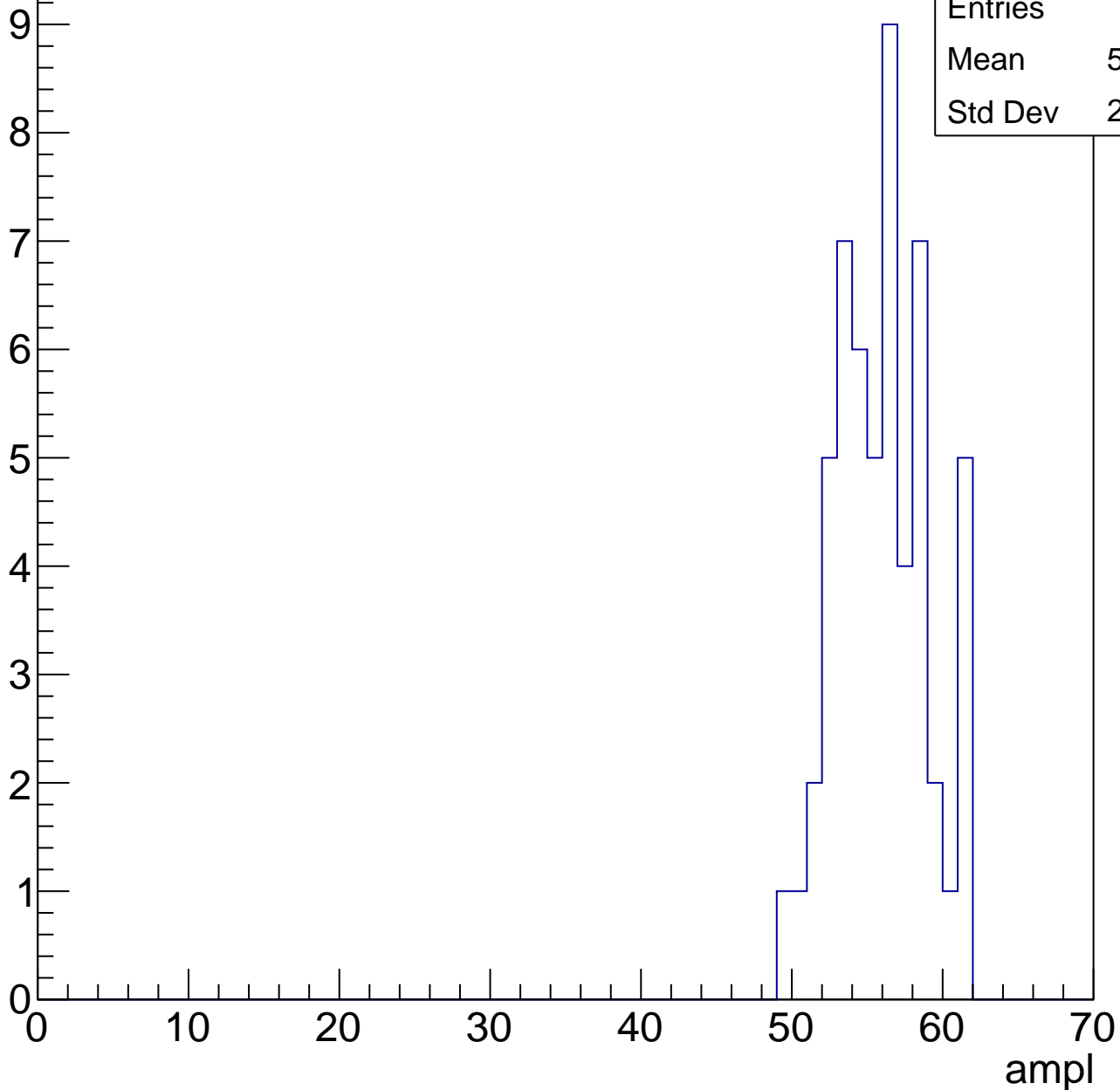


# B1L000S, U25-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

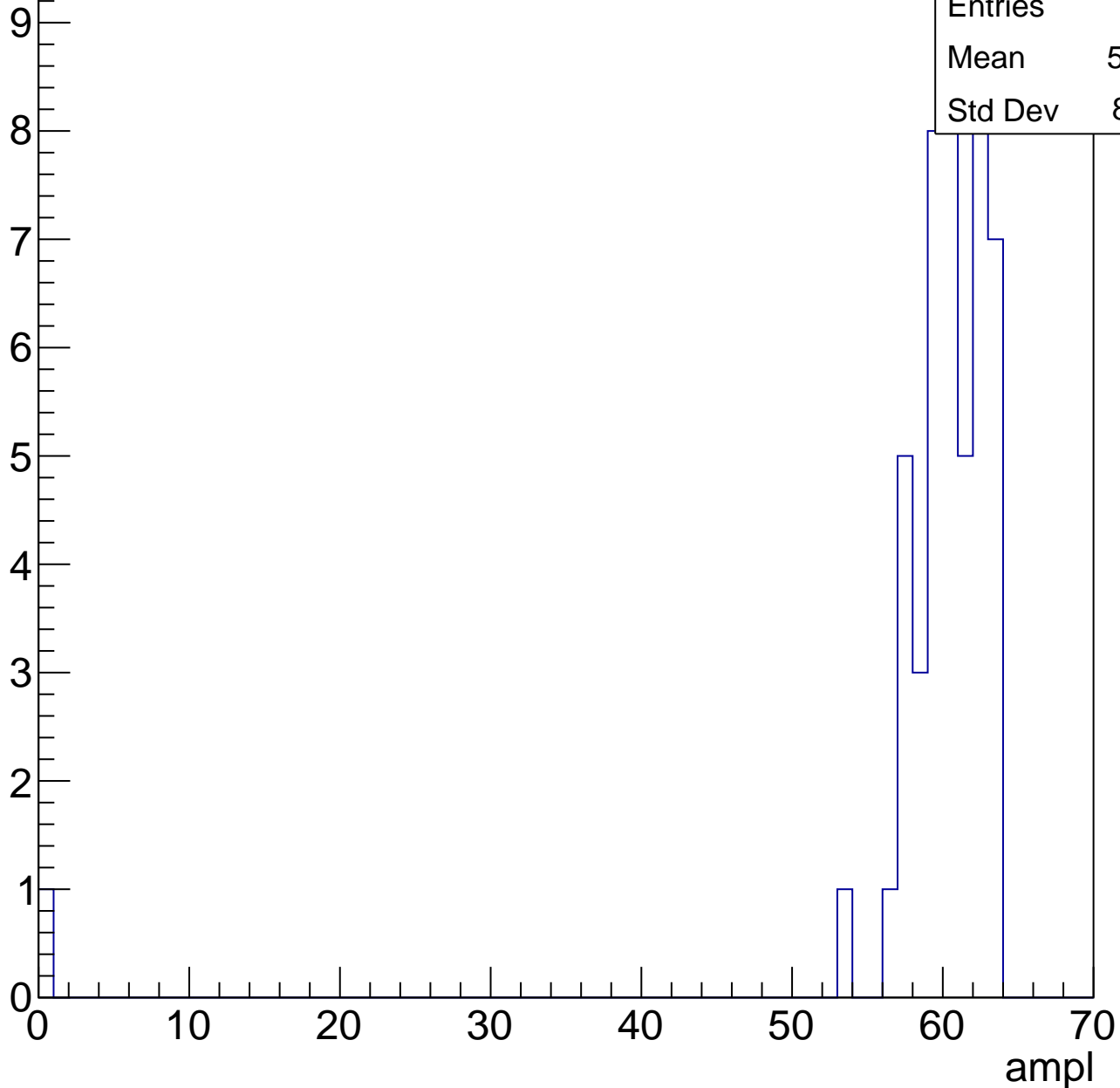
Entries	55
Mean	55.49
Std Dev	2.966



# B1L000S, U25-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch32, adc0

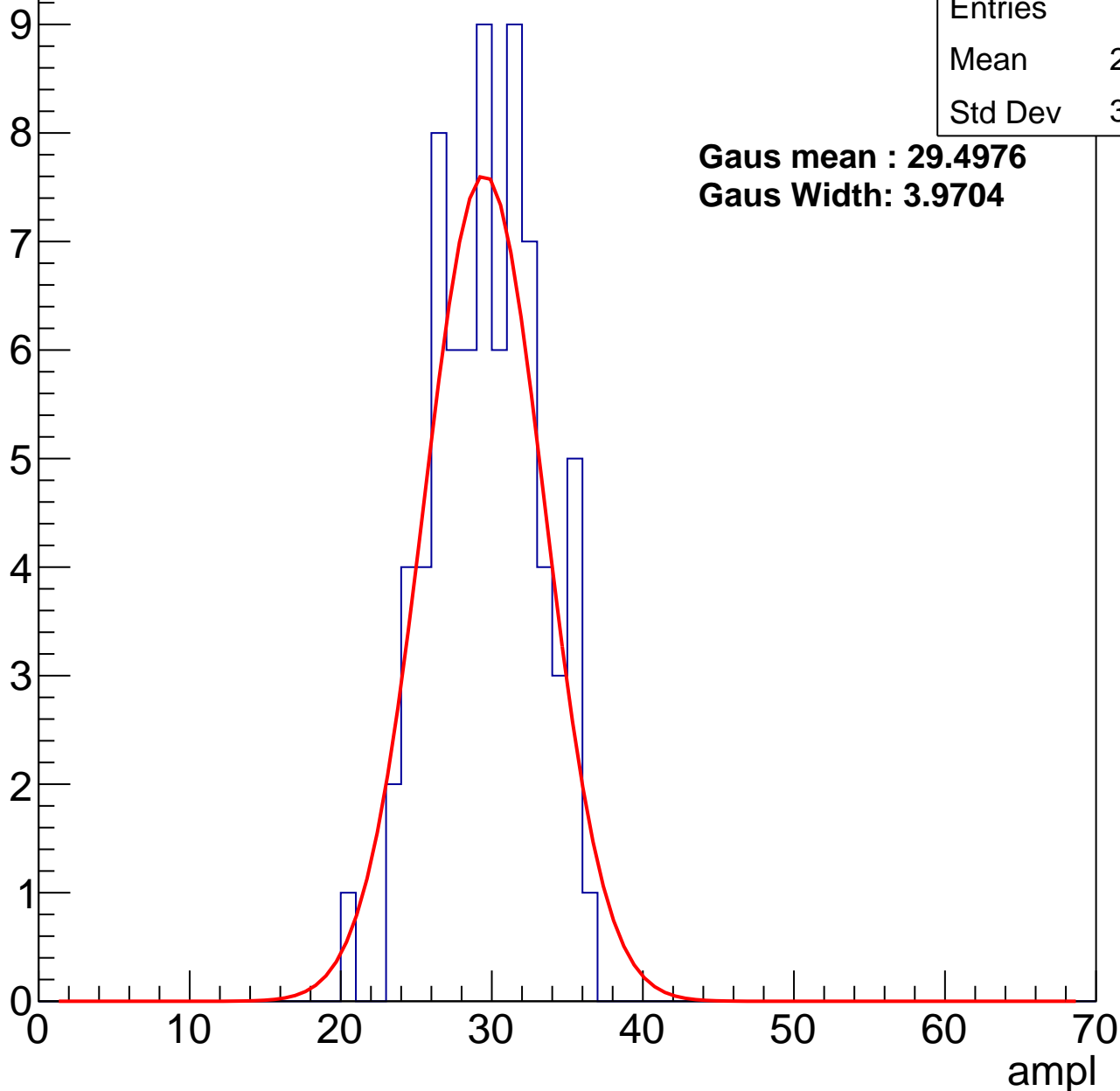
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	29.19
Std Dev	3.444

**Gaus mean : 29.4976**

**Gaus Width: 3.9704**



# B1L000S, U25-ch32, adc1

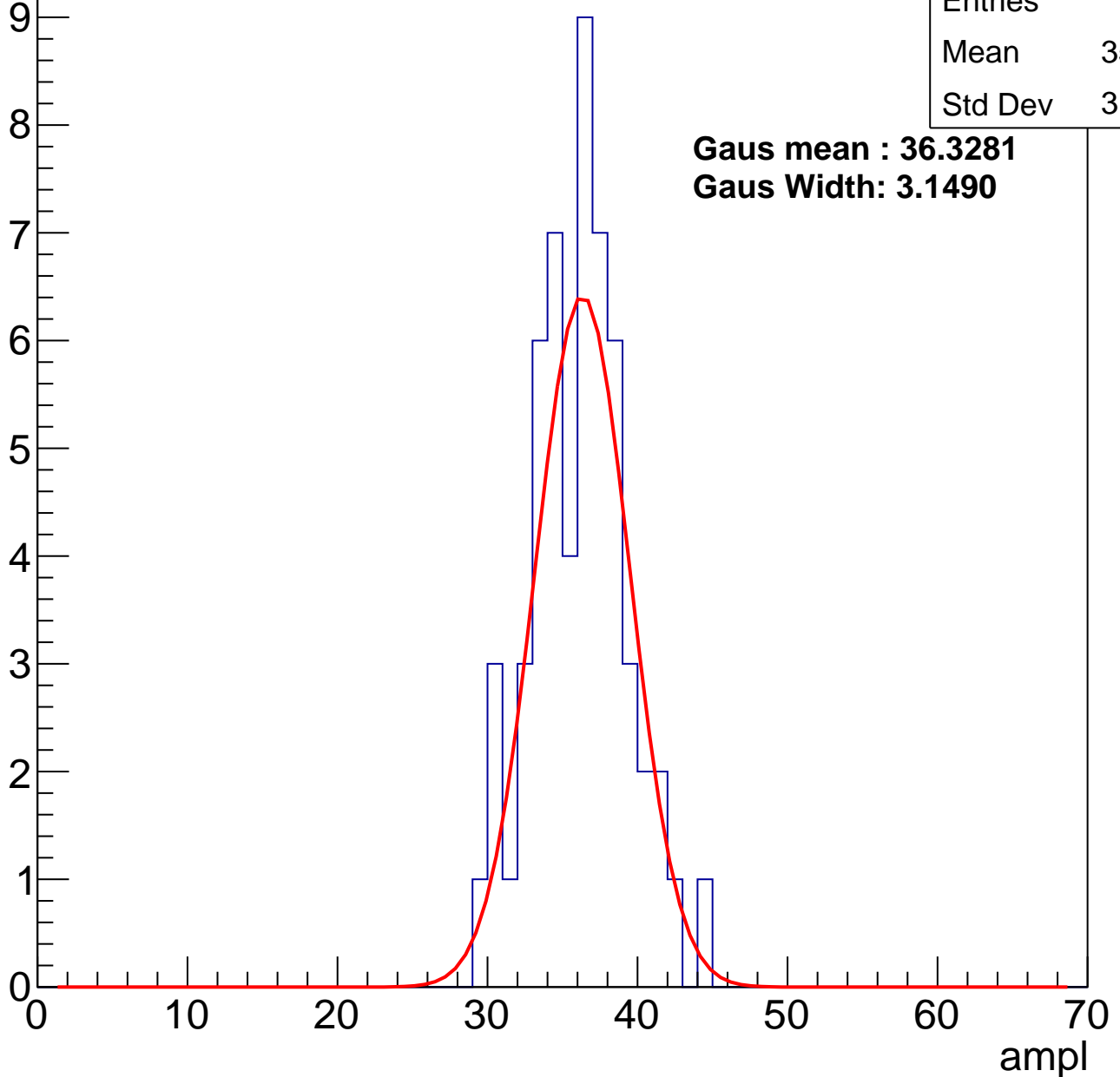
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.68
Std Dev	3.135

**Gaus mean : 36.3281**

**Gaus Width: 3.1490**



# B1L000S, U25-ch32, adc2

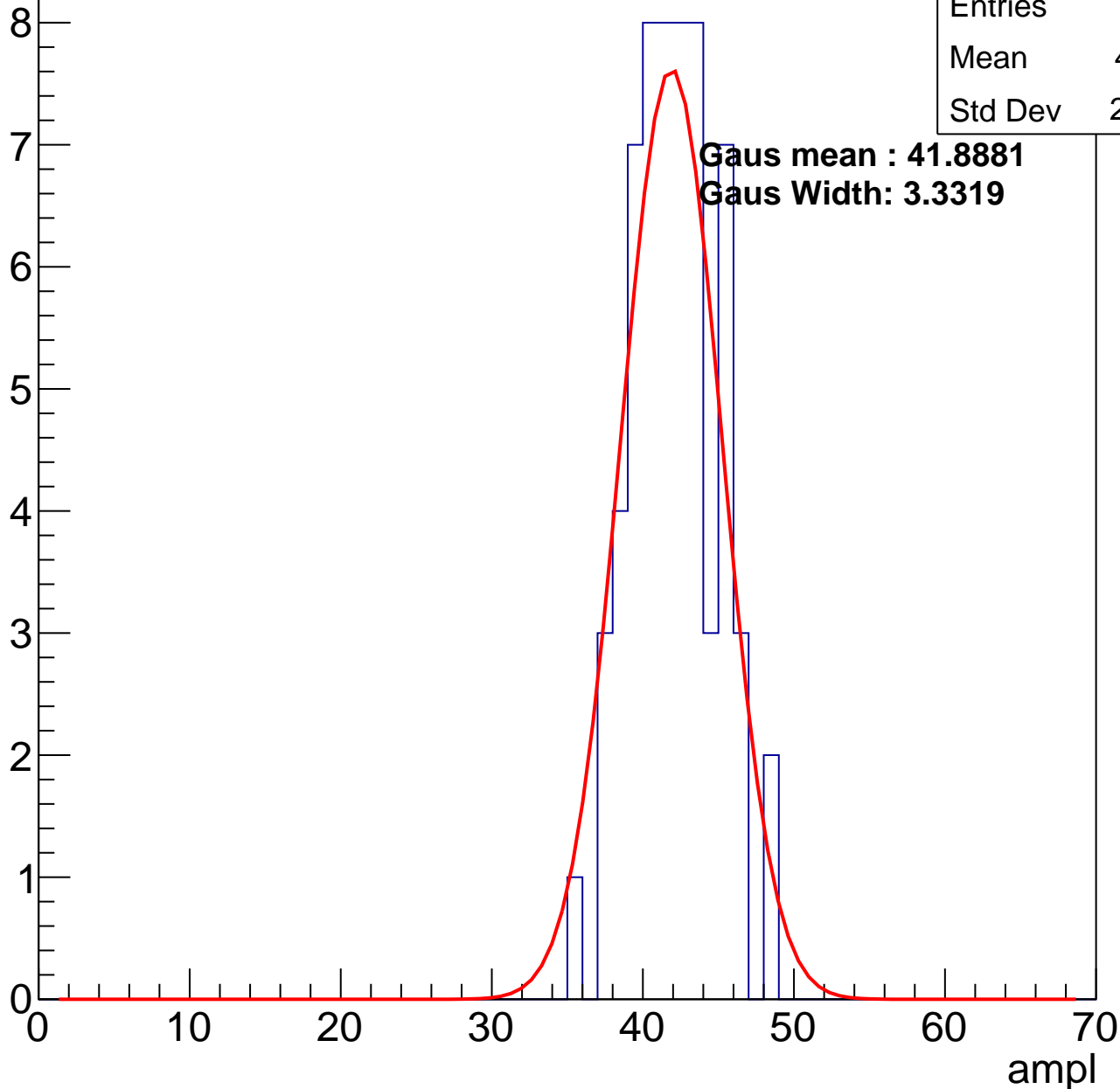
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.61
Std Dev	2.796

**Gaus mean : 41.8881**

**Gaus Width: 3.3319**



# B1L000S, U25-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

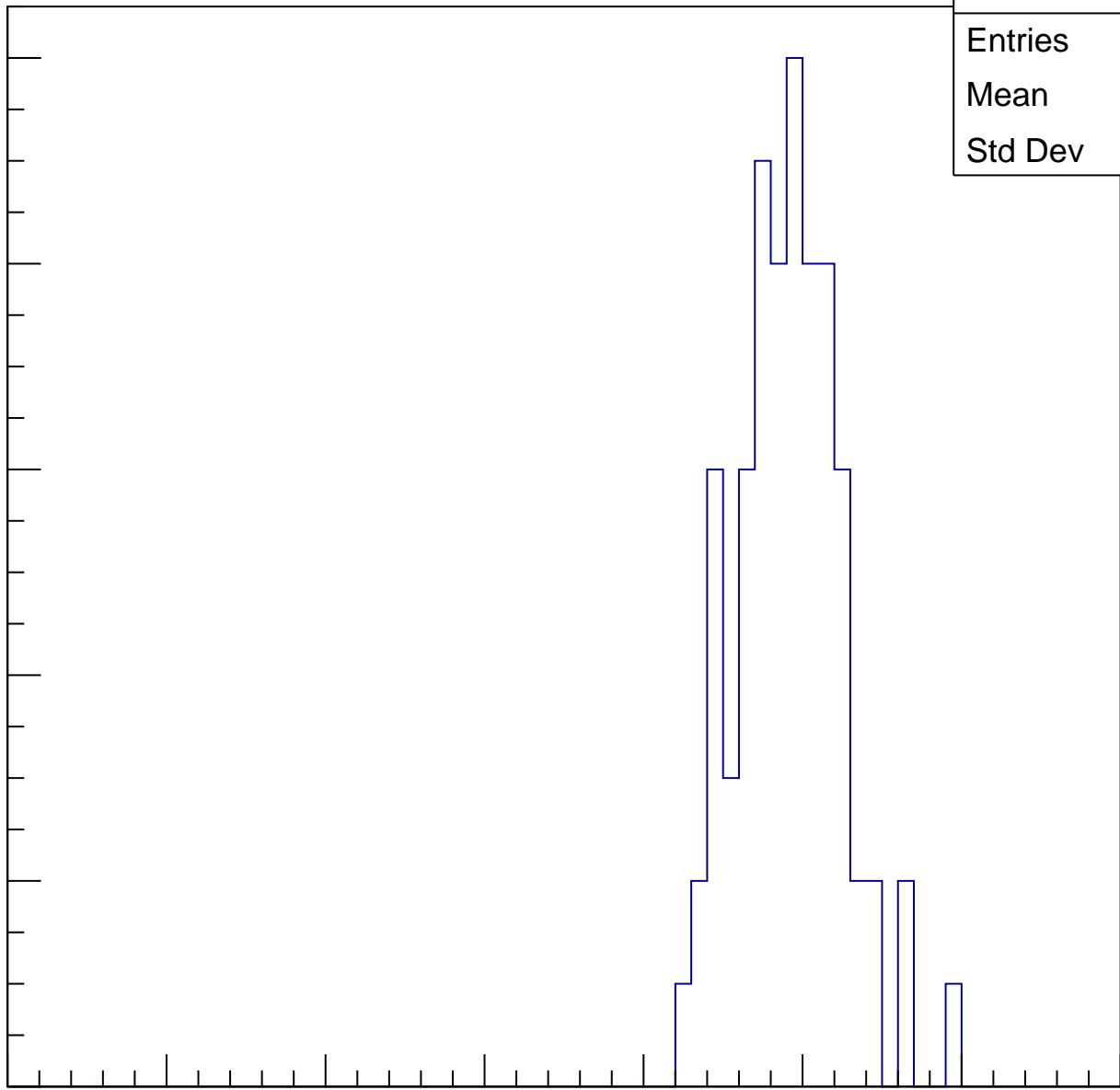
Entries	74
Mean	48.72
Std Dev	3.257

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

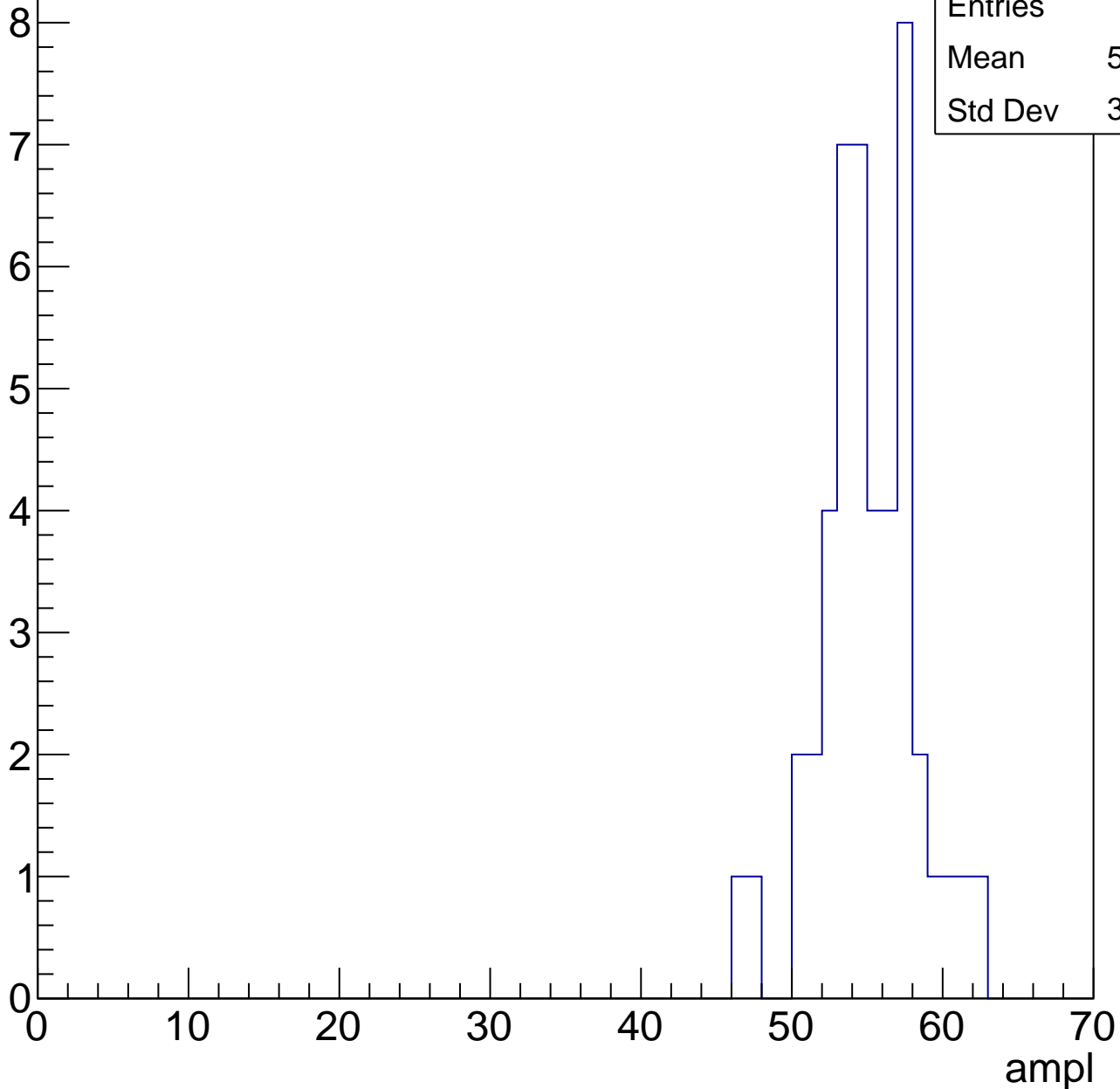


# B1L000S, U25-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	54.57
Std Dev	3.194

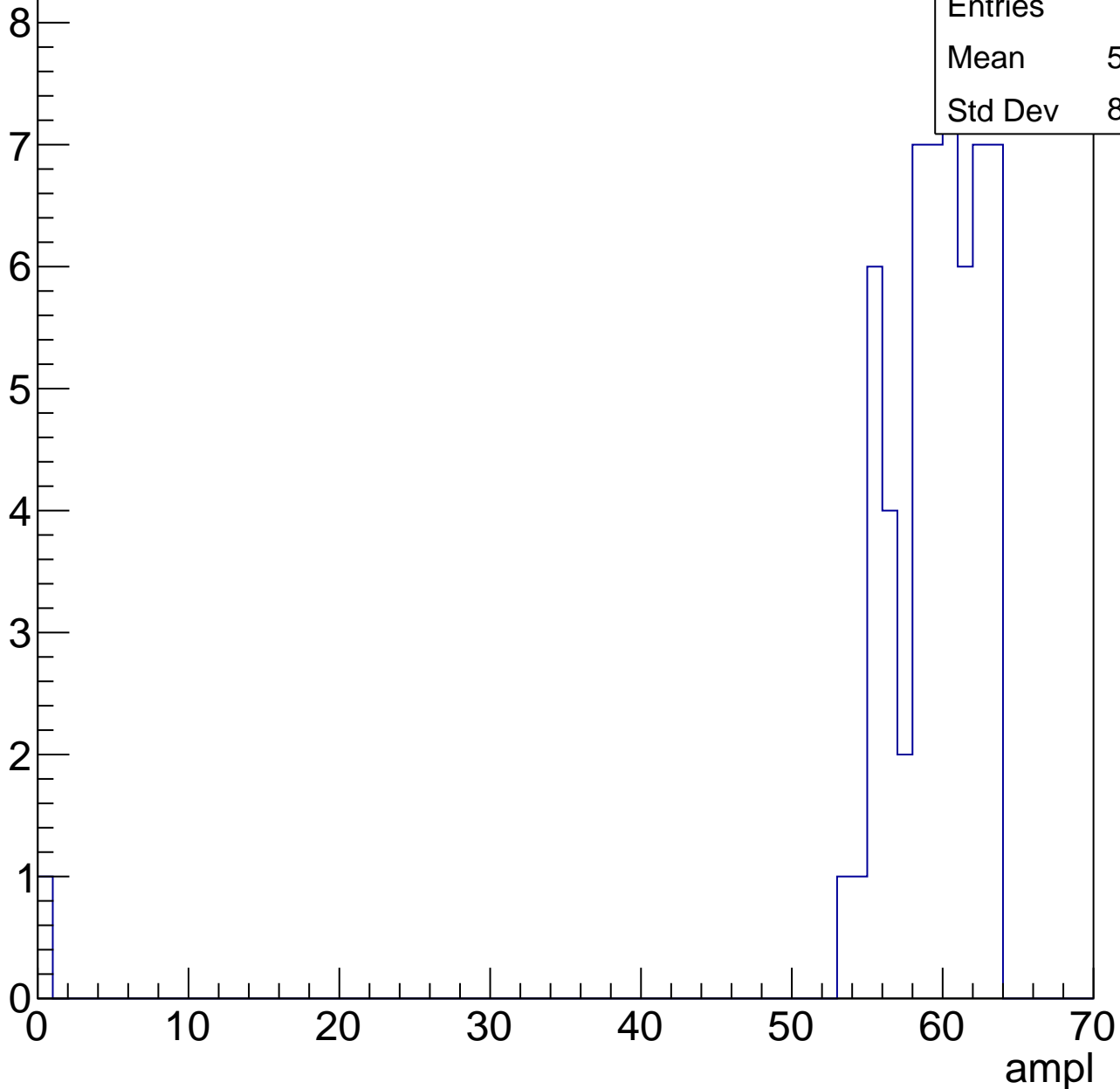


# B1L000S, U25-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	58.16
Std Dev	8.224



# B1L000S, U25-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.18
Std Dev	1.585



# B1L000S, U25-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch33, adc0

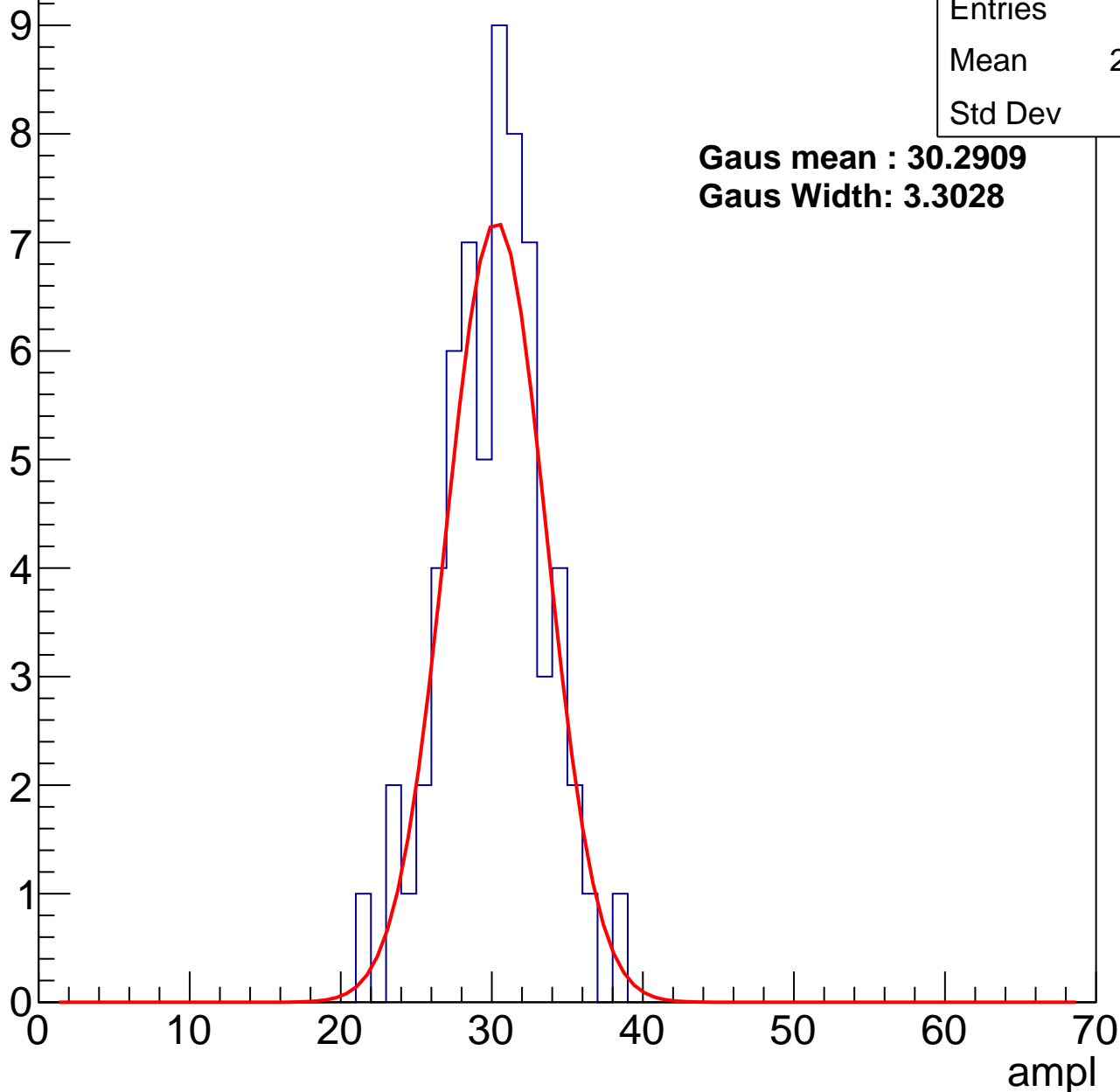
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	29.67
Std Dev	3.3

**Gaus mean : 30.2909**

**Gaus Width: 3.3028**



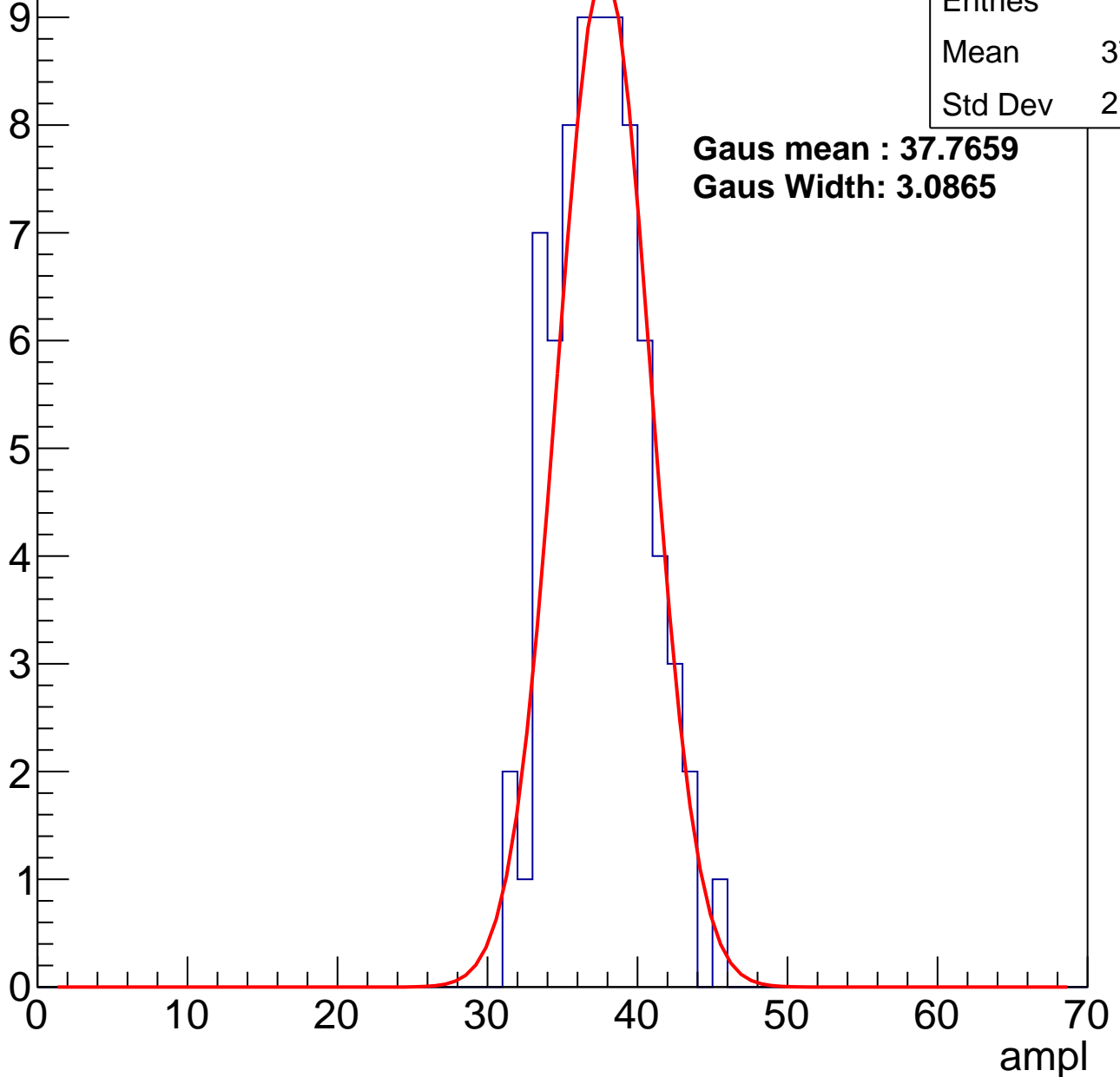
# B1L000S, U25-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	37.08
Std Dev	2.997

**Gaus mean : 37.7659**  
**Gaus Width: 3.0865**



# B1L000S, U25-ch33, adc2

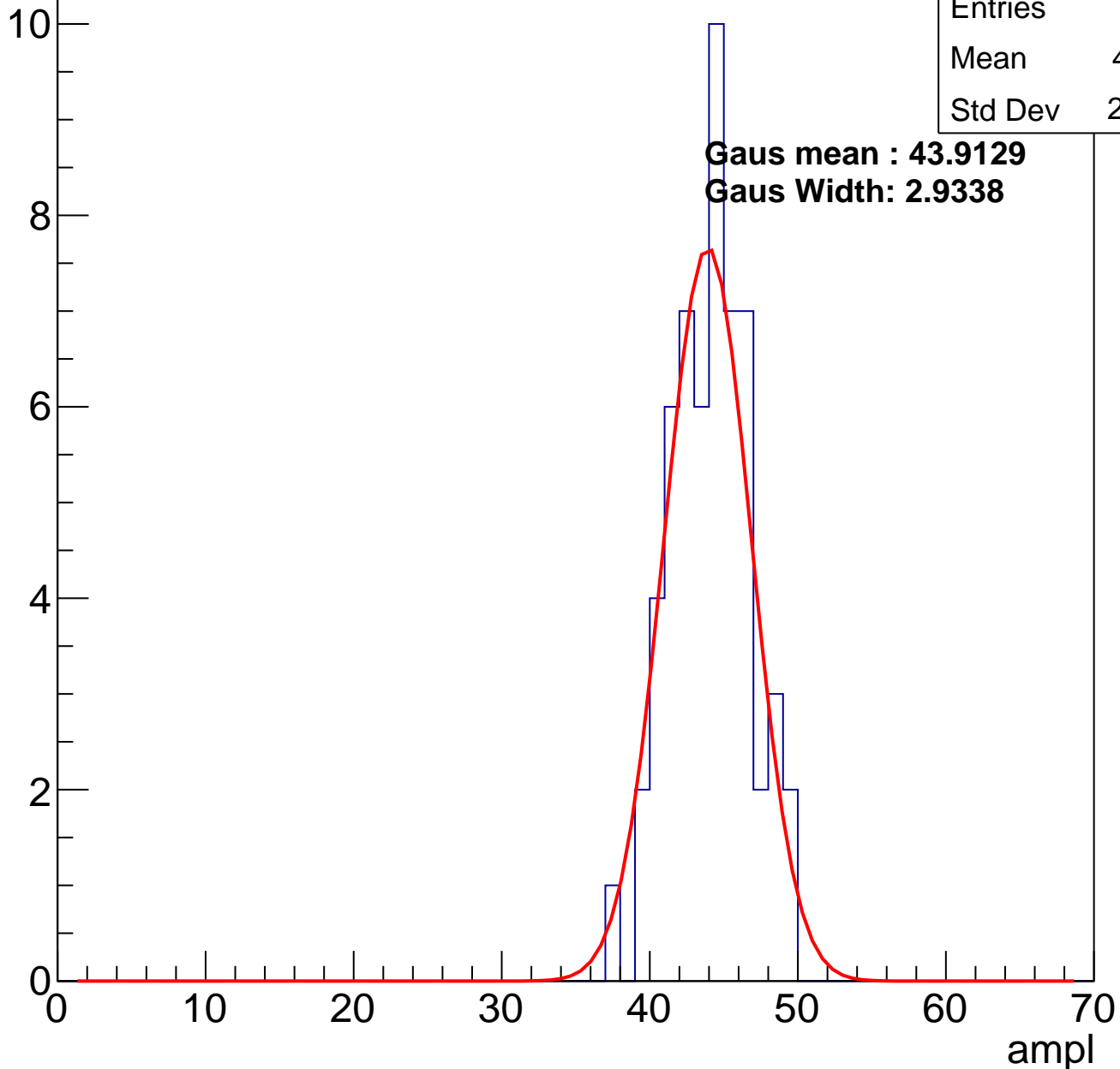
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	43.61
Std Dev	2.627

**Gaus mean : 43.9129**

**Gaus Width: 2.9338**

Entry

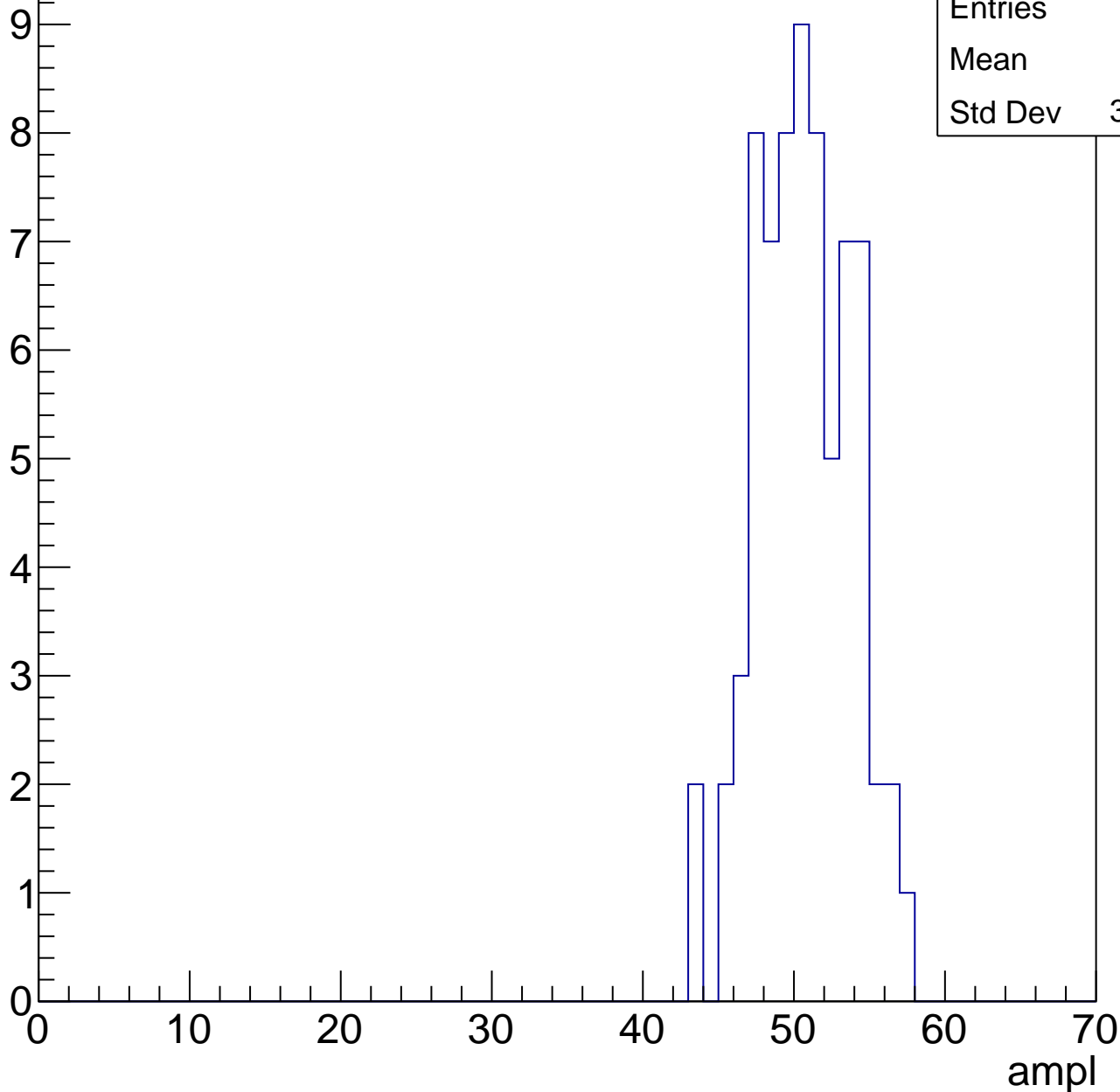


# B1L000S, U25-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	50.2
Std Dev	3.084

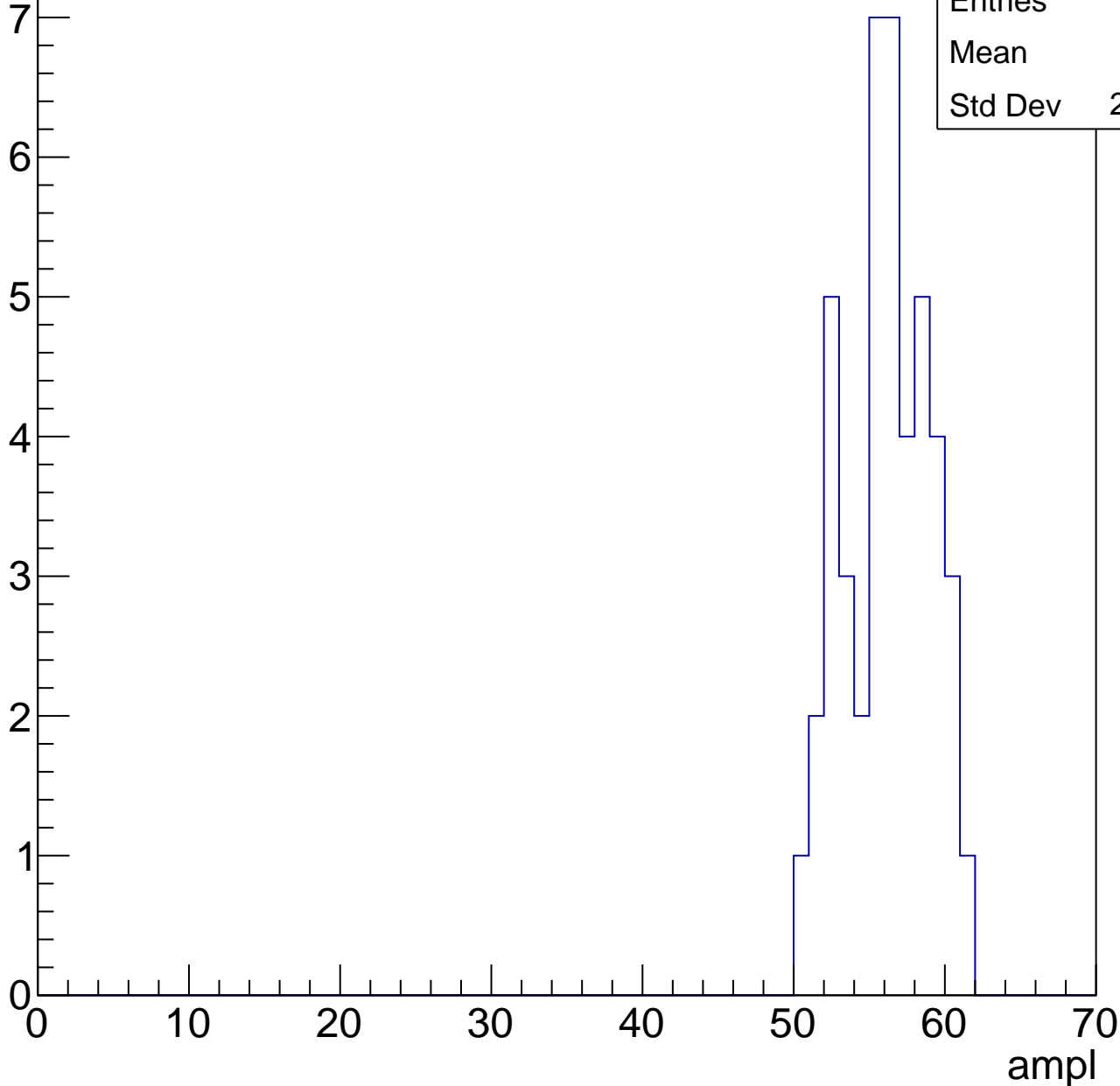


# B1L000S, U25-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

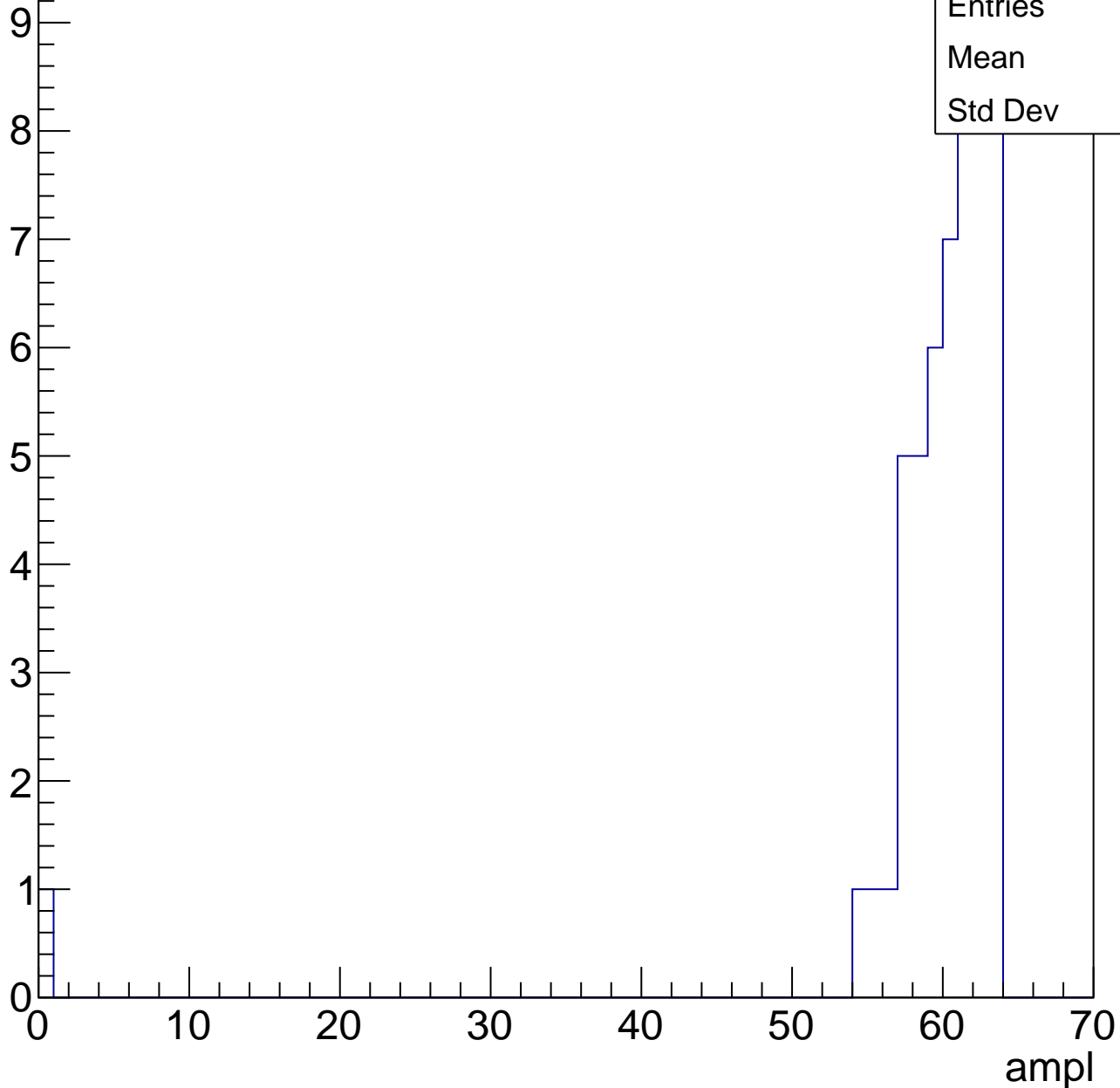
Entries	44
Mean	55.7
Std Dev	2.768



# B1L000S, U25-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch34, adc0

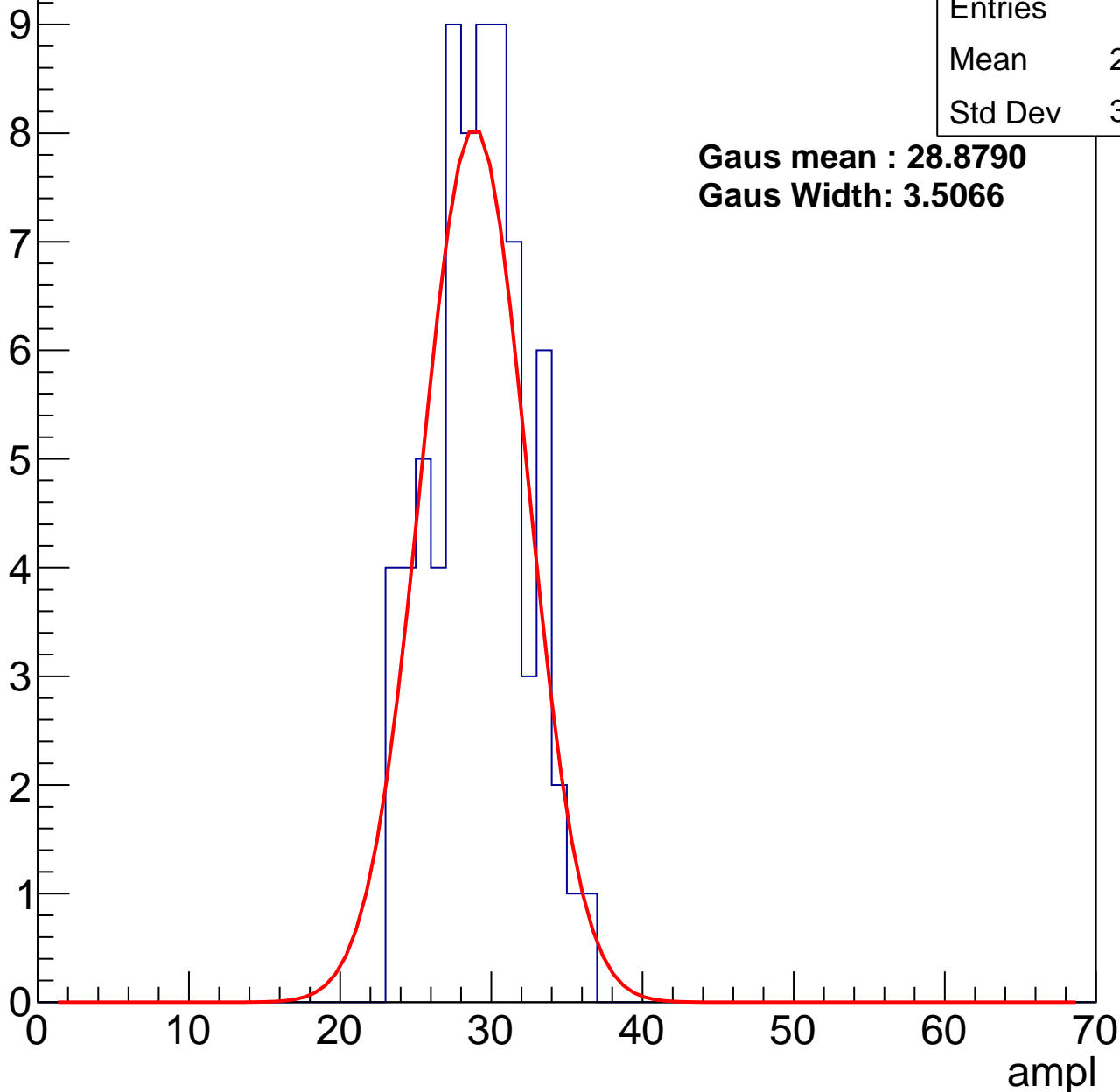
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	28.68
Std Dev	3.099

**Gaus mean : 28.8790**

**Gaus Width: 3.5066**

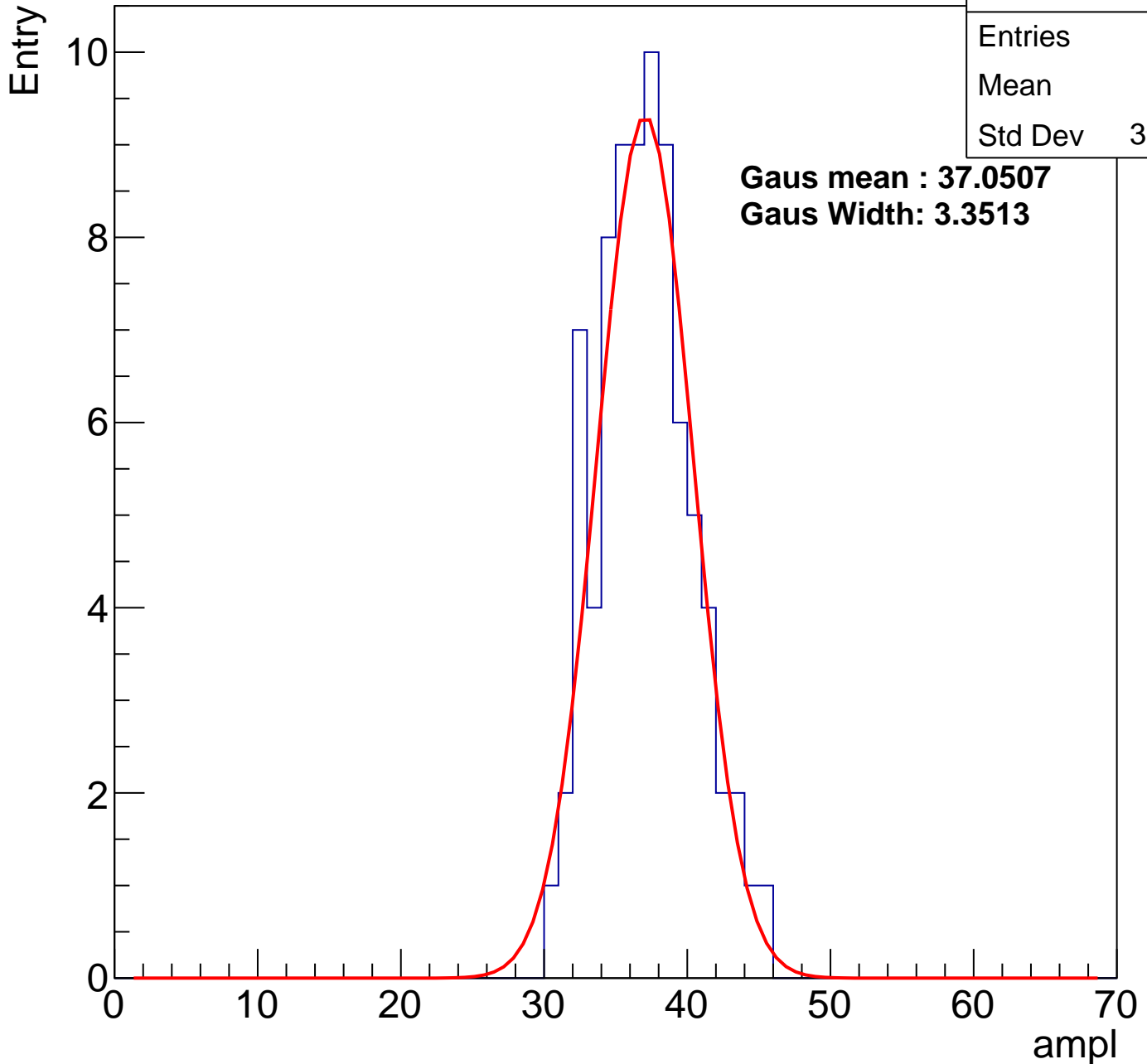


# B1L000S, U25-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	36.6
Std Dev	3.243

**Gaus mean : 37.0507**  
**Gaus Width: 3.3513**



# B1L000S, U25-ch34, adc2

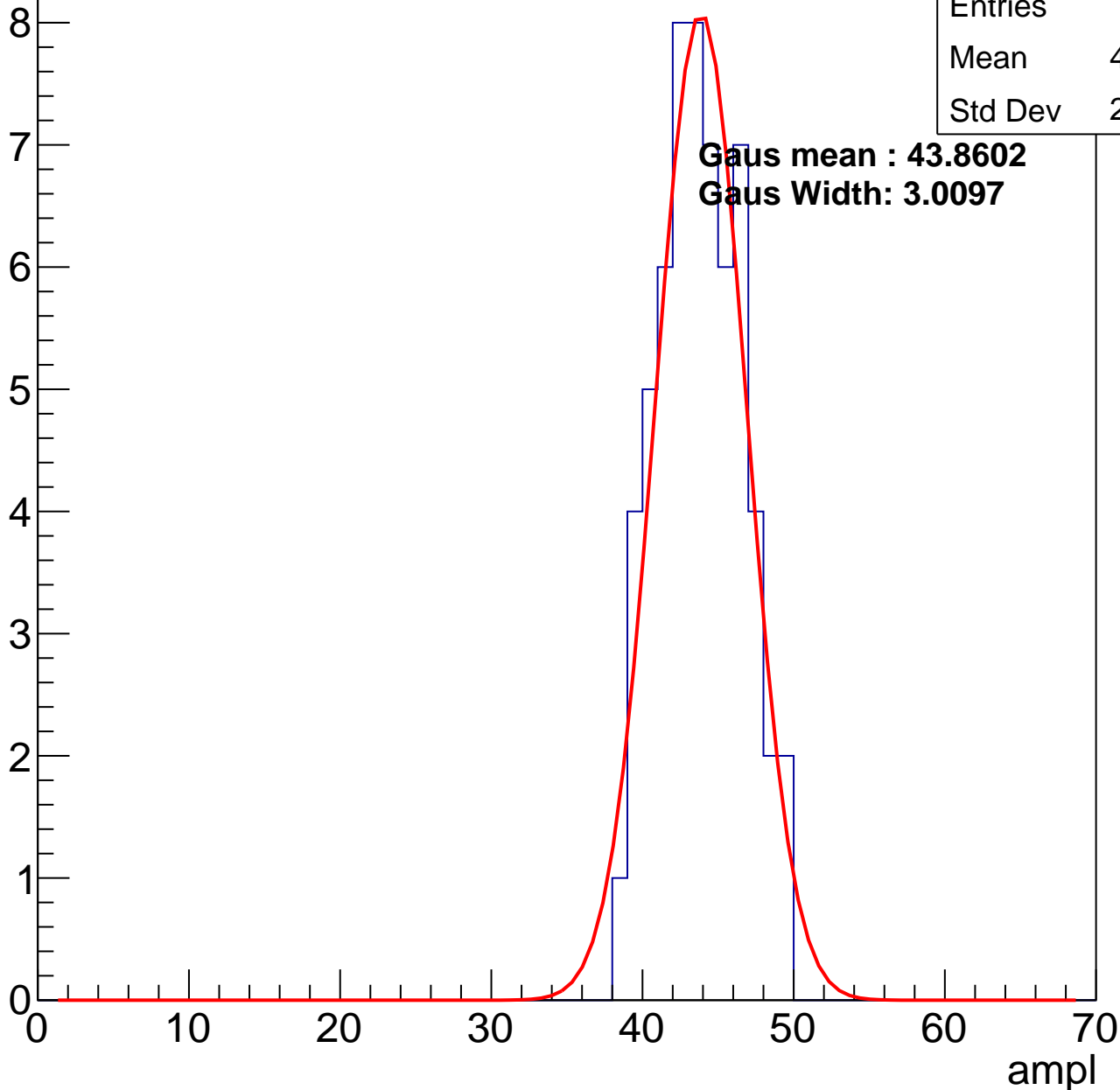
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	43.37
Std Dev	2.702

**Gaus mean : 43.8602**

**Gaus Width: 3.0097**

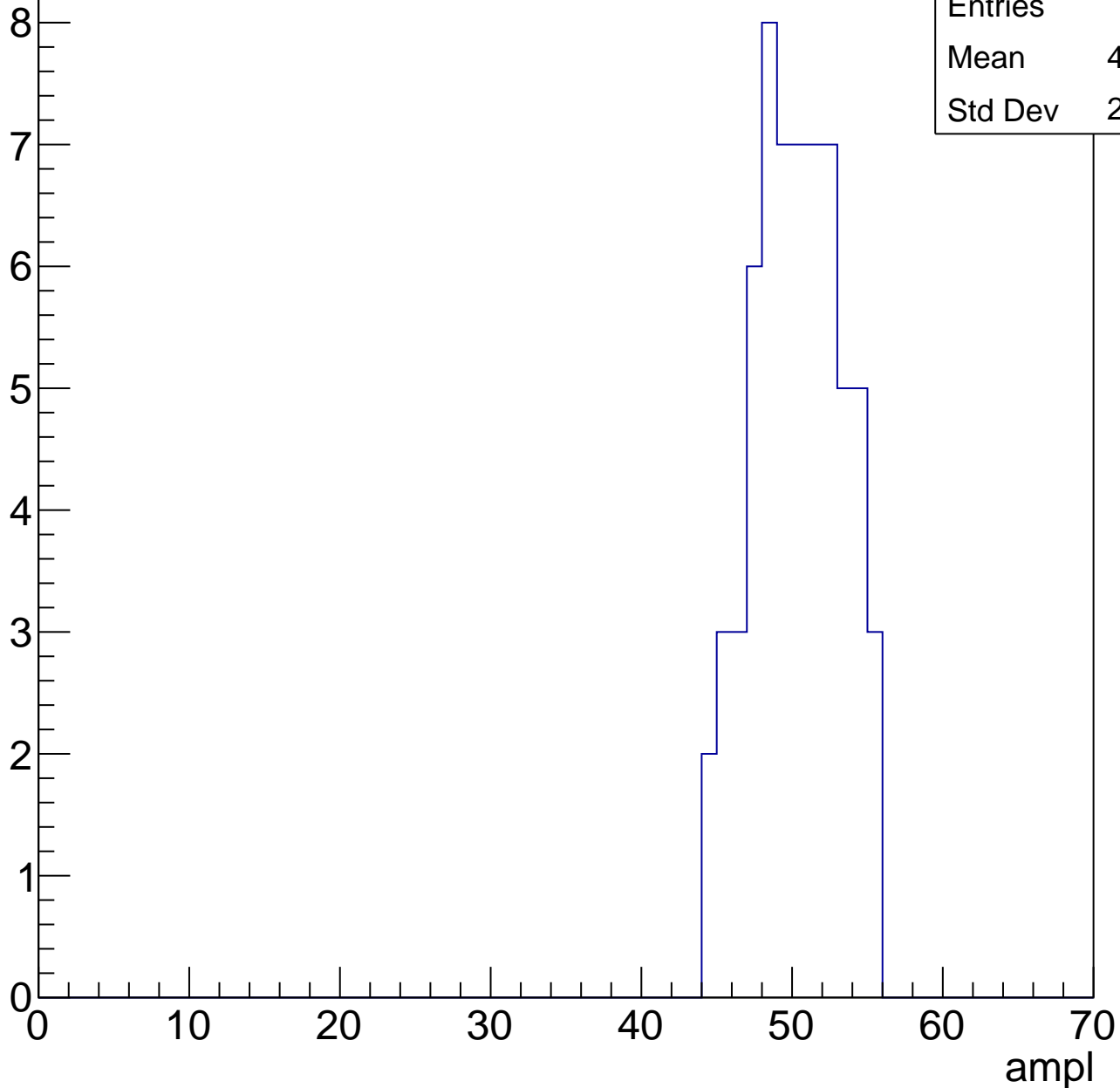


# B1L000S, U25-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	49.86
Std Dev	2.878

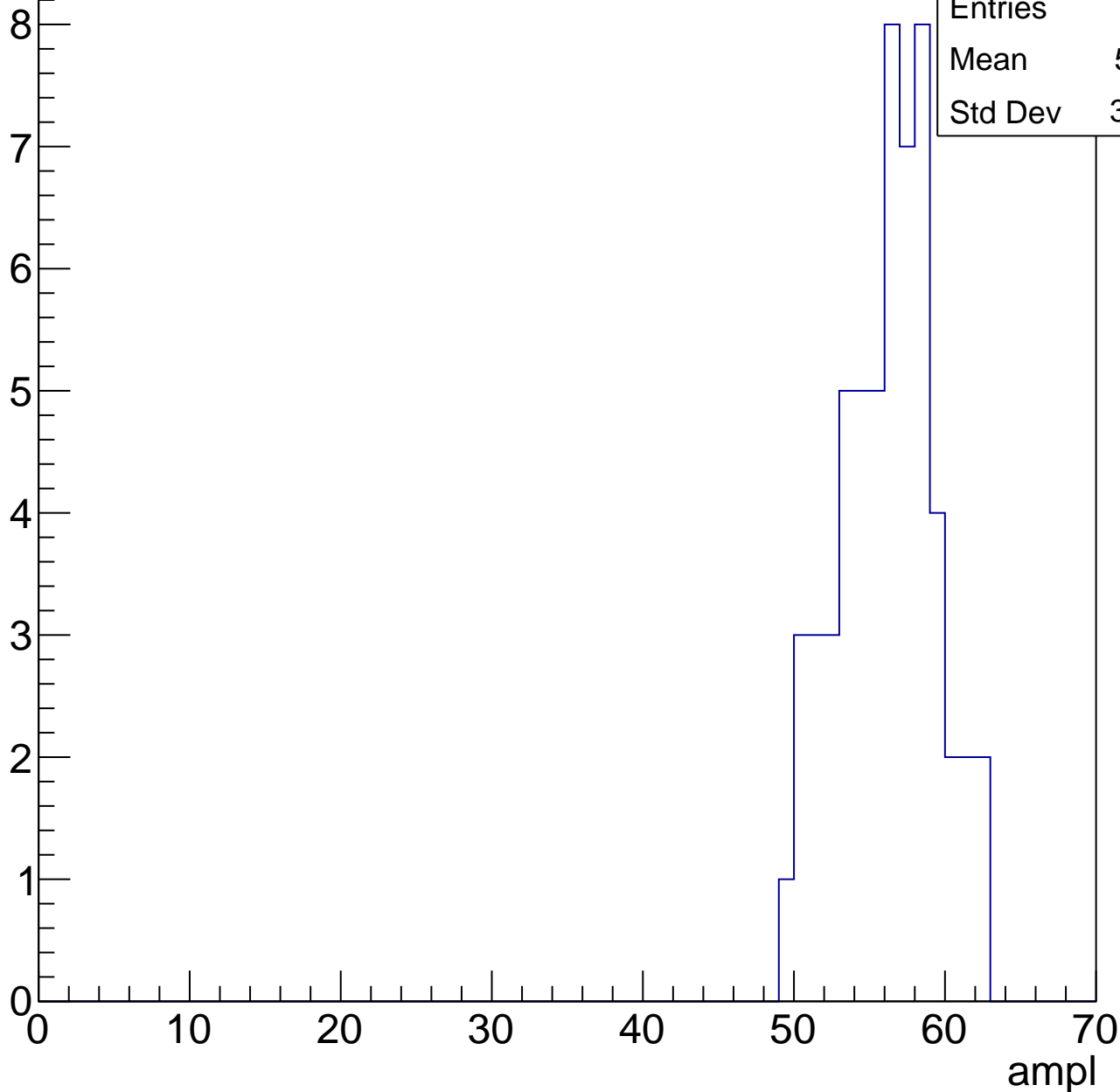


# B1L000S, U25-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	55.71
Std Dev	3.146



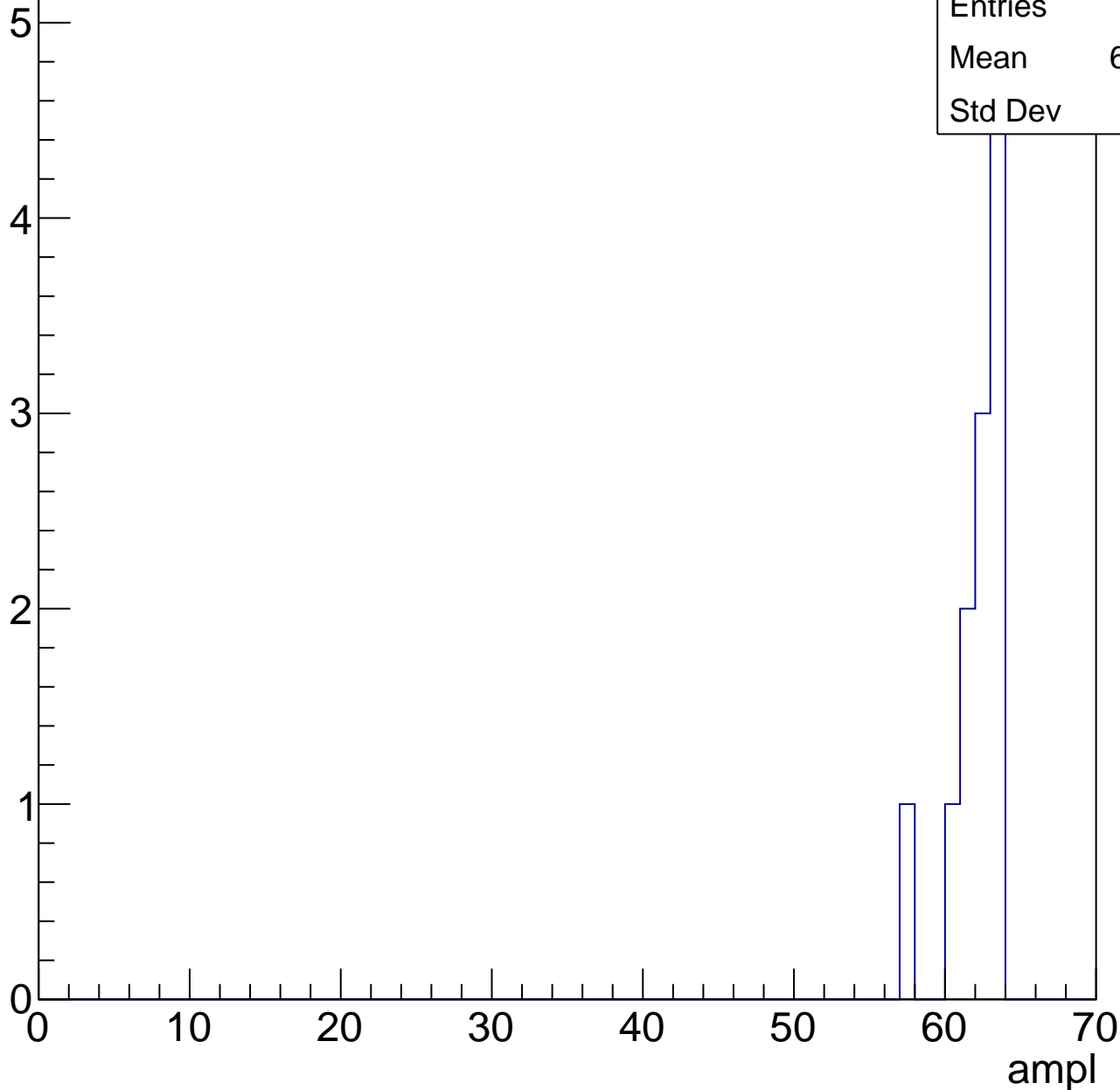


# B1L000S, U25-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.67
Std Dev	1.7





# B1L000S, U25-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch35, adc0

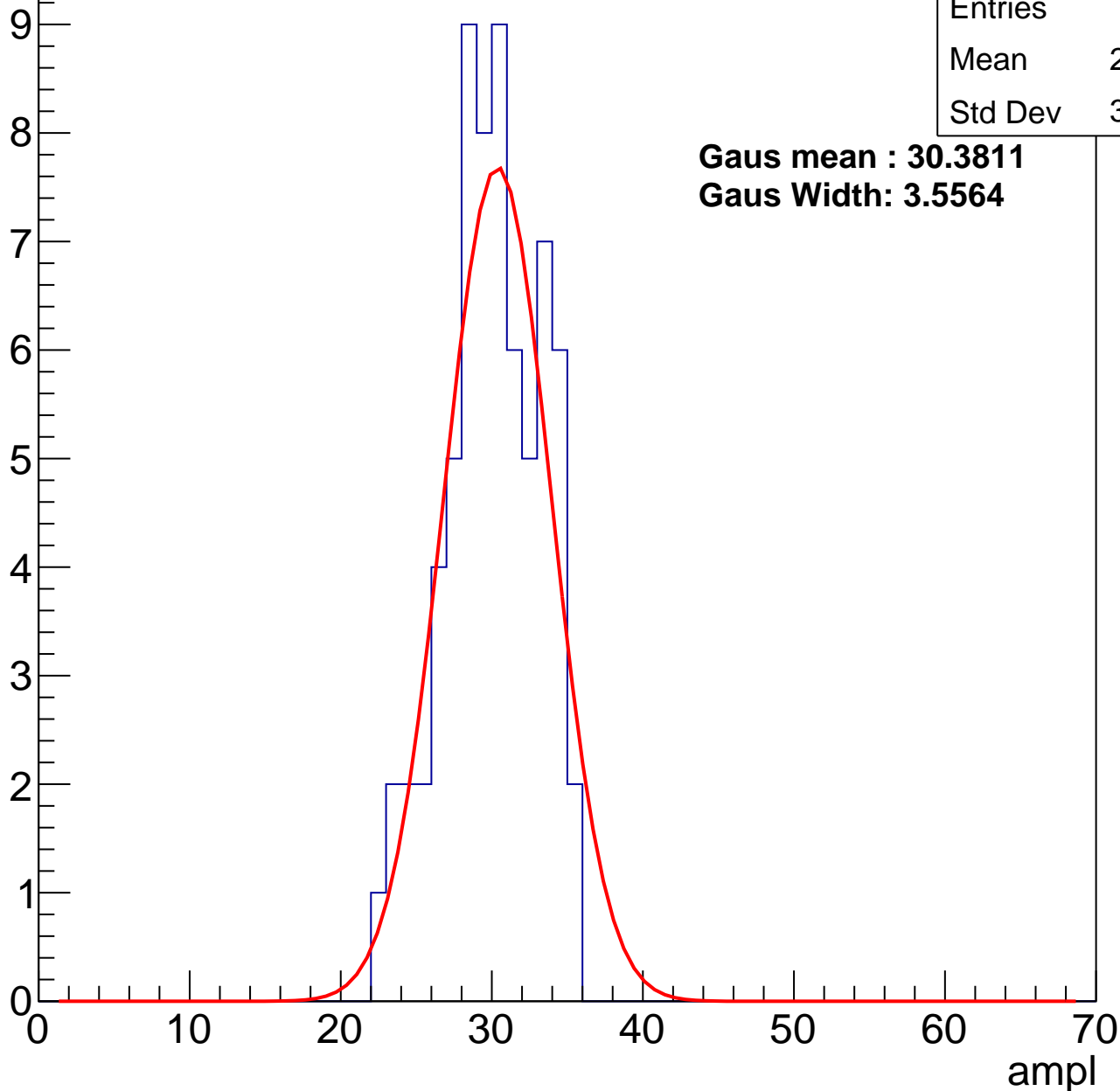
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.56
Std Dev	3.113

**Gaus mean : 30.3811**

**Gaus Width: 3.5564**



# B1L000S, U25-ch35, adc1

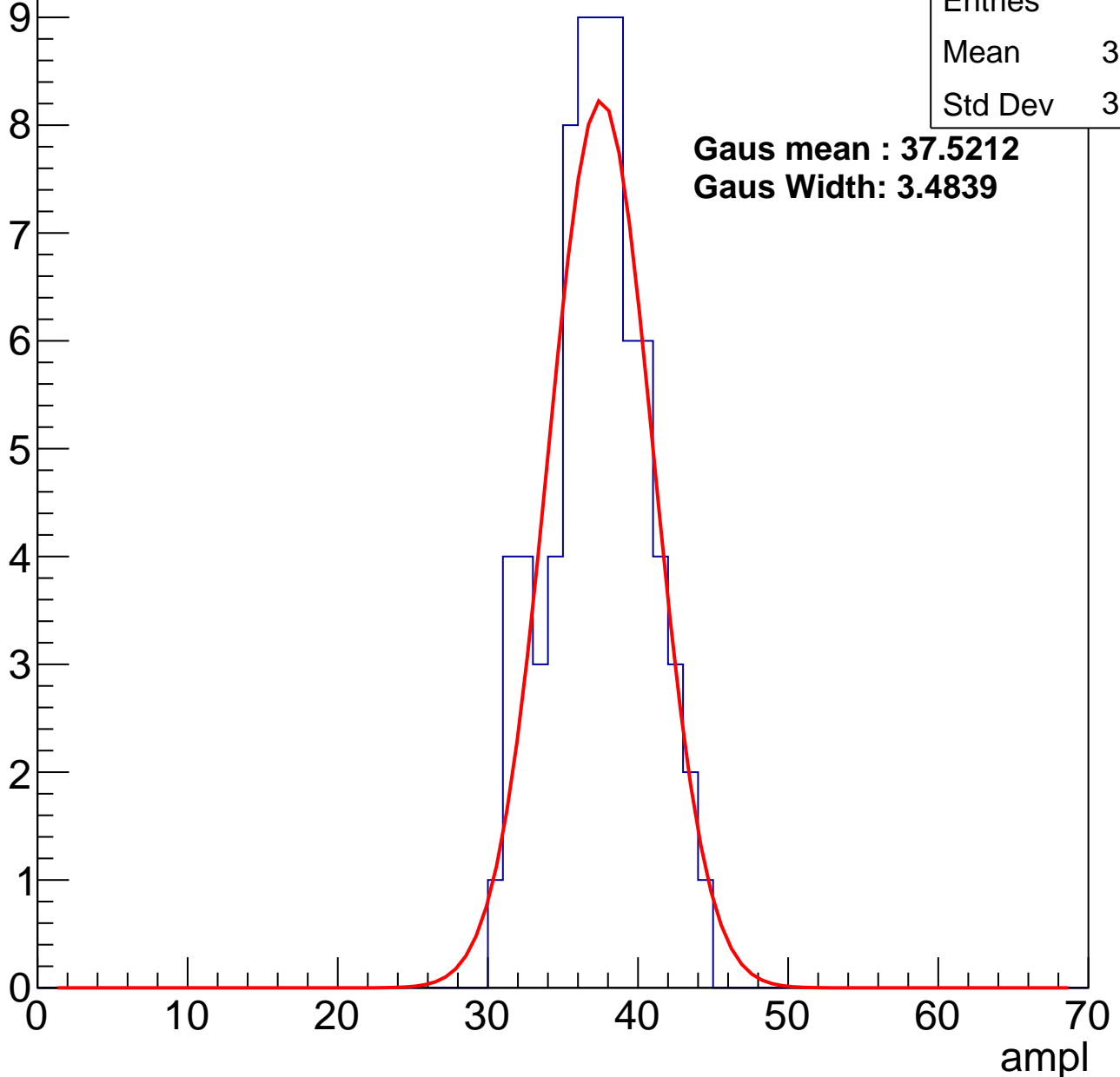
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	36.85
Std Dev	3.234

**Gaus mean : 37.5212**

**Gaus Width: 3.4839**



# B1L000S, U25-ch35, adc2

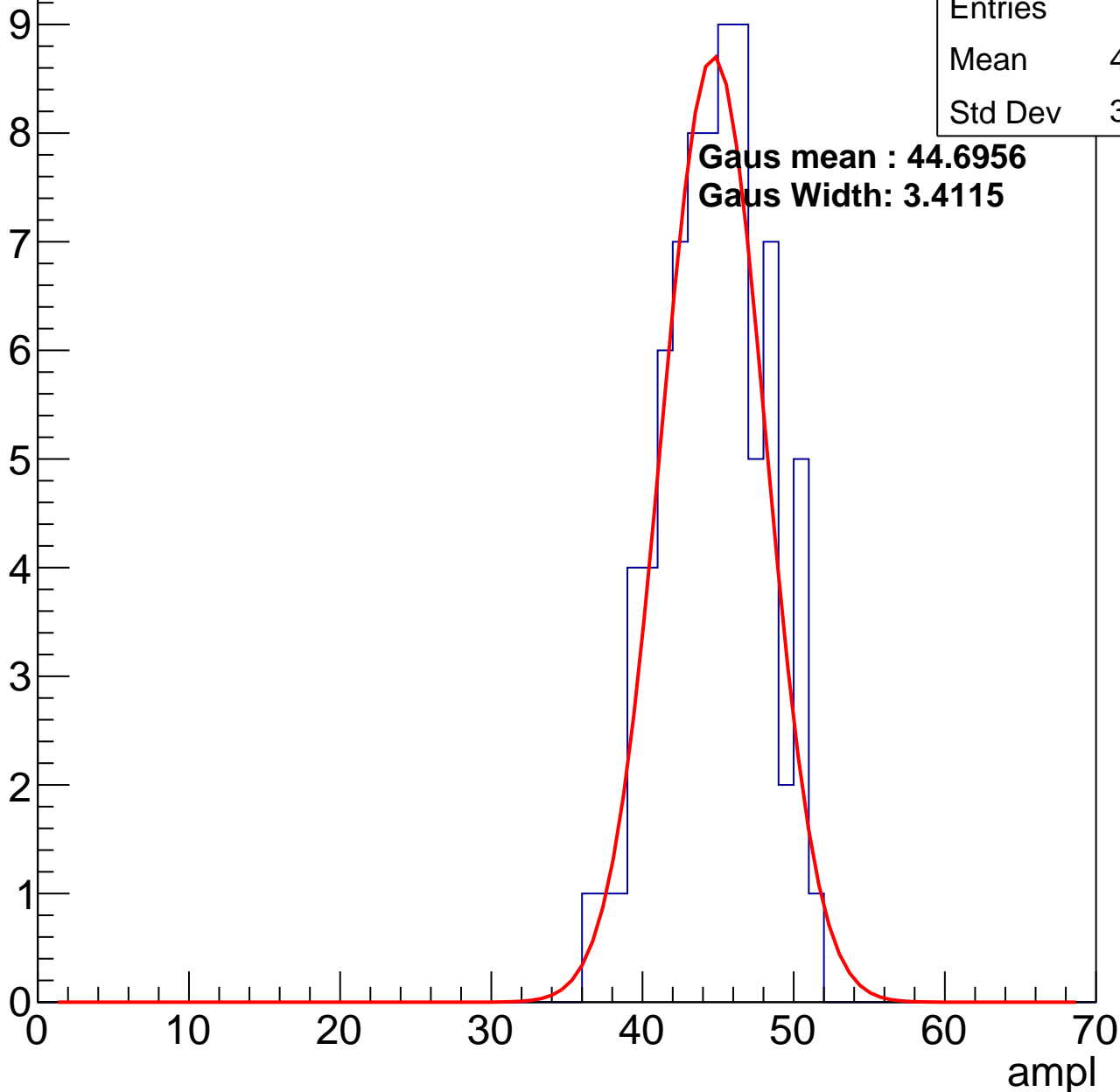
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	44.26
Std Dev	3.357

**Gaus mean : 44.6956**

**Gaus Width: 3.4115**

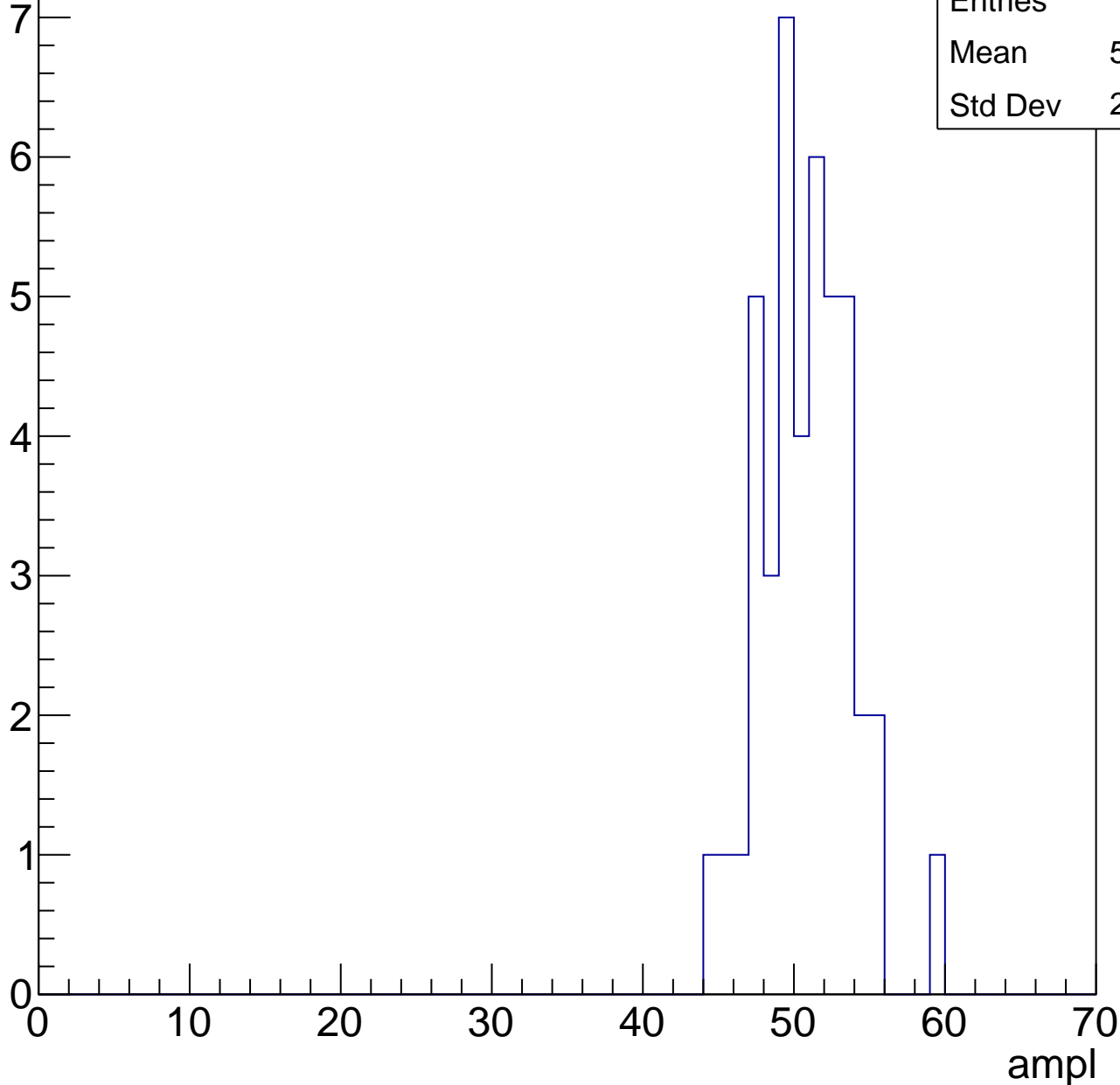


# B1L000S, U25-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

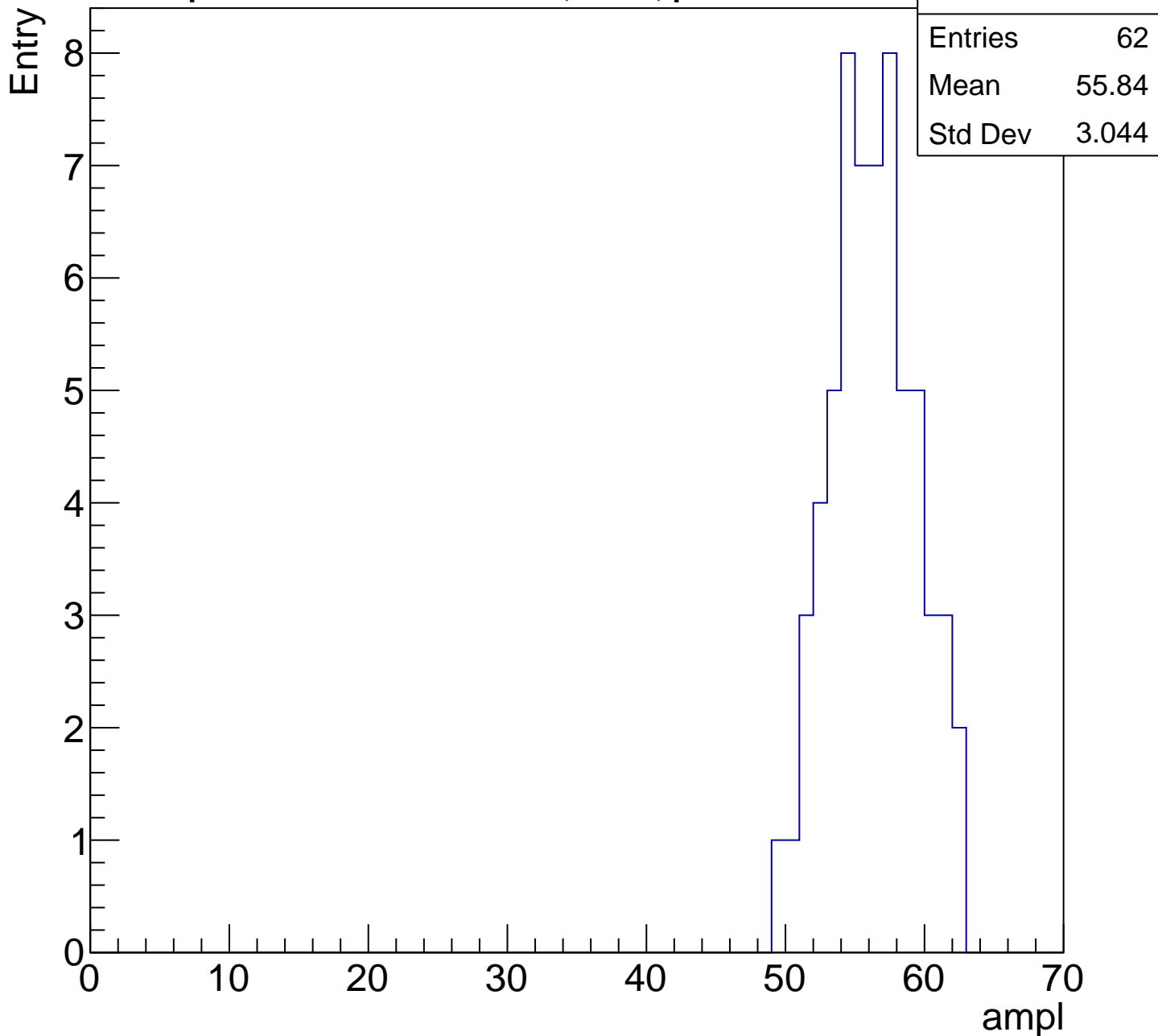
Entry

Entries	43
Mean	50.35
Std Dev	2.932



# B1L000S, U25-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

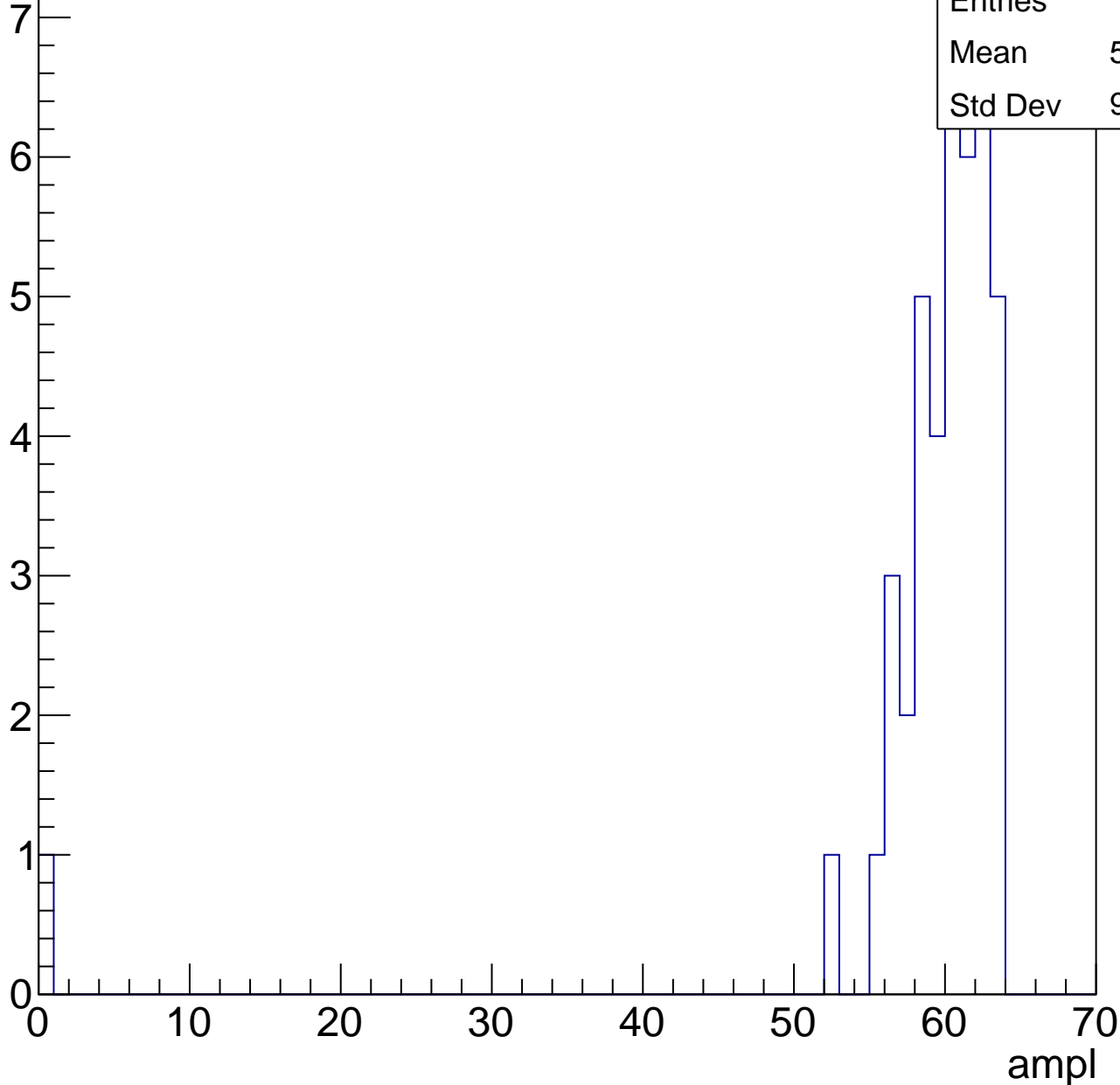


# B1L000S, U25-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	58.33
Std Dev	9.438

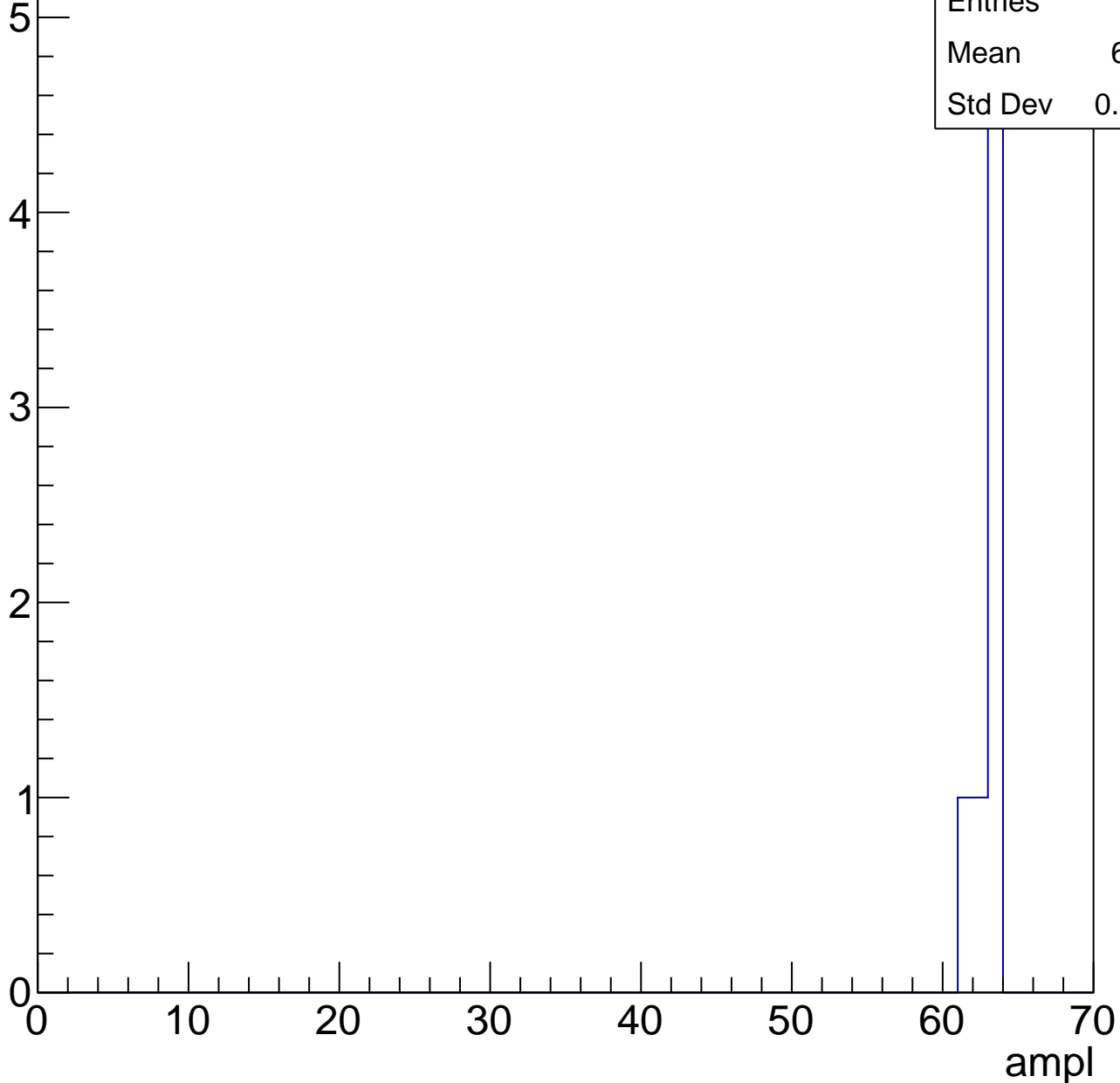


# B1L000S, U25-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	7
Mean	62.57
Std Dev	0.7284

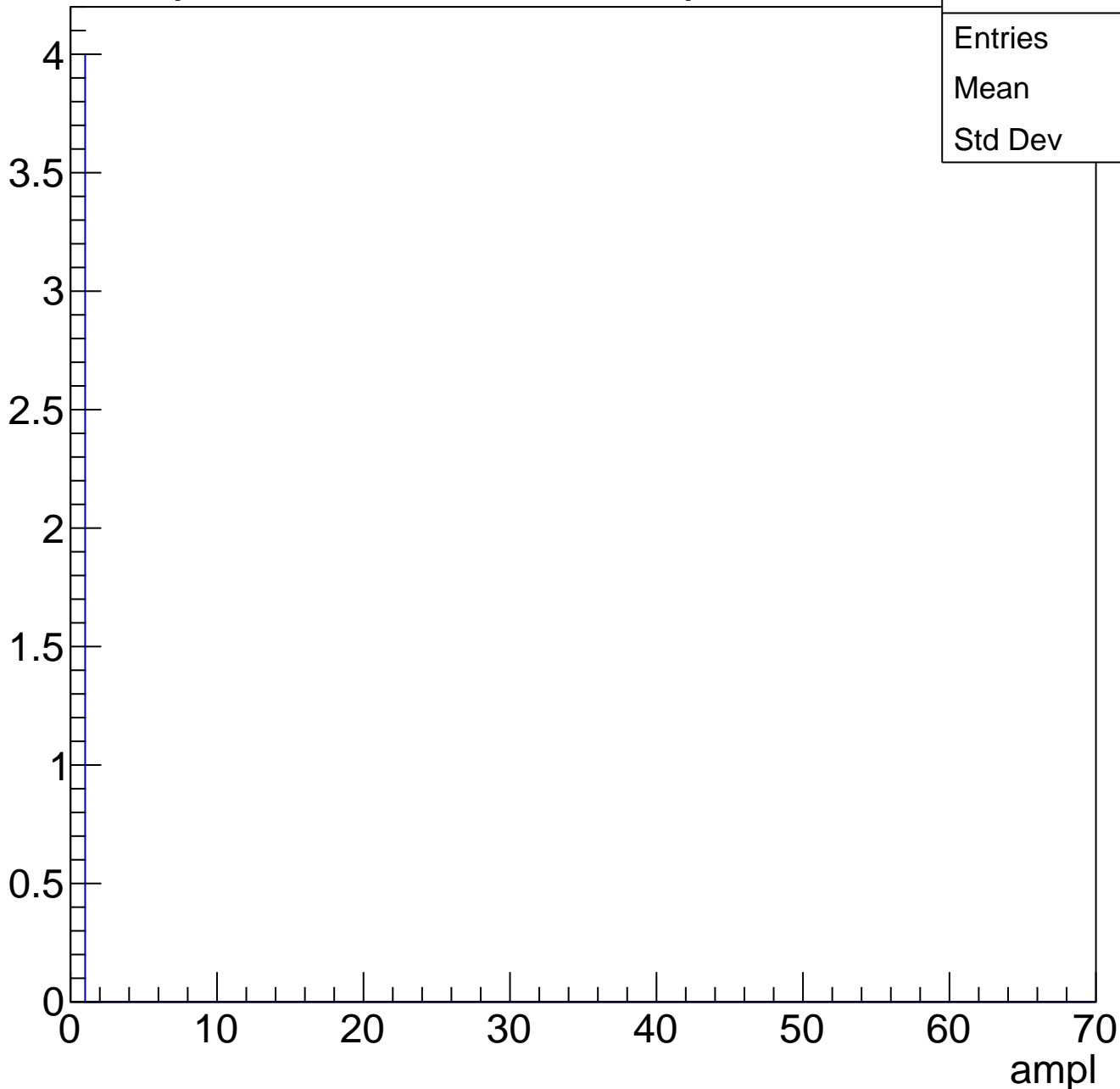




# B1L000S, U25-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	0
Std Dev	0

# B1L000S, U25-ch36, adc0

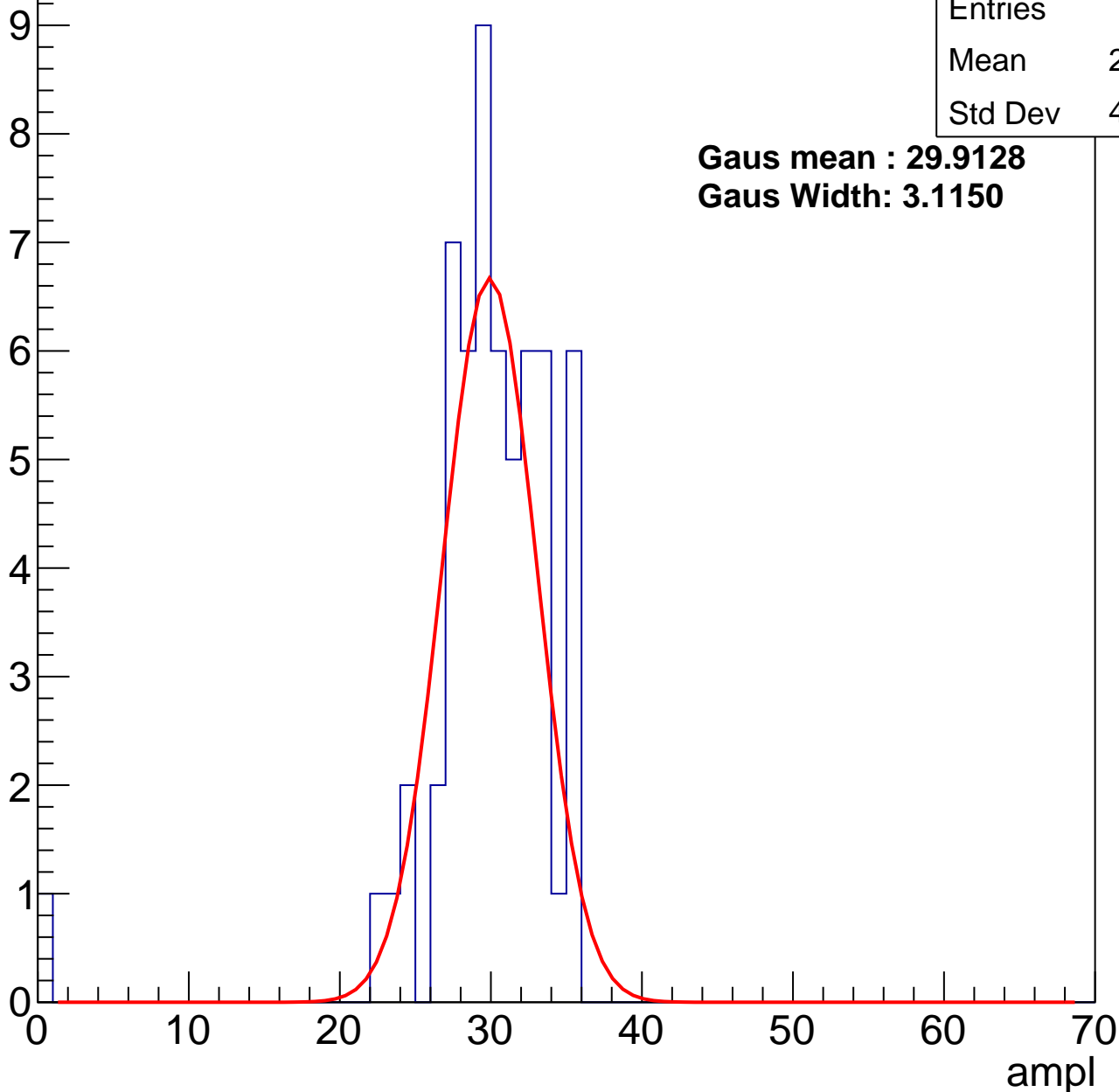
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	29.36
Std Dev	4.936

**Gaus mean : 29.9128**

**Gaus Width: 3.1150**



# B1L000S, U25-ch36, adc1

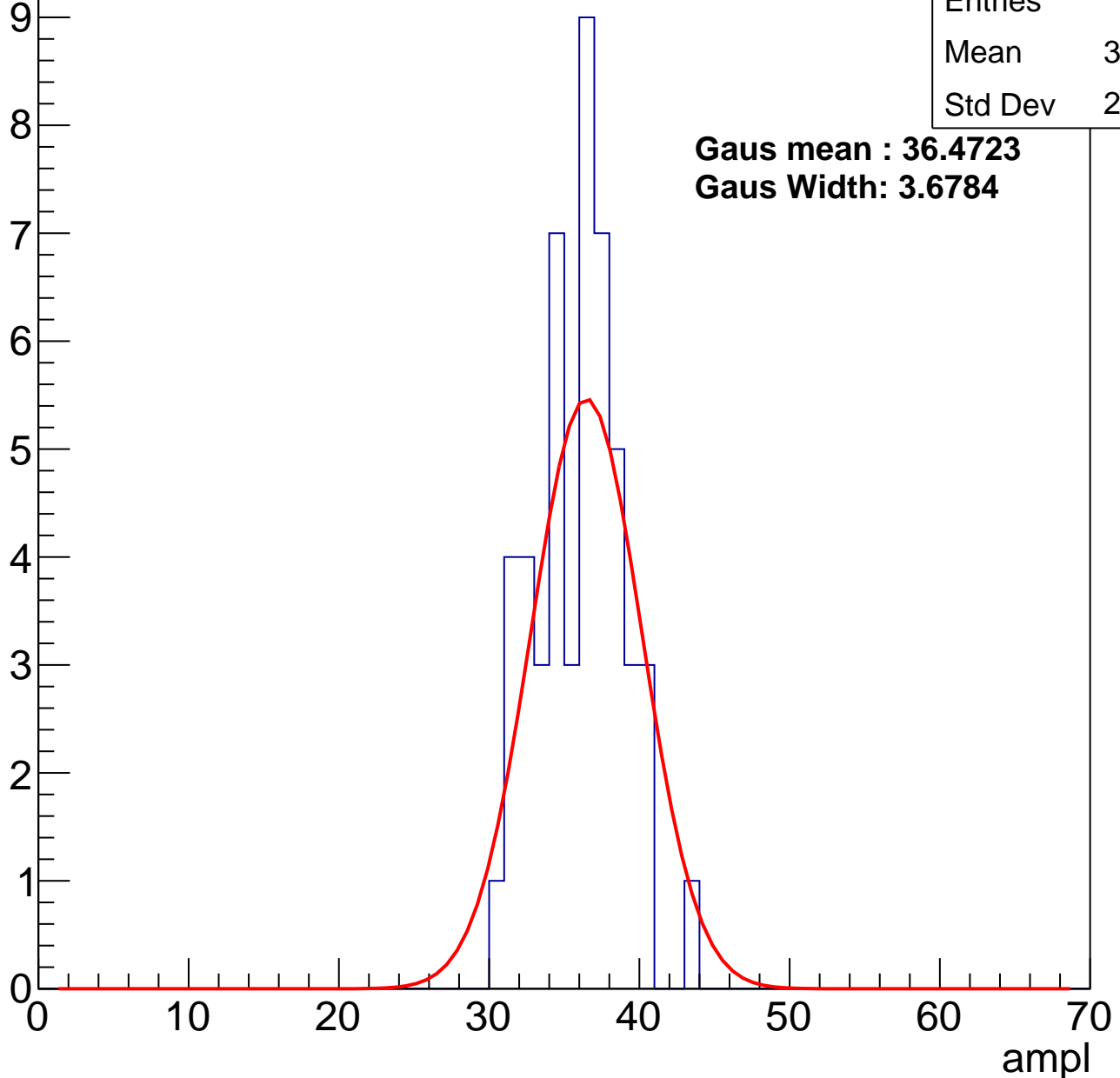
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	35.54
Std Dev	2.823

**Gaus mean : 36.4723**

**Gaus Width: 3.6784**



# B1L000S, U25-ch36, adc2

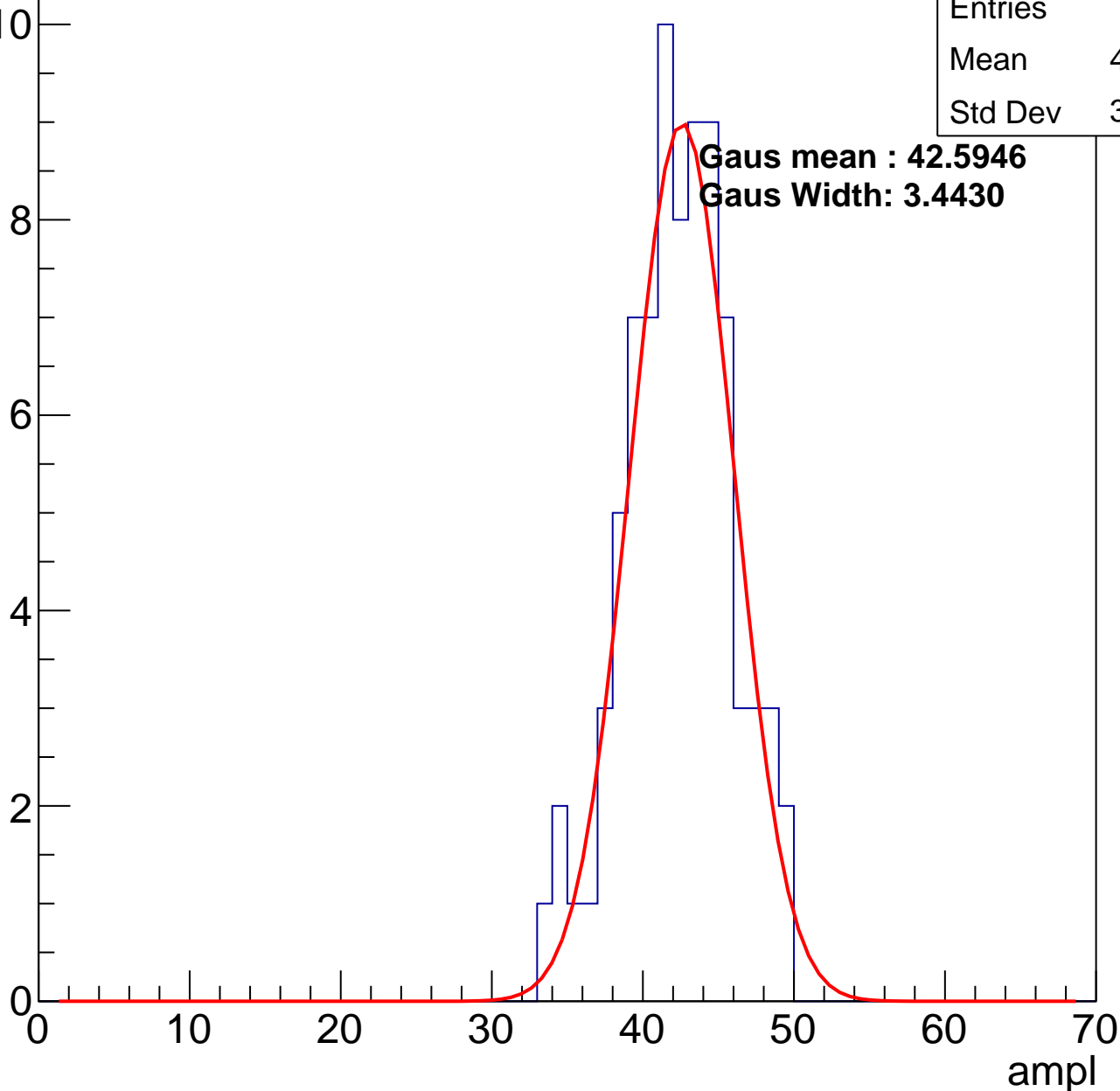
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	81
Mean	41.86
Std Dev	3.488

**Gaus mean : 42.5946**

**Gaus Width: 3.4430**



# B1L000S, U25-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

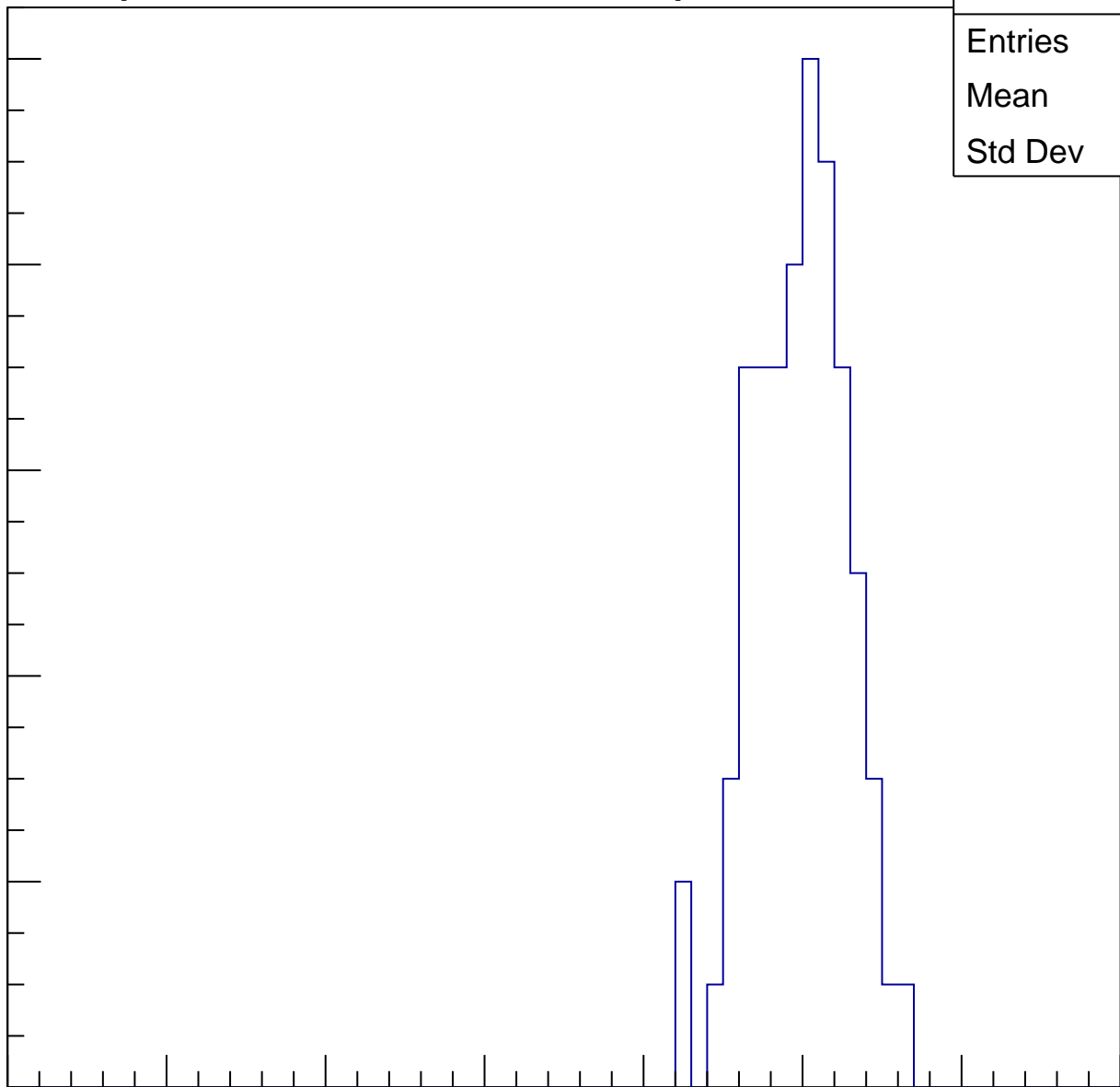
Entries	71
Mean	49.34
Std Dev	2.931

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

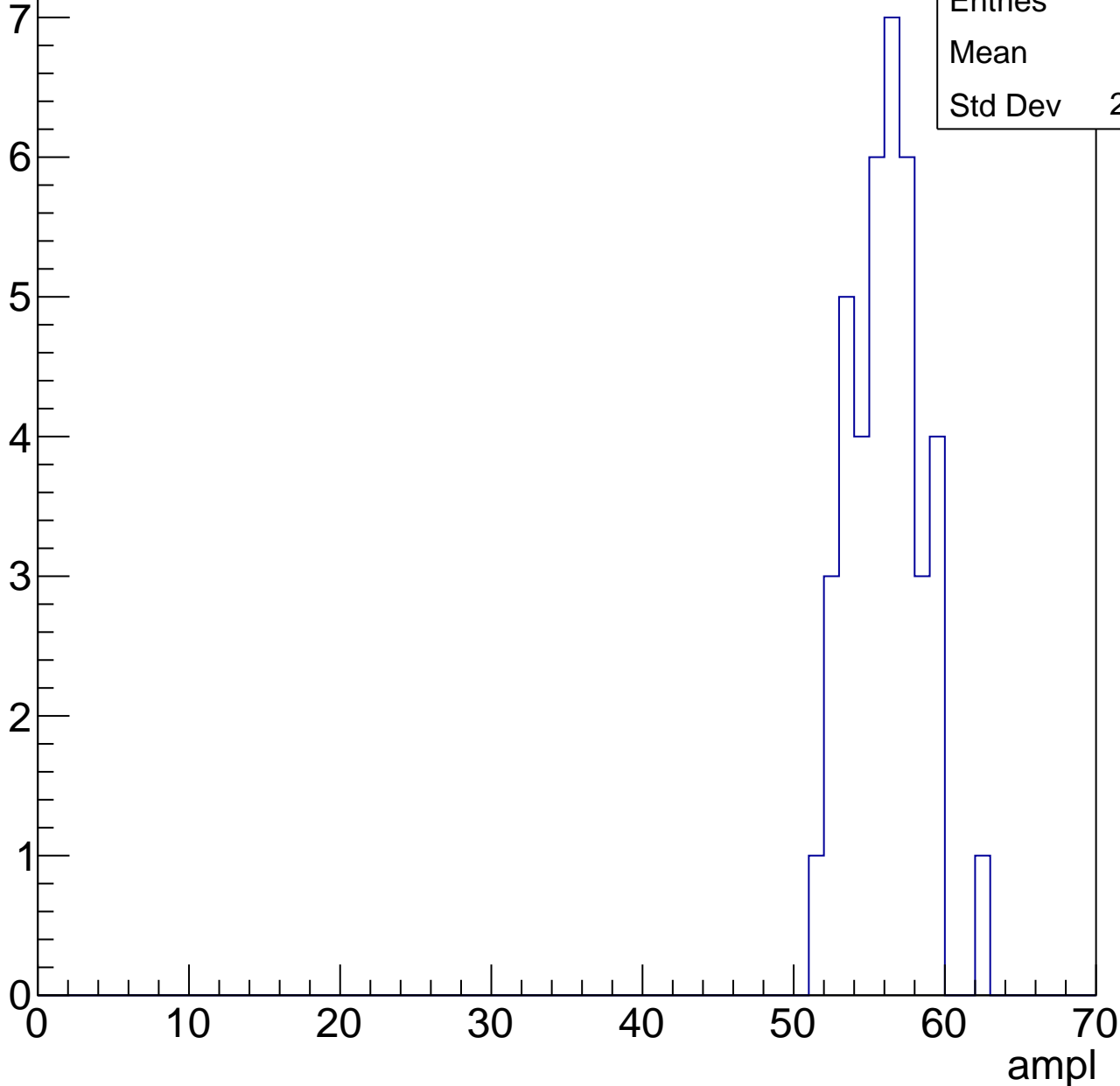


# B1L000S, U25-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	55.6
Std Dev	2.364



# B1L000S, U25-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 57

Mean 59.56

Std Dev 2.596

8

6

4

2

0

0

10

20

30

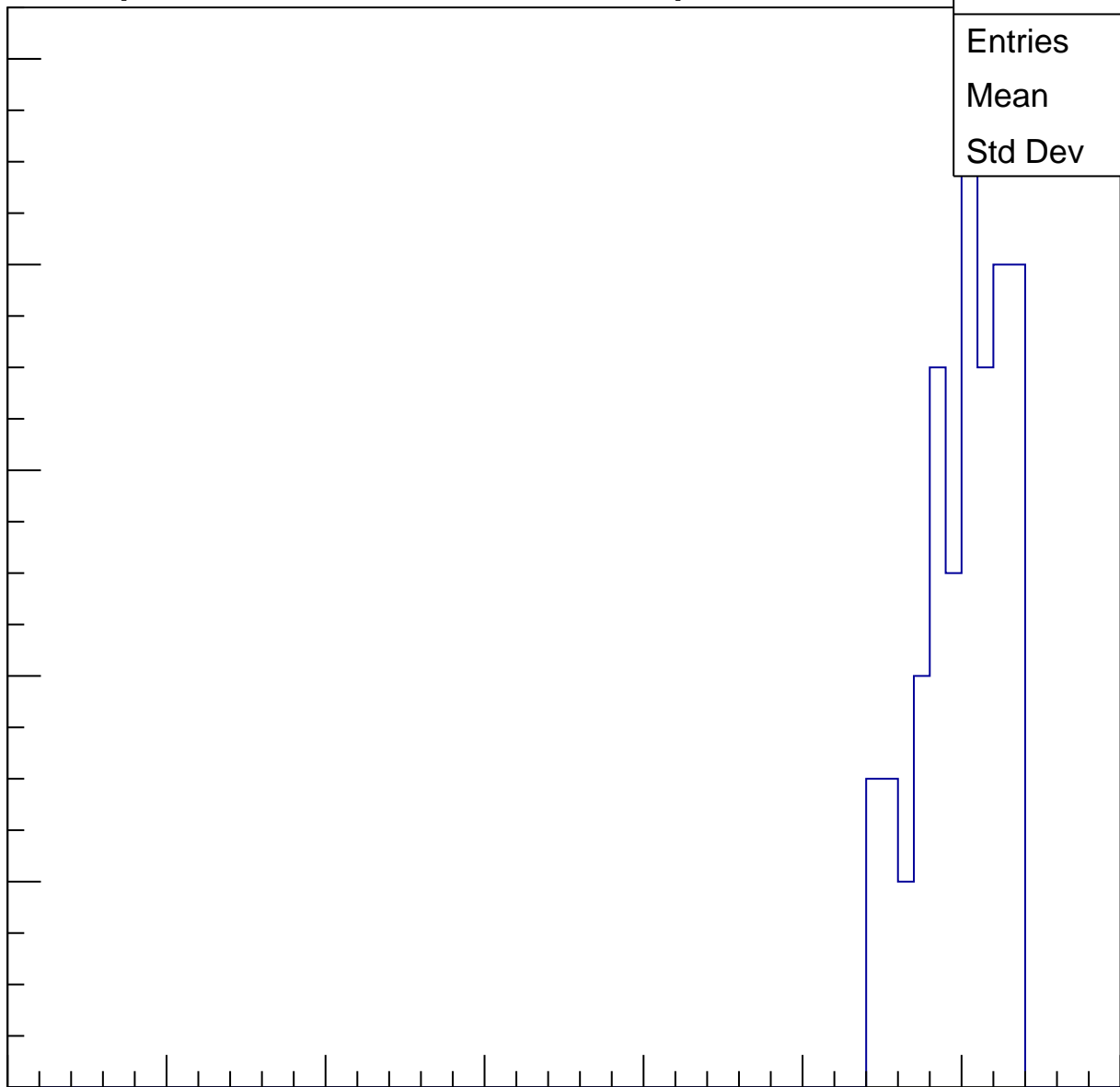
40

50

60

70

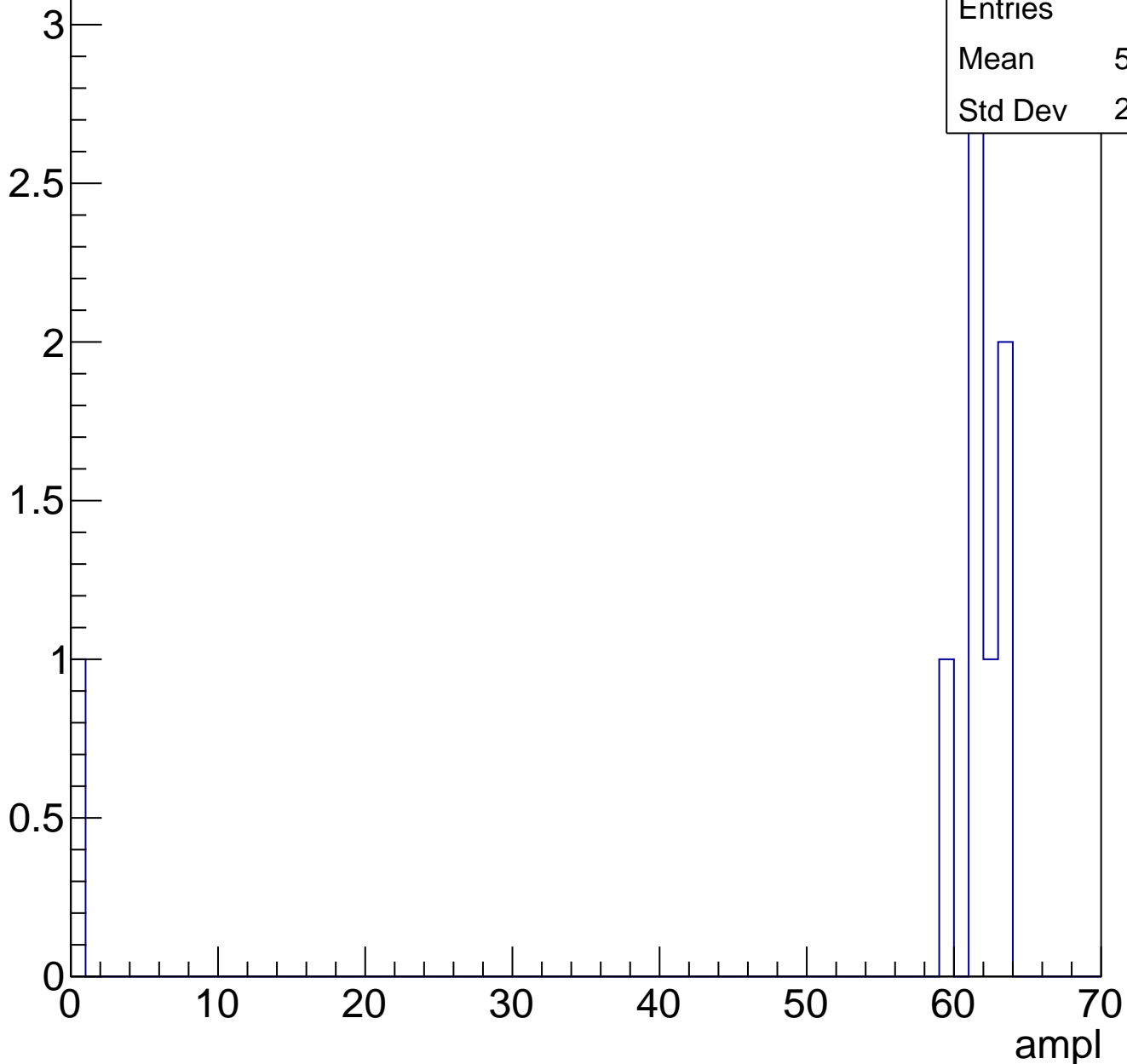
ampl



# B1L000S, U25-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch37, adc0

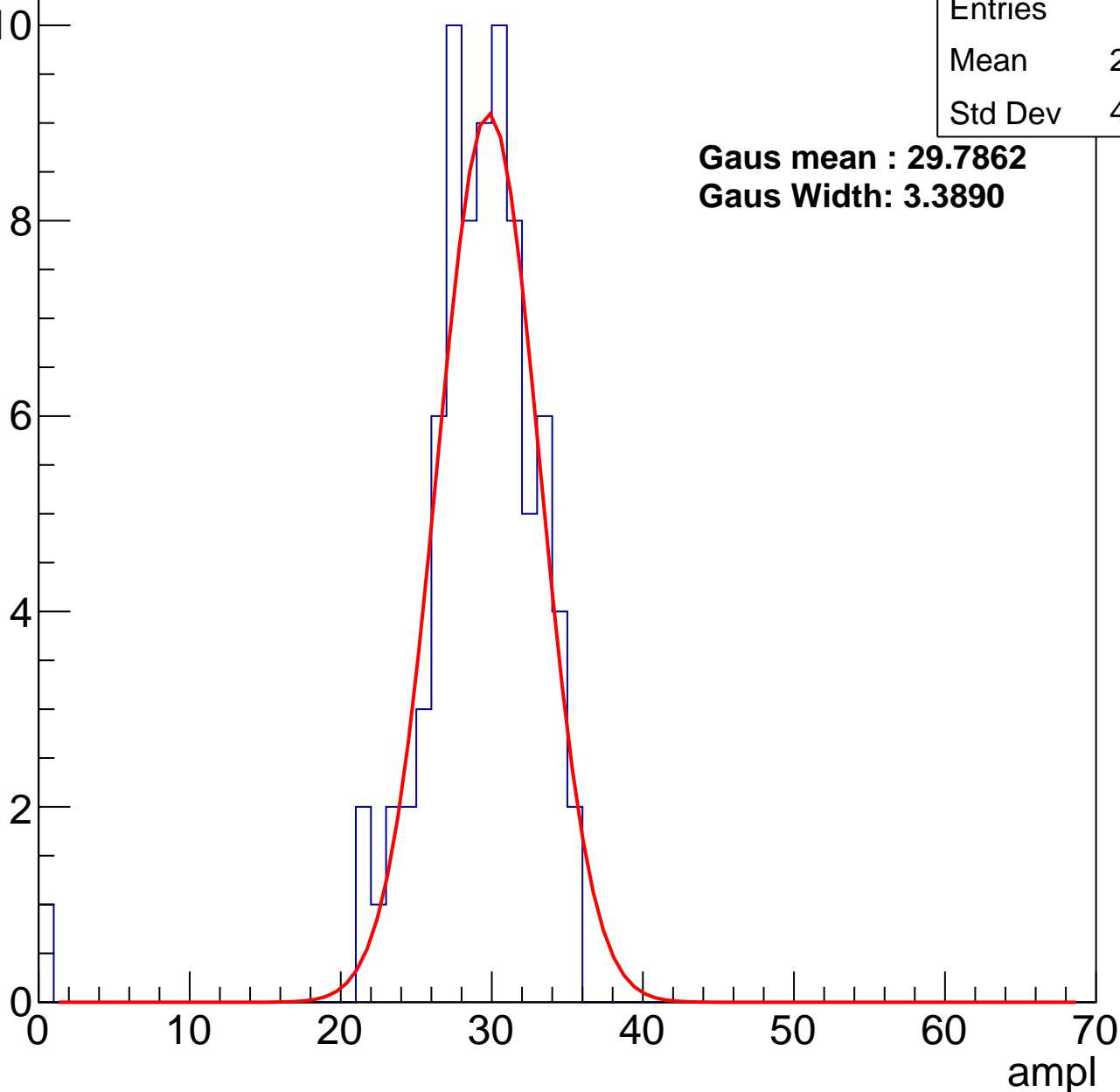
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	28.56
Std Dev	4.542

**Gaus mean : 29.7862**

**Gaus Width: 3.3890**



# B1L000S, U25-ch37, adc1

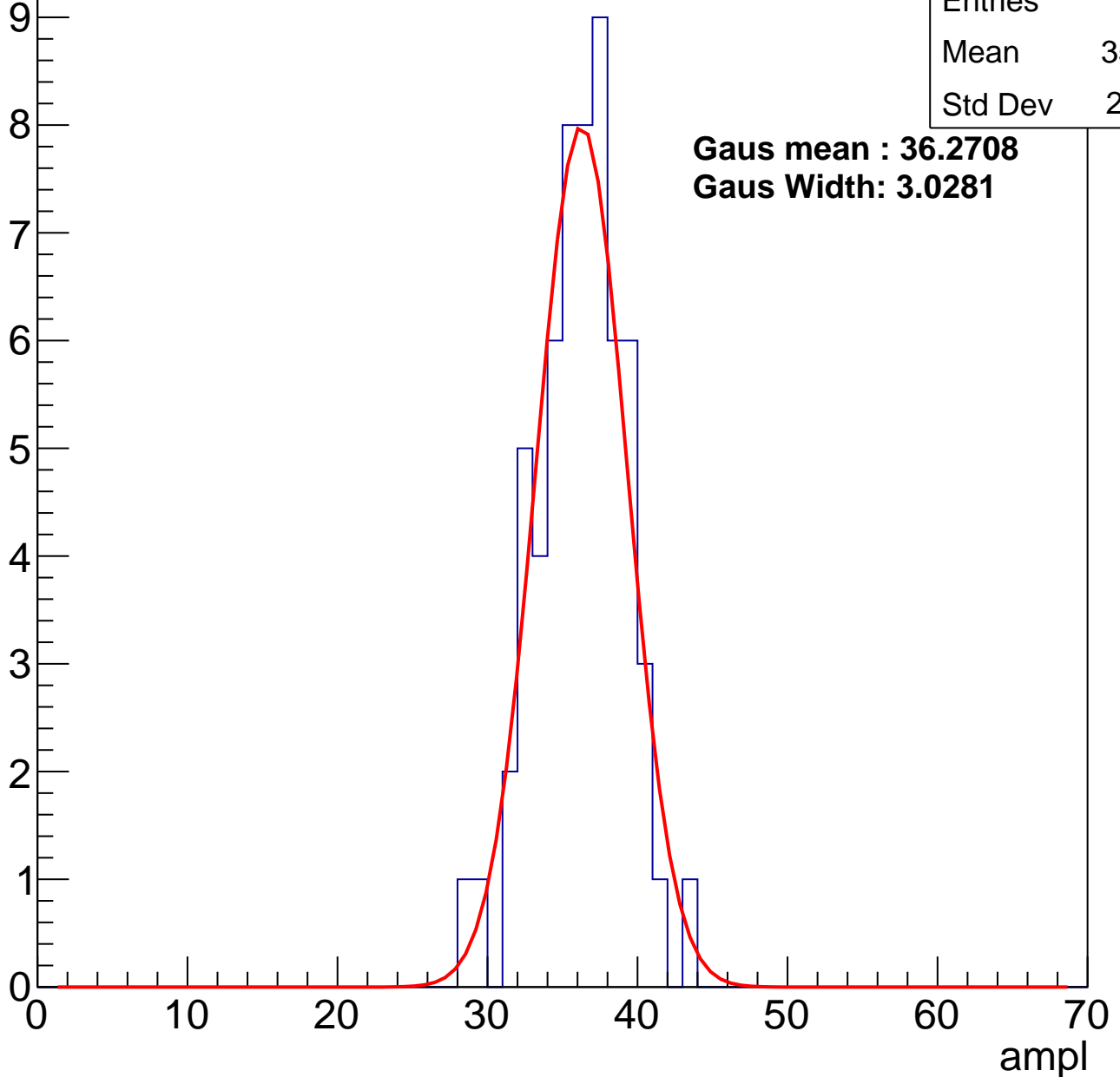
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.77
Std Dev	2.911

**Gaus mean : 36.2708**

**Gaus Width: 3.0281**



# B1L000S, U25-ch37, adc2

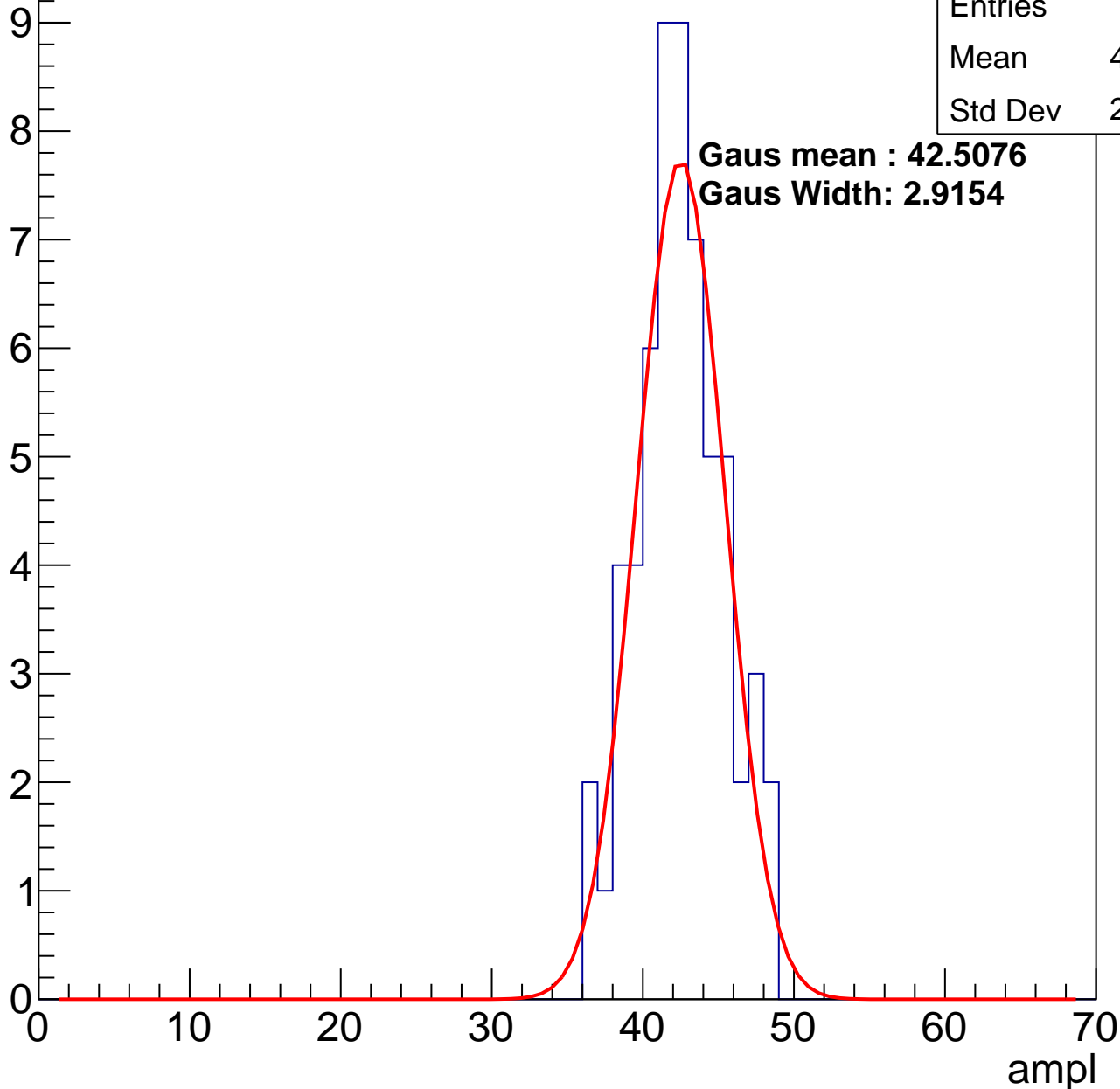
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.02
Std Dev	2.855

**Gaus mean : 42.5076**

**Gaus Width: 2.9154**

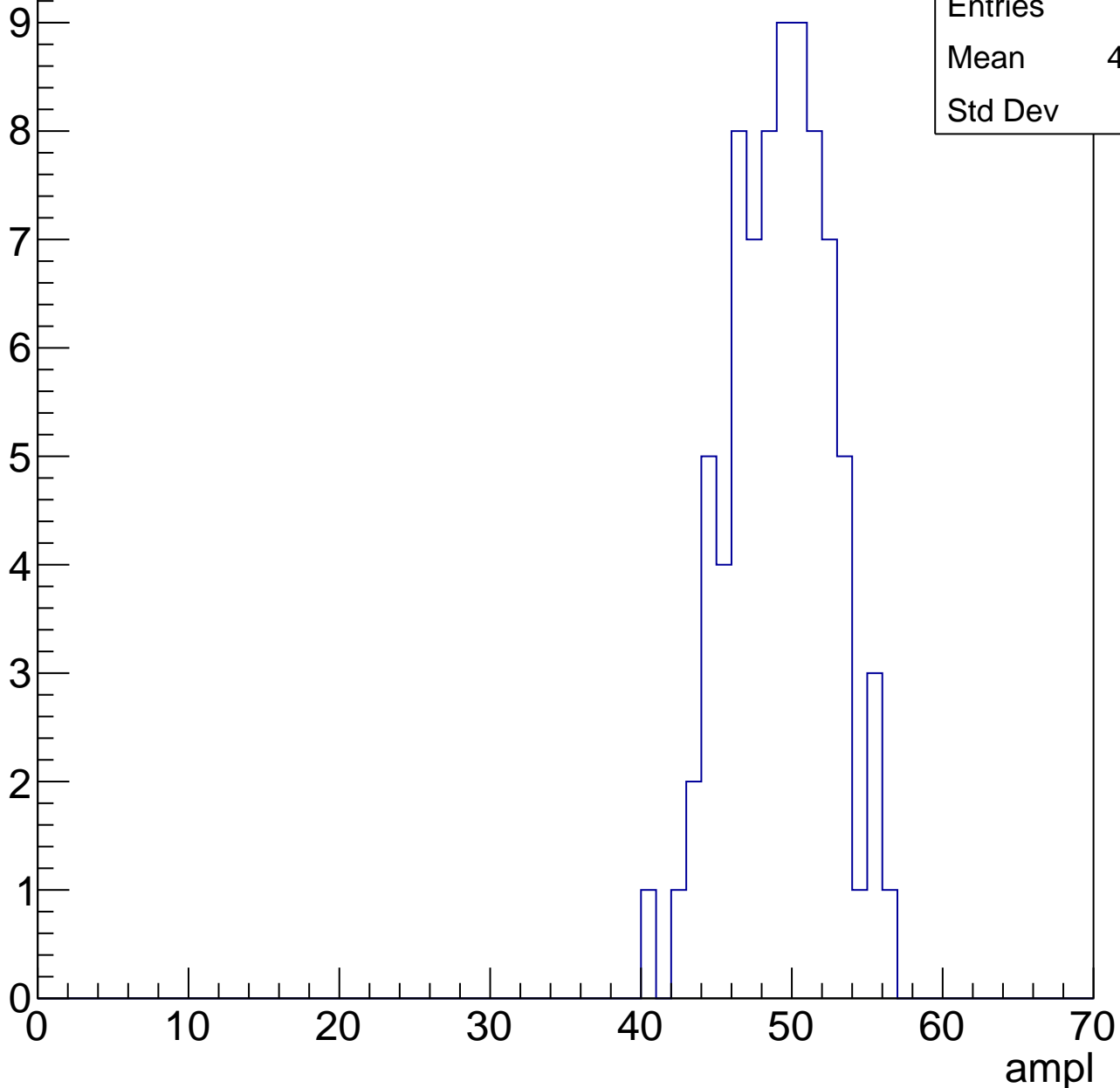


# B1L000S, U25-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	48.76
Std Dev	3.3

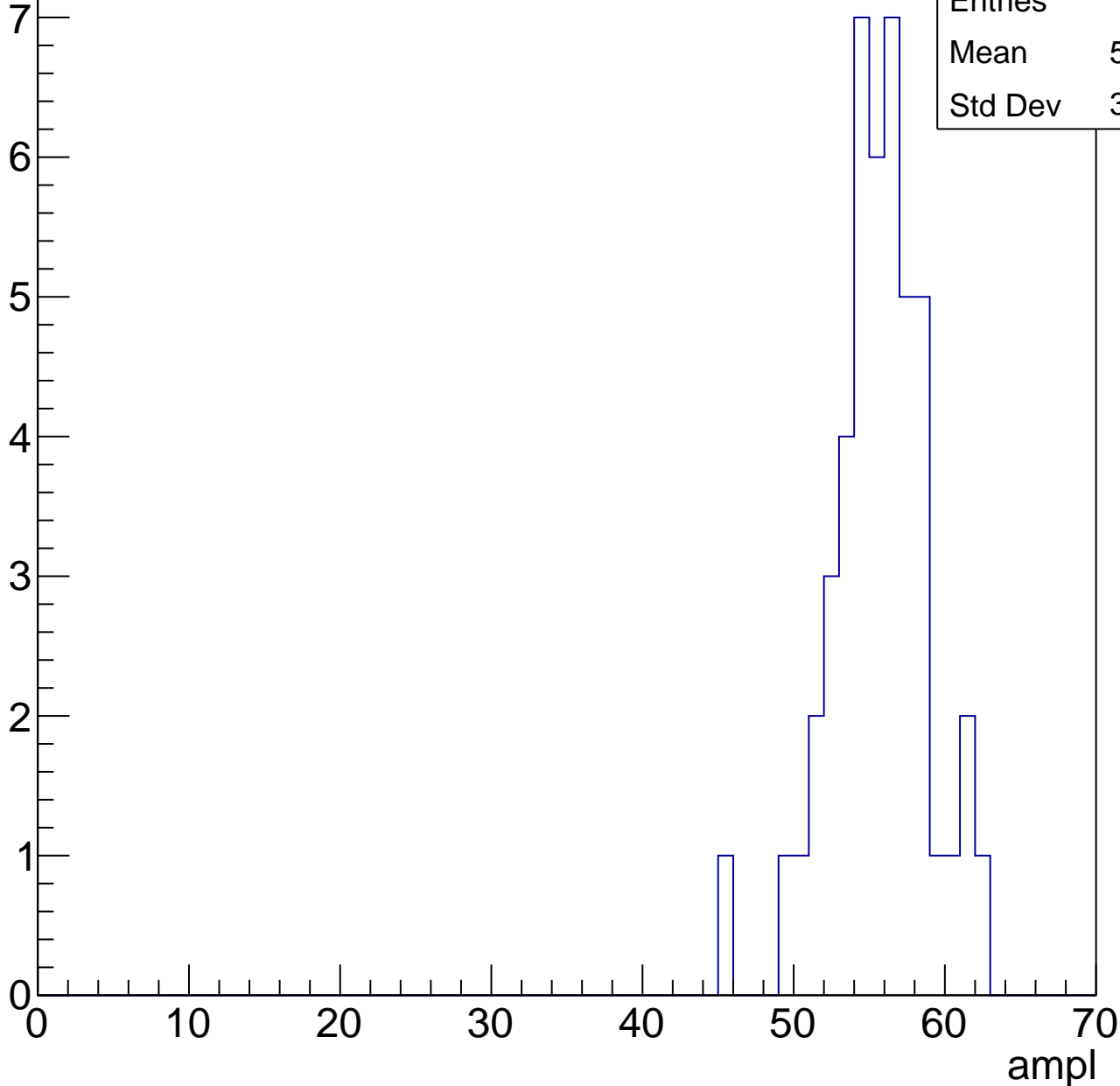


# B1L000S, U25-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	55.15
Std Dev	3.182

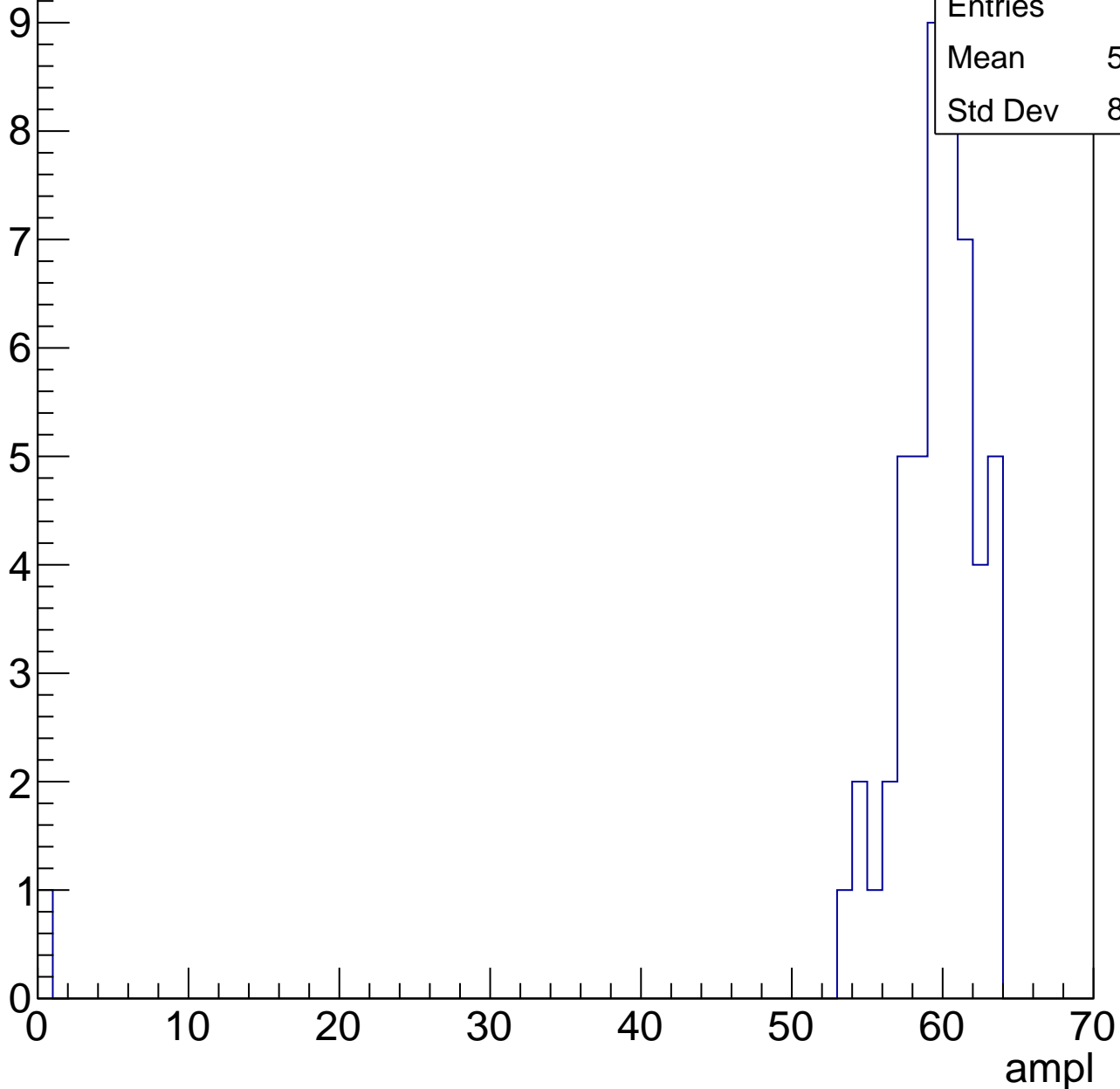


# B1L000S, U25-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	58.12
Std Dev	8.563

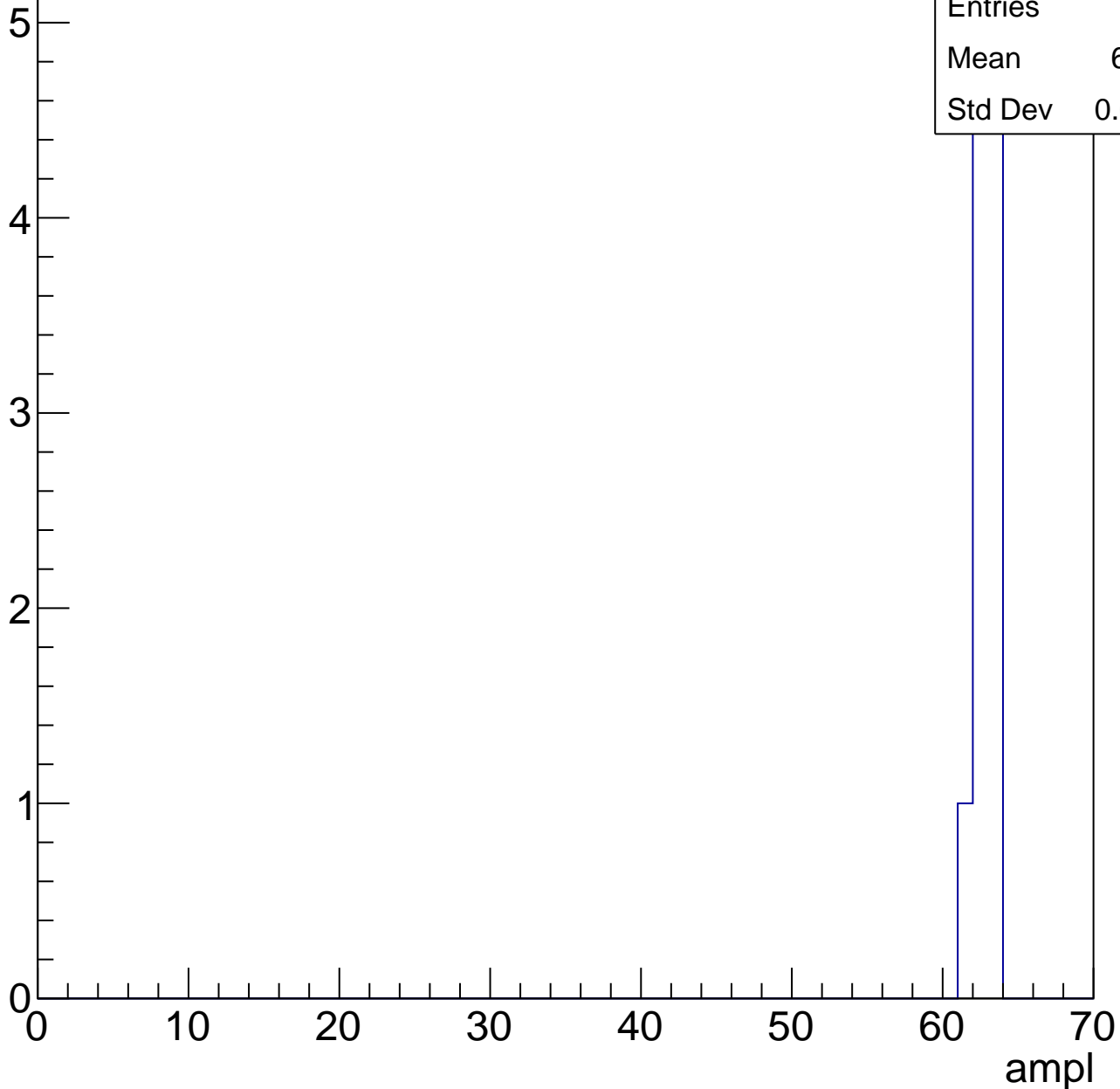


# B1L000S, U25-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	11
Mean	62.36
Std Dev	0.6428





# B1L000S, U25-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch38, adc0

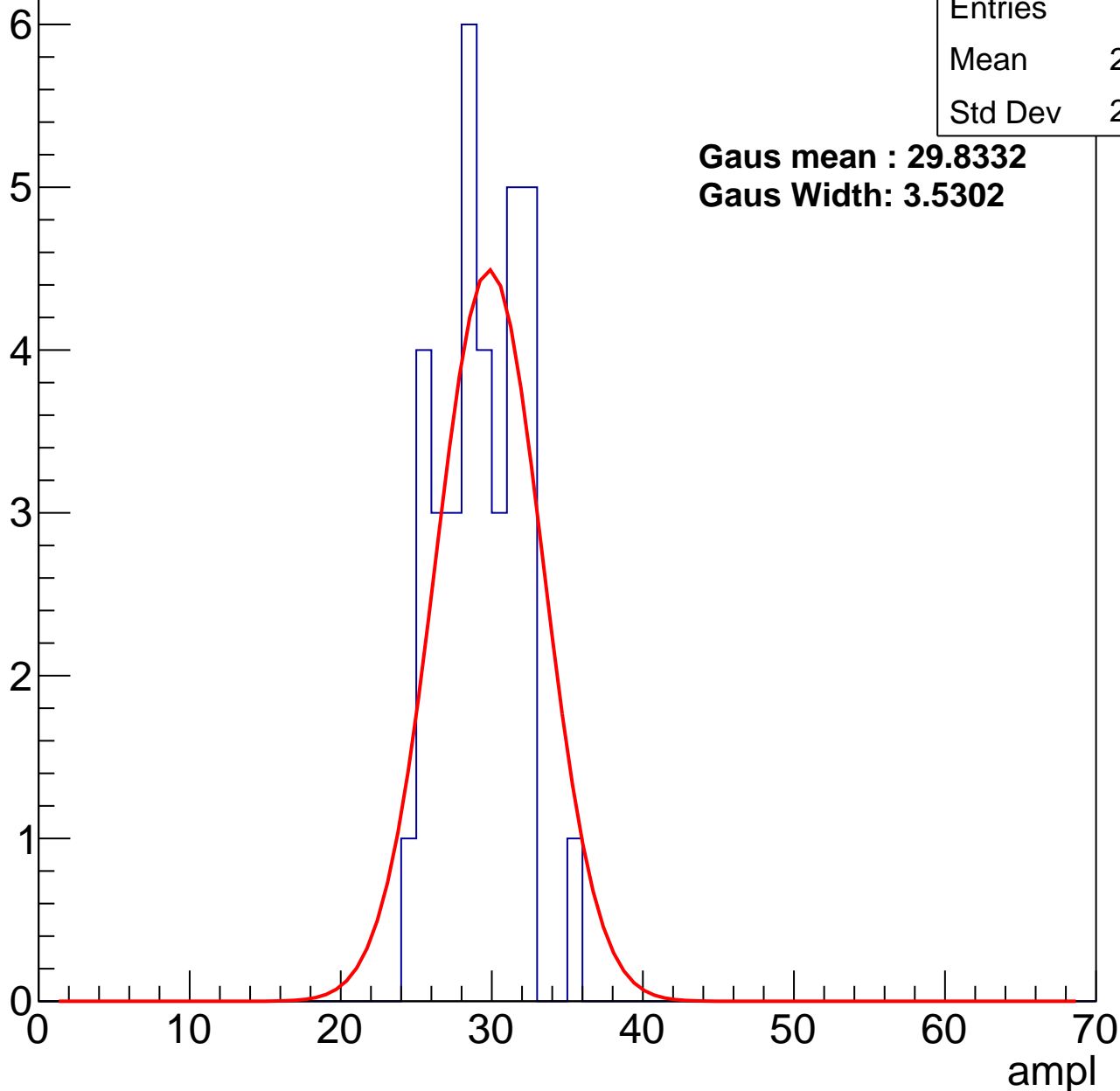
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	28.77
Std Dev	2.598

**Gaus mean : 29.8332**

**Gaus Width: 3.5302**



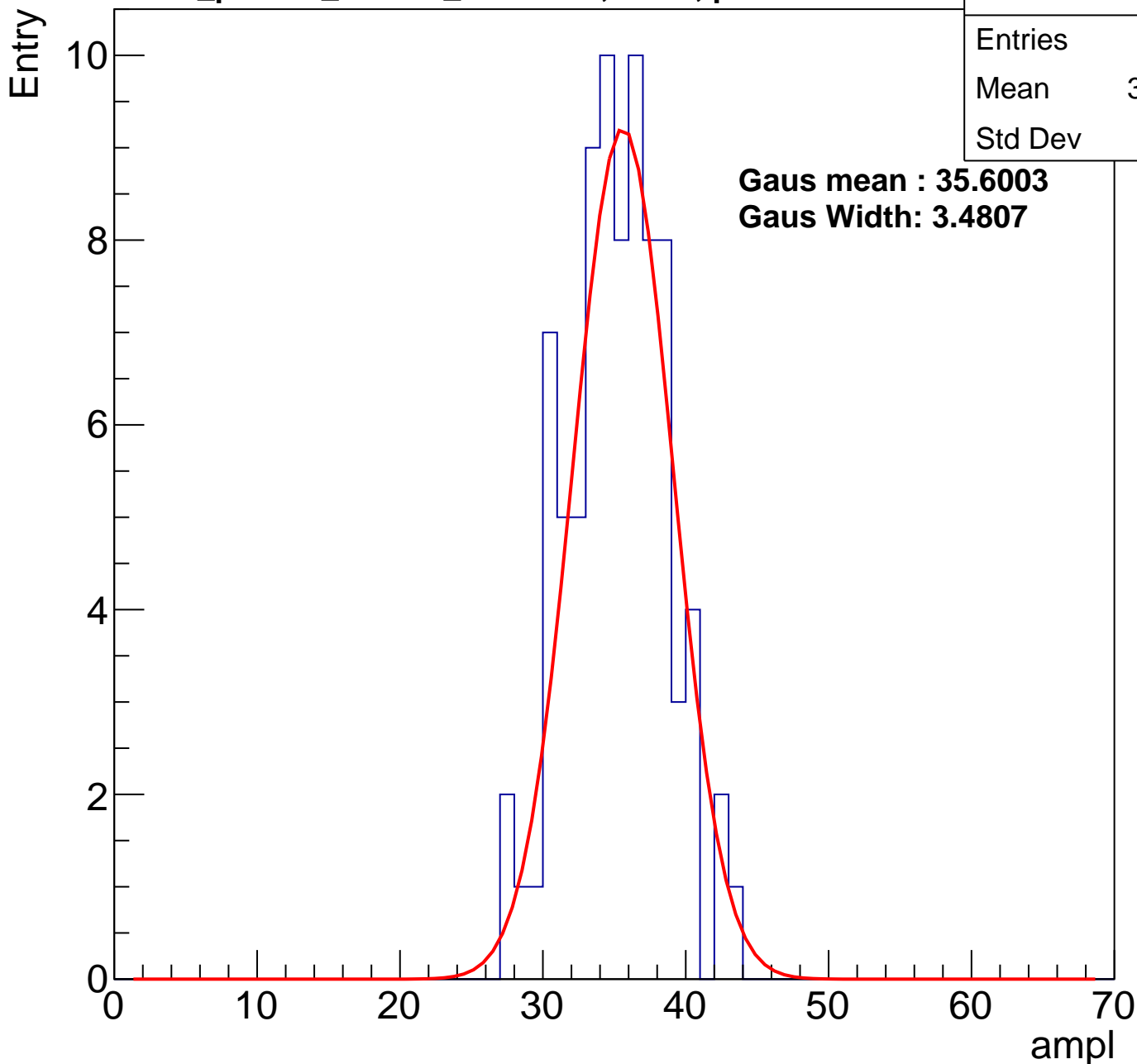
# B1L000S, U25-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	34.73
Std Dev	3.41

**Gaus mean : 35.6003**

**Gaus Width: 3.4807**



# B1L000S, U25-ch38, adc2

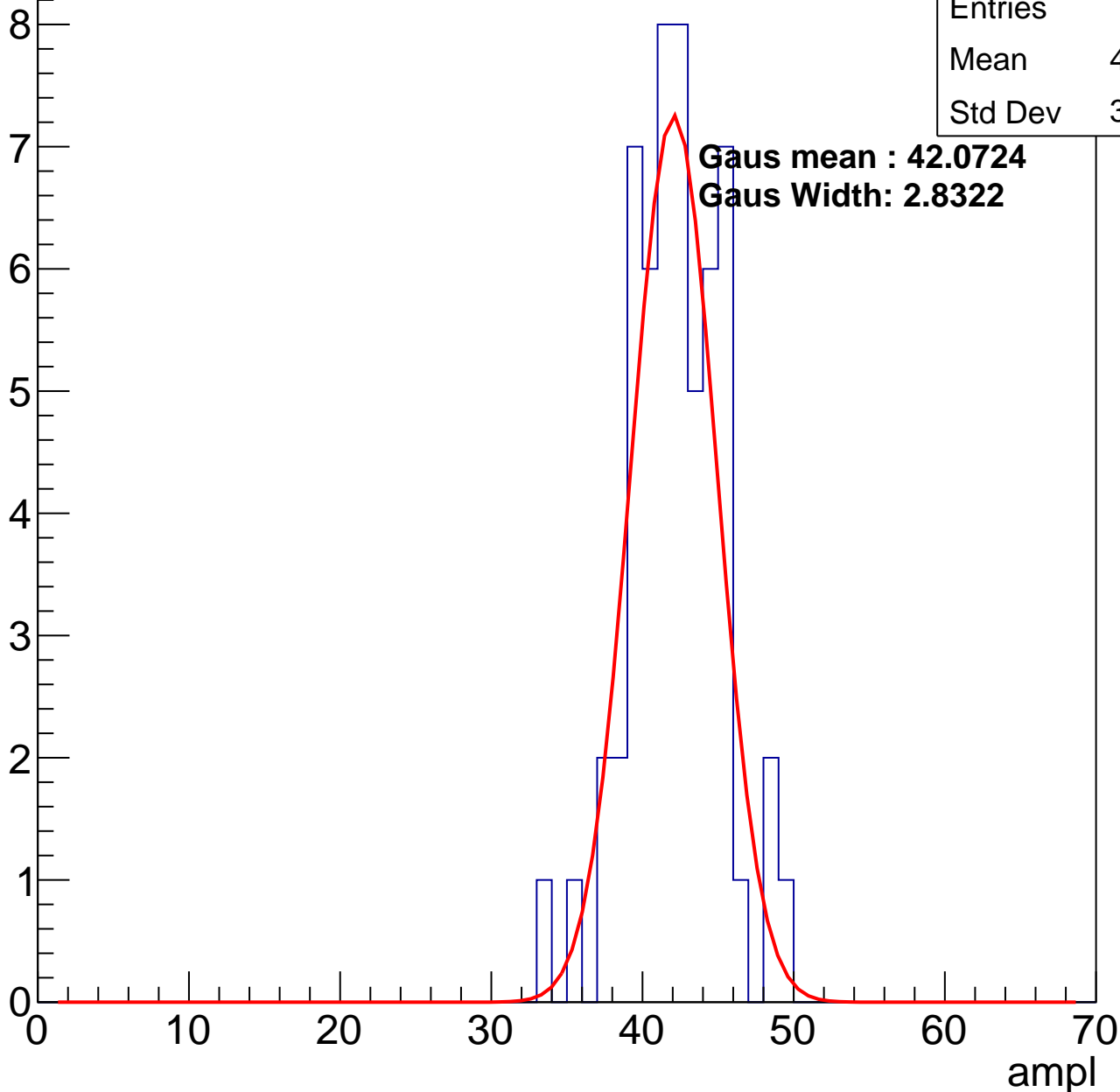
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.75
Std Dev	3.057

**Gaus mean : 42.0724**

**Gaus Width: 2.8322**

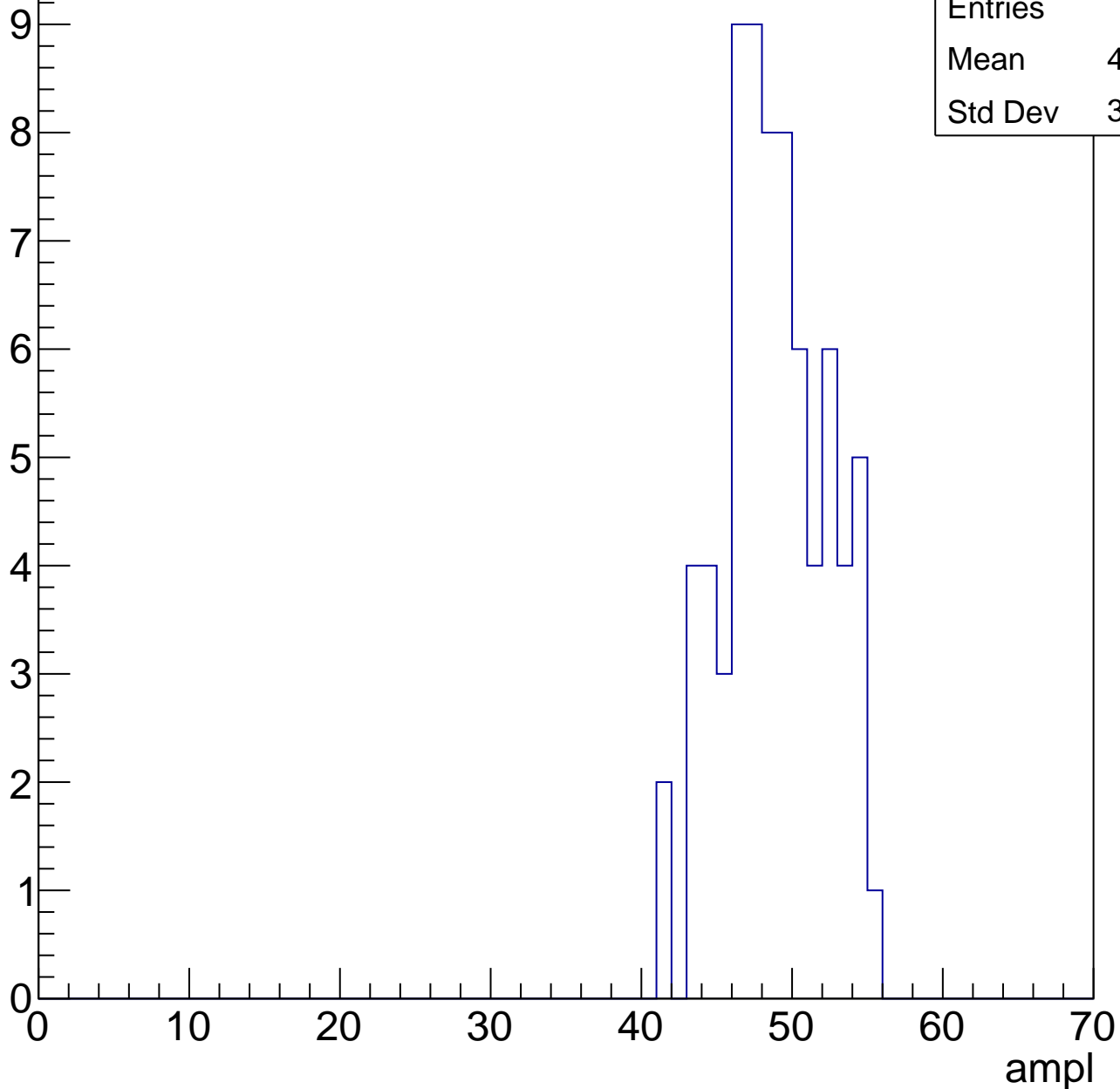


# B1L000S, U25-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

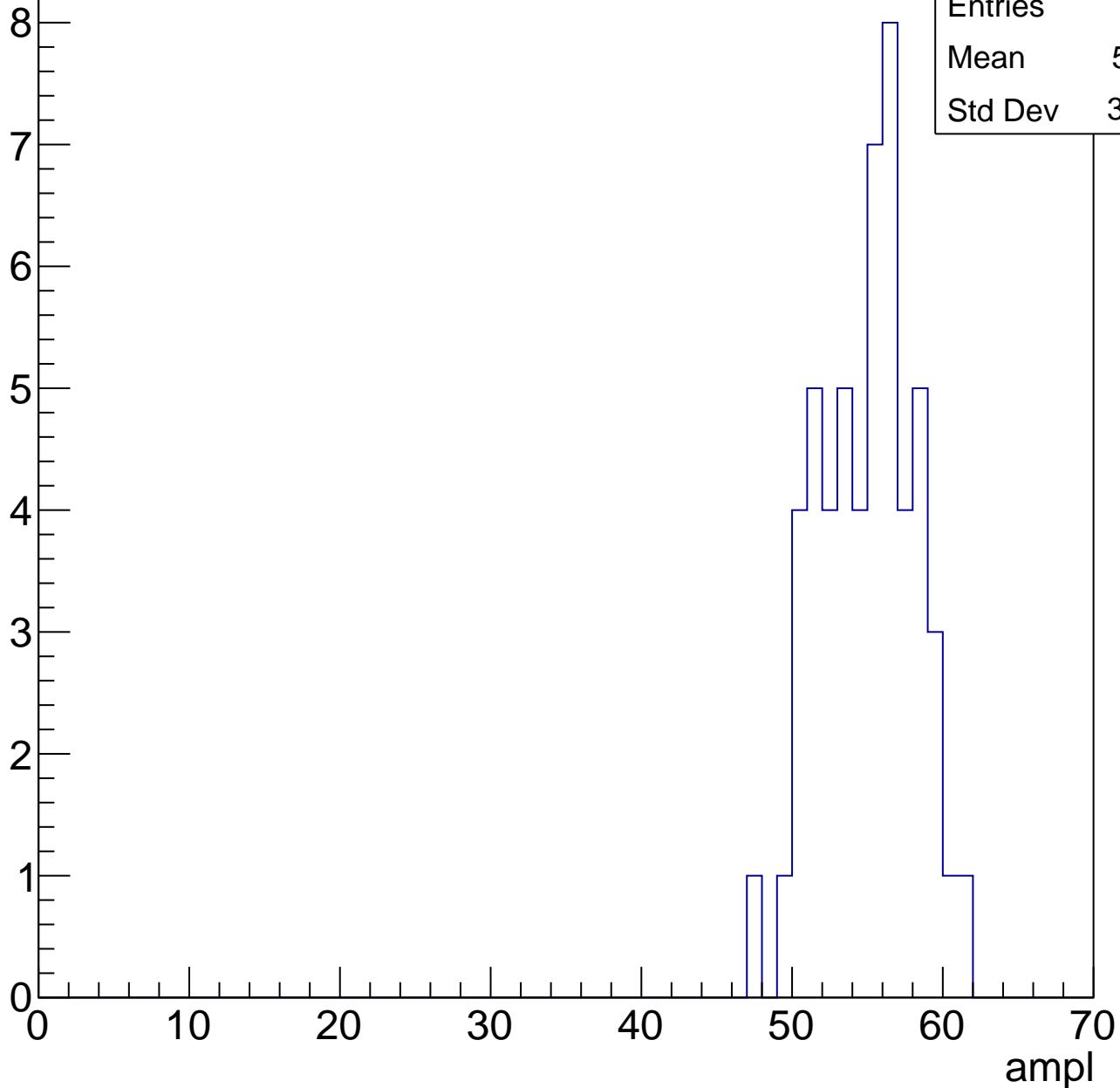
Entries	73
Mean	48.37
Std Dev	3.345



# B1L000S, U25-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch38, adc5

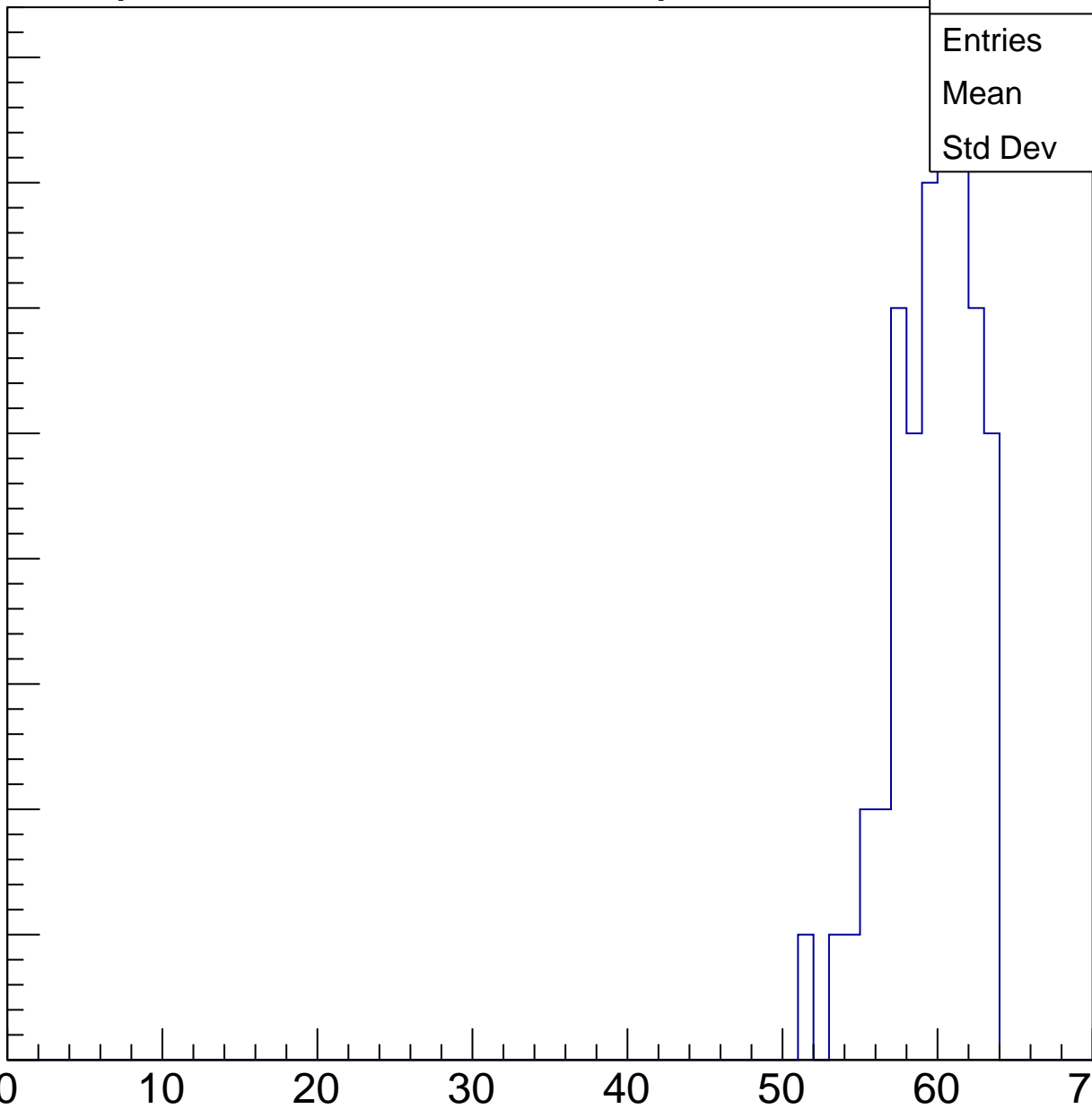
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	59.23
Std Dev	2.686

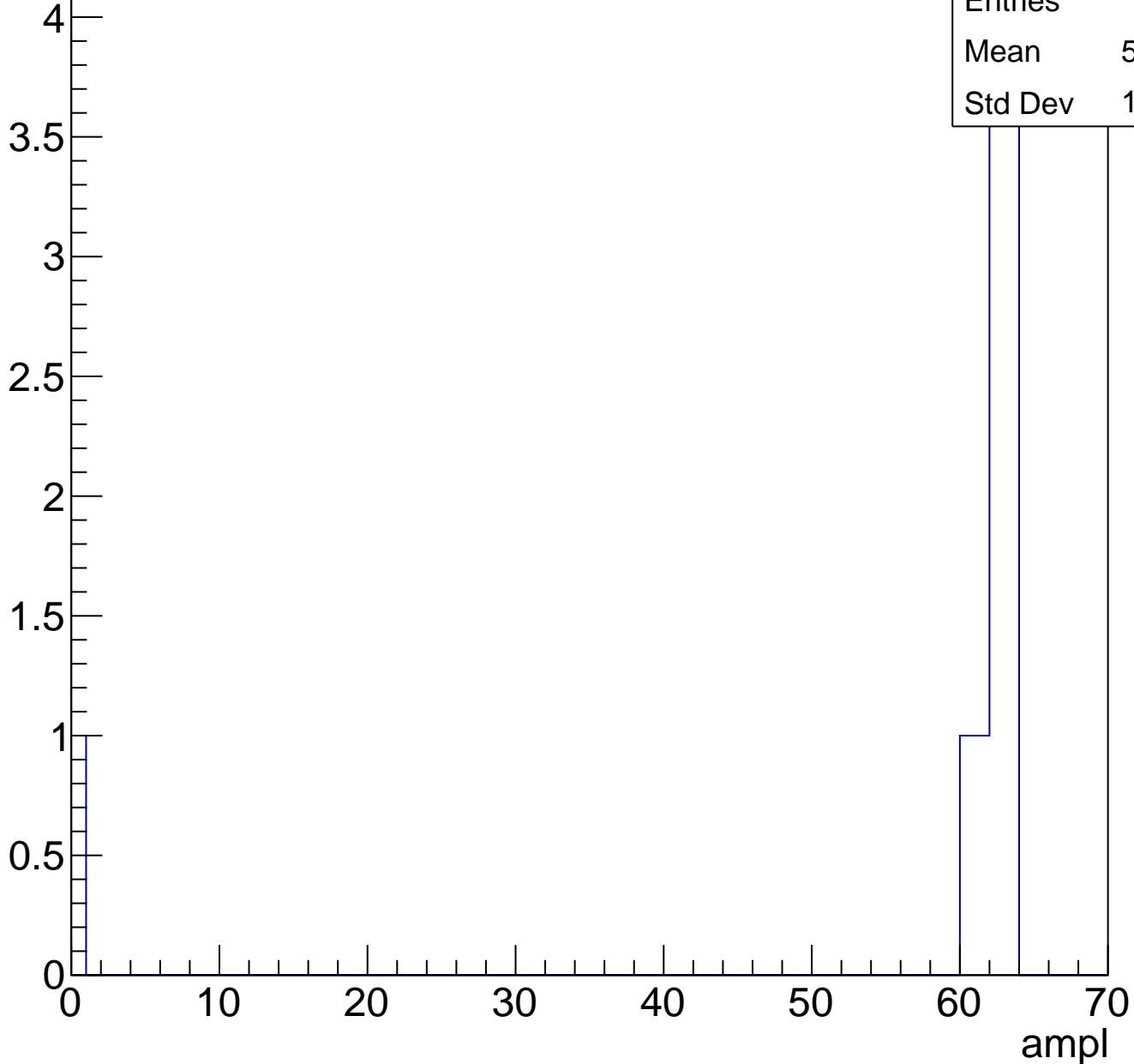
ampl



# B1L000S, U25-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

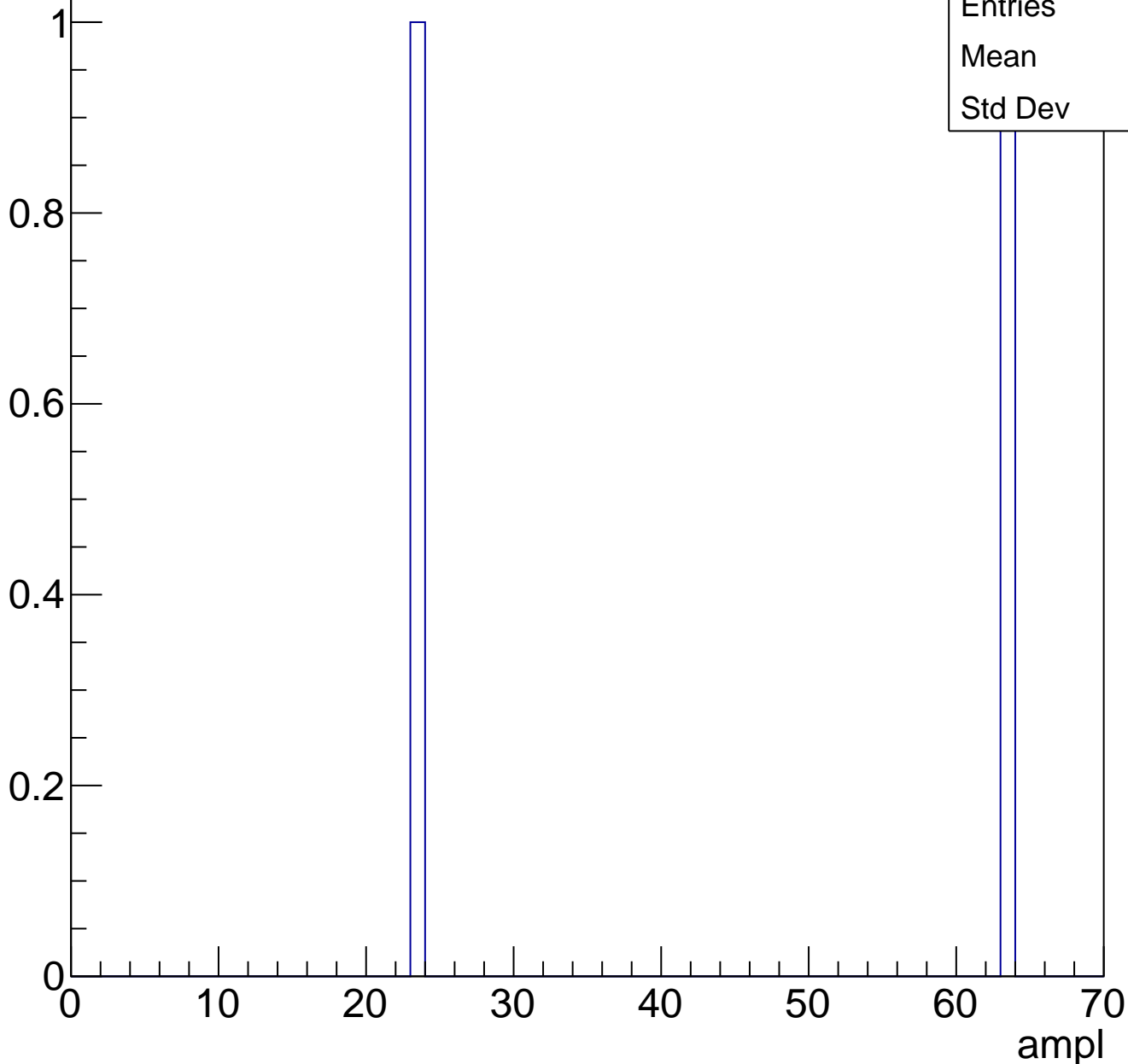




# B1L000S, U25-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch39, adc0

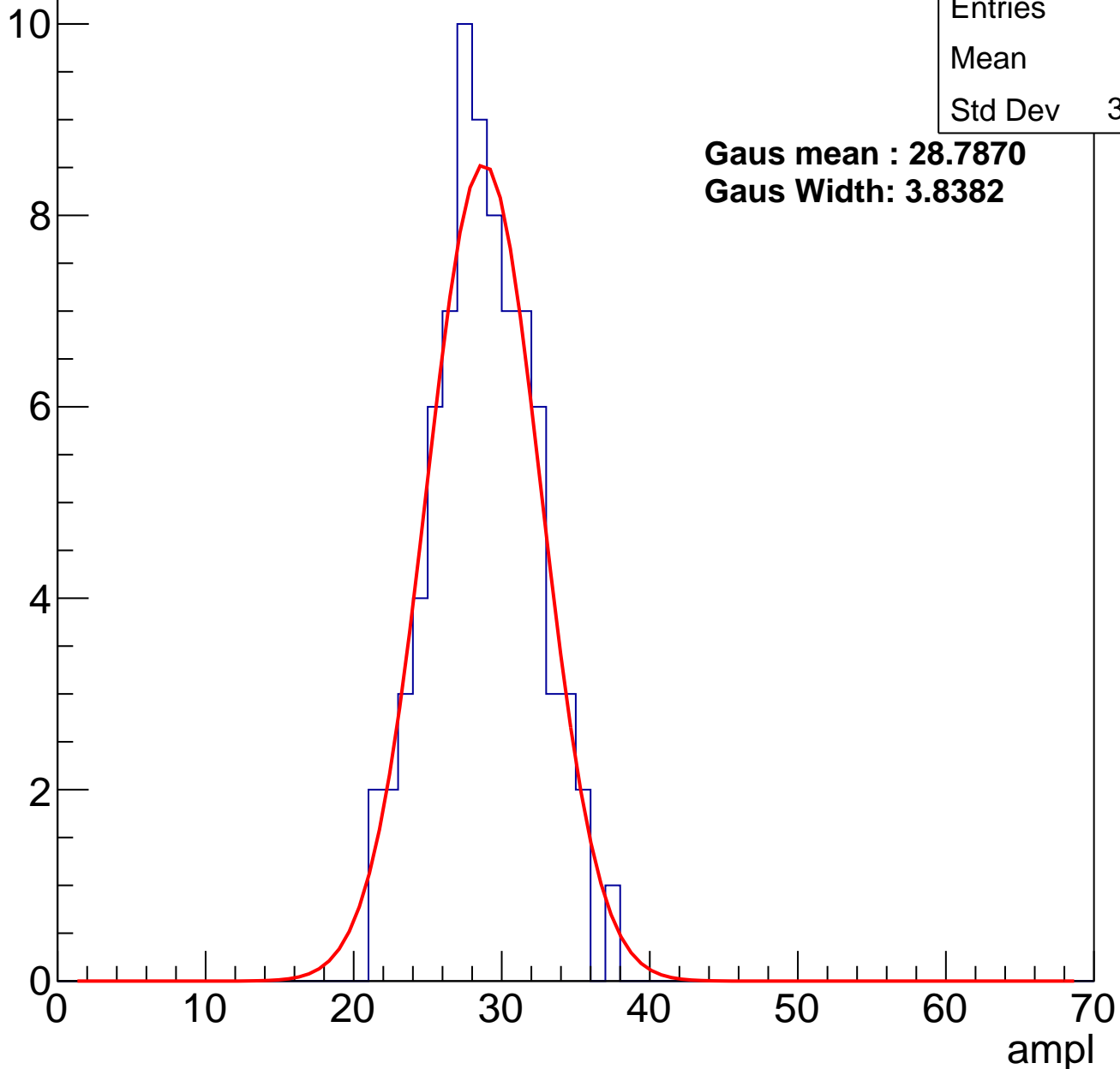
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	80
Mean	28.3
Std Dev	3.447

**Gaus mean : 28.7870**

**Gaus Width: 3.8382**

Entry



# B1L000S, U25-ch39, adc1

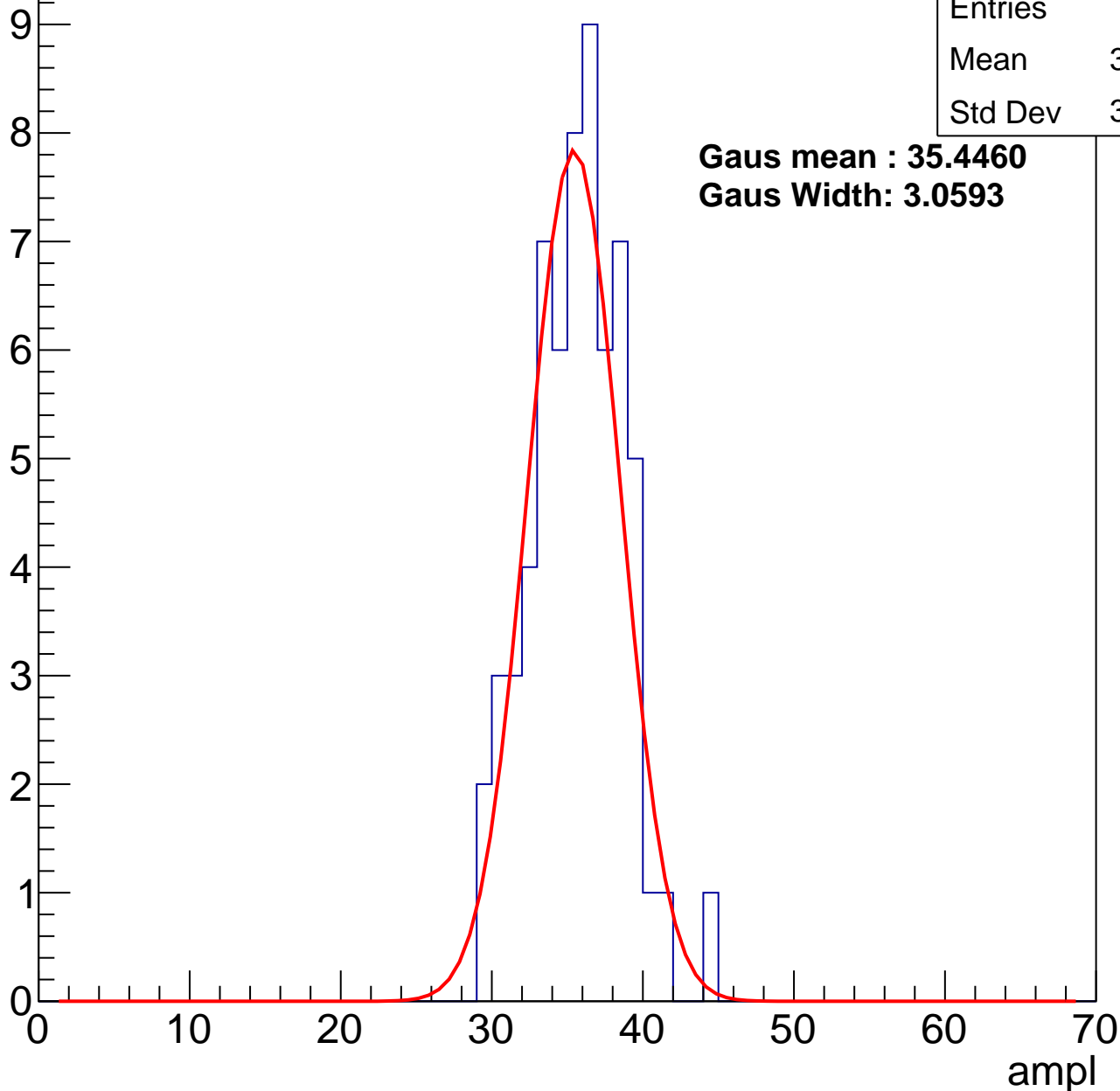
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	35.17
Std Dev	3.037

**Gaus mean : 35.4460**

**Gaus Width: 3.0593**



# B1L000S, U25-ch39, adc2

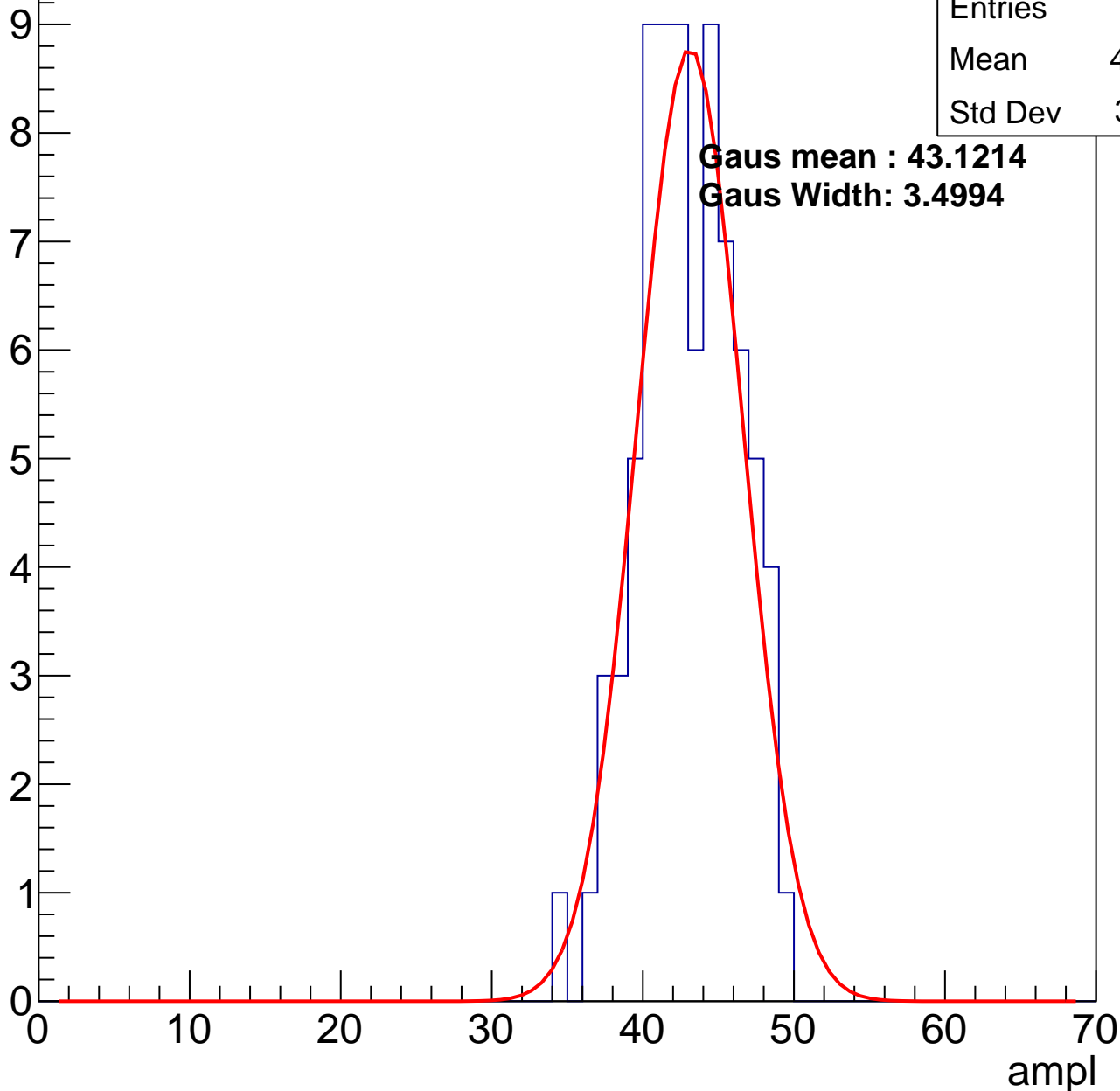
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	42.54
Std Dev	3.221

**Gaus mean : 43.1214**

**Gaus Width: 3.4994**

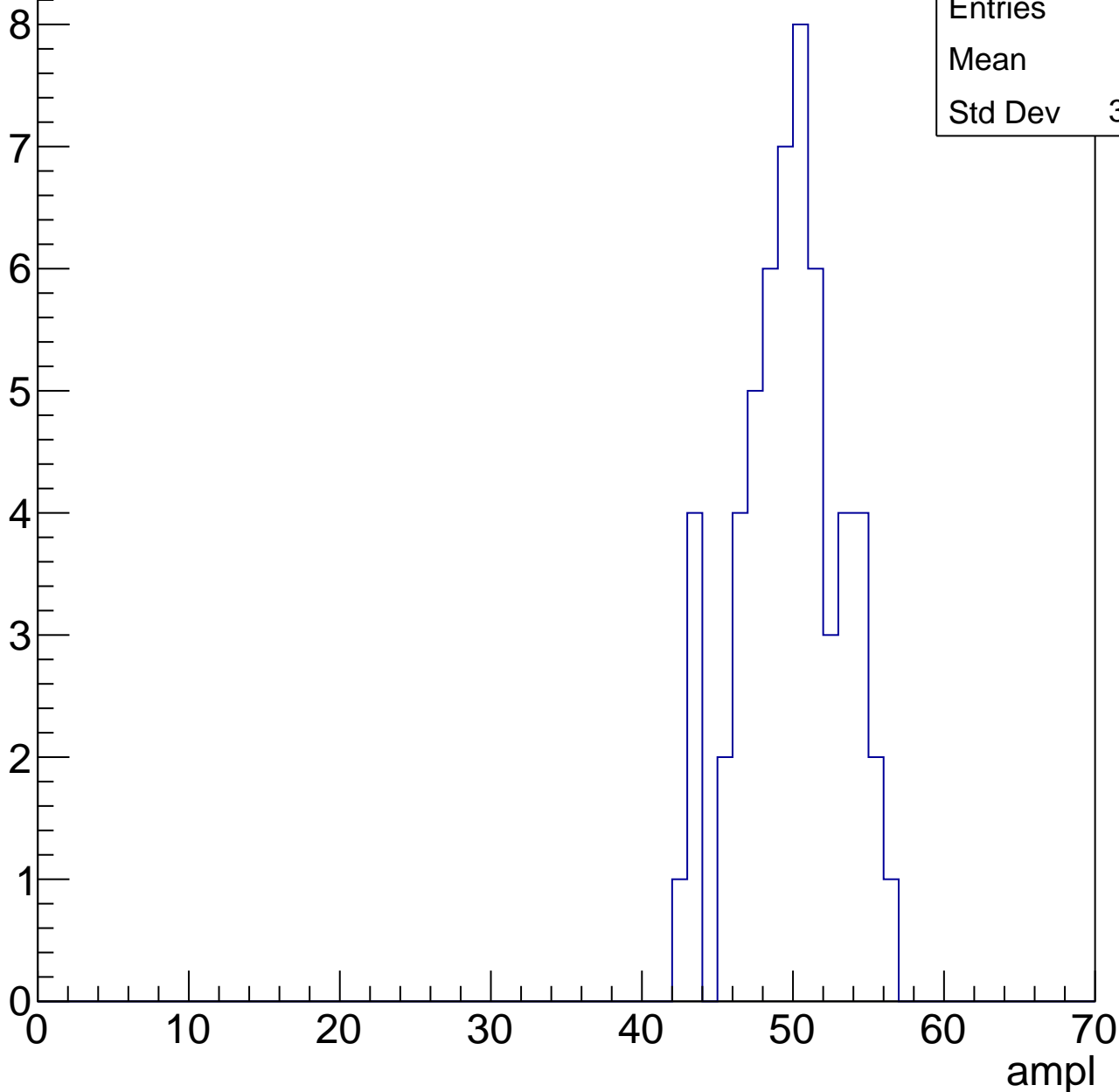


# B1L000S, U25-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	49.3
Std Dev	3.314

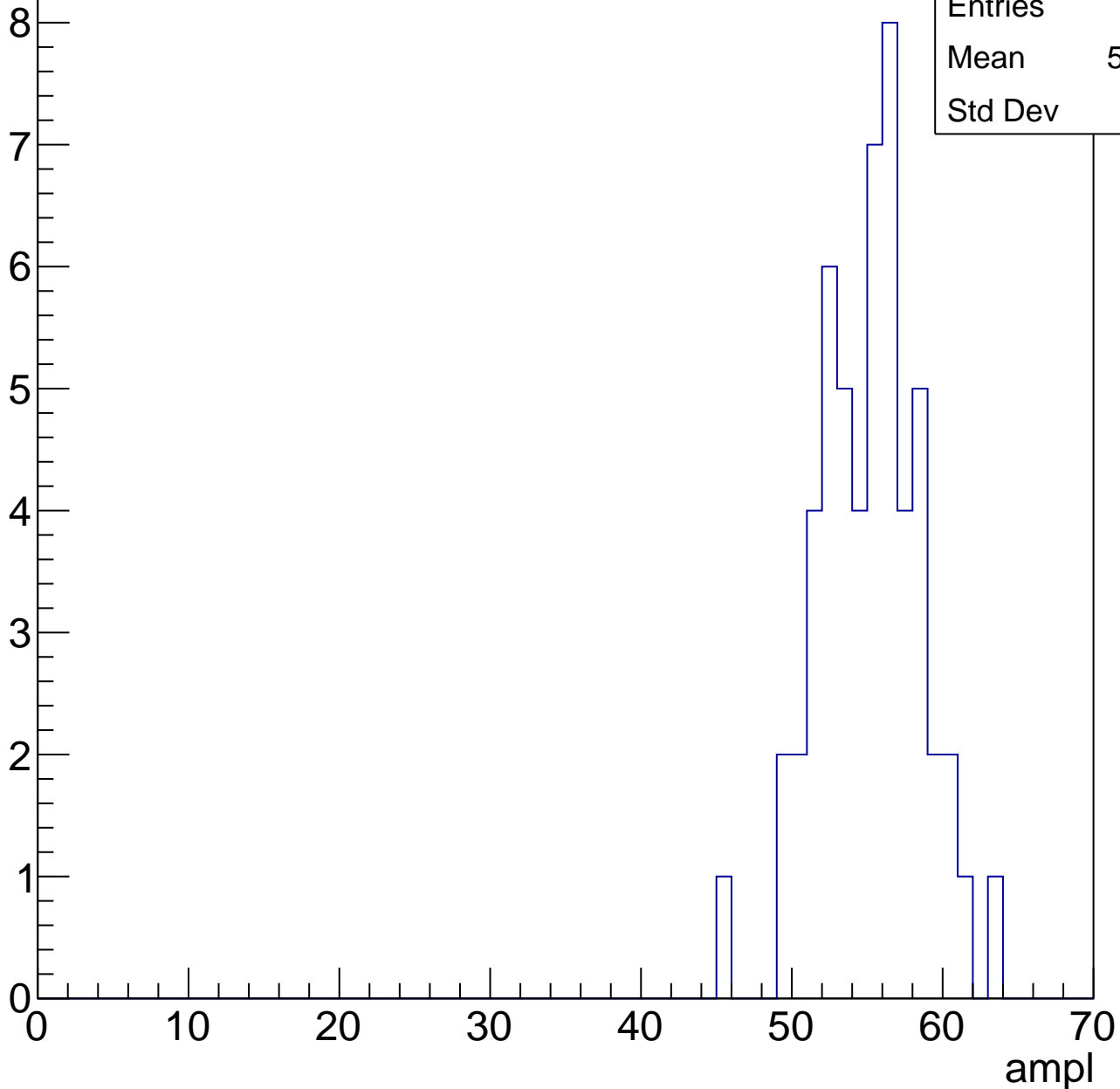


# B1L000S, U25-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

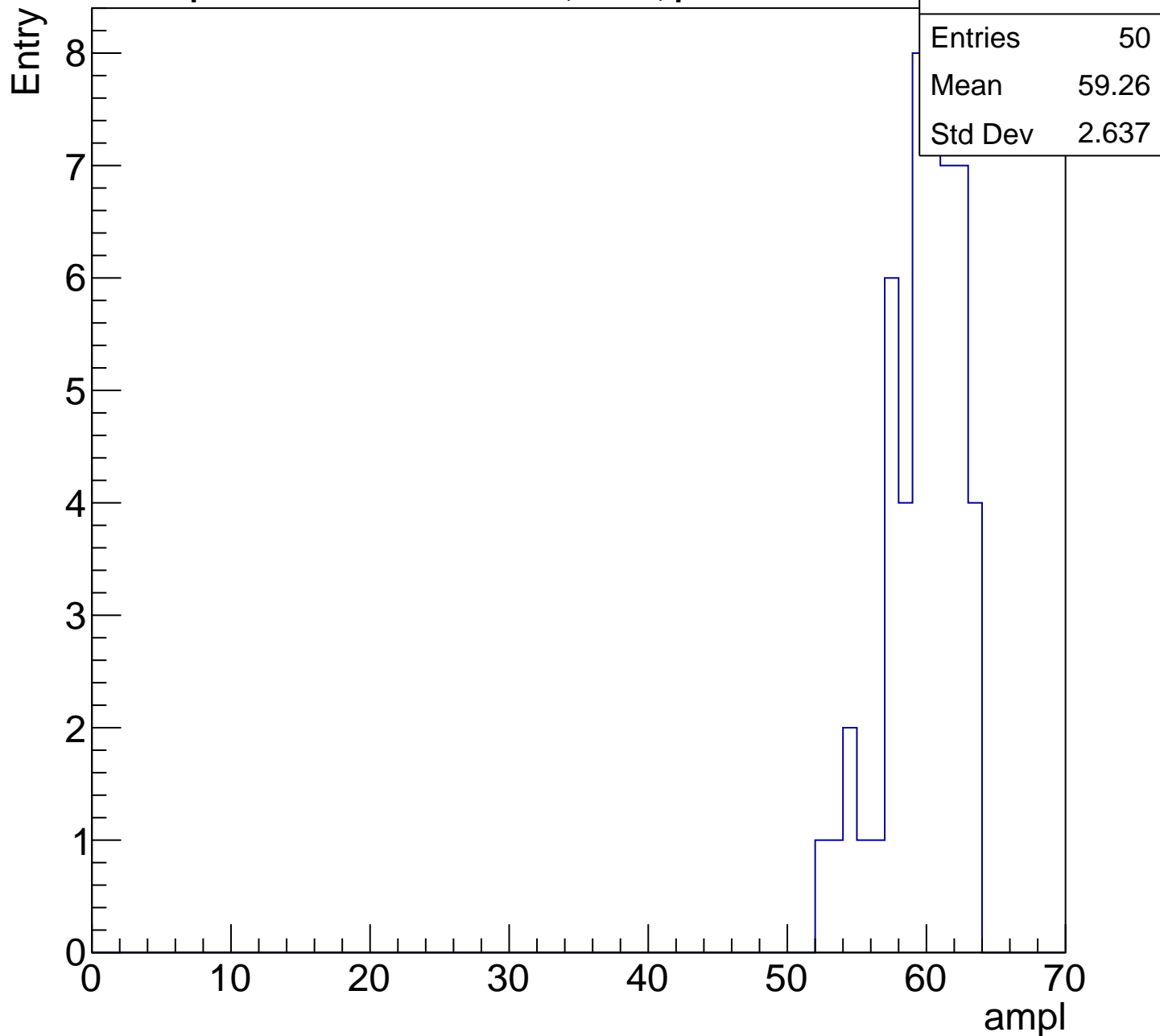
Entry

Entries	54
Mean	54.69
Std Dev	3.36



# B1L000S, U25-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

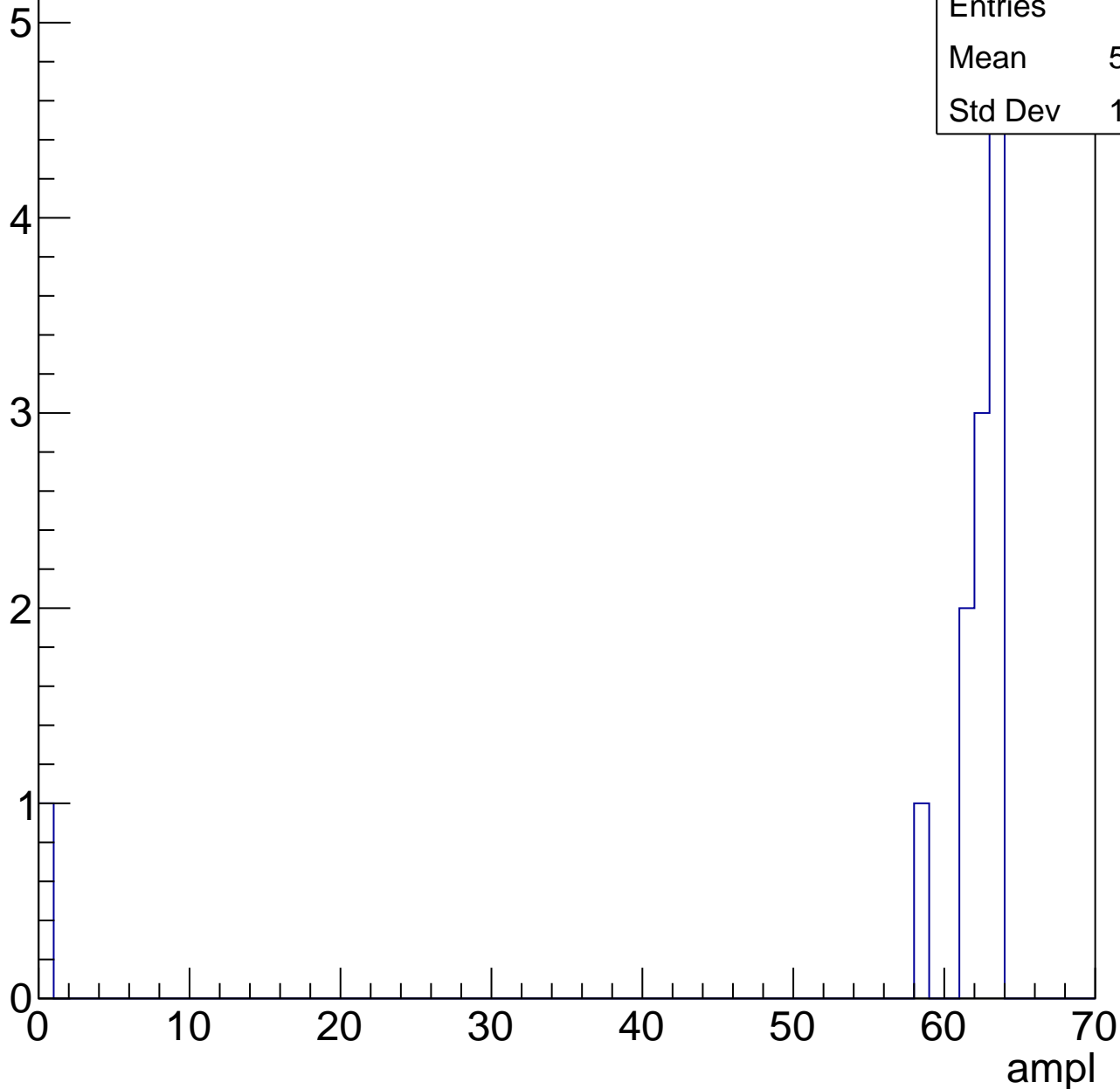


# B1L000S, U25-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.75
Std Dev	17.17





# B1L000S, U25-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L000S, U25-ch40, adc0

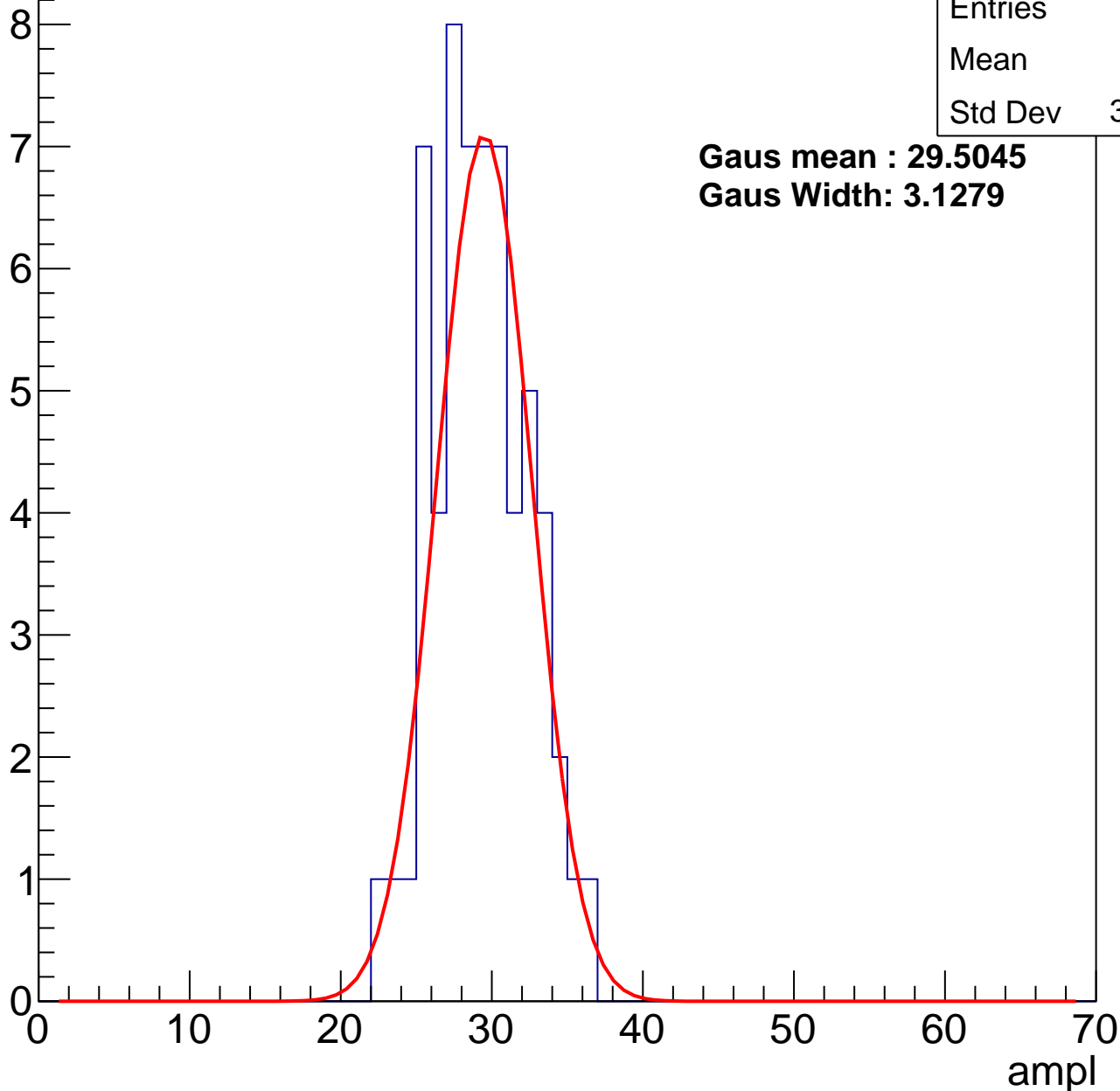
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.8
Std Dev	3.059

**Gaus mean : 29.5045**

**Gaus Width: 3.1279**



# B1L000S, U25-ch40, adc1

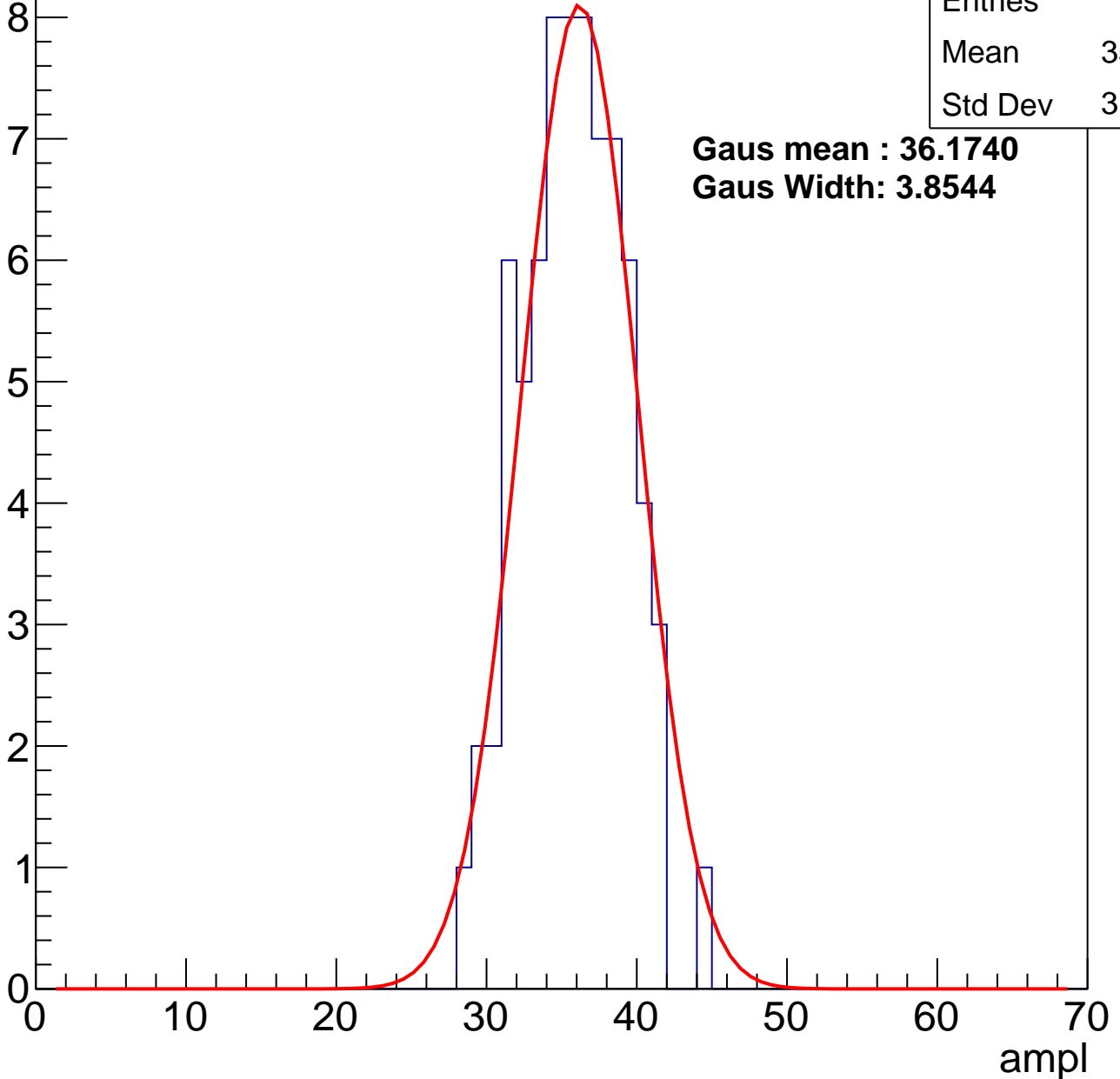
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	35.35
Std Dev	3.327

**Gaus mean : 36.1740**

**Gaus Width: 3.8544**



# B1L000S, U25-ch40, adc2

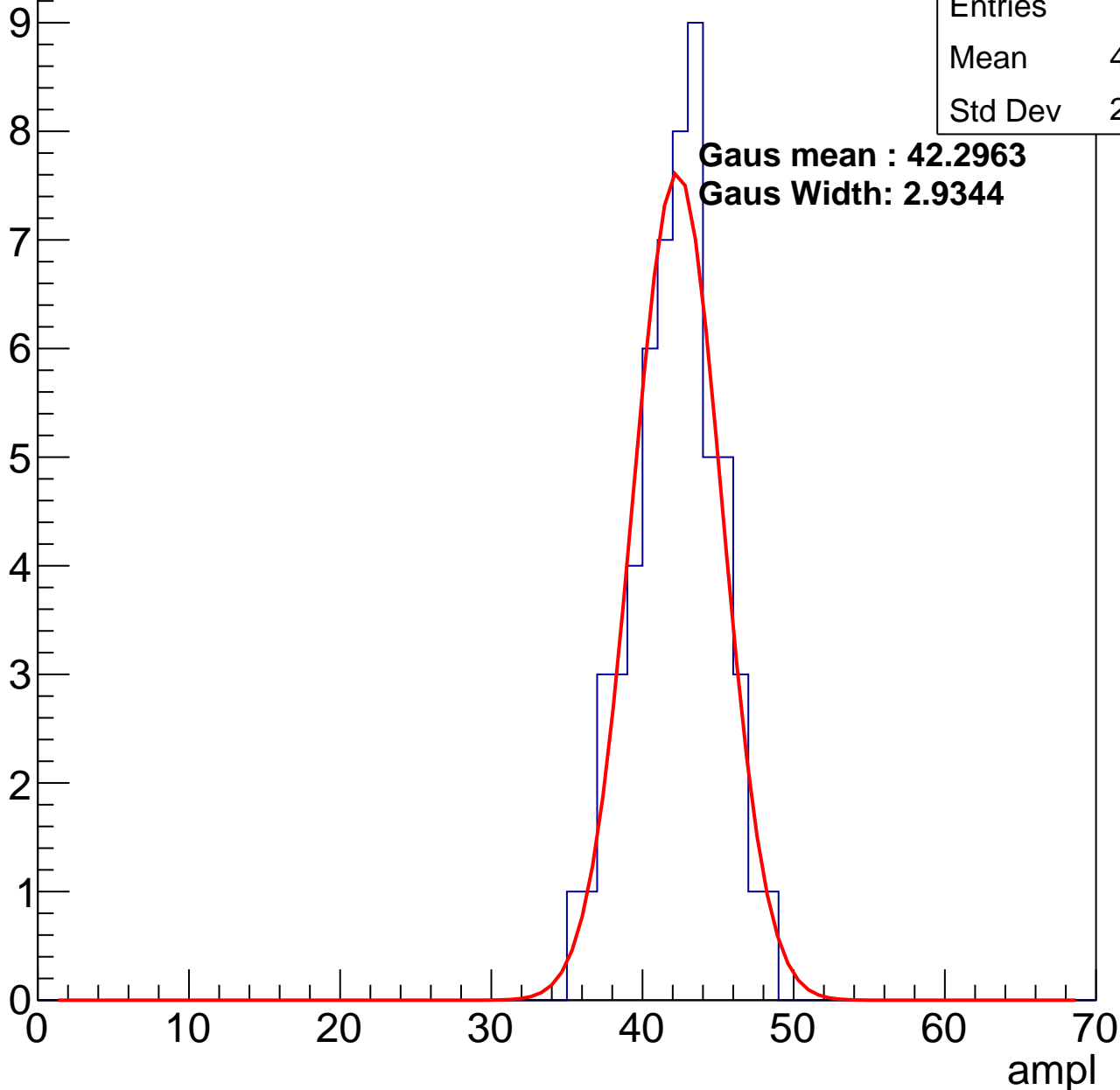
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	41.75
Std Dev	2.824

**Gaus mean : 42.2963**

**Gaus Width: 2.9344**

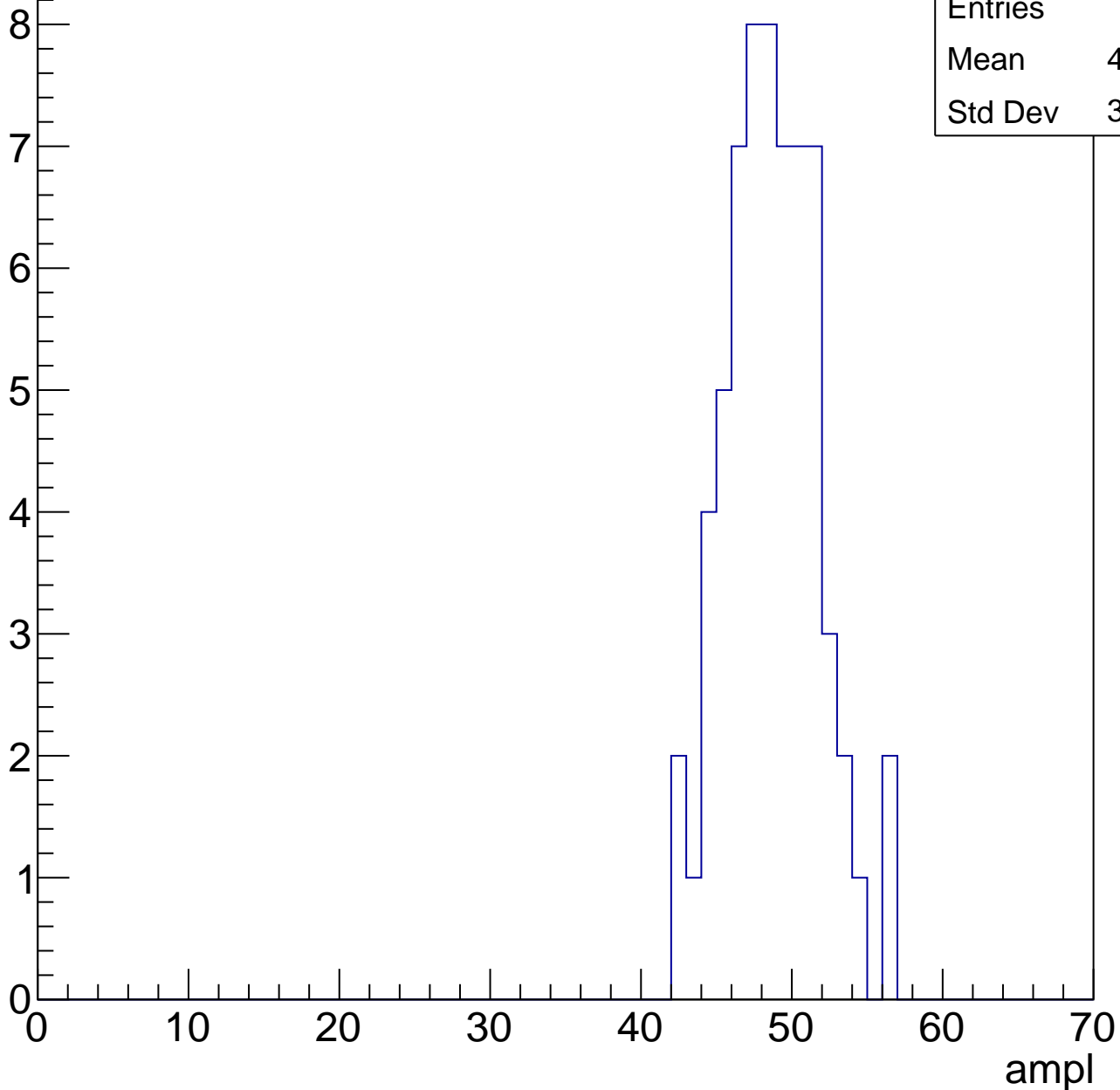


# B1L000S, U25-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.25
Std Dev	3.057

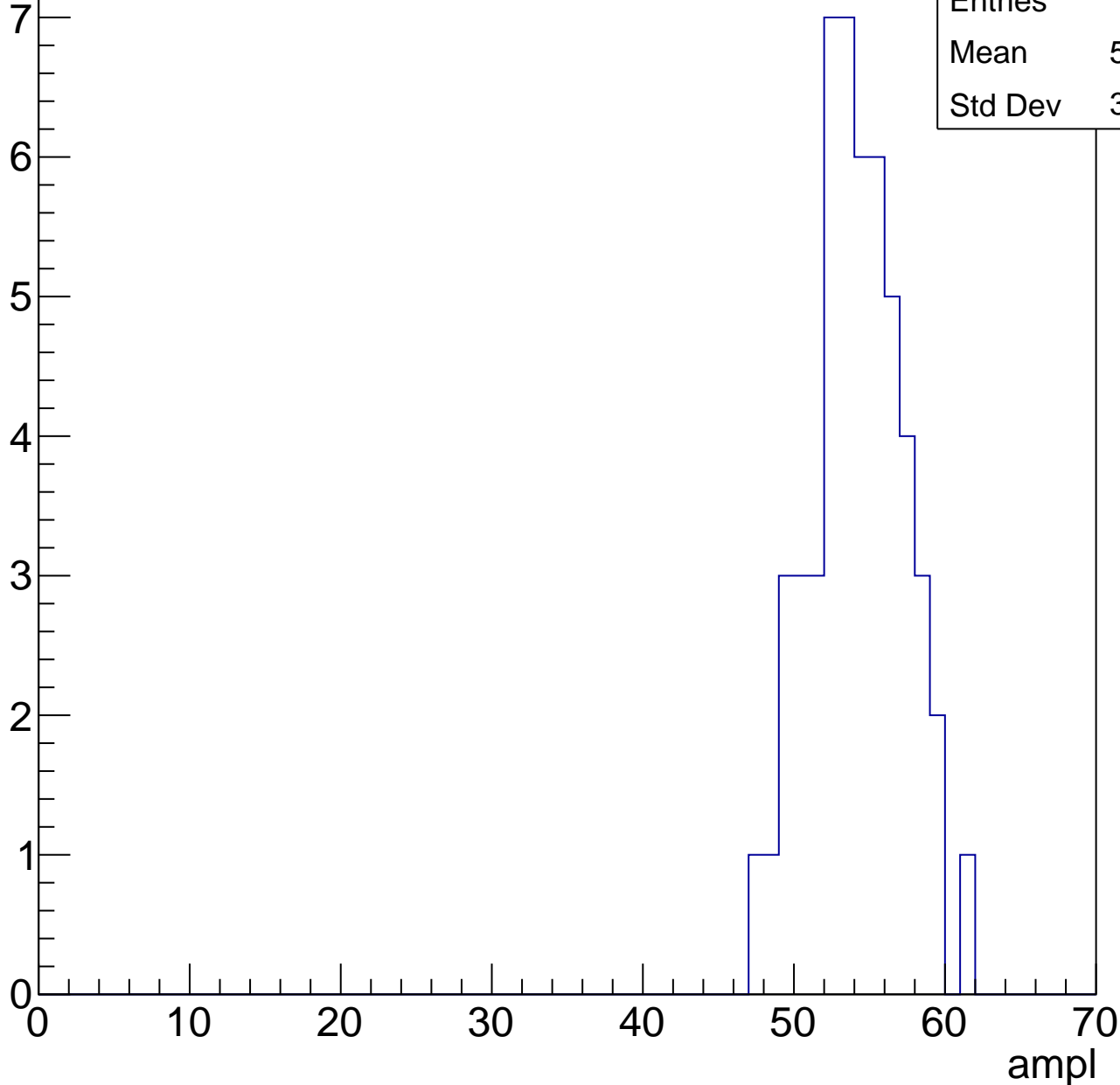


# B1L000S, U25-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	53.75
Std Dev	3.025

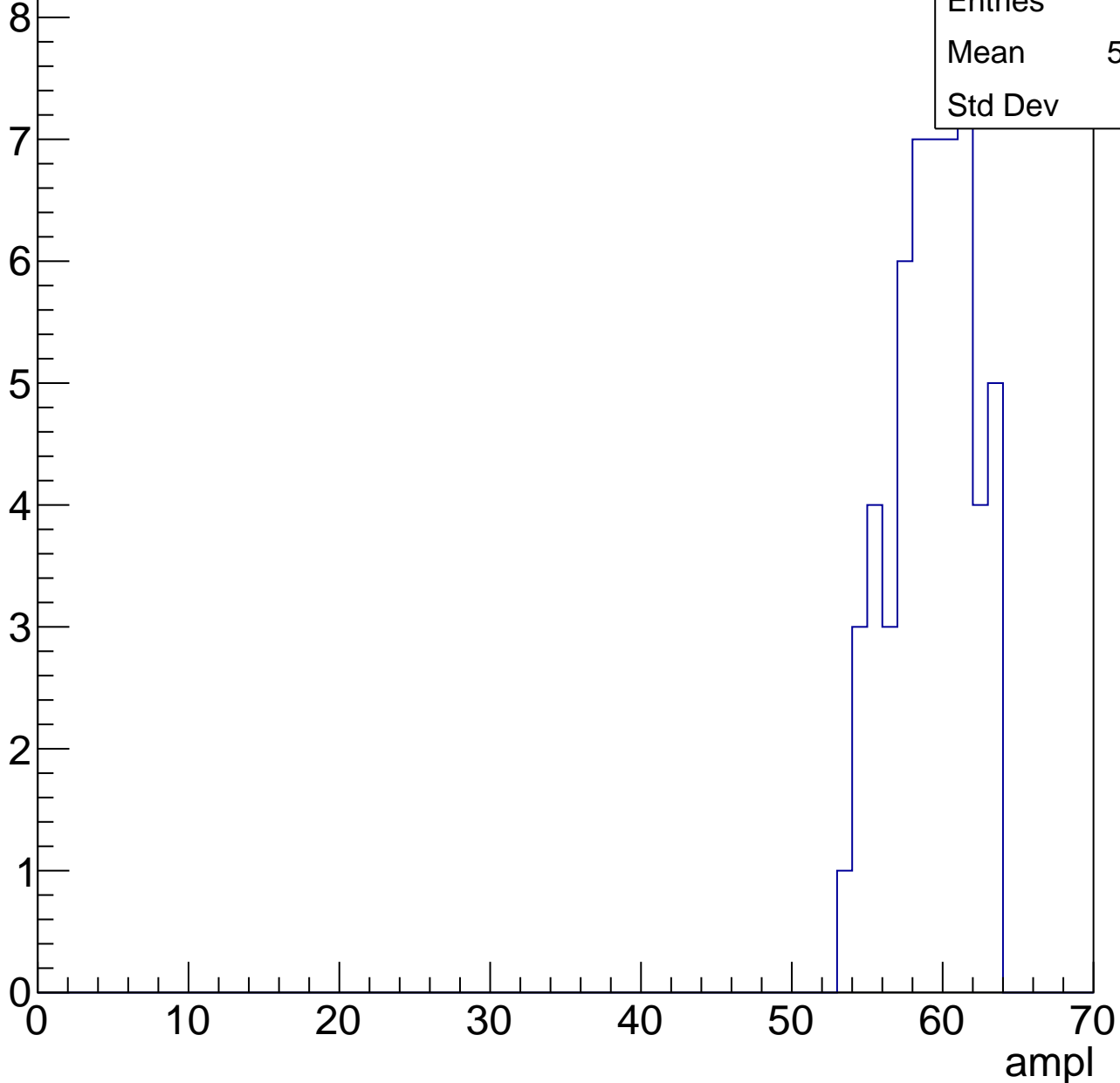


# B1L000S, U25-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.82
Std Dev	2.65

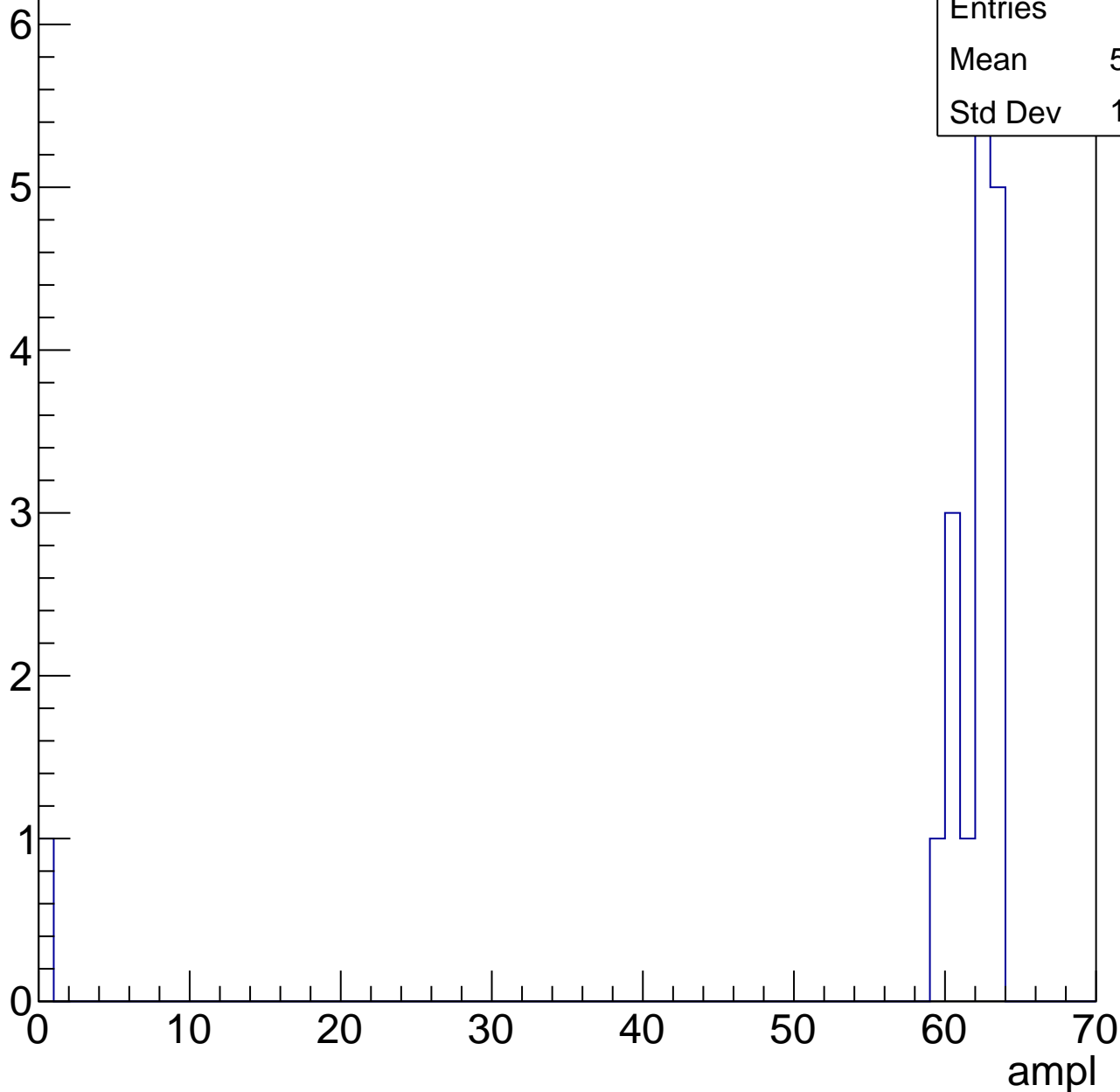


# B1L000S, U25-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.06
Std Dev	14.57





# B1L000S, U25-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L000S, U25-ch41, adc0

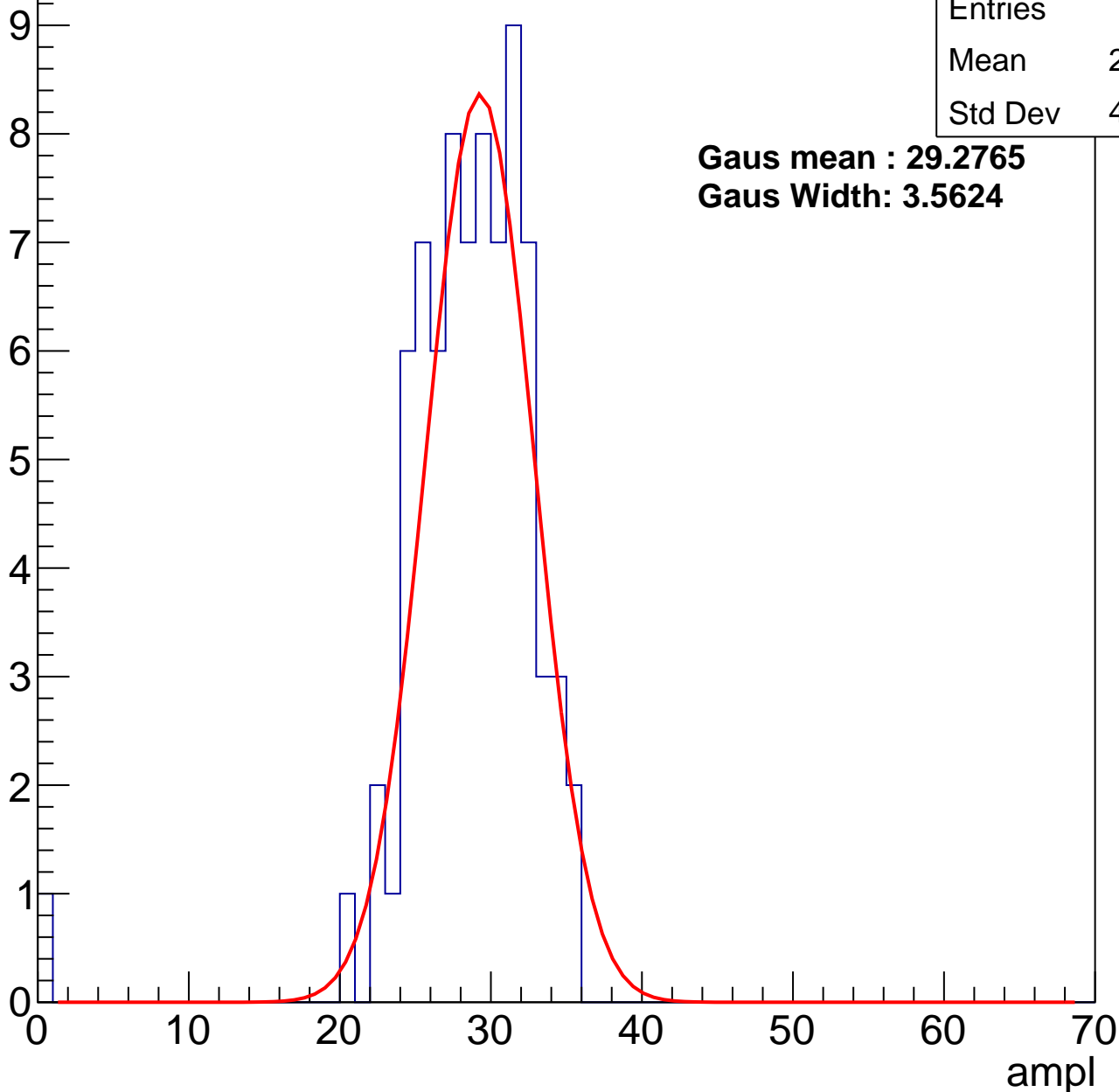
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	28.08
Std Dev	4.593

**Gaus mean : 29.2765**

**Gaus Width: 3.5624**



# B1L000S, U25-ch41, adc1

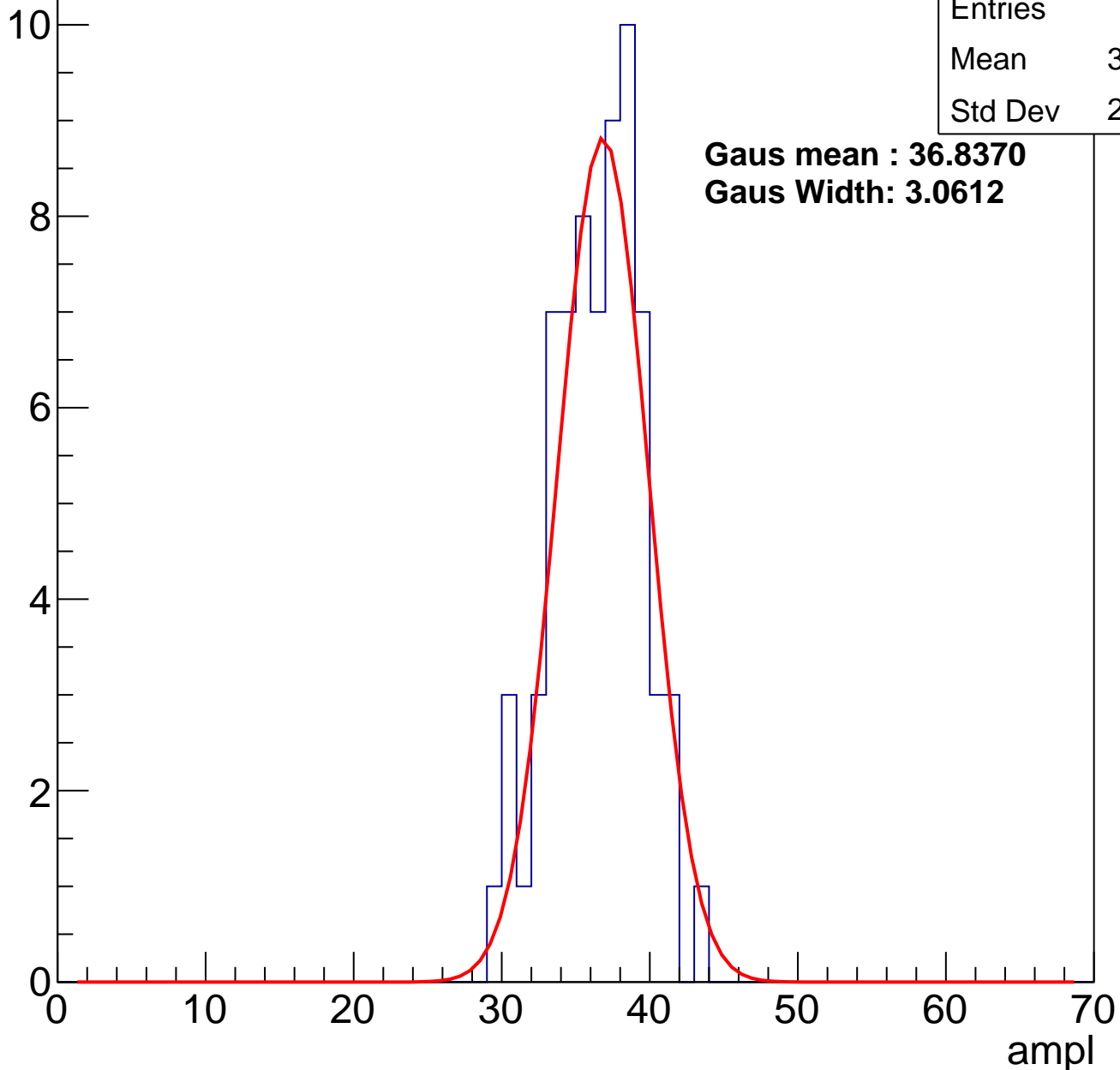
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	35.99
Std Dev	2.959

**Gaus mean : 36.8370**

**Gaus Width: 3.0612**

Entry



# B1L000S, U25-ch41, adc2

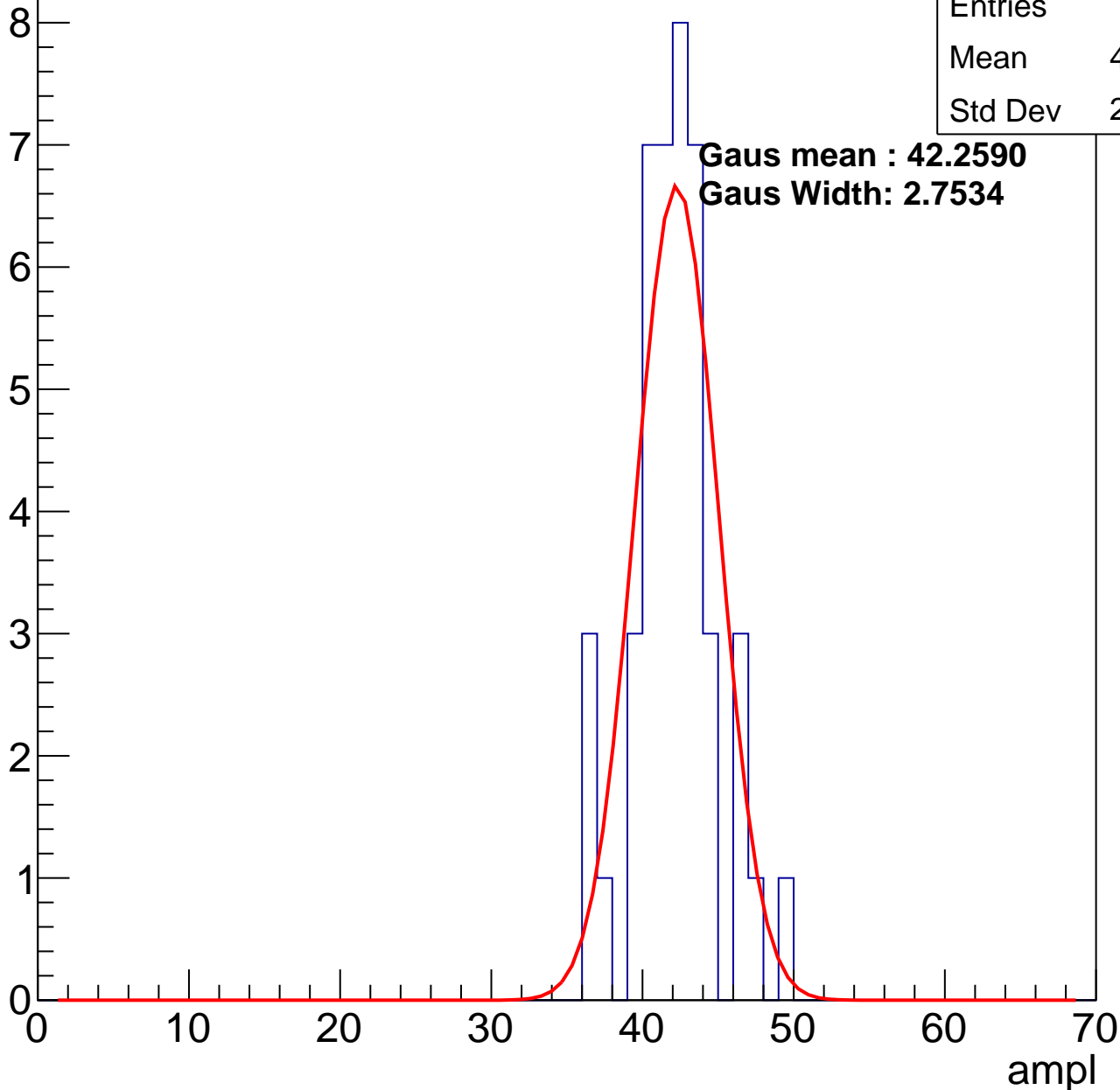
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	41.64
Std Dev	2.739

**Gaus mean : 42.2590**

**Gaus Width: 2.7534**



# B1L000S, U25-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

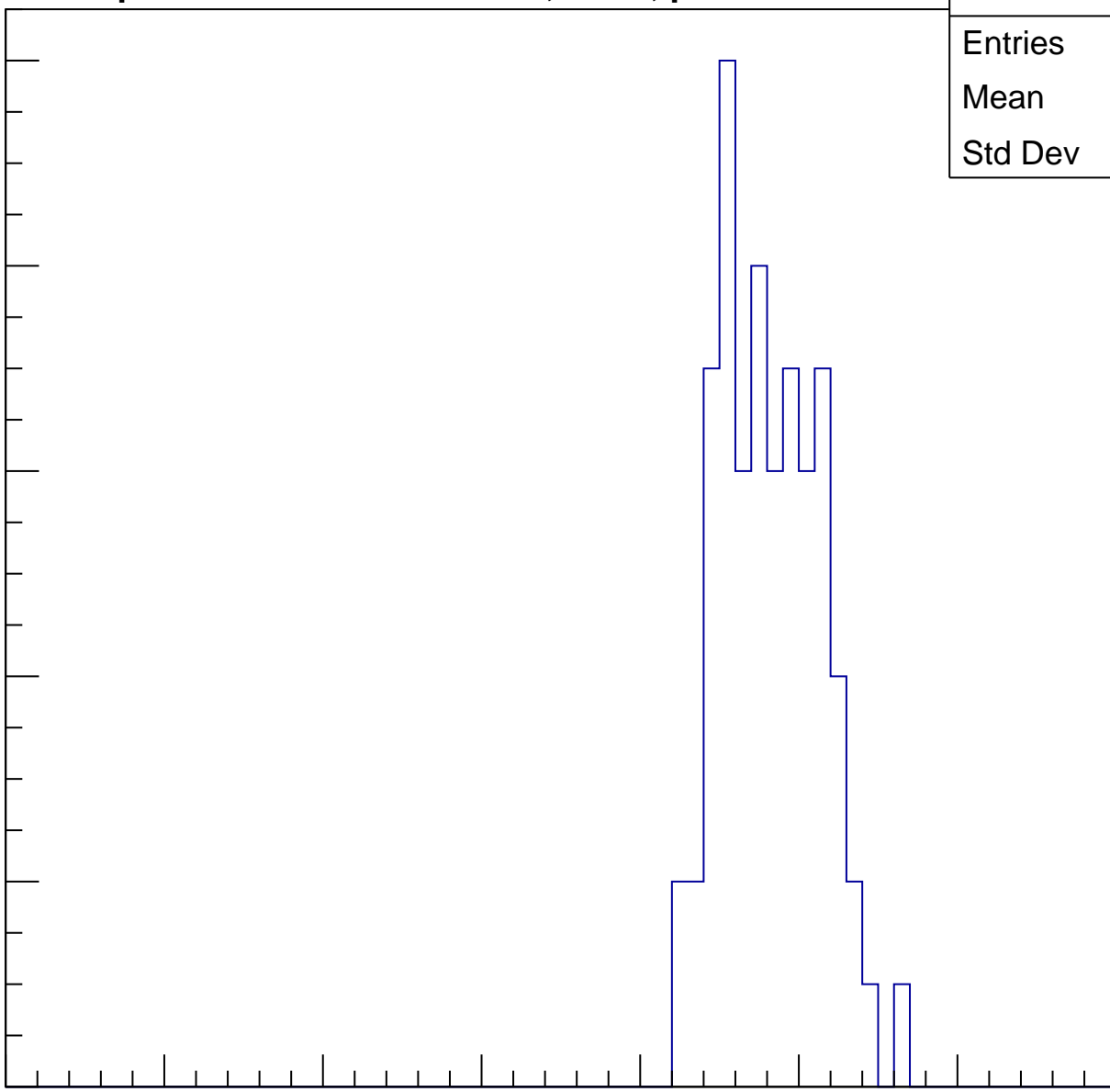
Entries	69
Mean	47.71
Std Dev	3.089

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

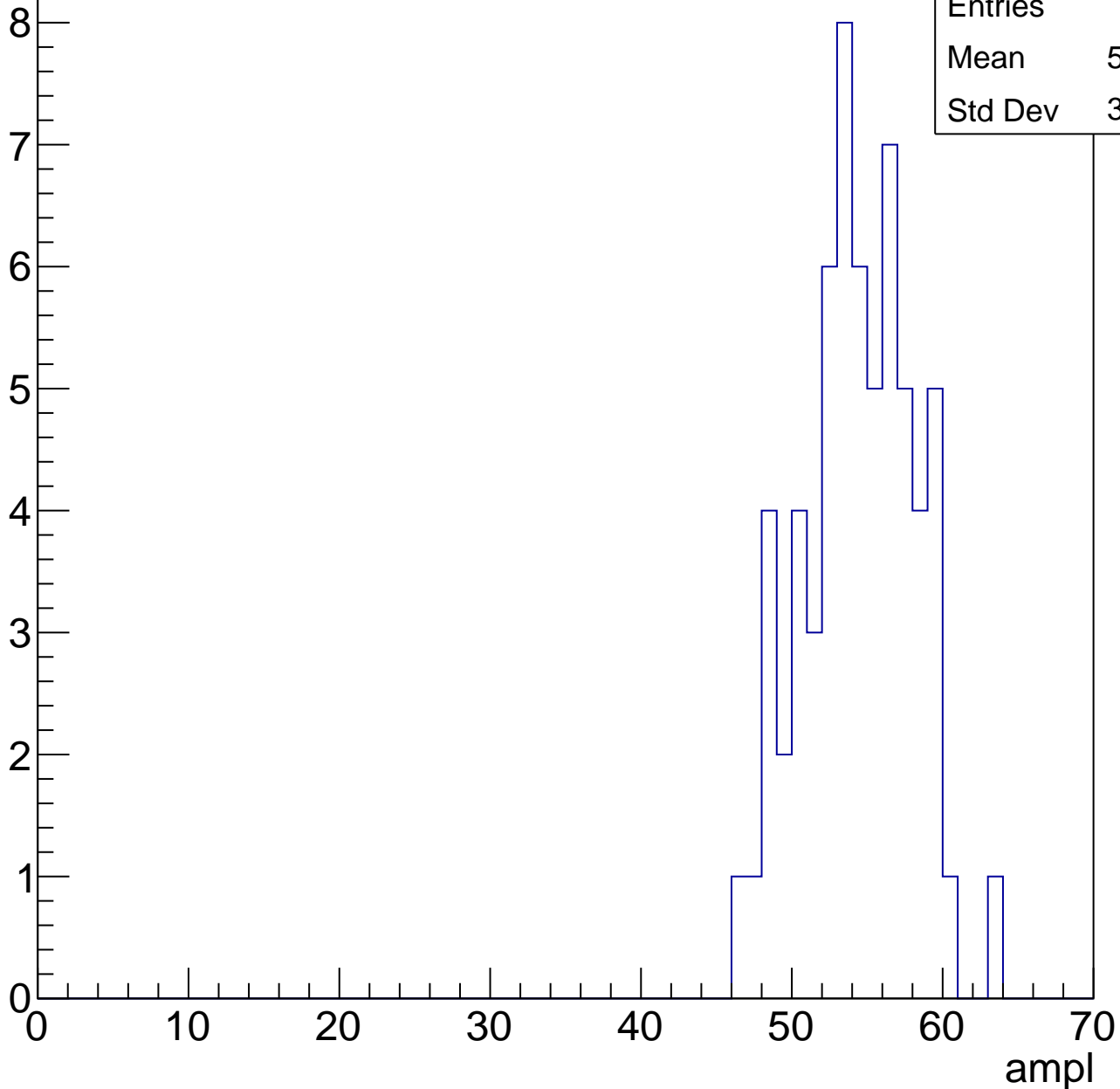


# B1L000S, U25-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	53.94
Std Dev	3.594



# B1L000S, U25-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

Entries 56

Mean 58.14

Std Dev 8.269

8

6

4

2

0

0

10

20

30

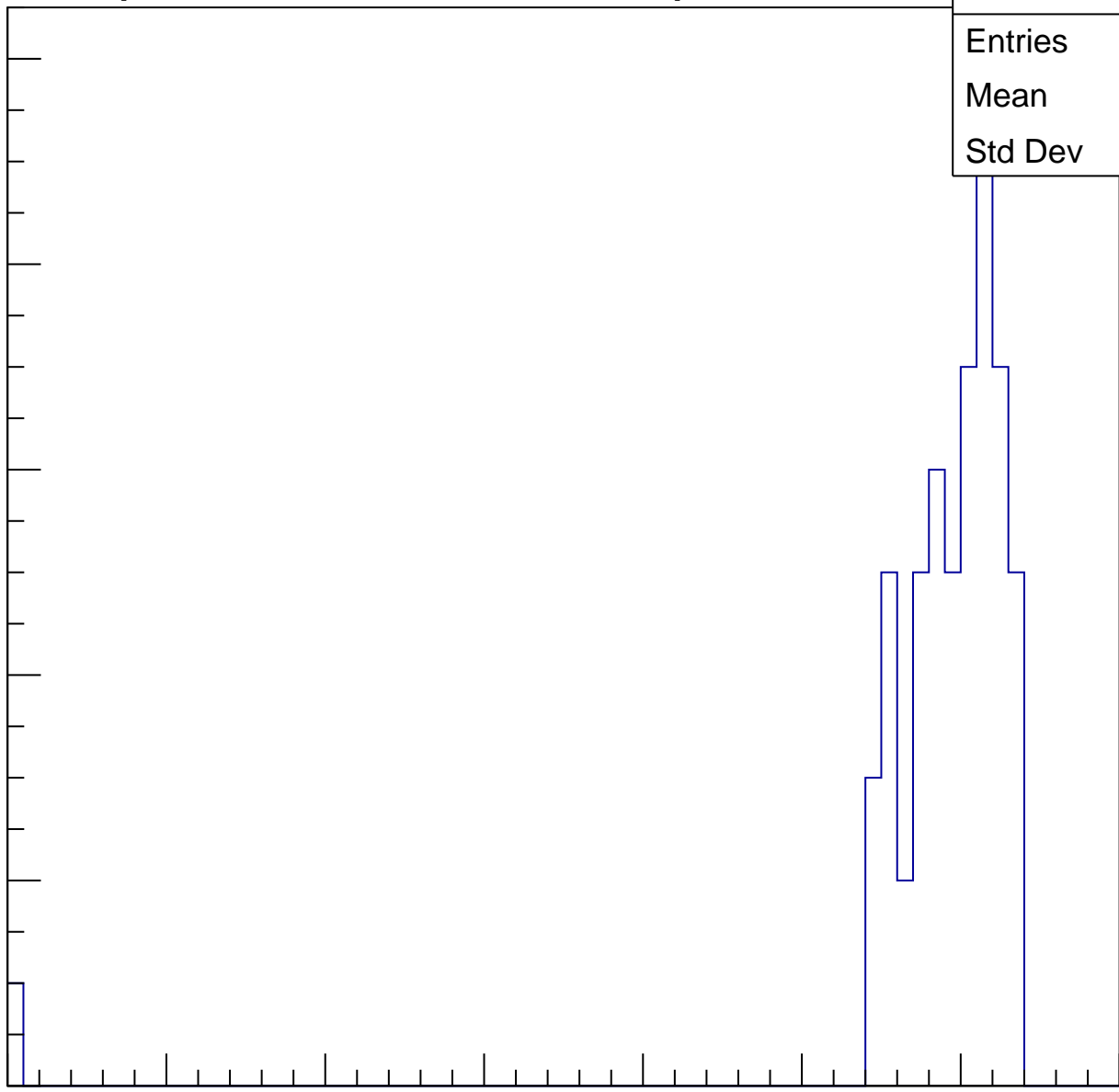
40

50

60

70

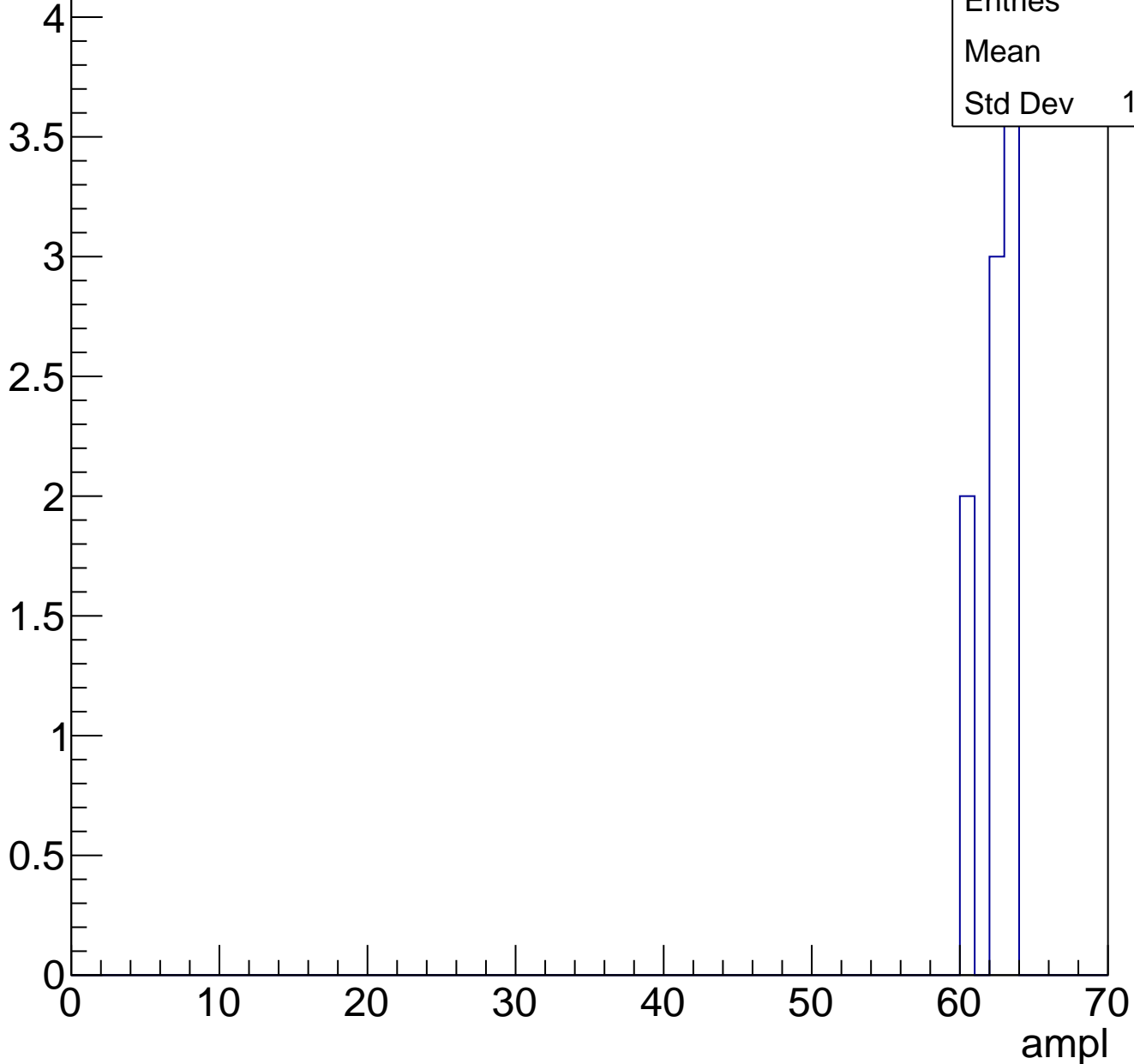
ampl



# B1L000S, U25-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch42, adc0

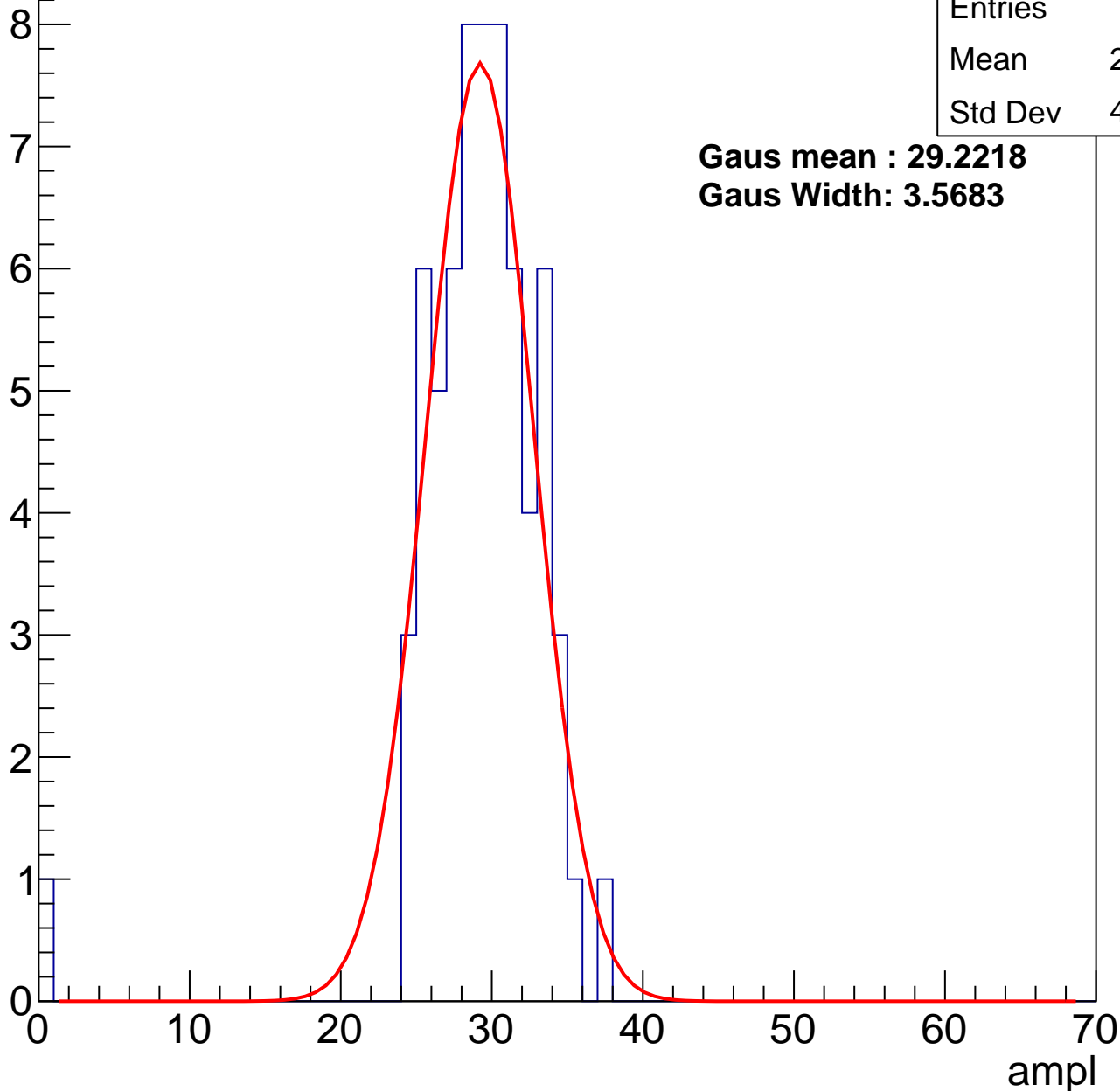
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	28.73
Std Dev	4.643

**Gaus mean : 29.2218**

**Gaus Width: 3.5683**



# B1L000S, U25-ch42, adc1

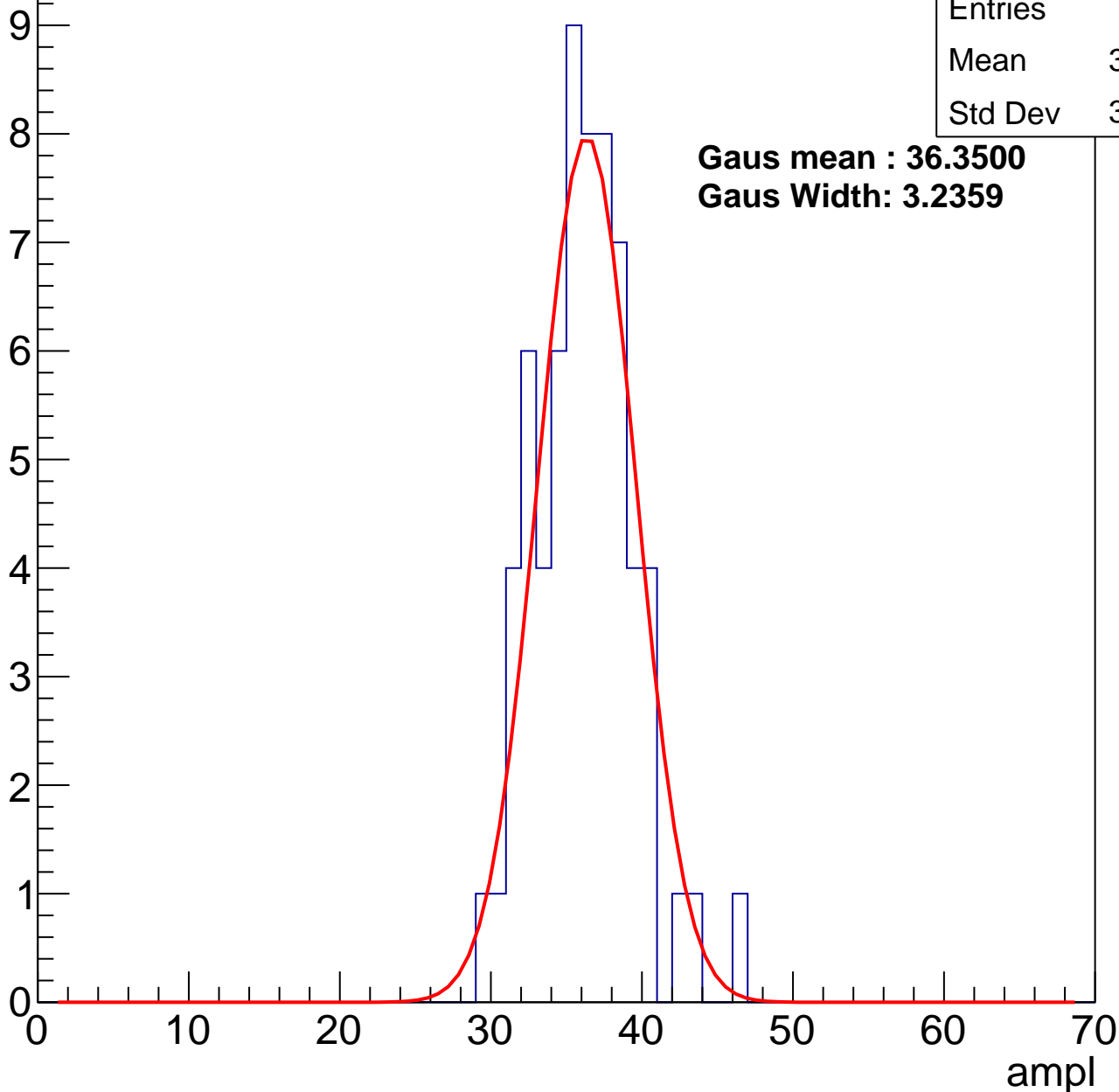
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.74
Std Dev	3.202

**Gaus mean : 36.3500**

**Gaus Width: 3.2359**



# B1L000S, U25-ch42, adc2

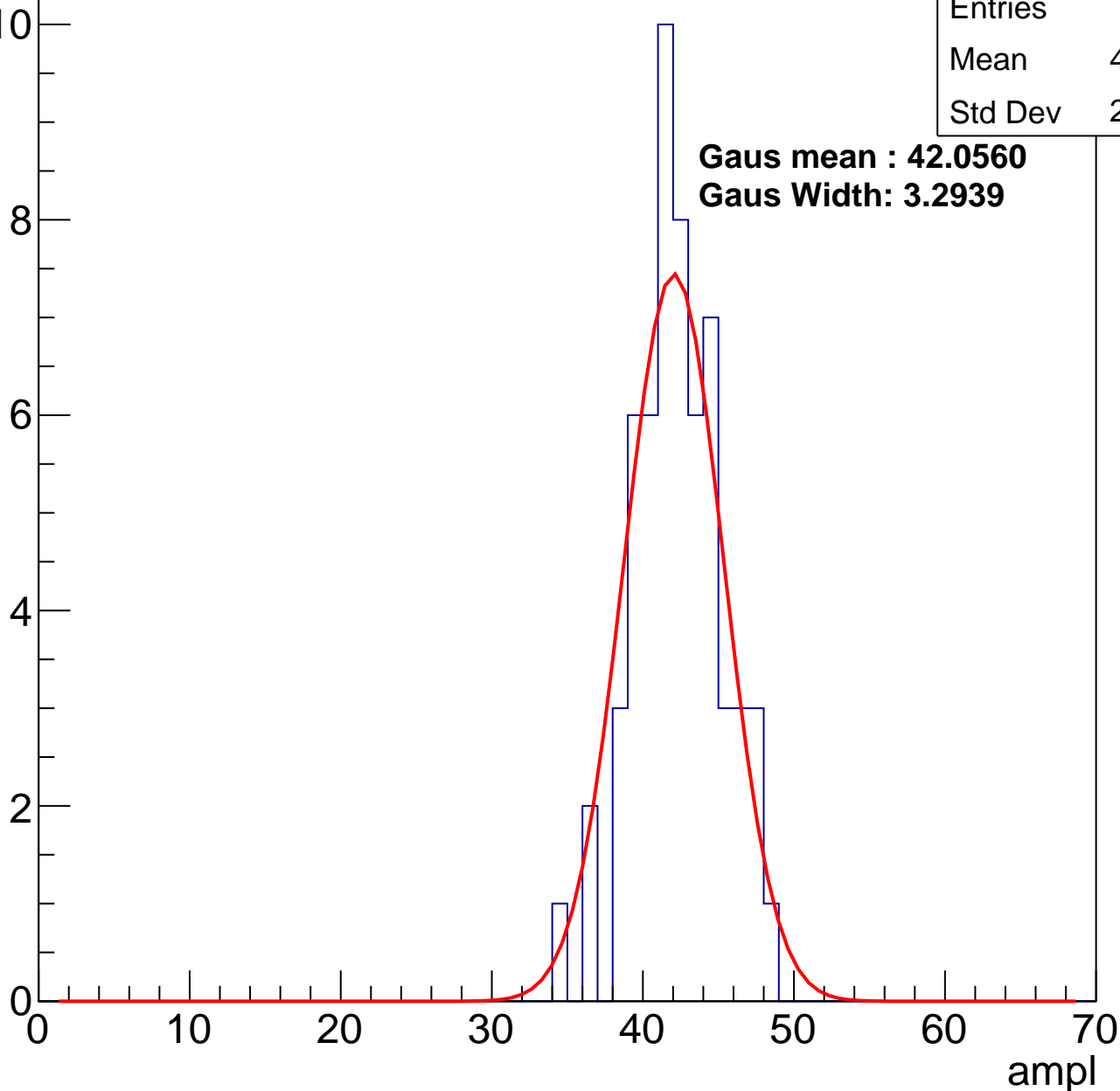
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.83
Std Dev	2.883

**Gaus mean : 42.0560**

**Gaus Width: 3.2939**

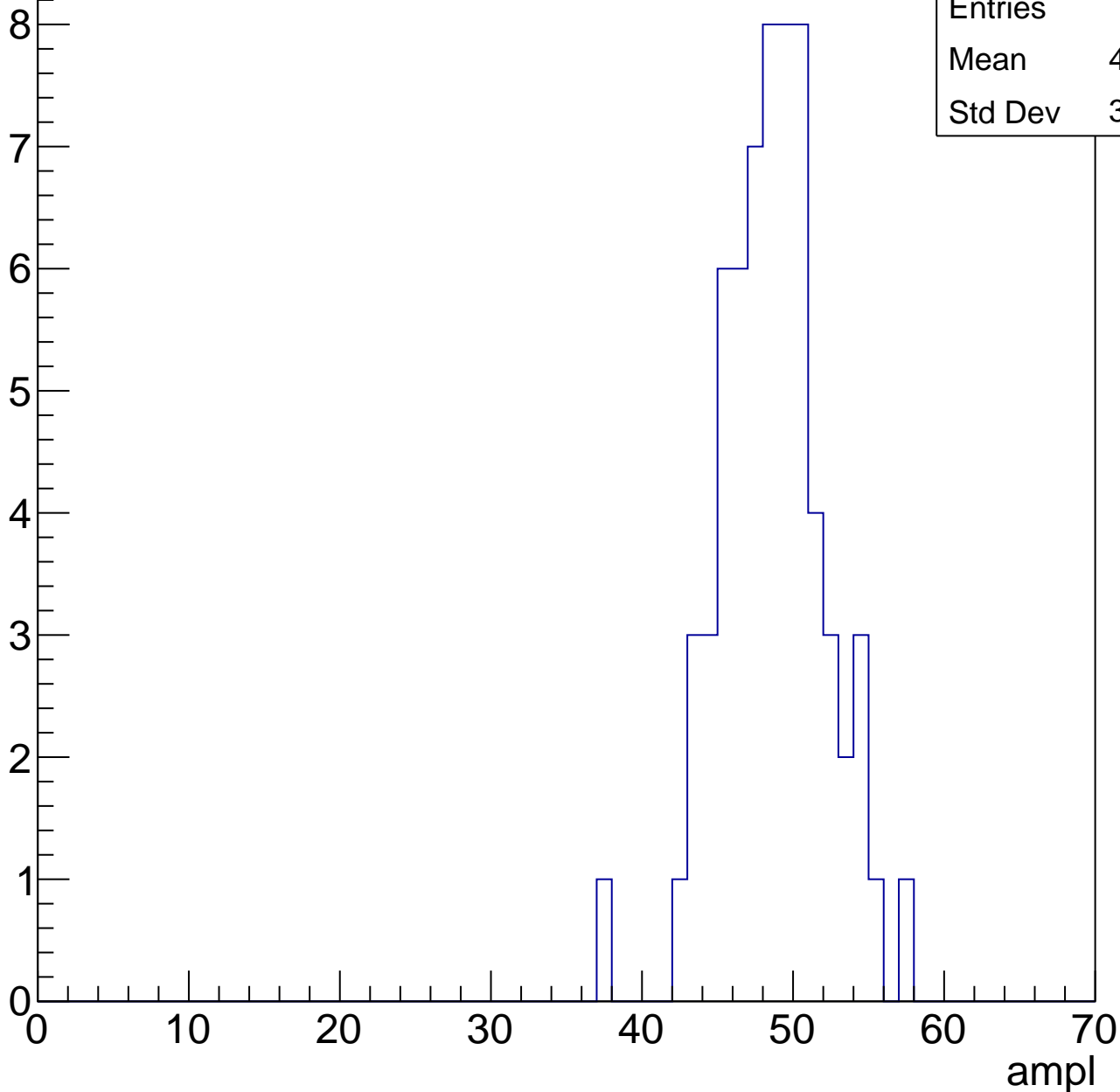


# B1L000S, U25-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	48.17
Std Dev	3.453

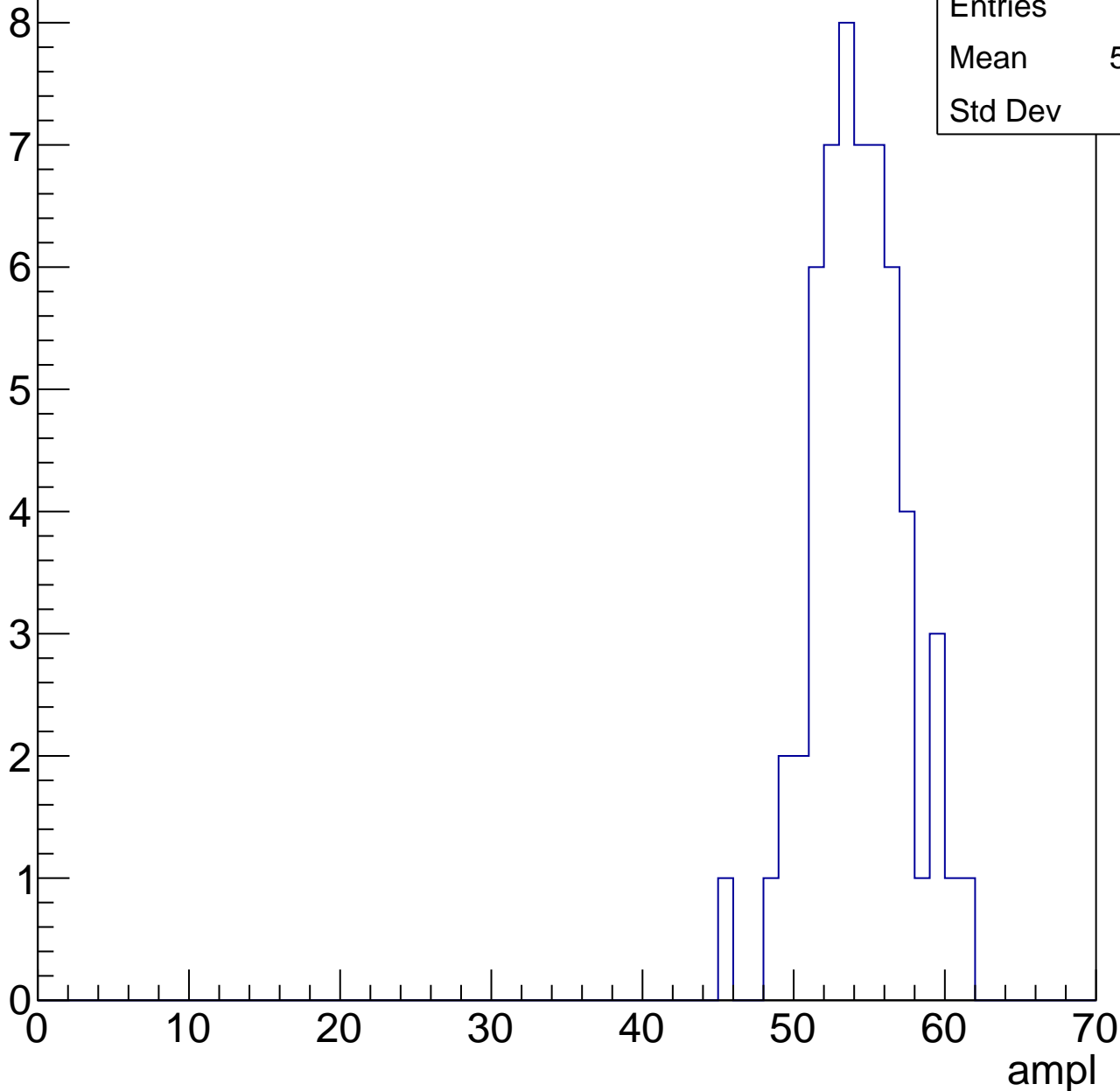


# B1L000S, U25-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

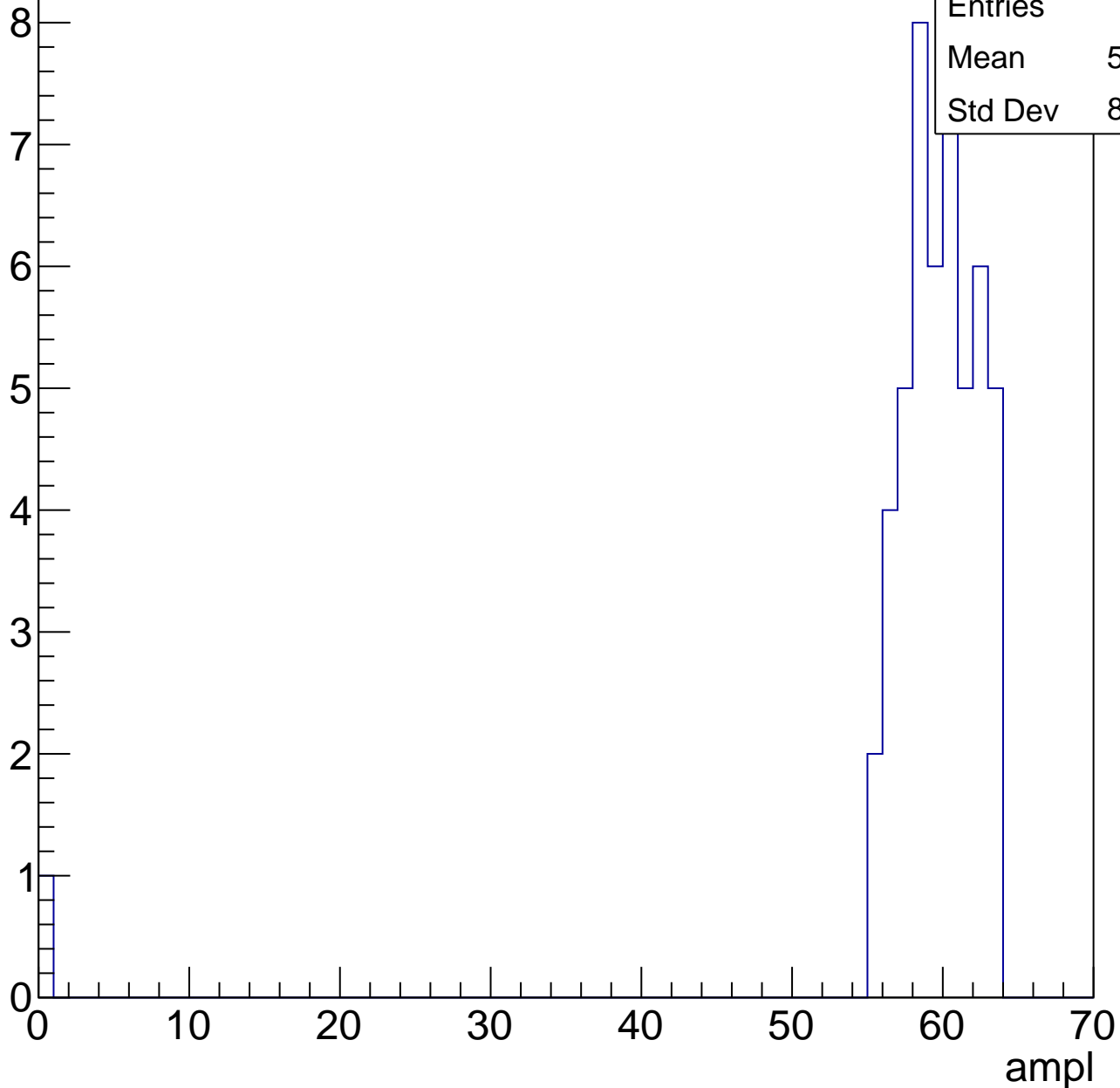
Entries	57
Mean	53.82
Std Dev	3.05



# B1L000S, U25-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

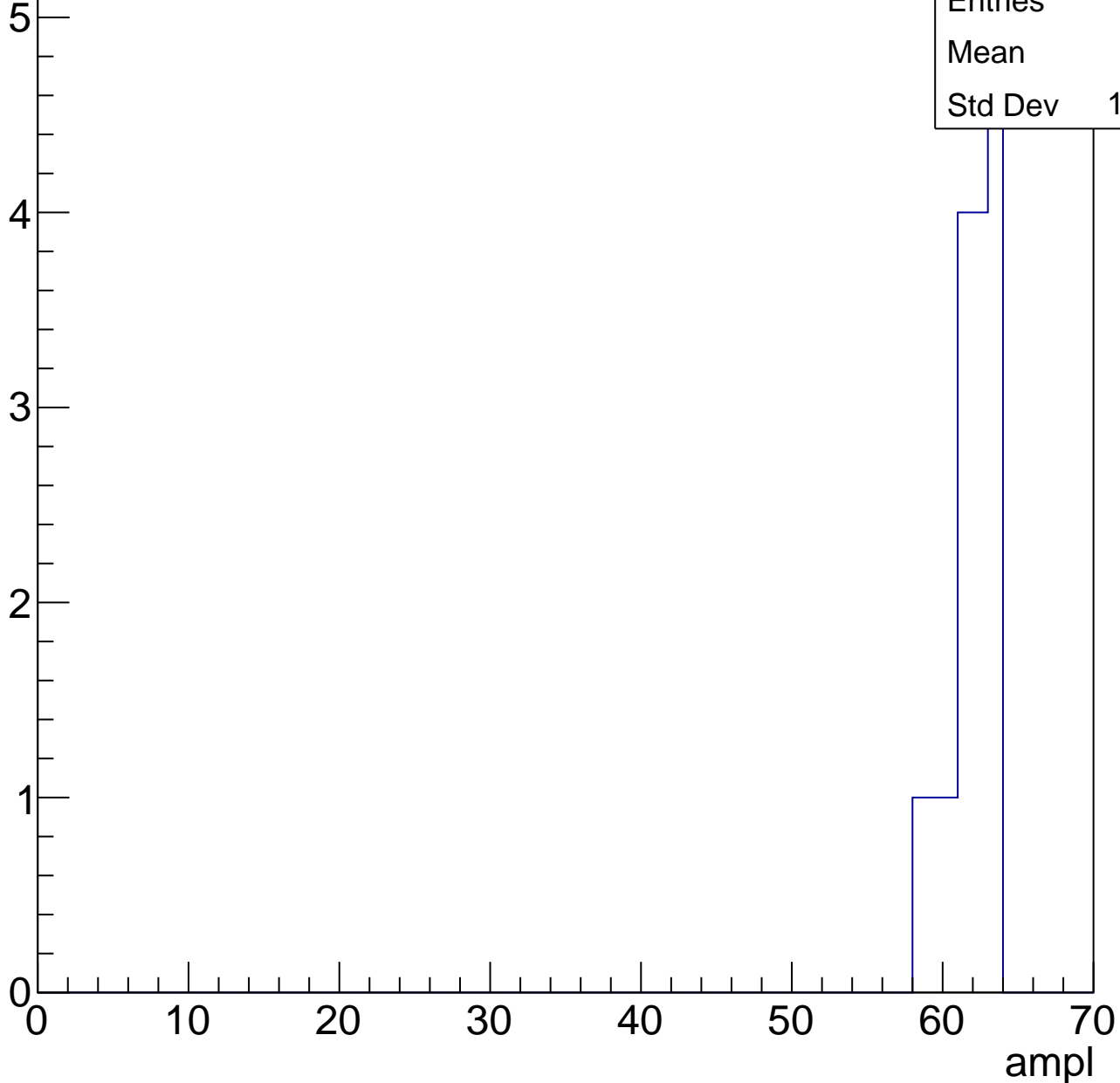


# B1L000S, U25-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	61.5
Std Dev	1.458

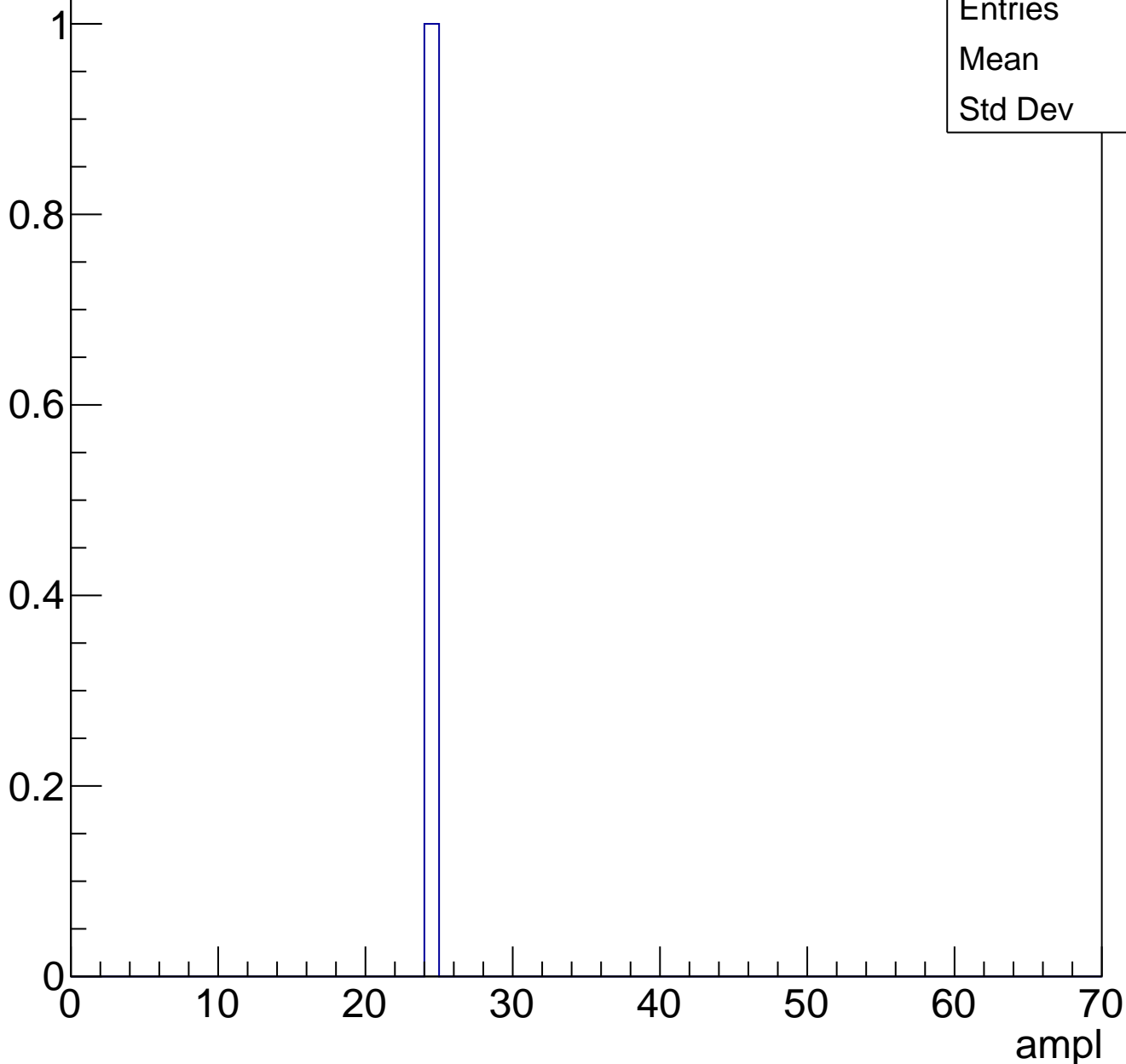




# B1L000S, U25-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch43, adc0

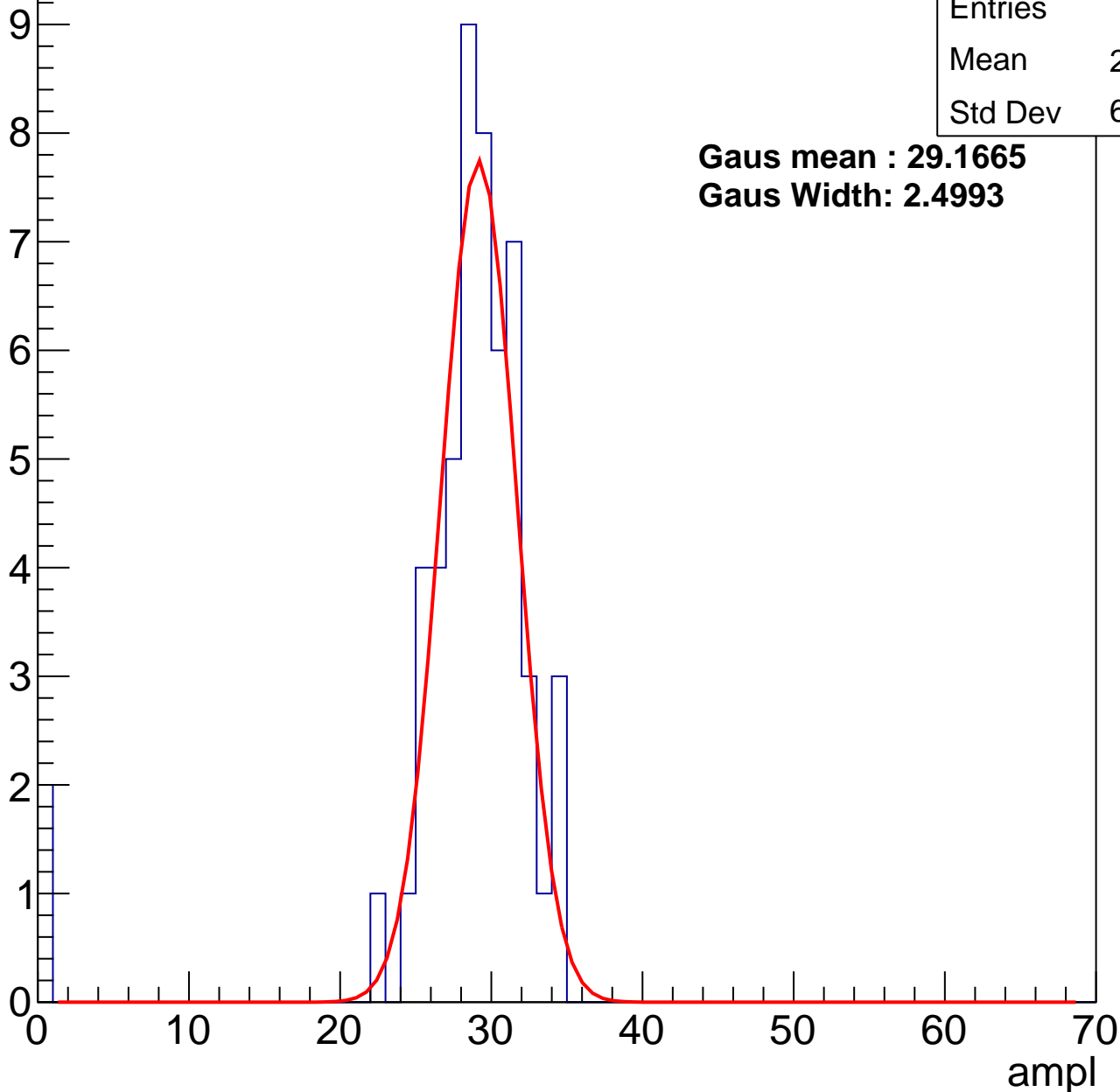
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	27.72
Std Dev	6.007

**Gaus mean : 29.1665**

**Gaus Width: 2.4993**

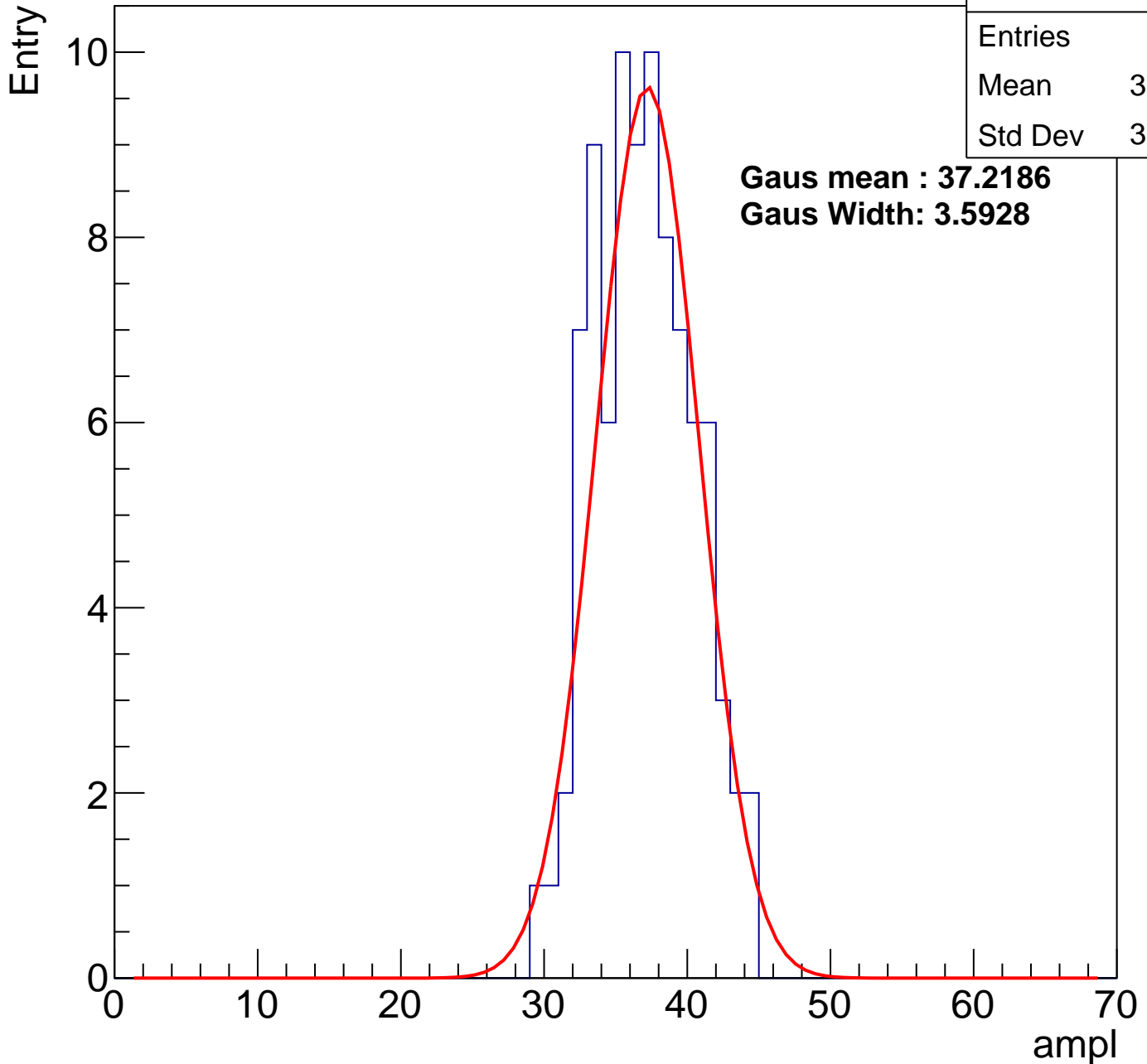


# B1L000S, U25-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	89
Mean	36.55
Std Dev	3.385

**Gaus mean : 37.2186**  
**Gaus Width: 3.5928**



# B1L000S, U25-ch43, adc2

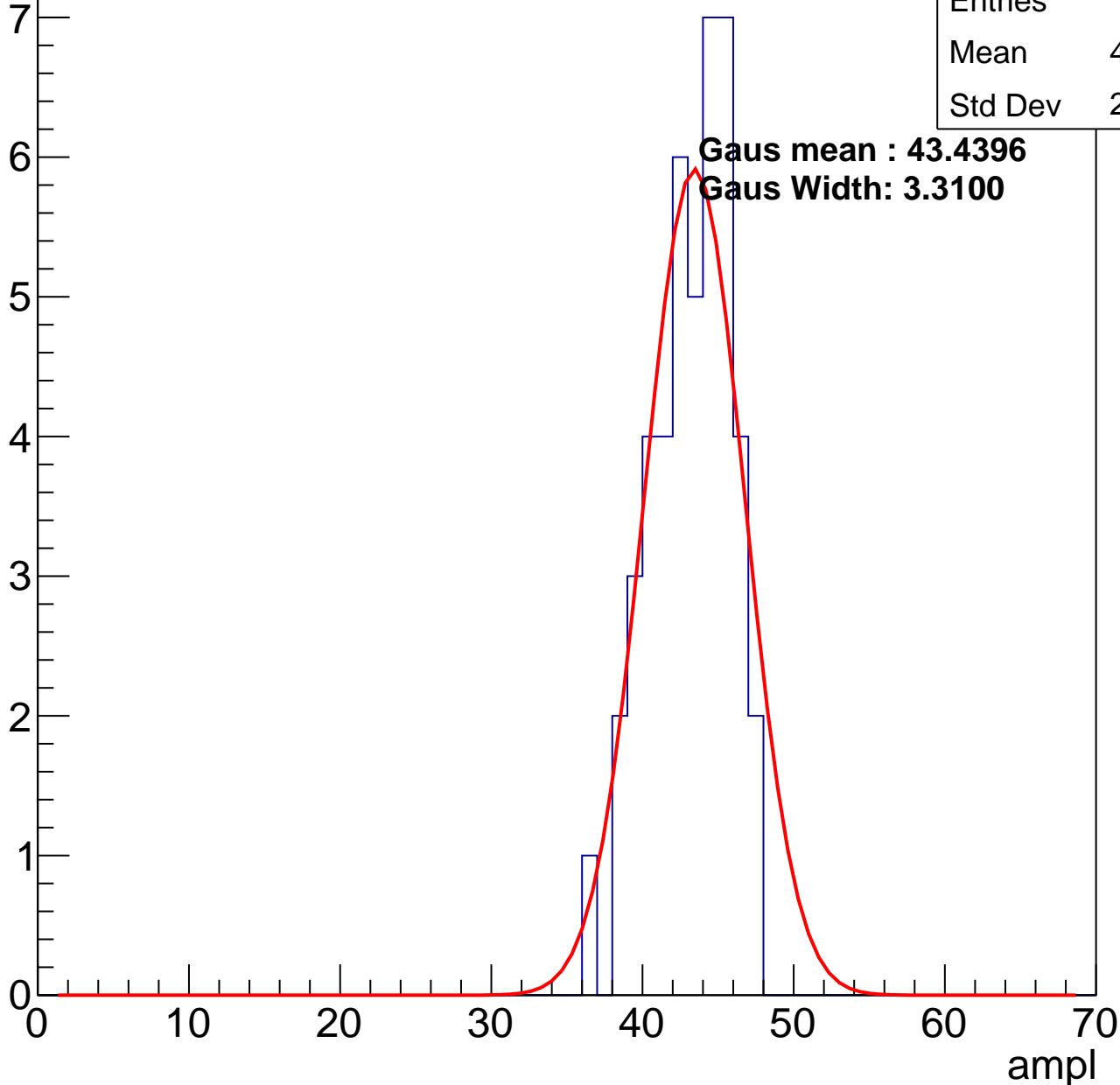
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	42.69
Std Dev	2.598

**Gaus mean : 43.4396**

**Gaus Width: 3.3100**



# B1L000S, U25-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

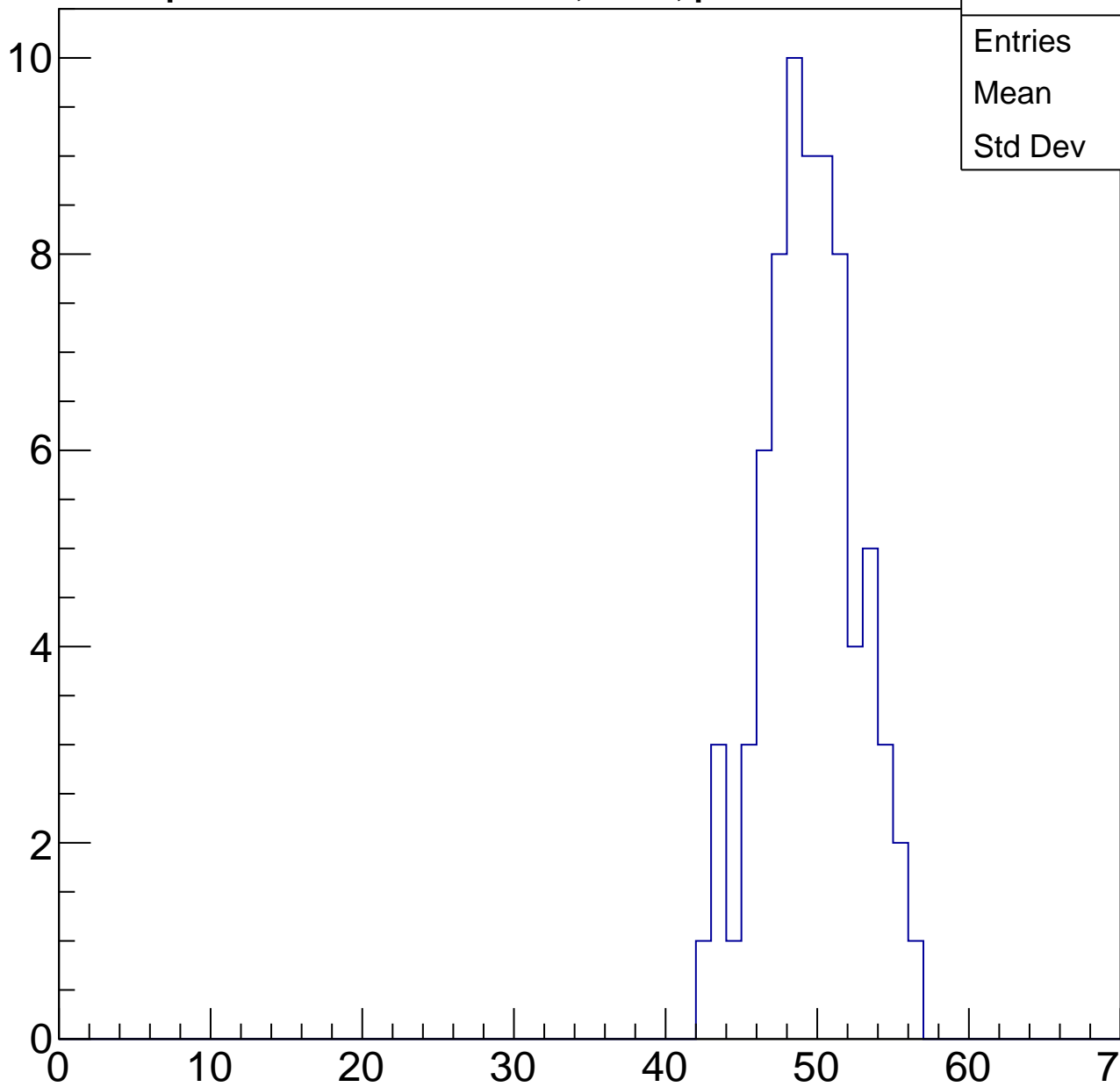
Entries	73
Mean	49.07
Std Dev	3.049

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	62
Mean	55.56
Std Dev	2.757

Entry

10

8

6

4

2

0

0

10

20

30

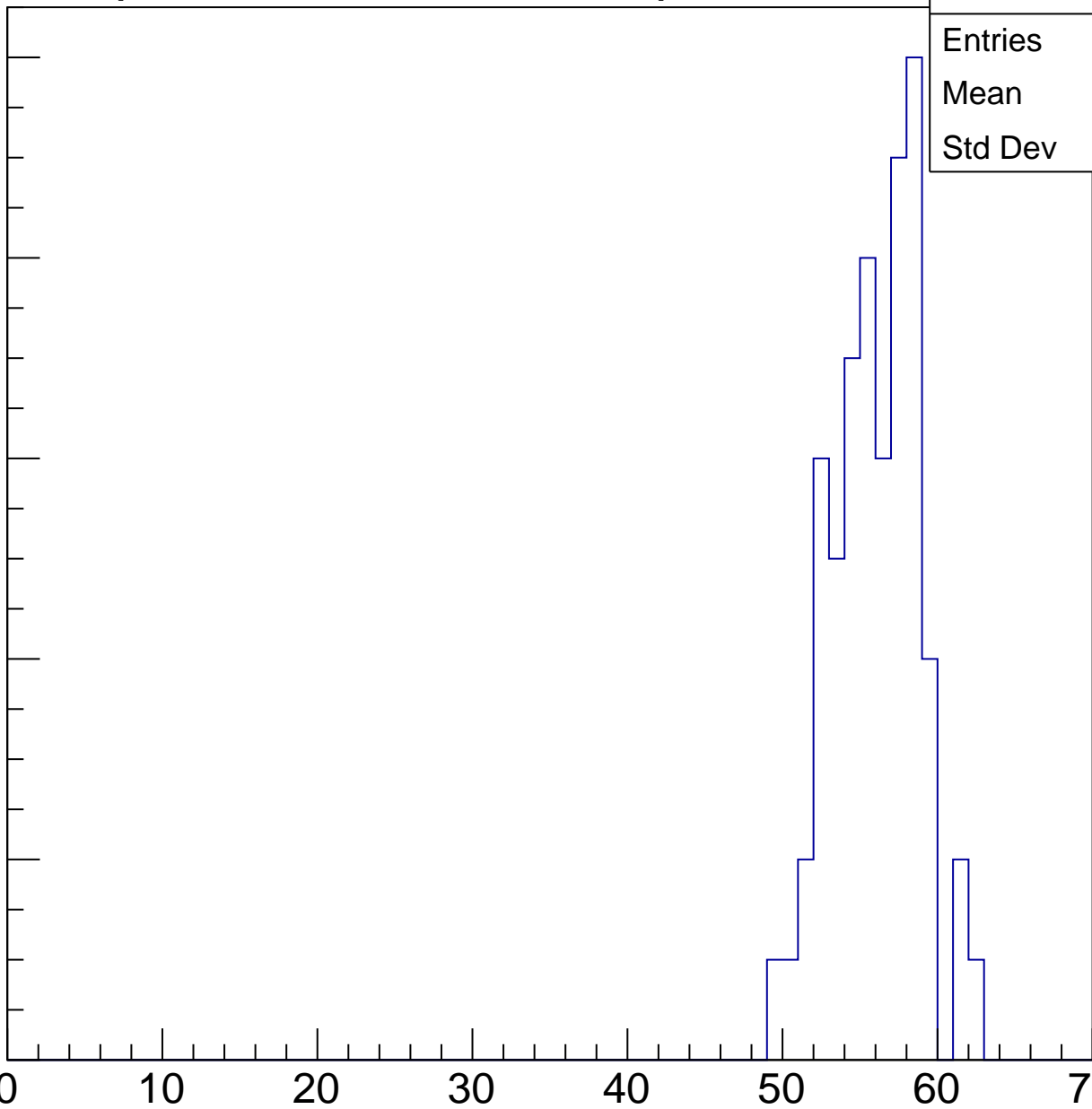
40

50

60

ampl

70



# B1L000S, U25-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

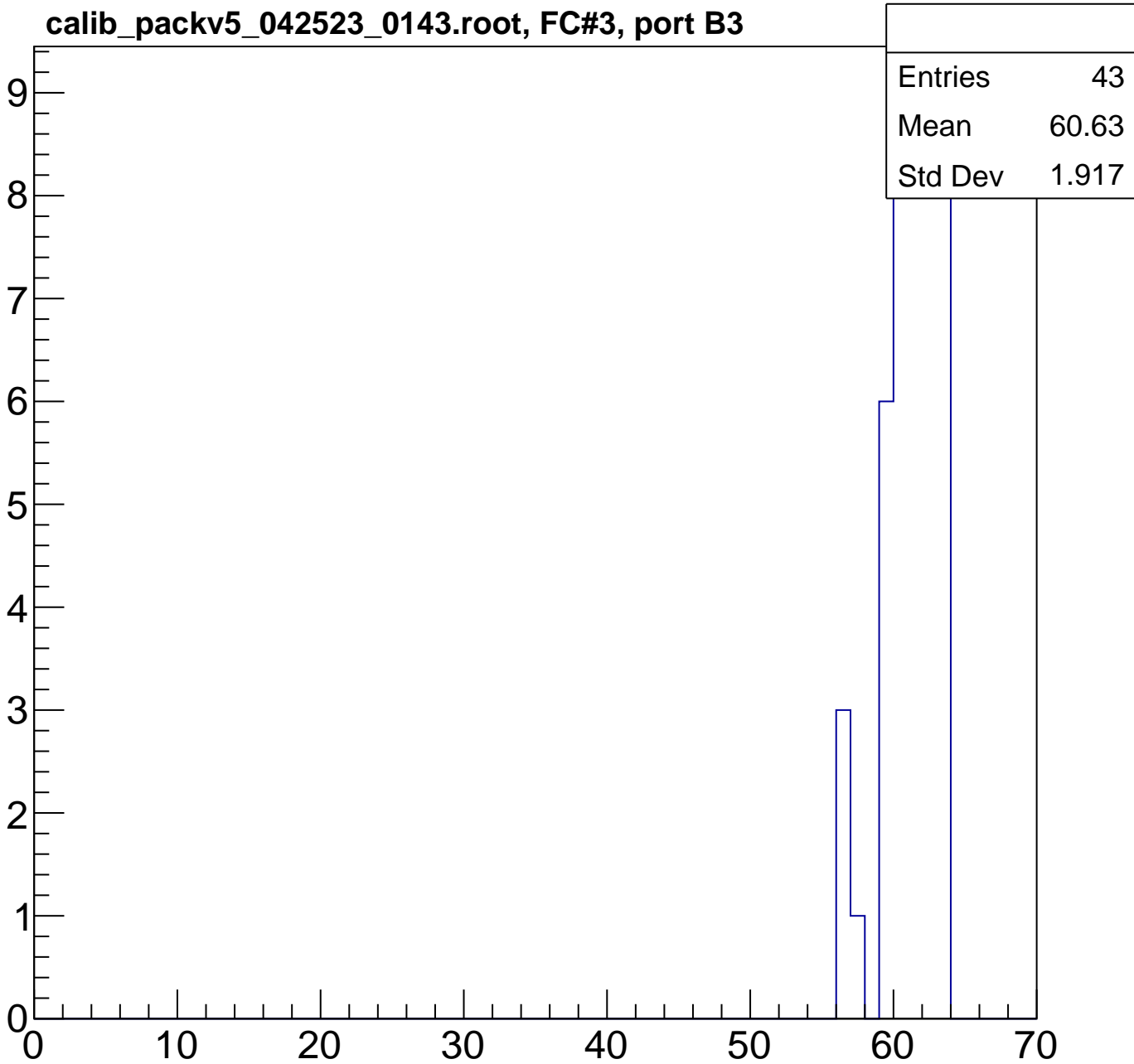
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	43
Mean	60.63
Std Dev	1.917

ampl

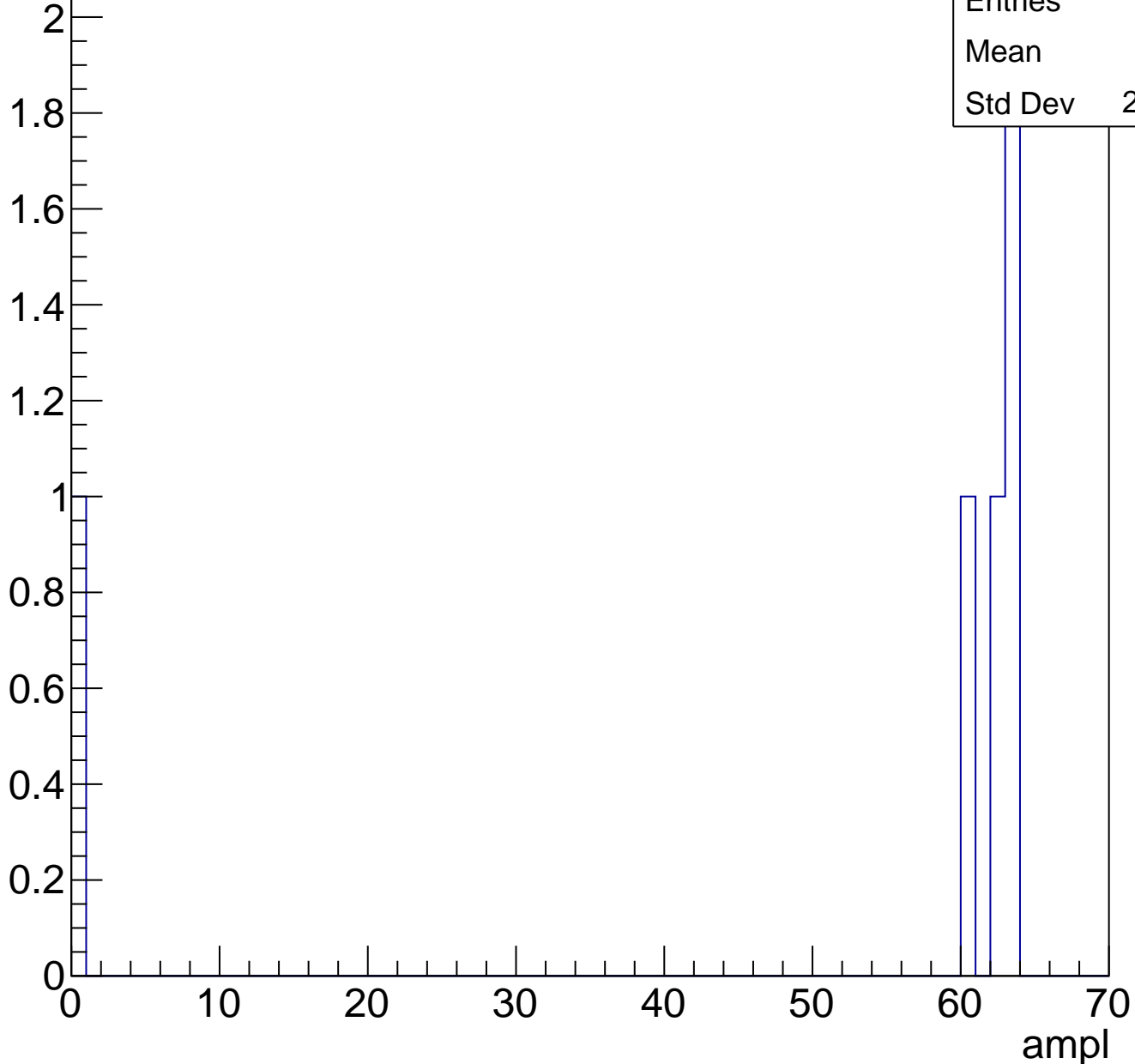
0 10 20 30 40 50 60 70



# B1L000S, U25-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	49.6
Std Dev	24.82



# B1L000S, U25-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch44, adc0

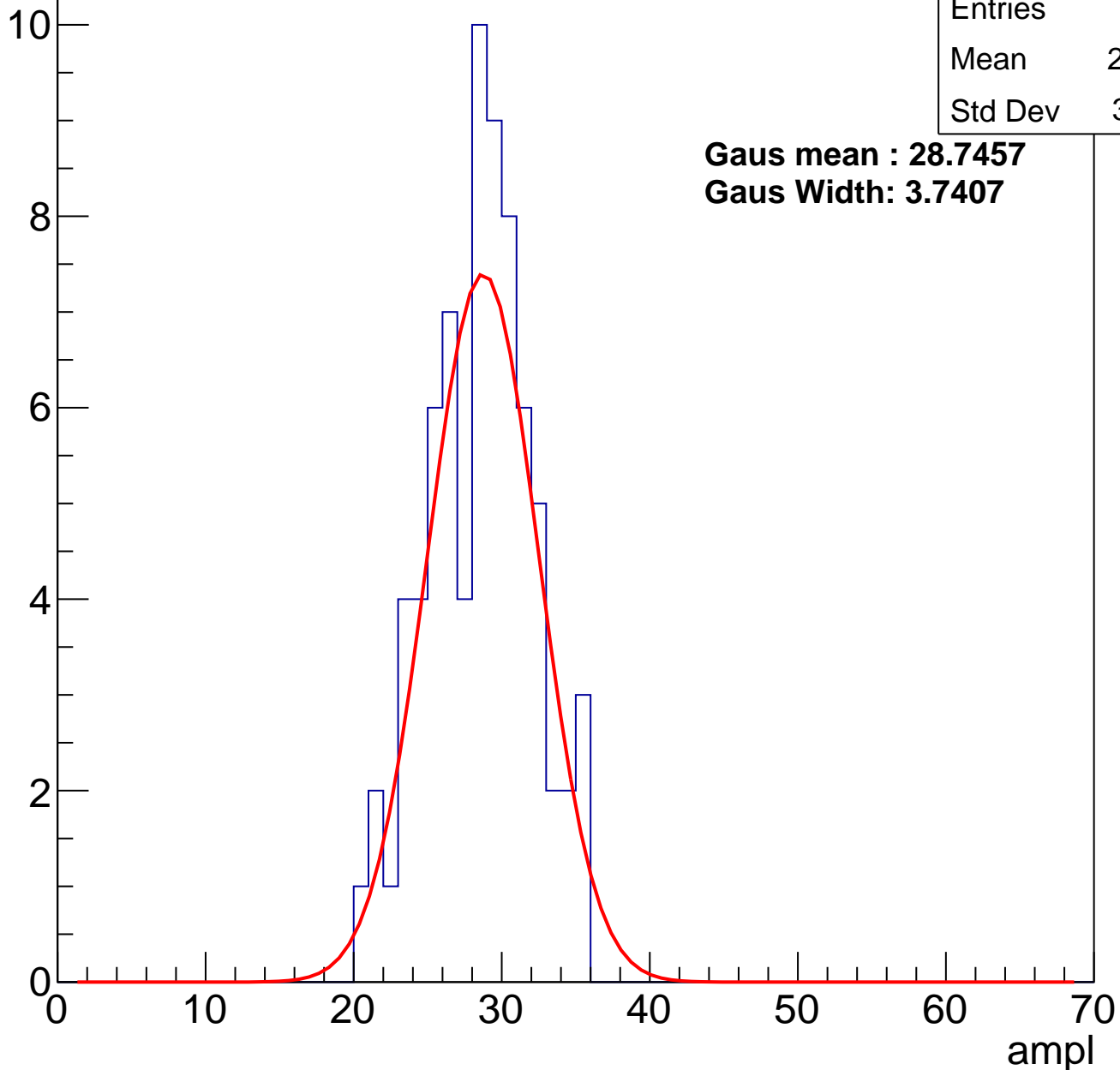
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	28.08
Std Dev	3.471

**Gaus mean : 28.7457**

**Gaus Width: 3.7407**

Entry



# B1L000S, U25-ch44, adc1

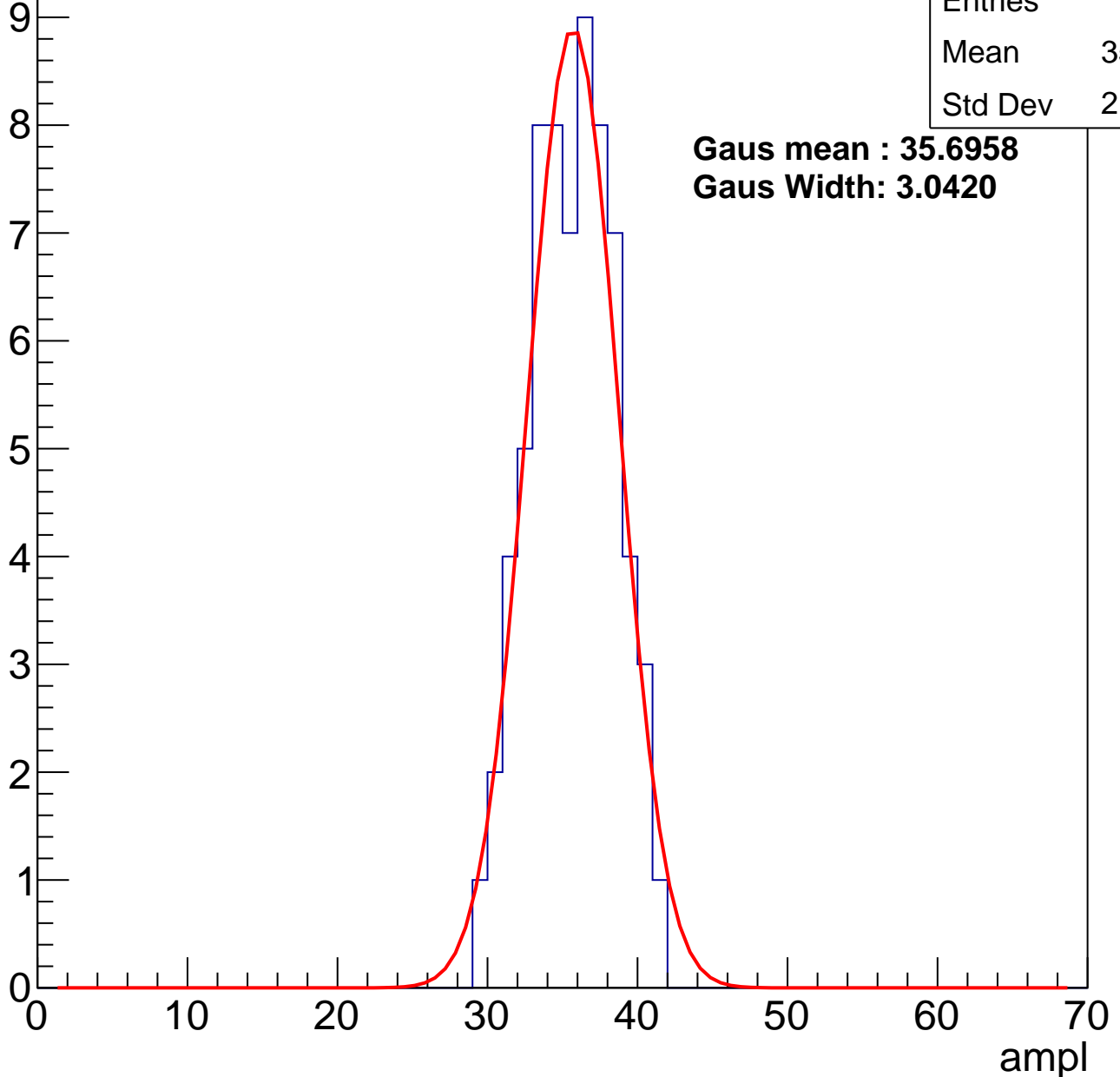
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	35.18
Std Dev	2.764

**Gaus mean : 35.6958**

**Gaus Width: 3.0420**



# B1L000S, U25-ch44, adc2

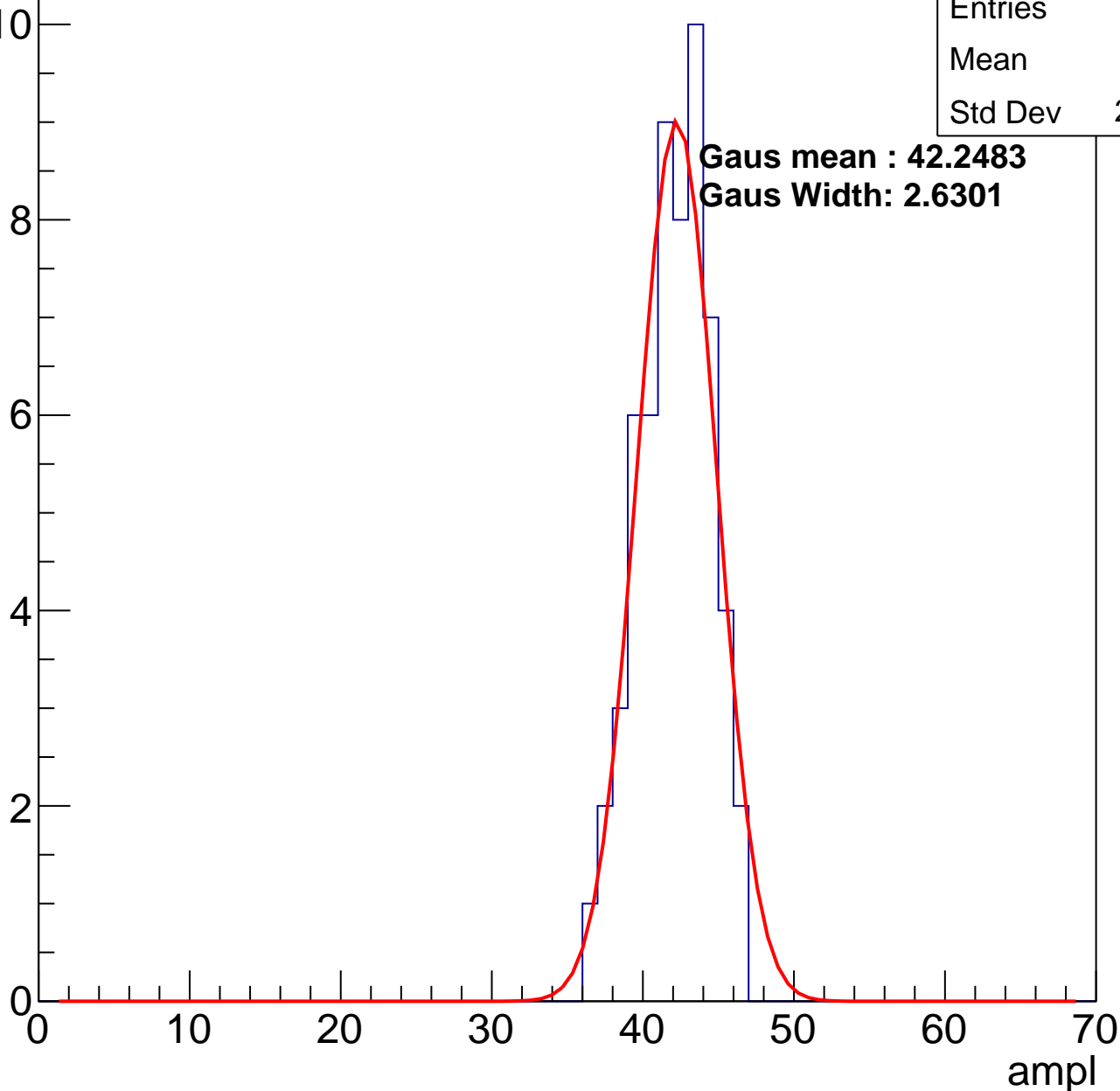
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.6
Std Dev	2.341

**Gaus mean : 42.2483**

**Gaus Width: 2.6301**

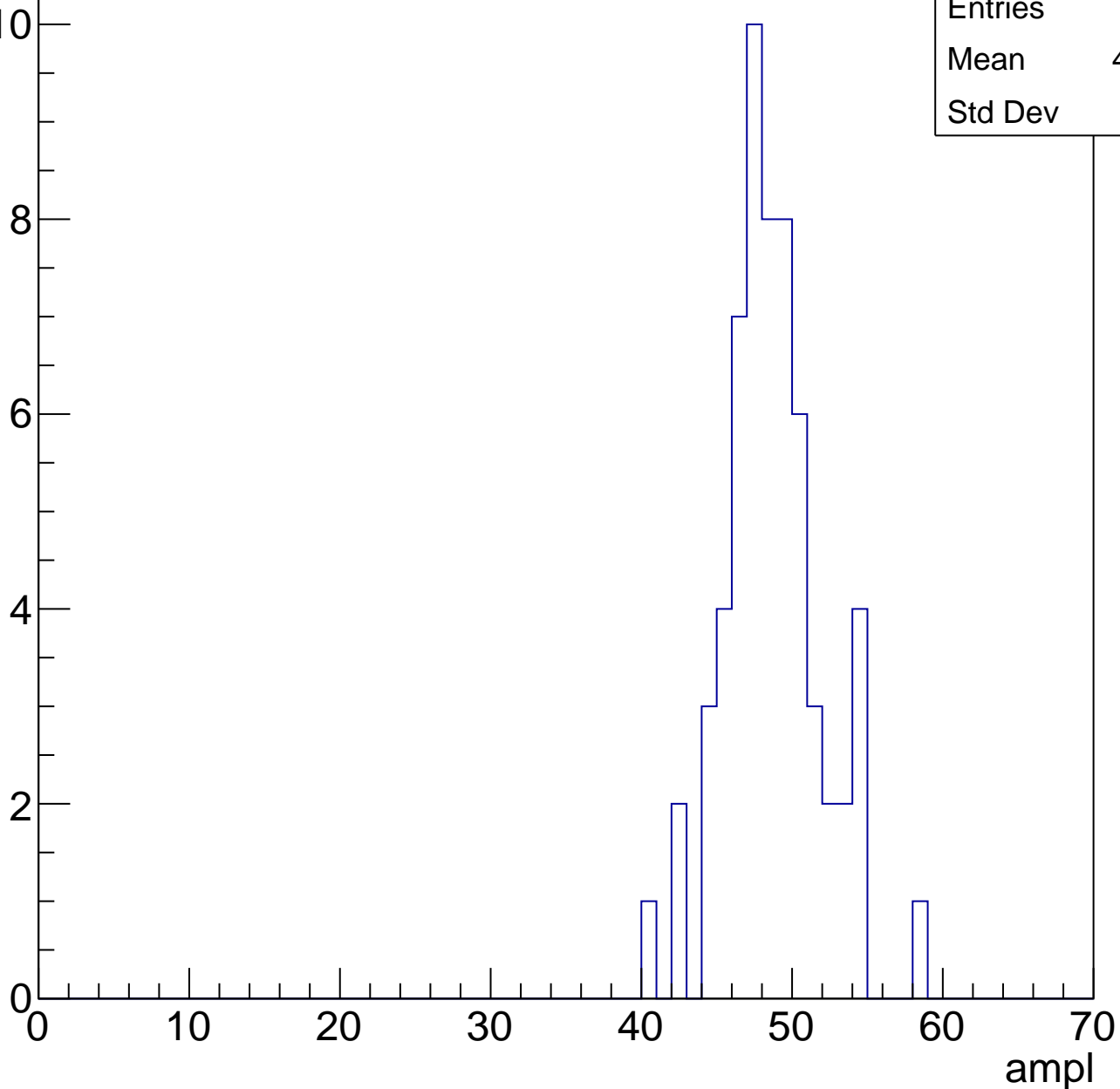


# B1L000S, U25-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	48.21
Std Dev	3.24

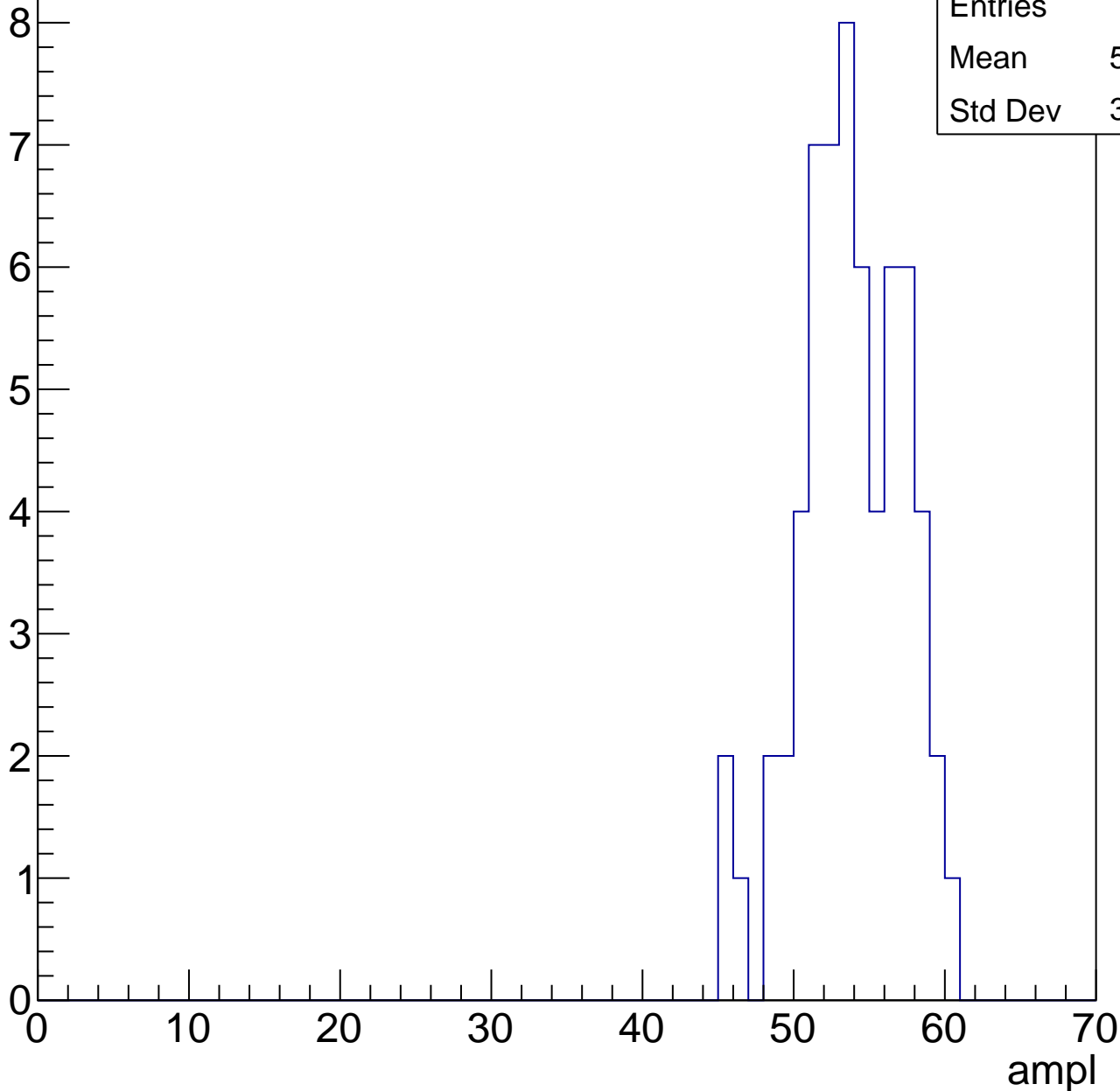


# B1L000S, U25-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	53.34
Std Dev	3.398



# B1L000S, U25-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries 50

Mean 58.9

Std Dev 2.602

ampl

0

10

20

30

40

50

60

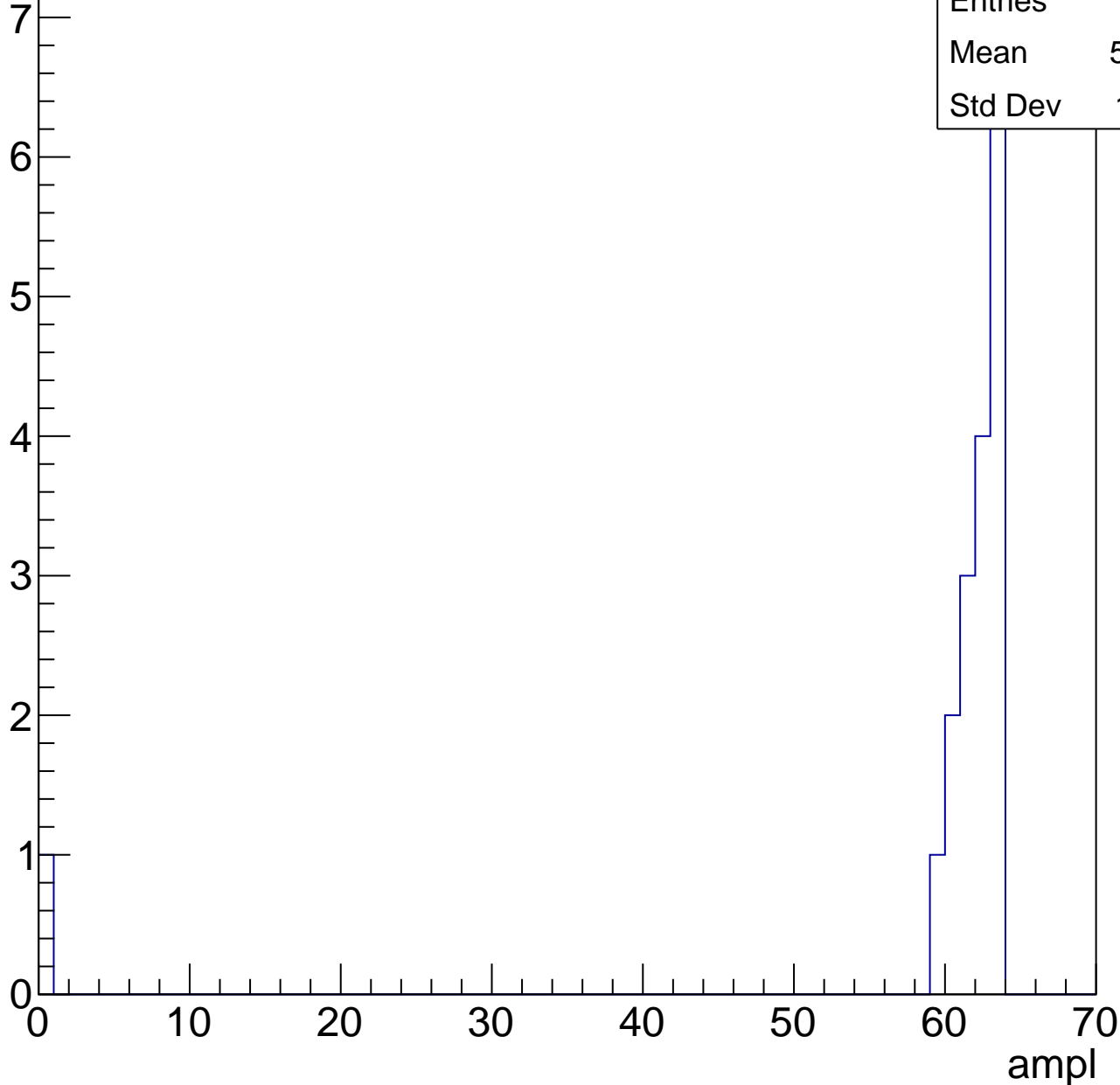
70

# B1L000S, U25-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.39
Std Dev	14.21





# B1L000S, U25-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch45, adc0

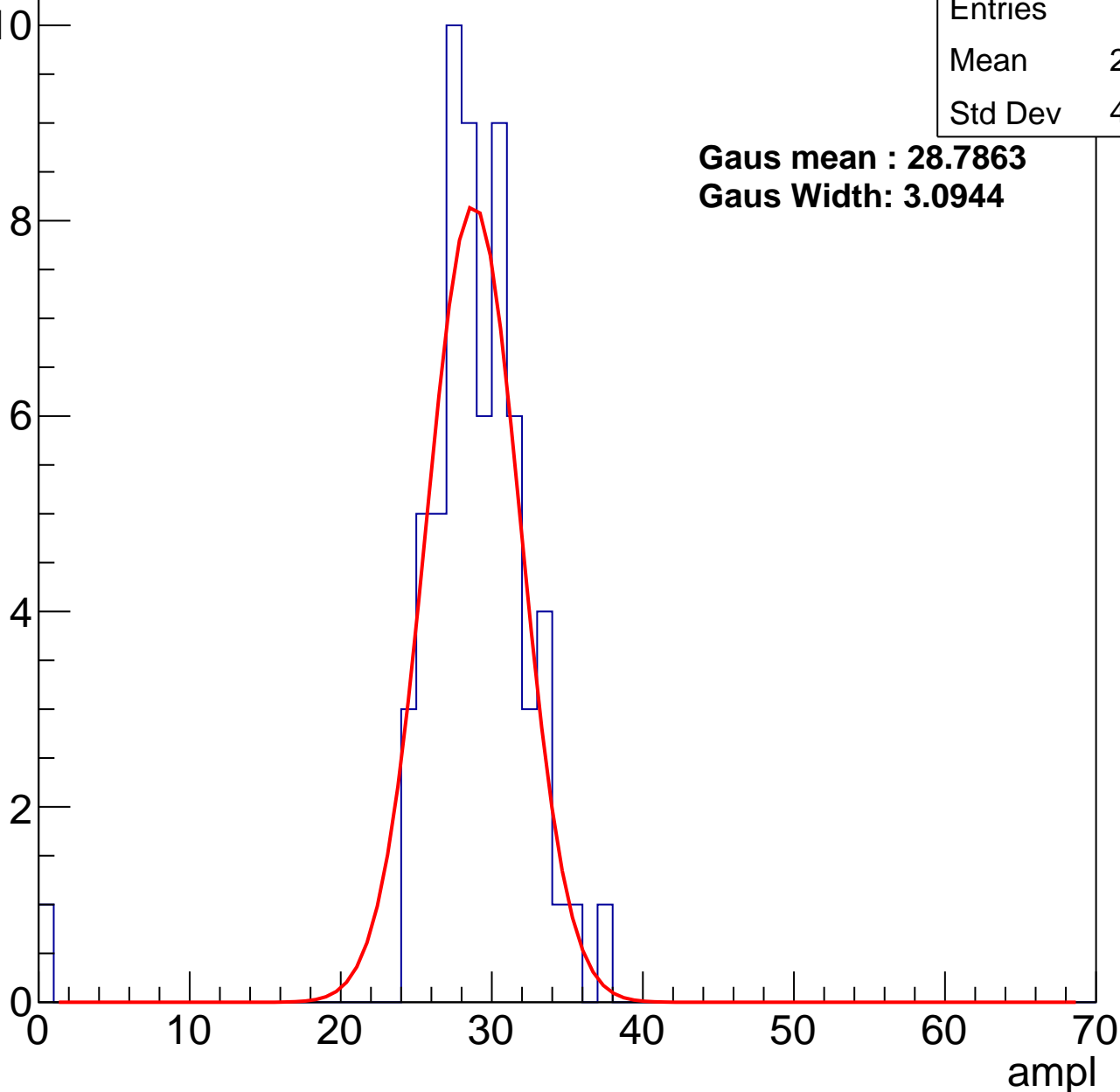
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.33
Std Dev	4.524

**Gaus mean : 28.7863**

**Gaus Width: 3.0944**



# B1L000S, U25-ch45, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	34.88
Std Dev	3.268

**Gaus mean : 35.6950**

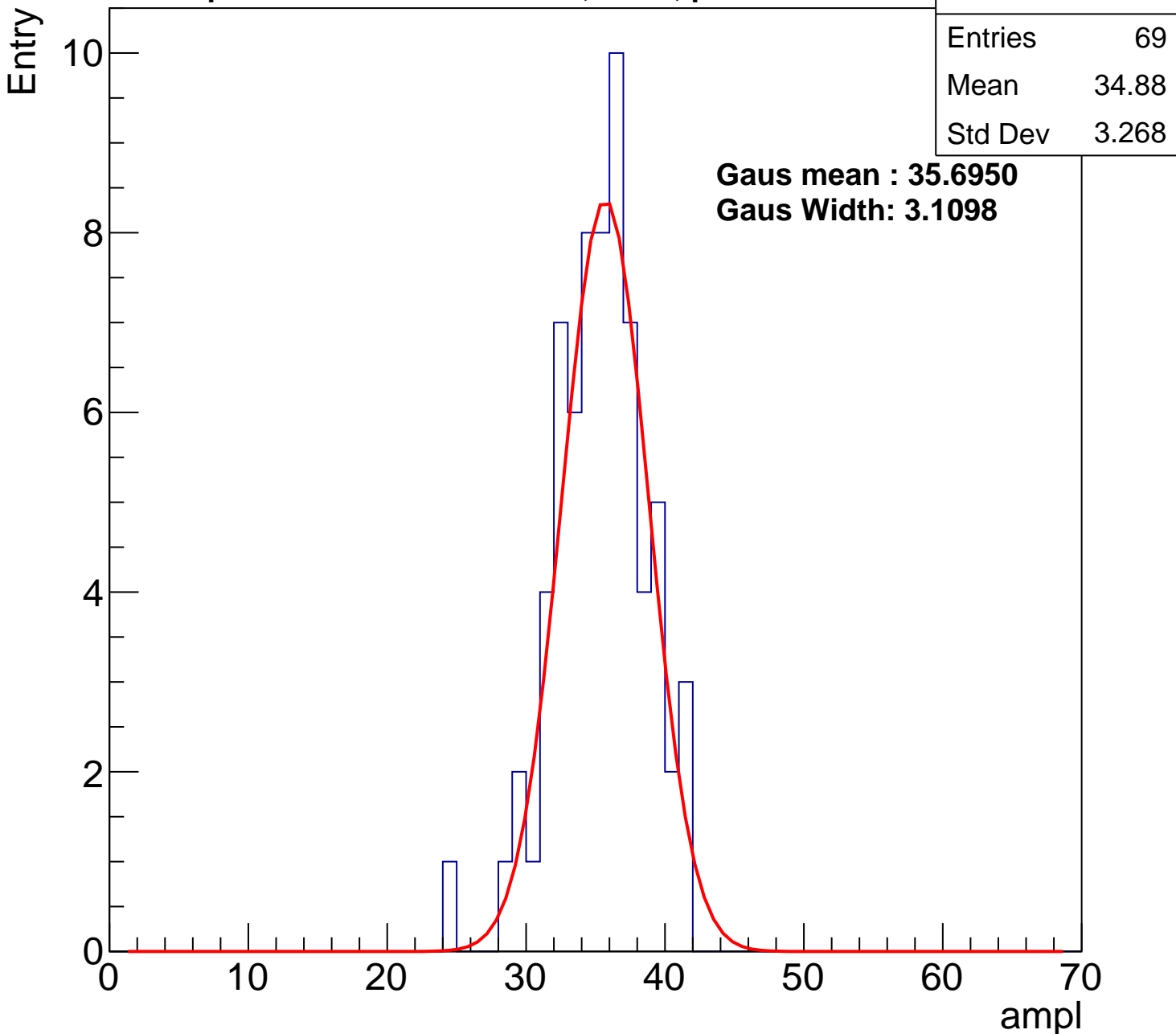
**Gaus Width: 3.1098**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch45, adc2

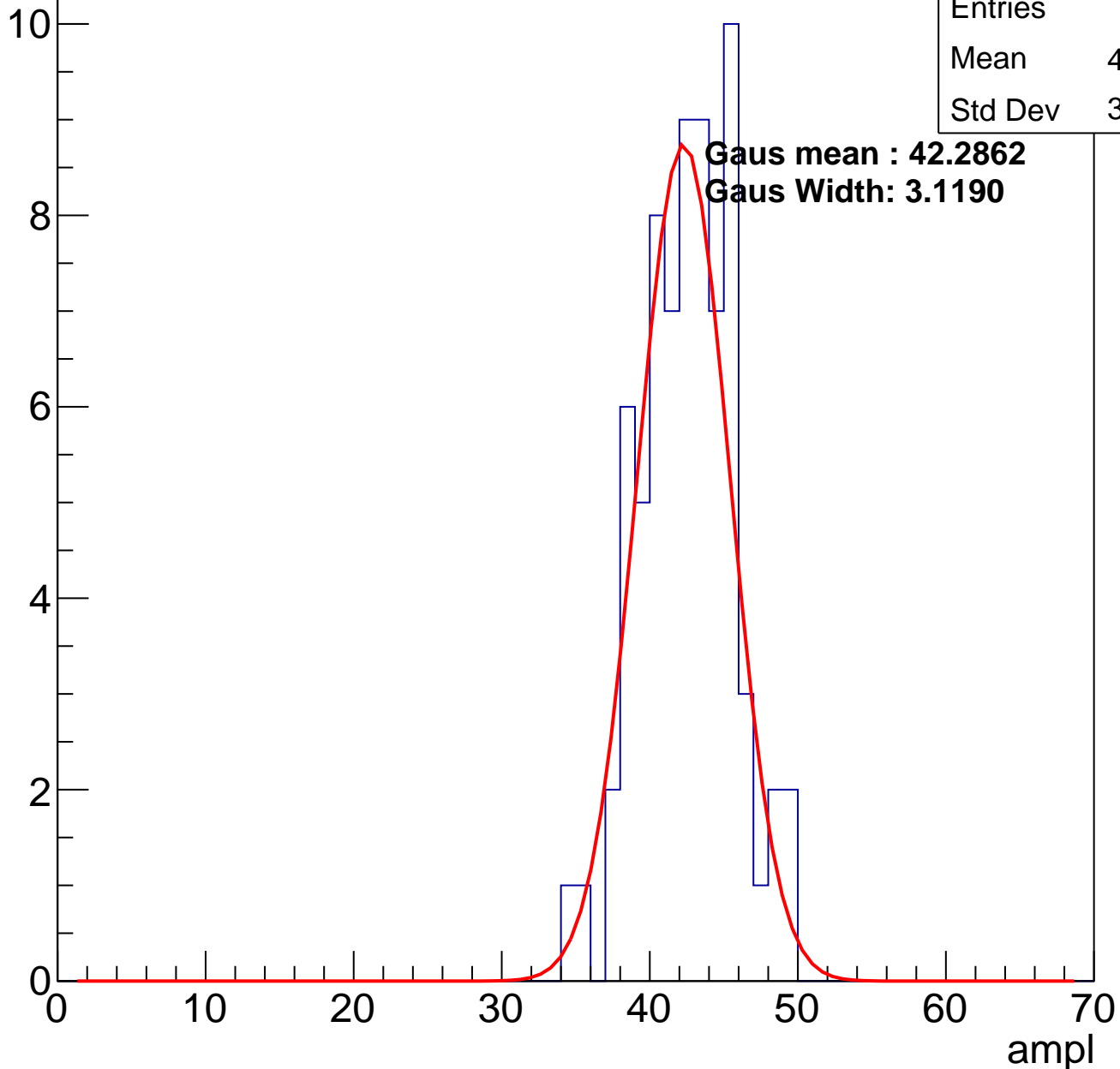
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	42.12
Std Dev	3.123

**Gaus mean : 42.2862**

**Gaus Width: 3.1190**

Entry



# B1L000S, U25-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	49
Std Dev	2.911

Entry

10

8

6

4

2

0

0

10

20

30

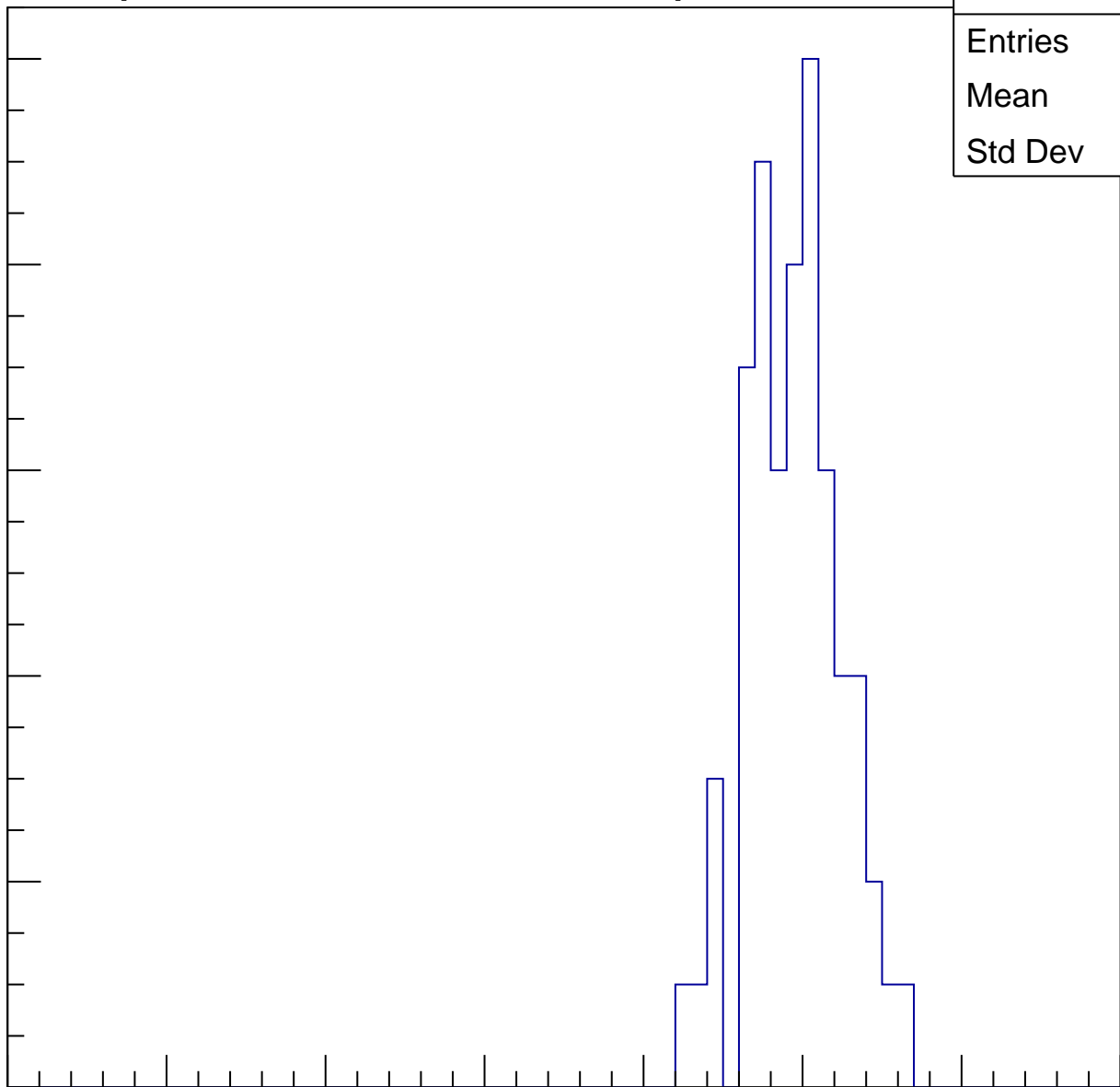
40

50

60

70

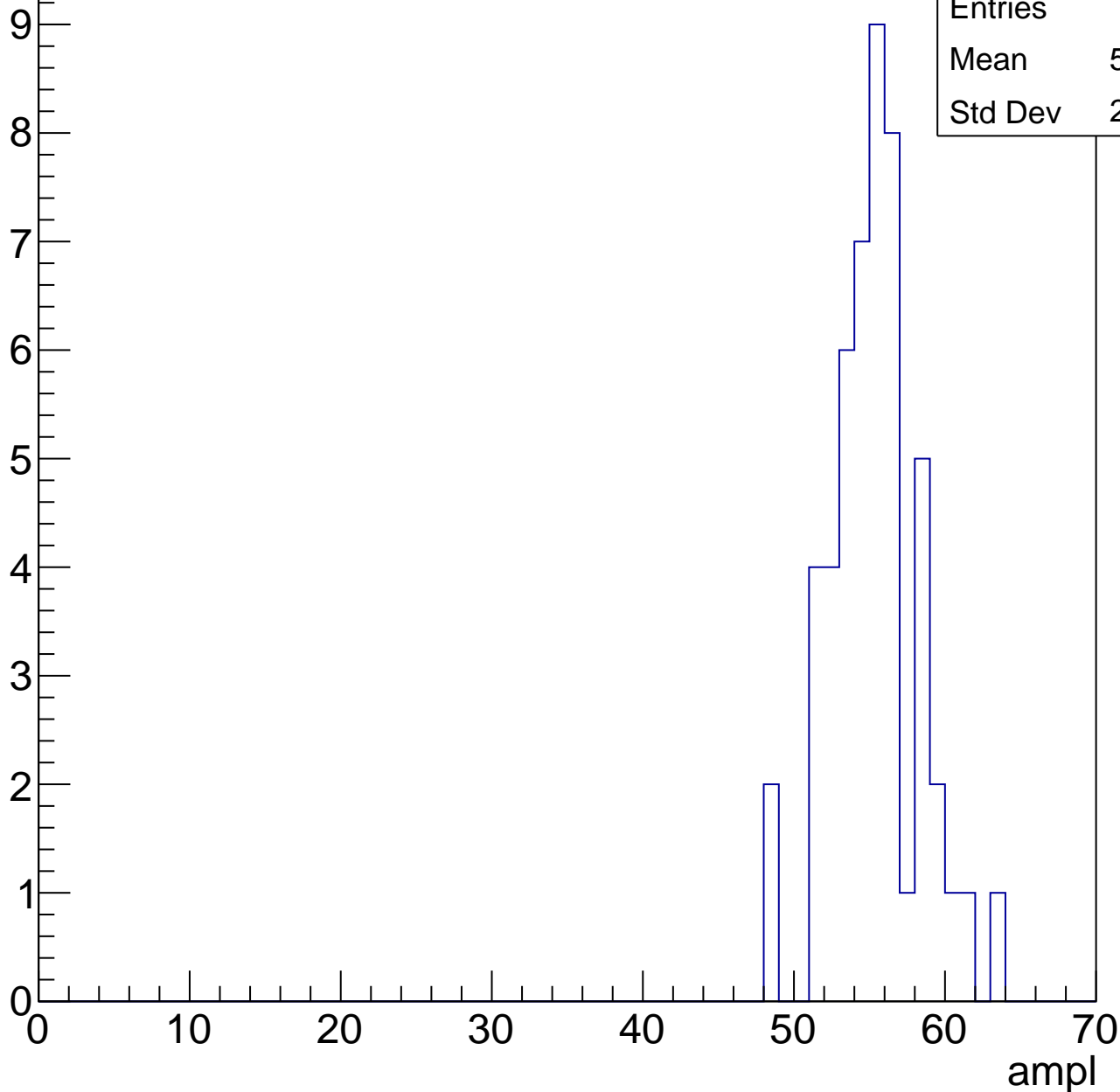
ampl



# B1L000S, U25-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

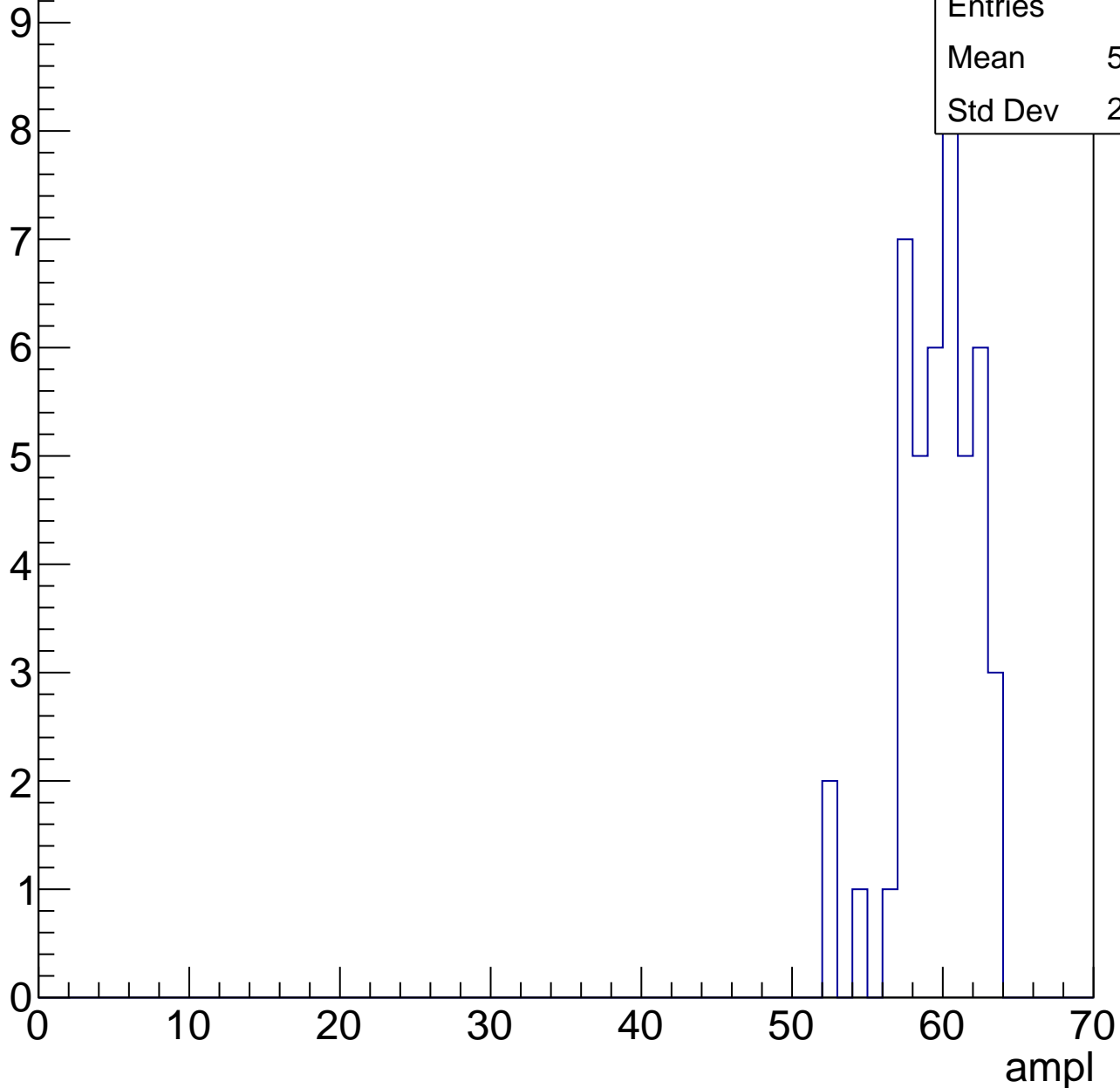
Entry



# B1L000S, U25-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

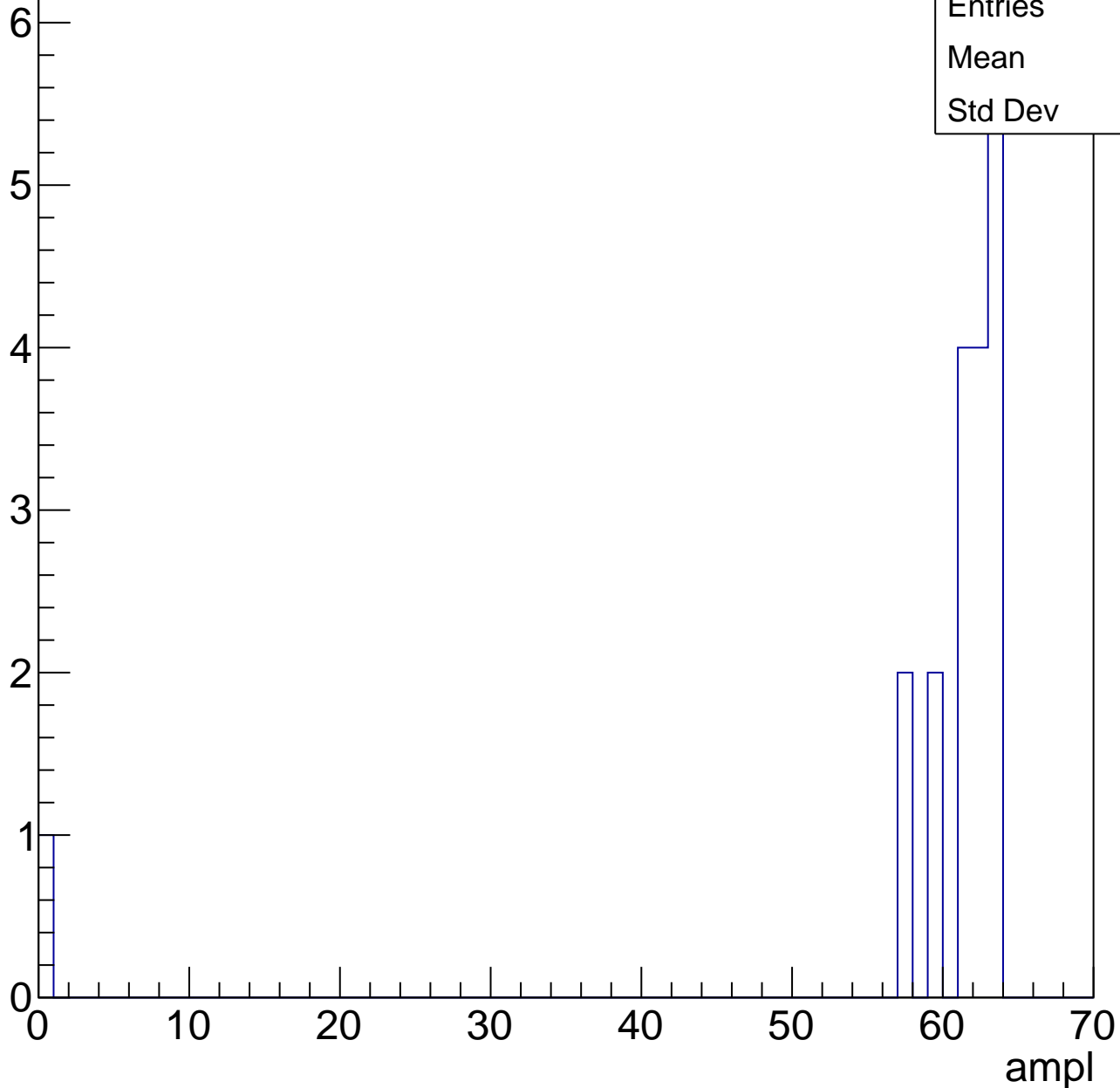


# B1L000S, U25-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58
Std Dev	13.8





# B1L000S, U25-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	8
Std Dev	11.31

# B1L000S, U25-ch46, adc0

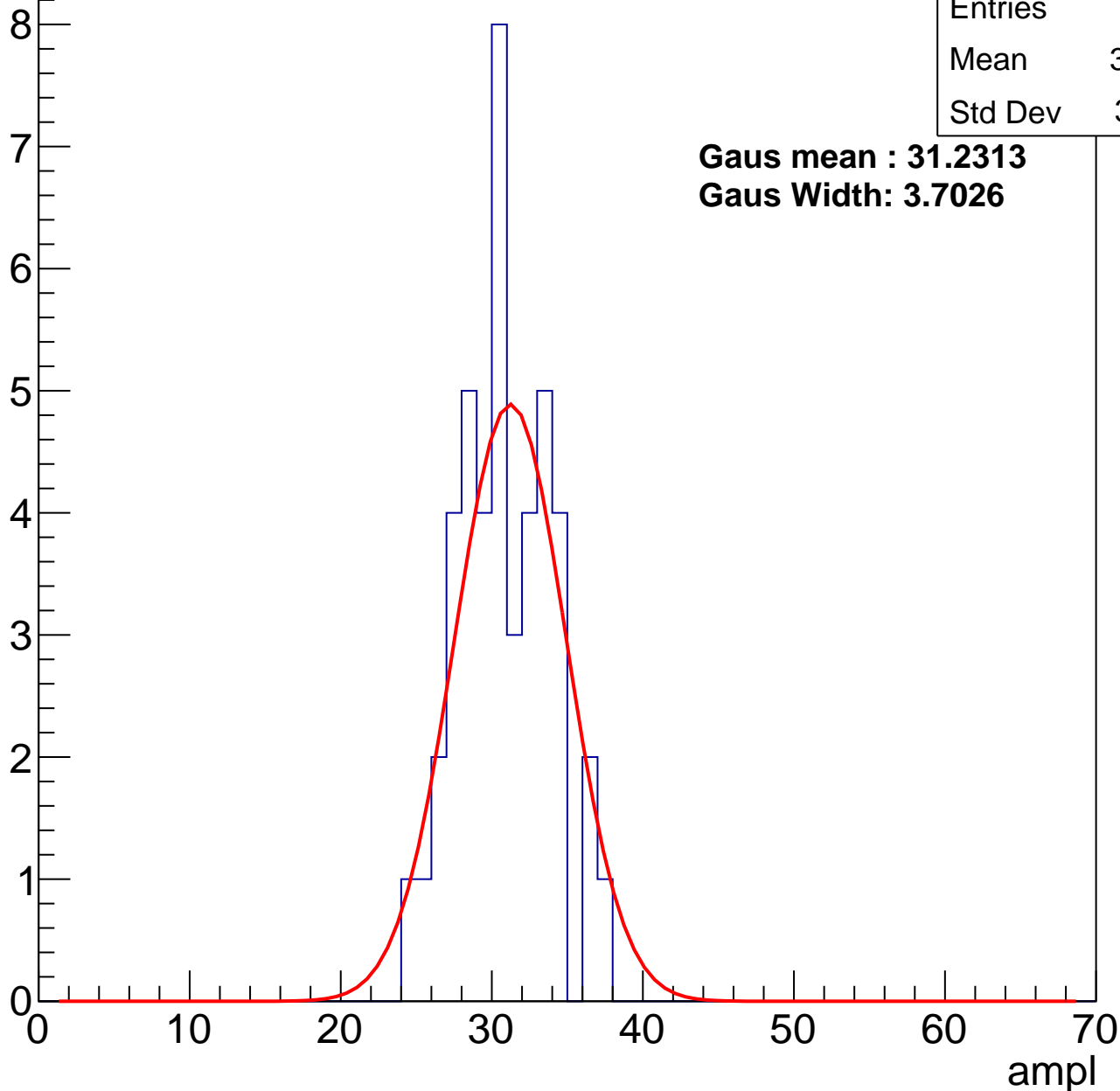
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	30.36
Std Dev	3.001

**Gaus mean : 31.2313**

**Gaus Width: 3.7026**



# B1L000S, U25-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	35.99
Std Dev	3.126

**Gaus mean : 36.5130**

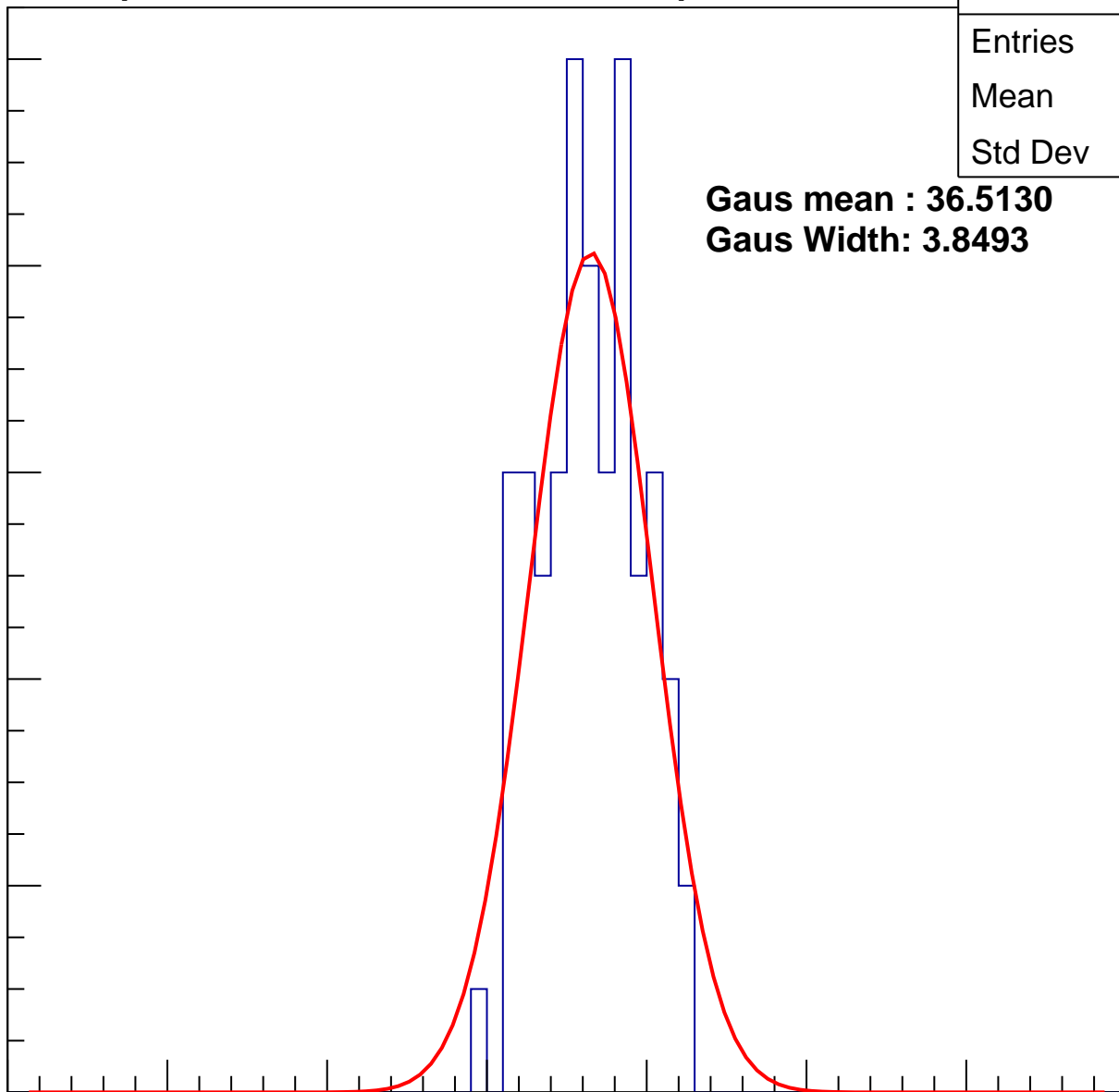
**Gaus Width: 3.8493**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch46, adc2

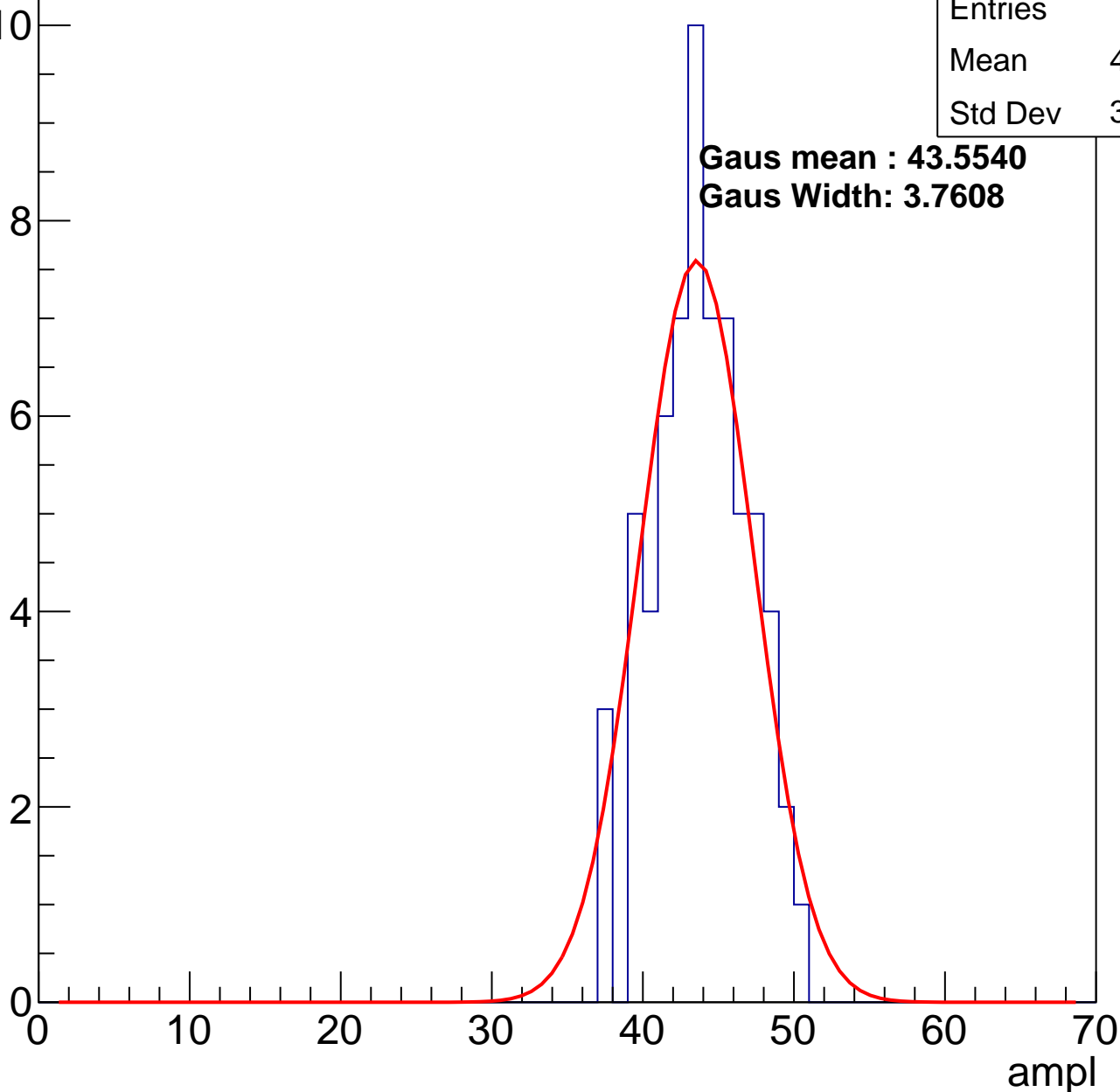
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.39
Std Dev	3.079

**Gaus mean : 43.5540**

**Gaus Width: 3.7608**

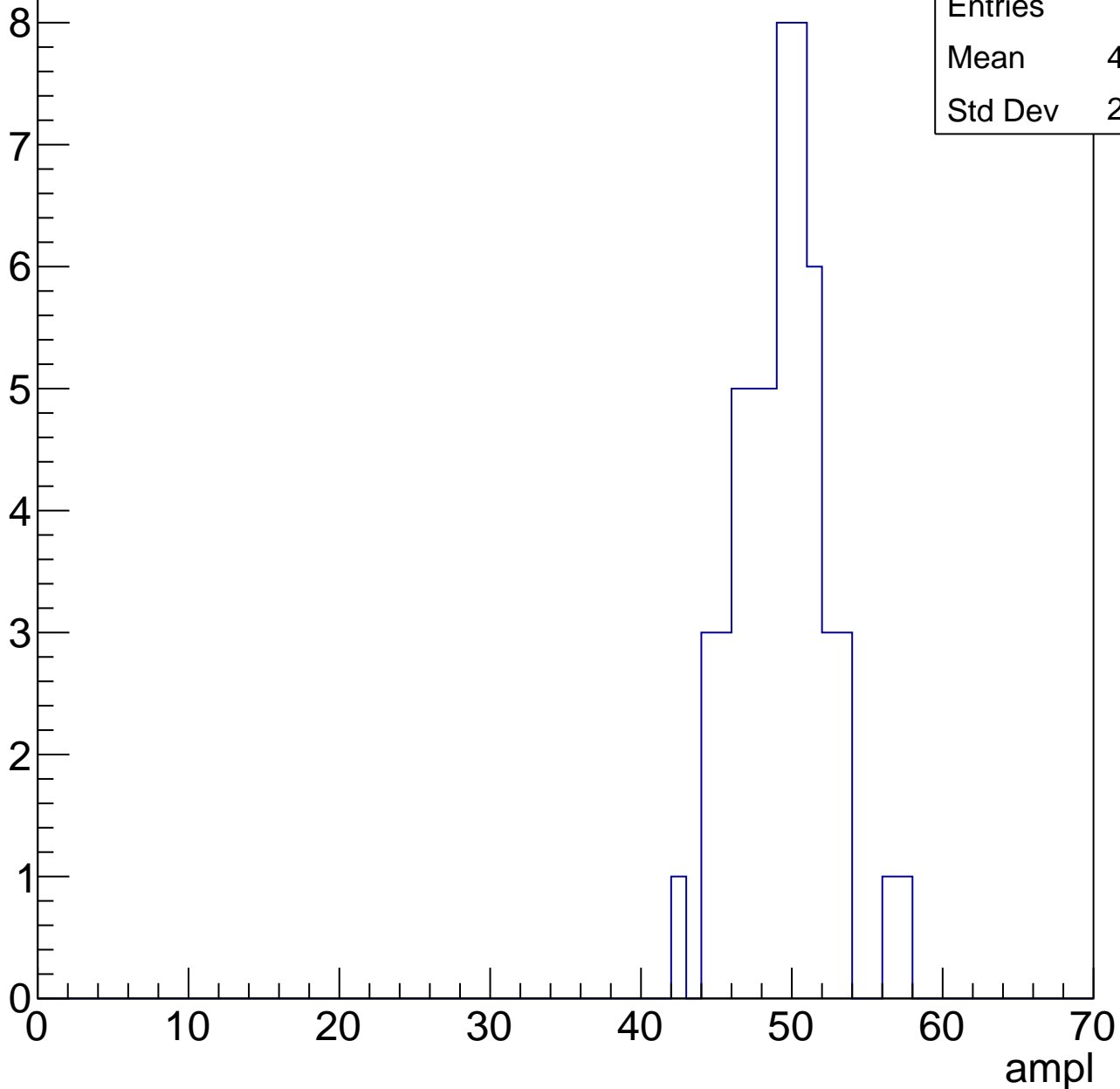


# B1L000S, U25-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	48.85
Std Dev	2.977

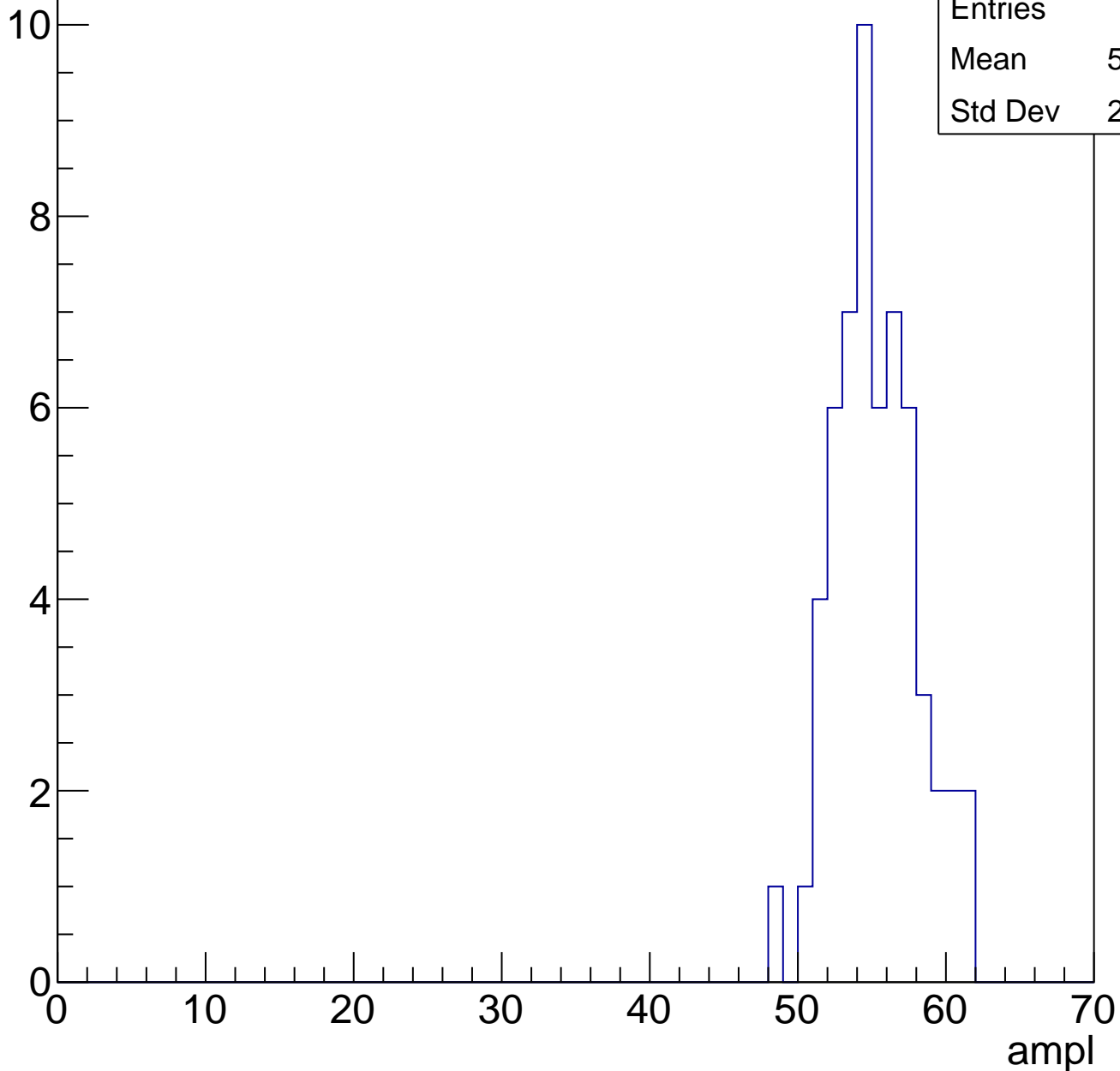


# B1L000S, U25-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	57
Mean	54.79
Std Dev	2.777

Entry



# B1L000S, U25-ch46, adc5

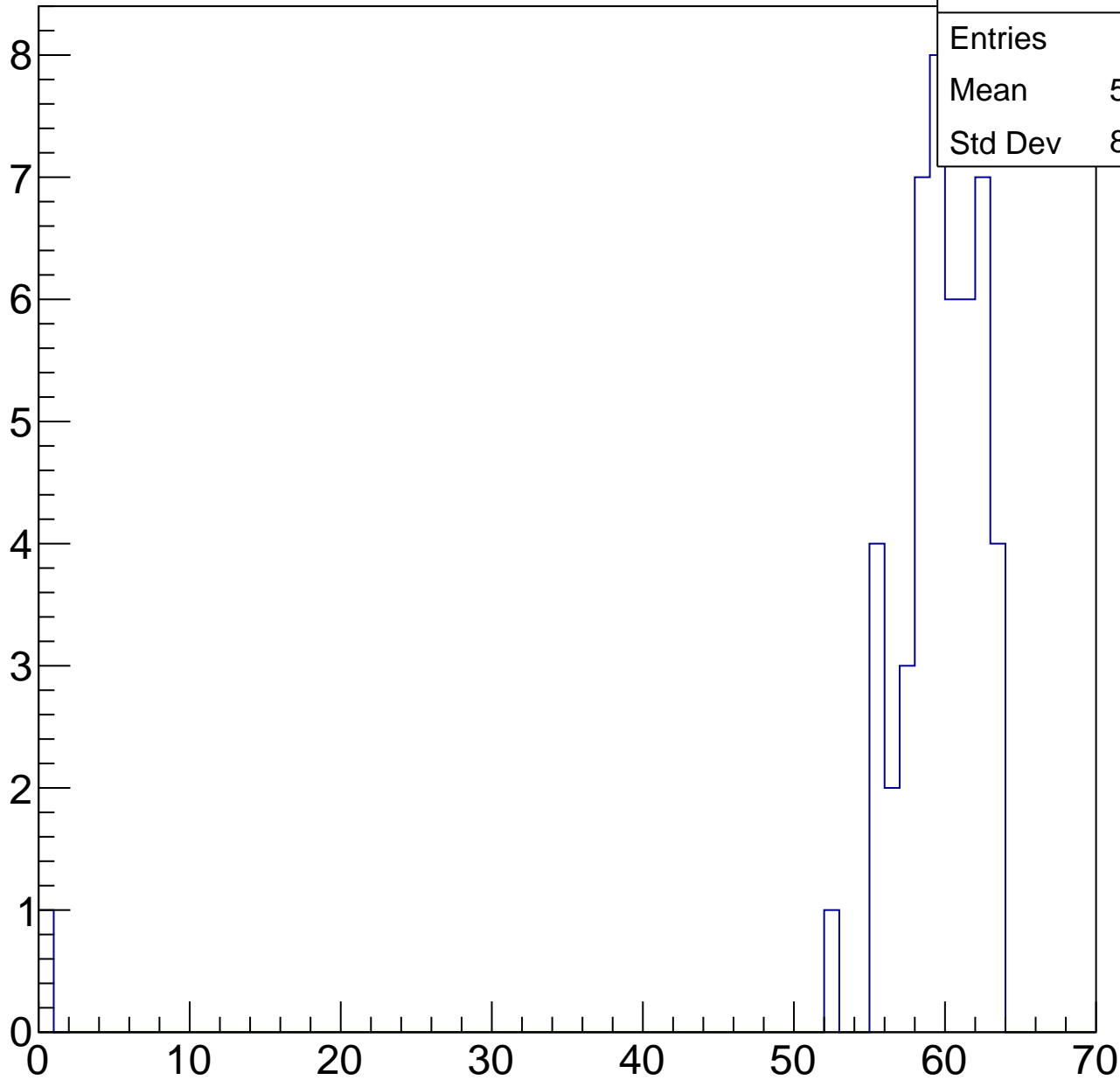
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.06
Std Dev	8.742

ampl

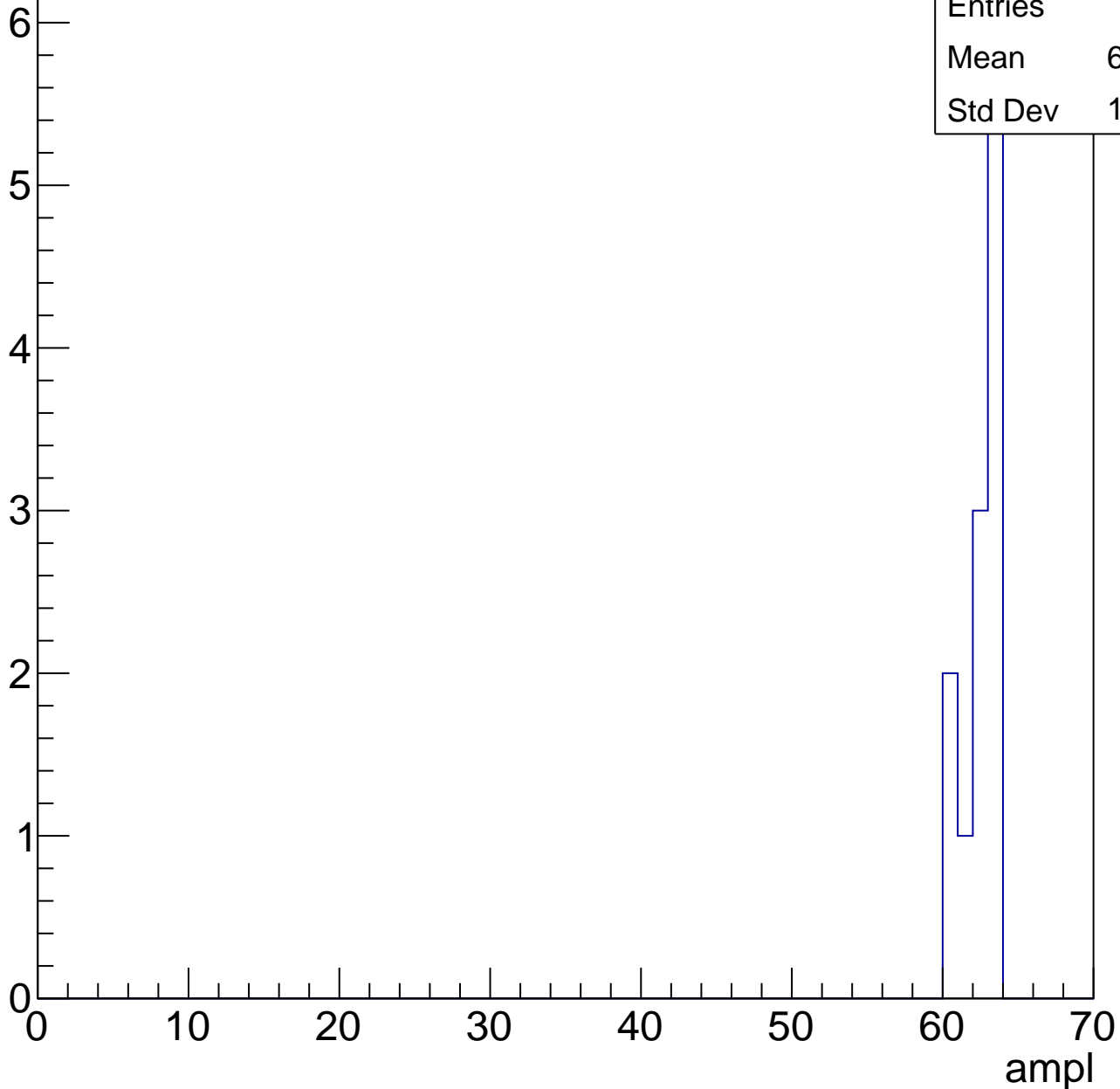


# B1L000S, U25-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	62.08
Std Dev	1.115





# B1L000S, U25-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch47, adc0

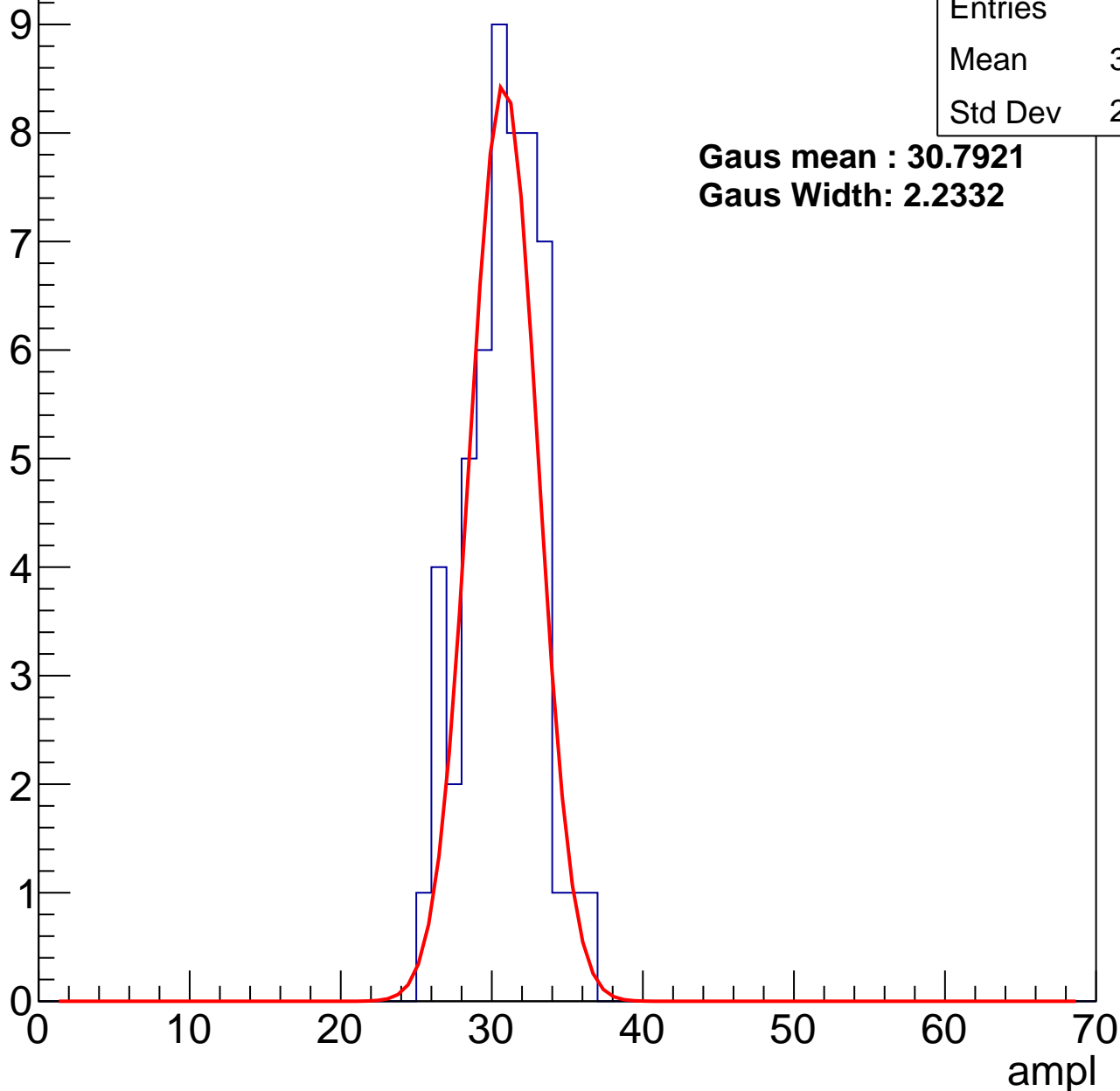
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	30.32
Std Dev	2.409

**Gaus mean : 30.7921**

**Gaus Width: 2.2332**



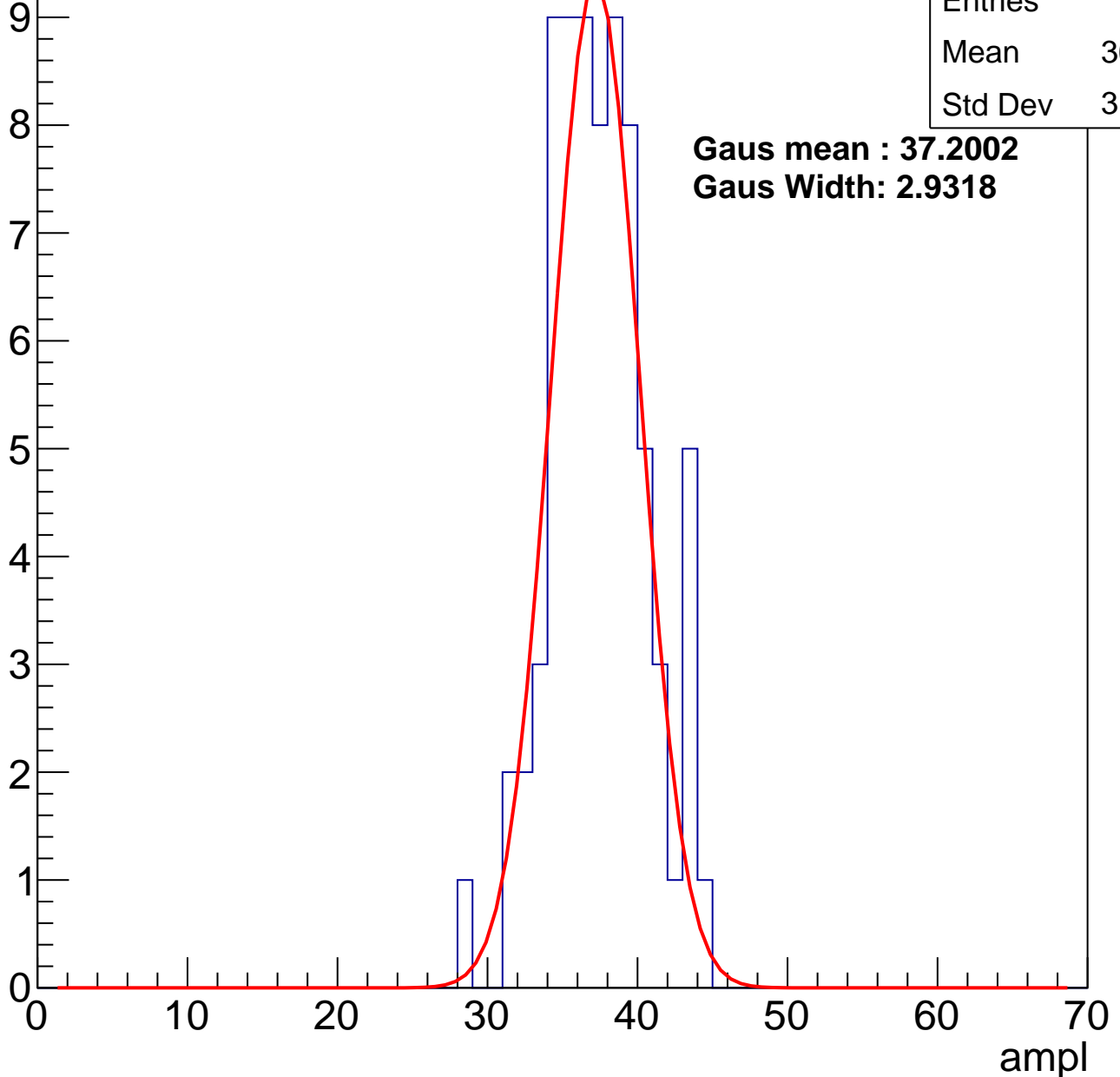
# B1L000S, U25-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	36.96
Std Dev	3.193

**Gaus mean : 37.2002**  
**Gaus Width: 2.9318**



# B1L000S, U25-ch47, adc2

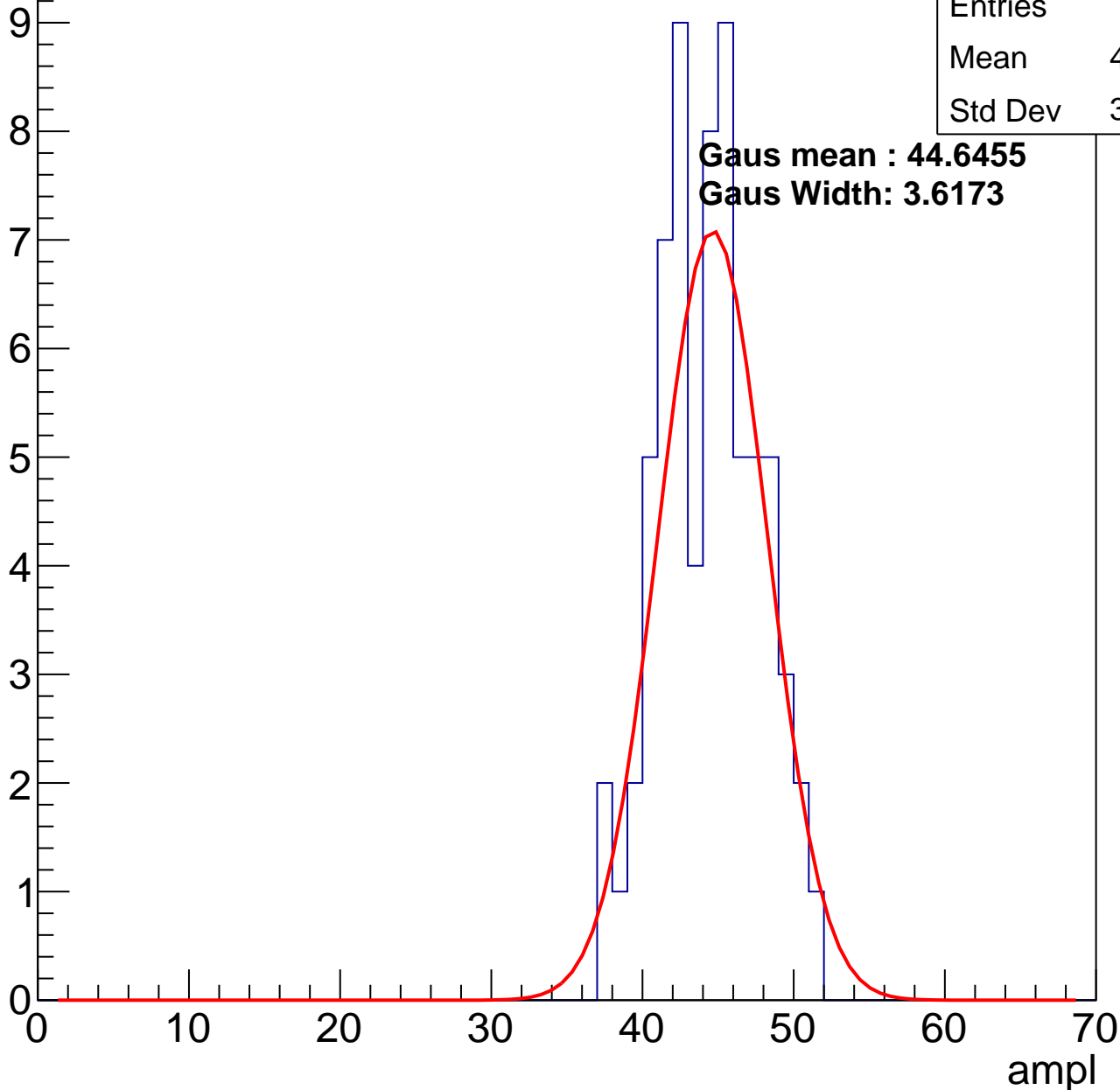
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	43.93
Std Dev	3.246

**Gaus mean : 44.6455**

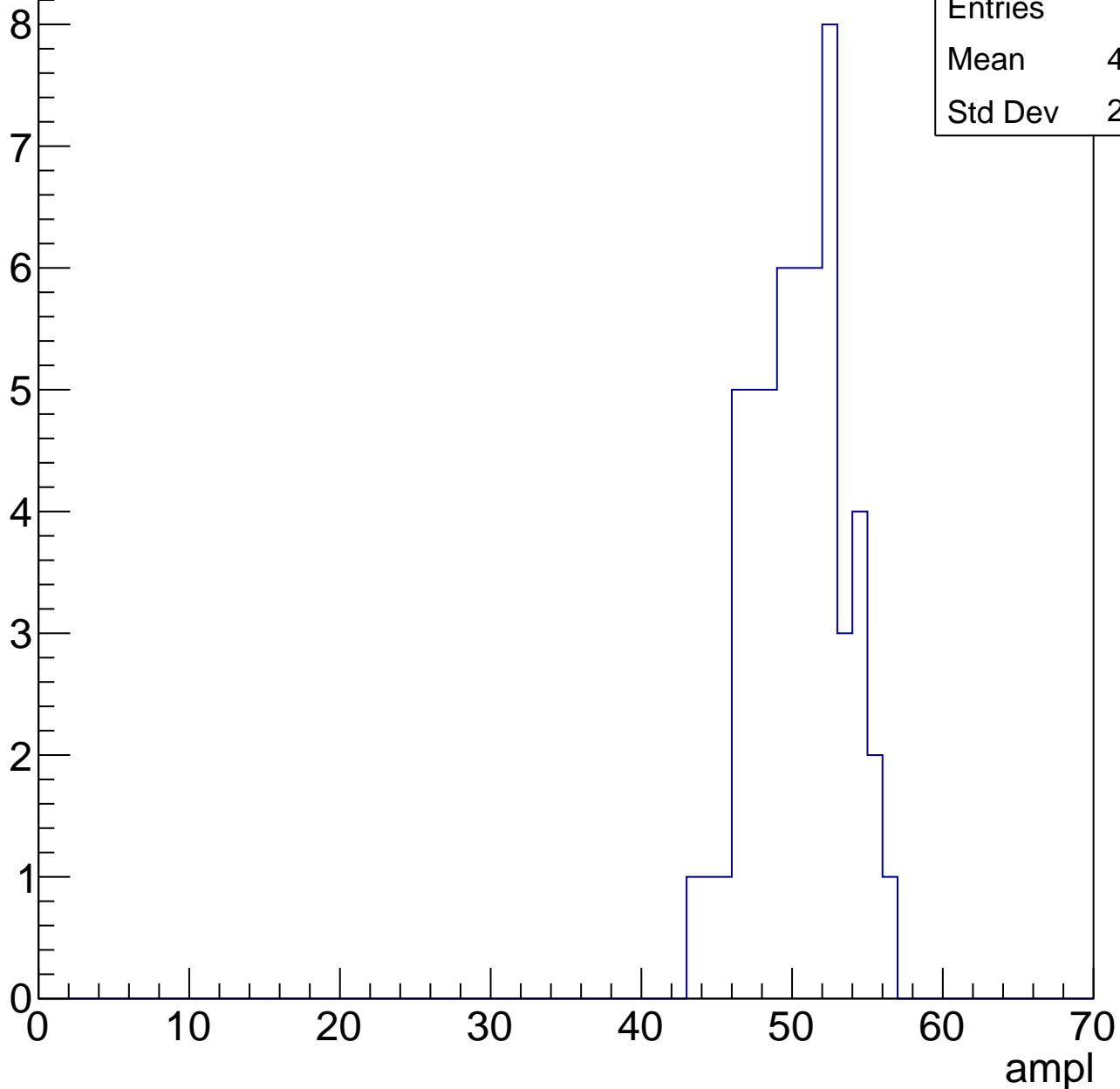
**Gaus Width: 3.6173**



# B1L000S, U25-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

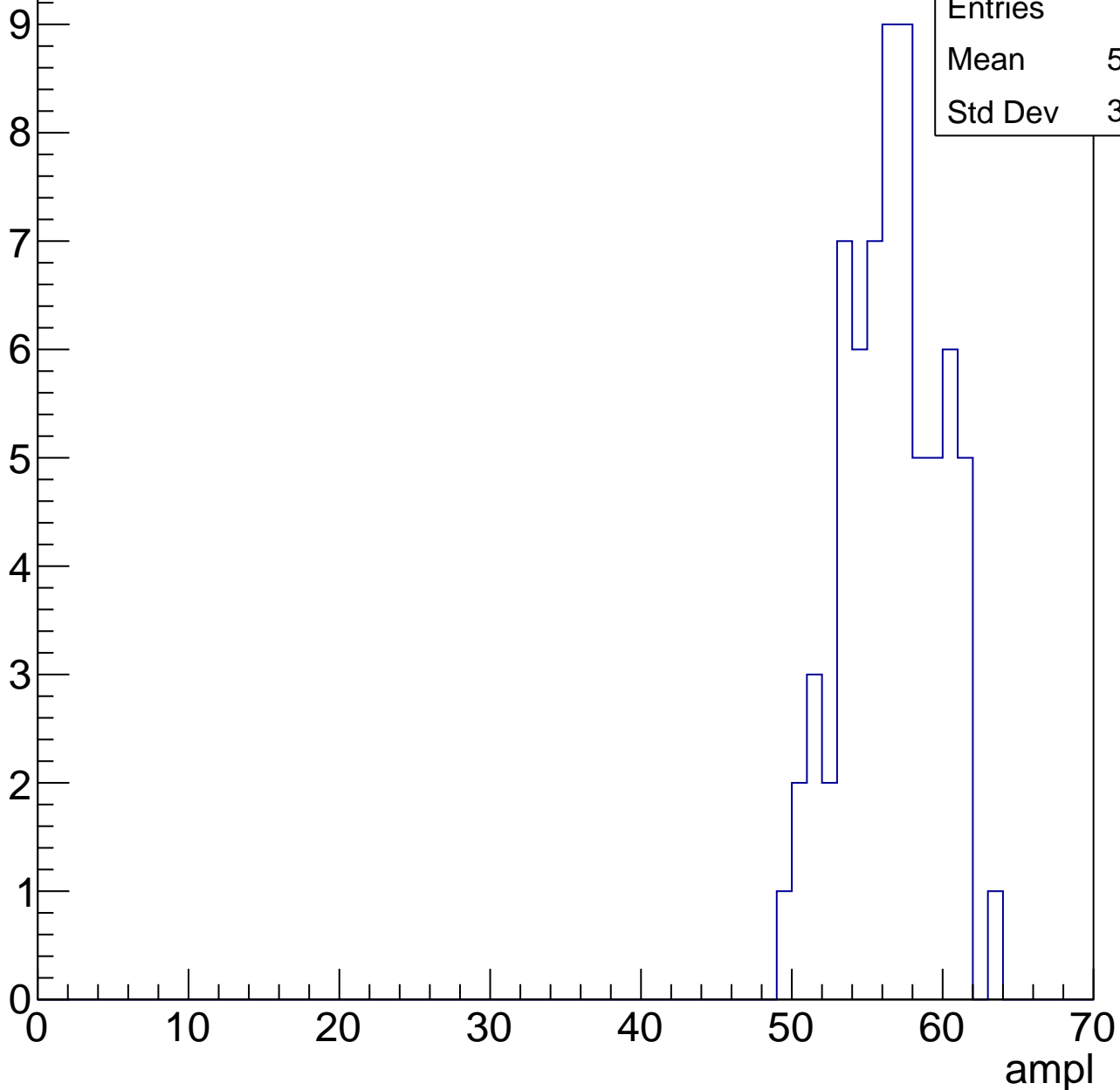


# B1L000S, U25-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	56.12
Std Dev	3.127

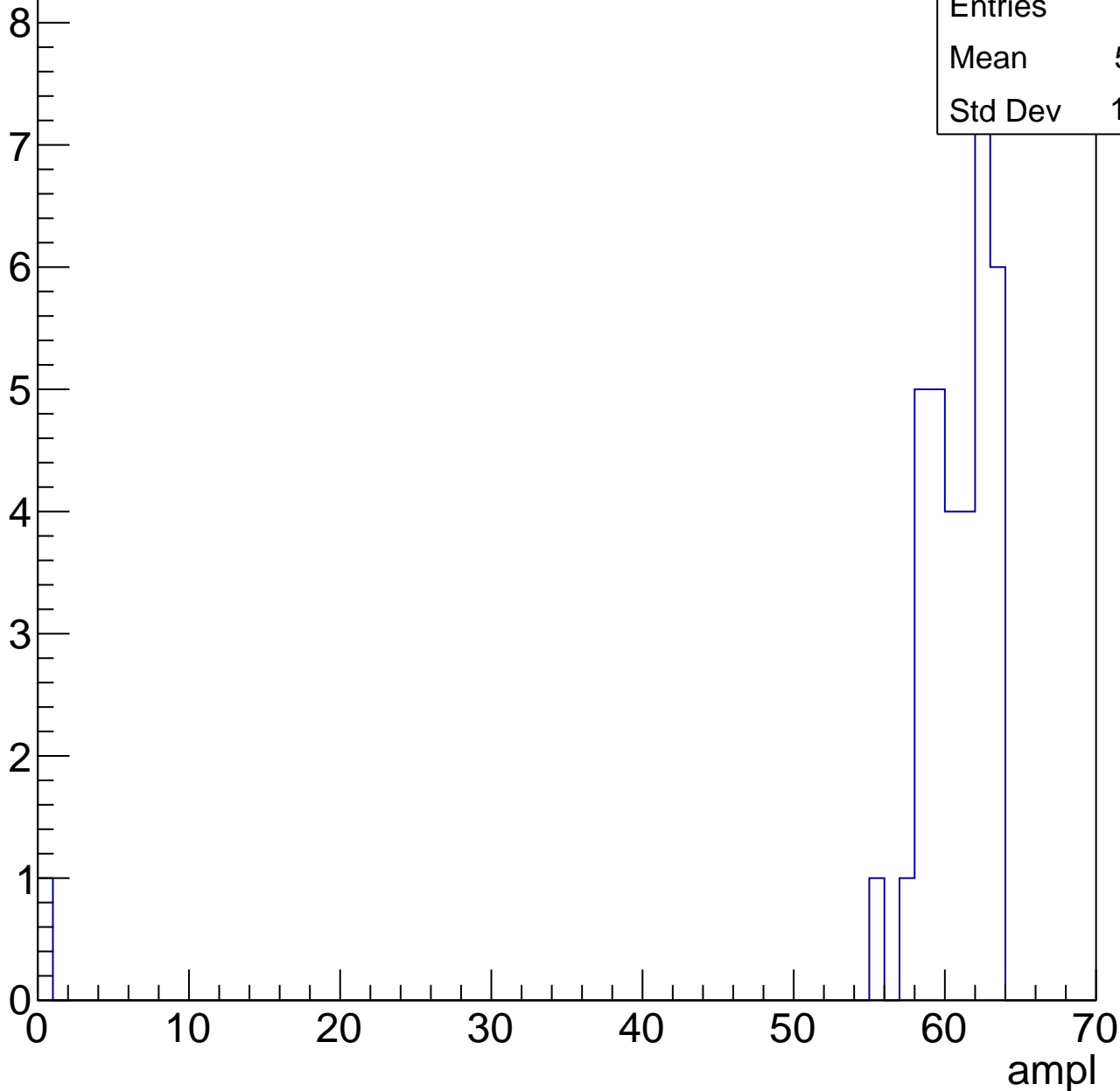


# B1L000S, U25-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	35
Mean	58.71
Std Dev	10.27



# B1L000S, U25-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

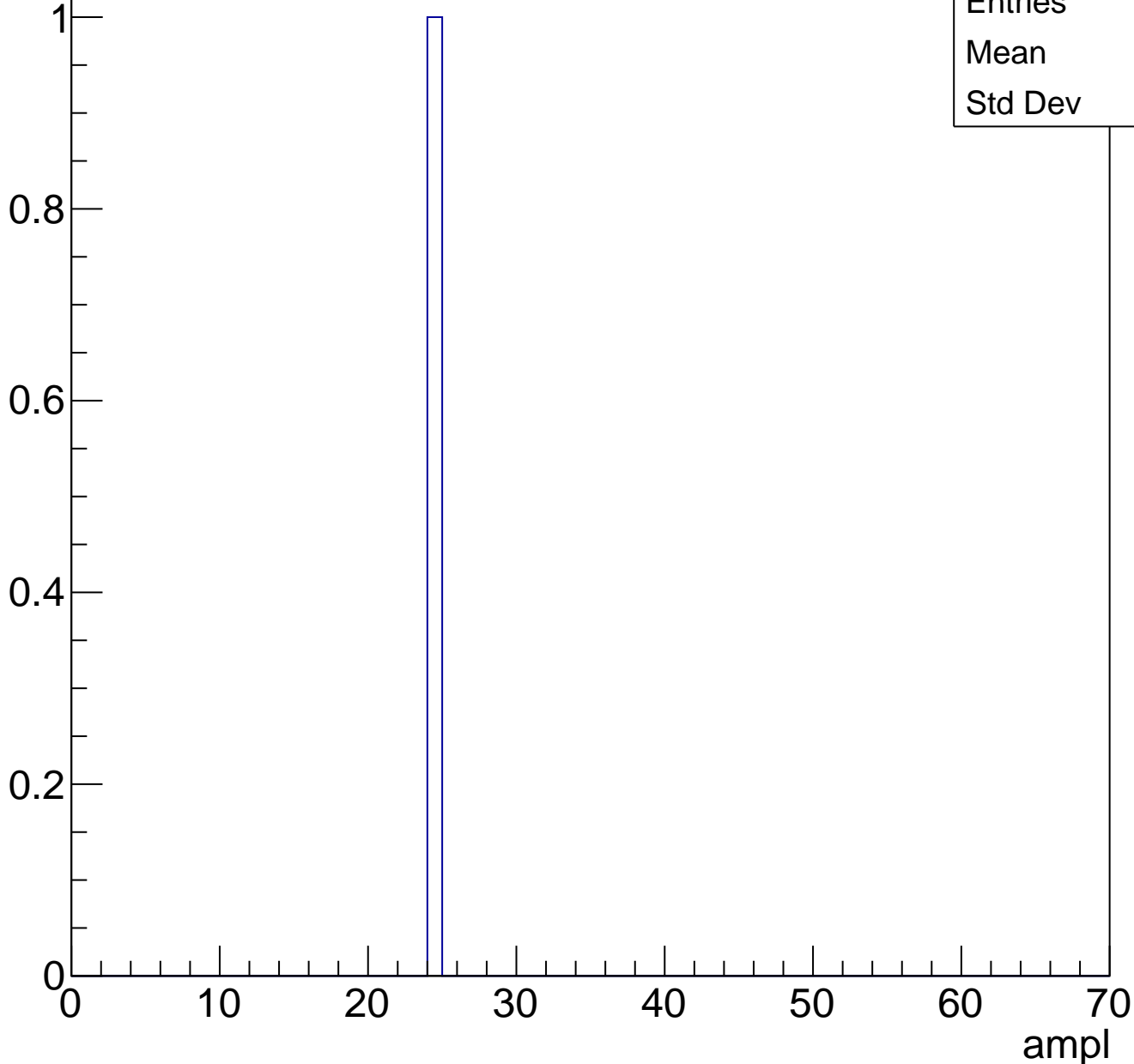




# B1L000S, U25-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch48, adc0

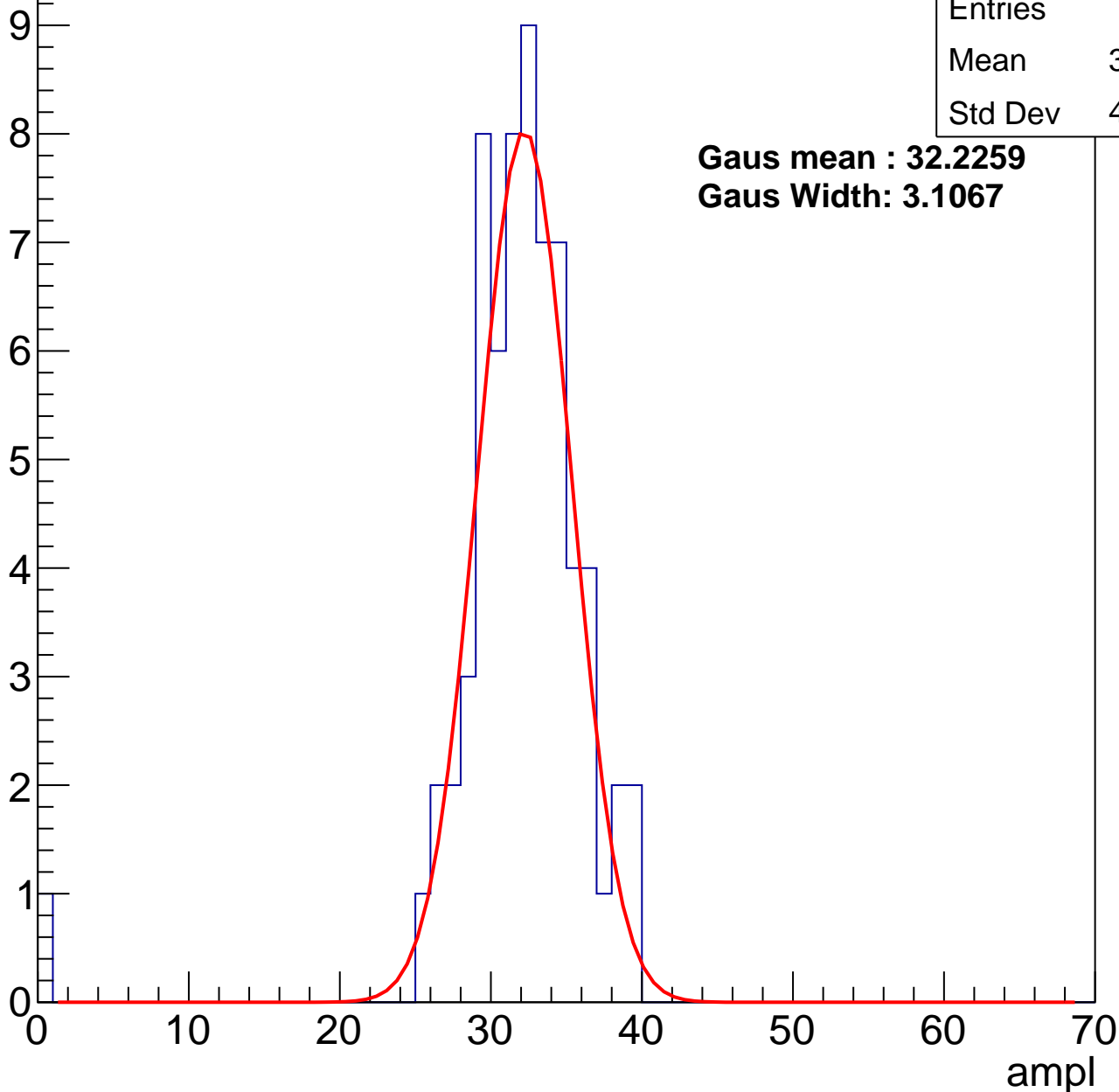
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	31.45
Std Dev	4.972

**Gaus mean : 32.2259**

**Gaus Width: 3.1067**



# B1L000S, U25-ch48, adc1

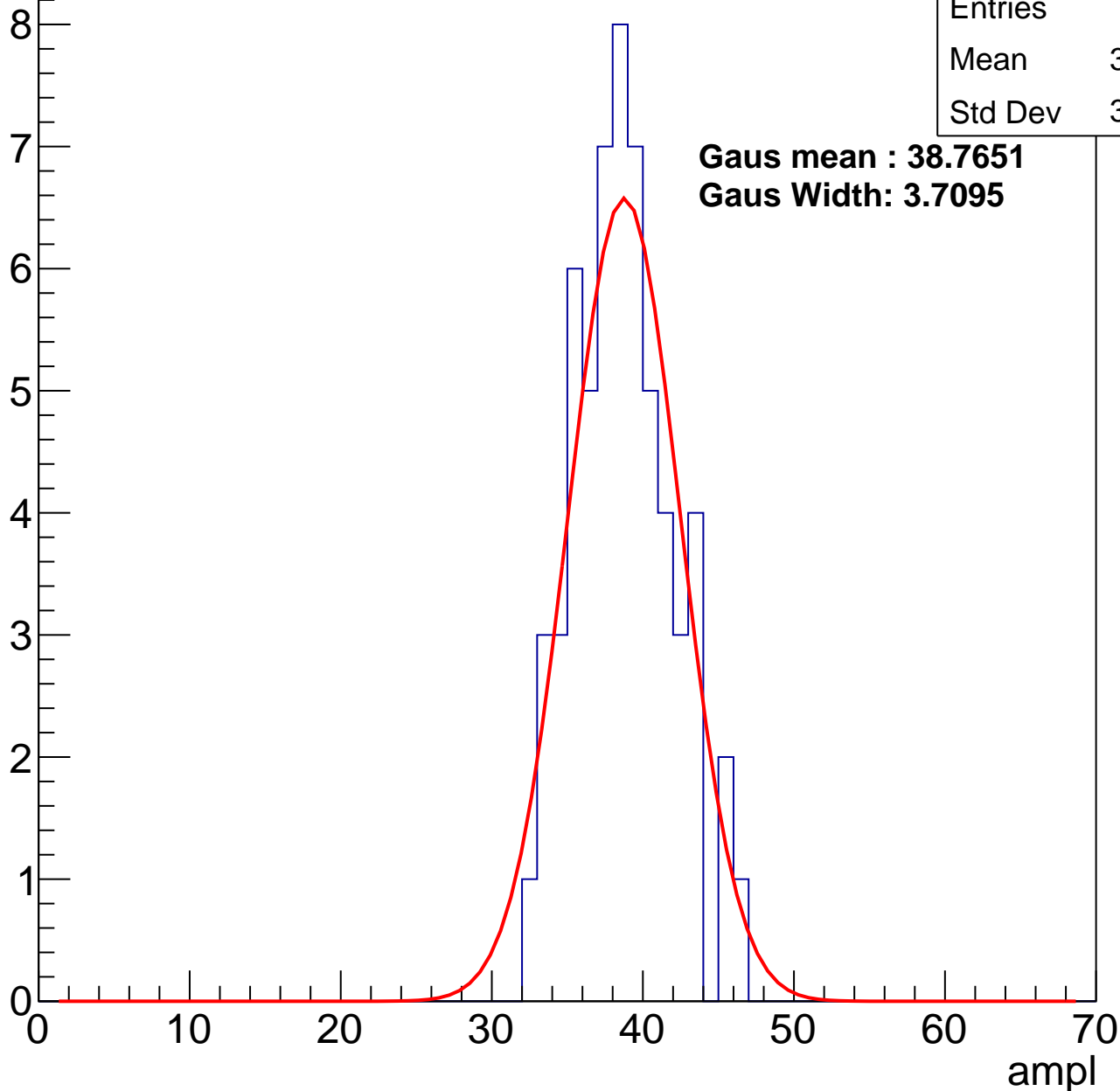
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	38.25
Std Dev	3.213

**Gaus mean : 38.7651**

**Gaus Width: 3.7095**



# B1L000S, U25-ch48, adc2

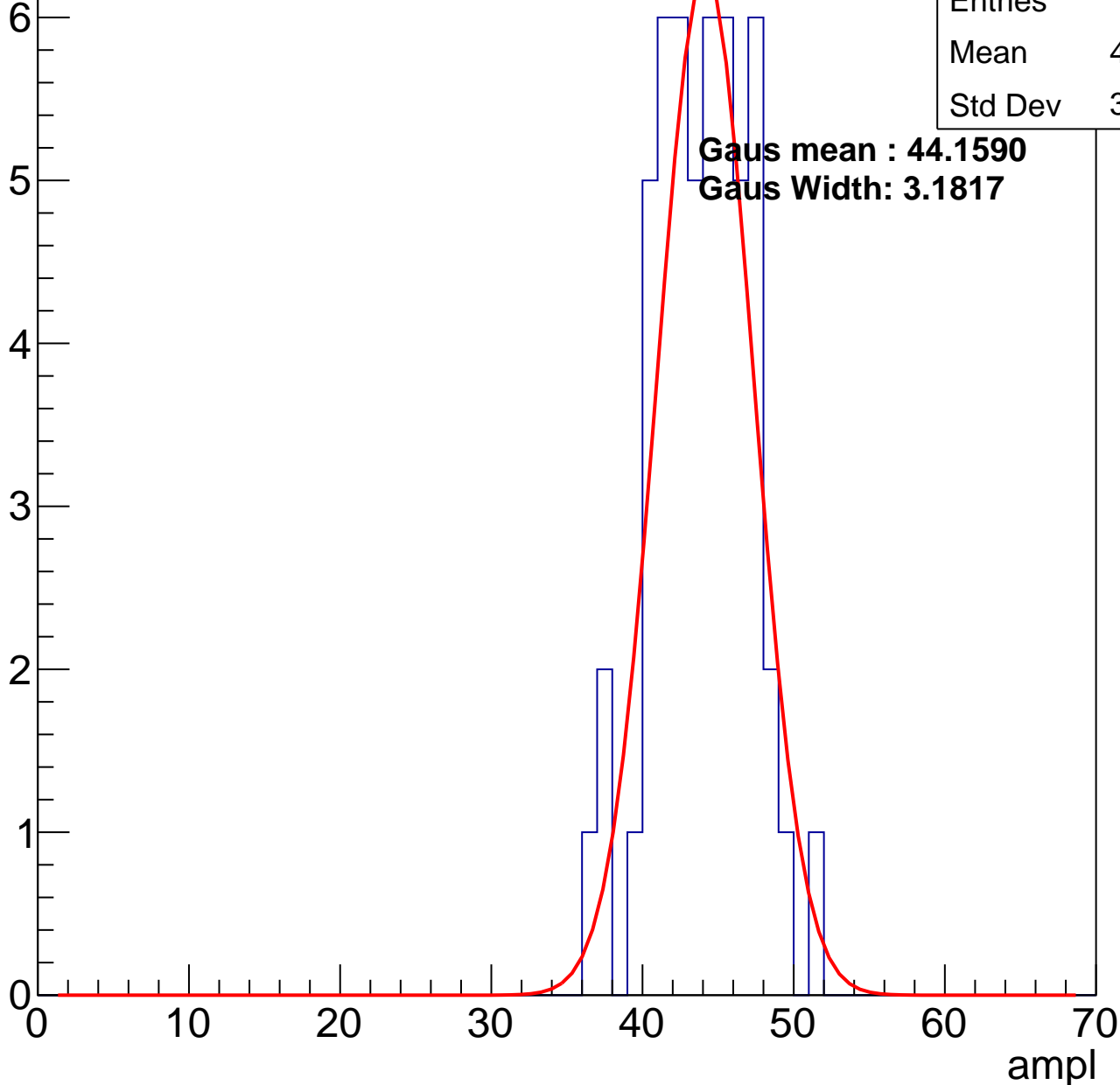
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	43.47
Std Dev	3.136

**Gaus mean : 44.1590**

**Gaus Width: 3.1817**

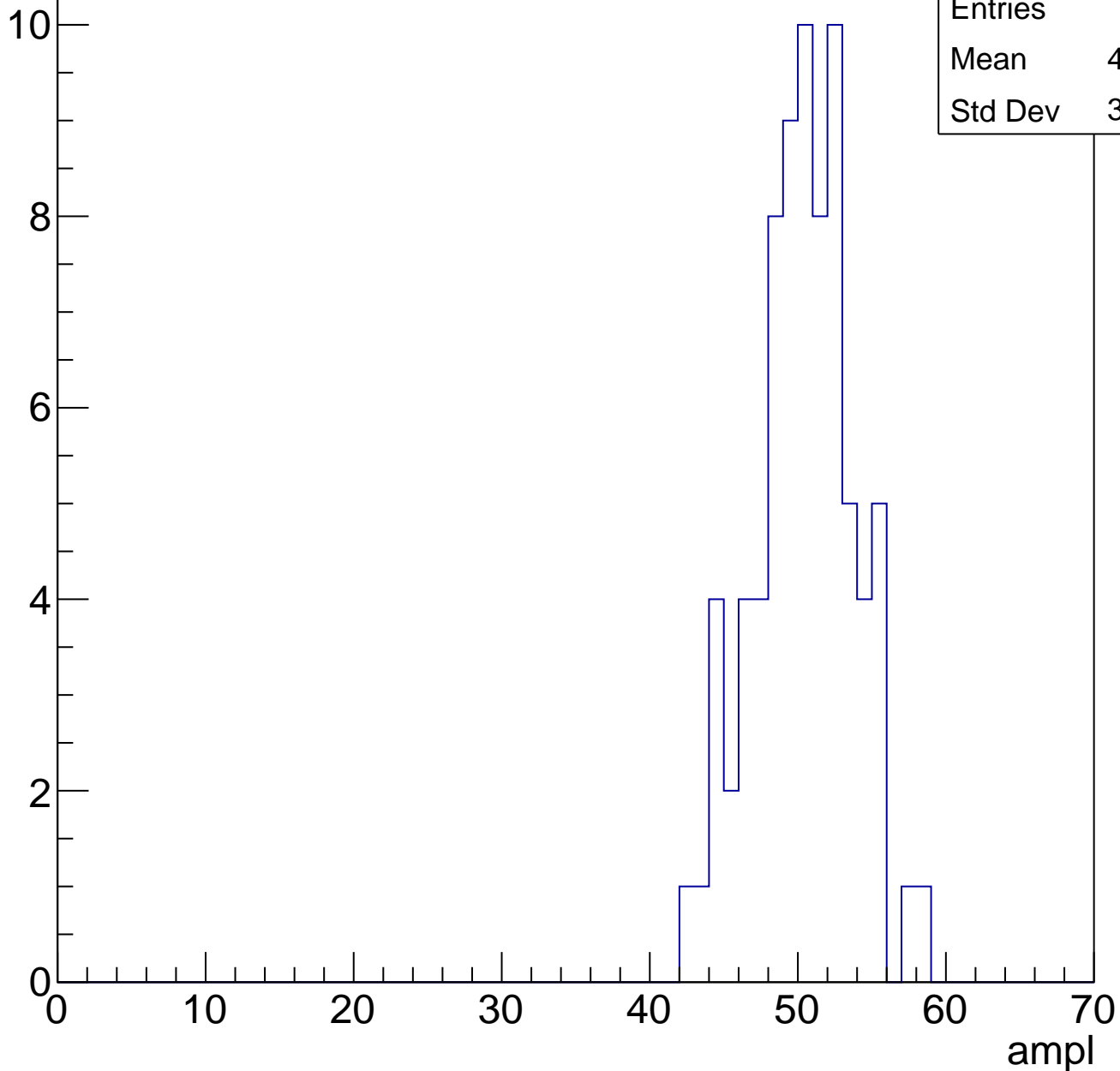


# B1L000S, U25-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	49.96
Std Dev	3.309

Entry

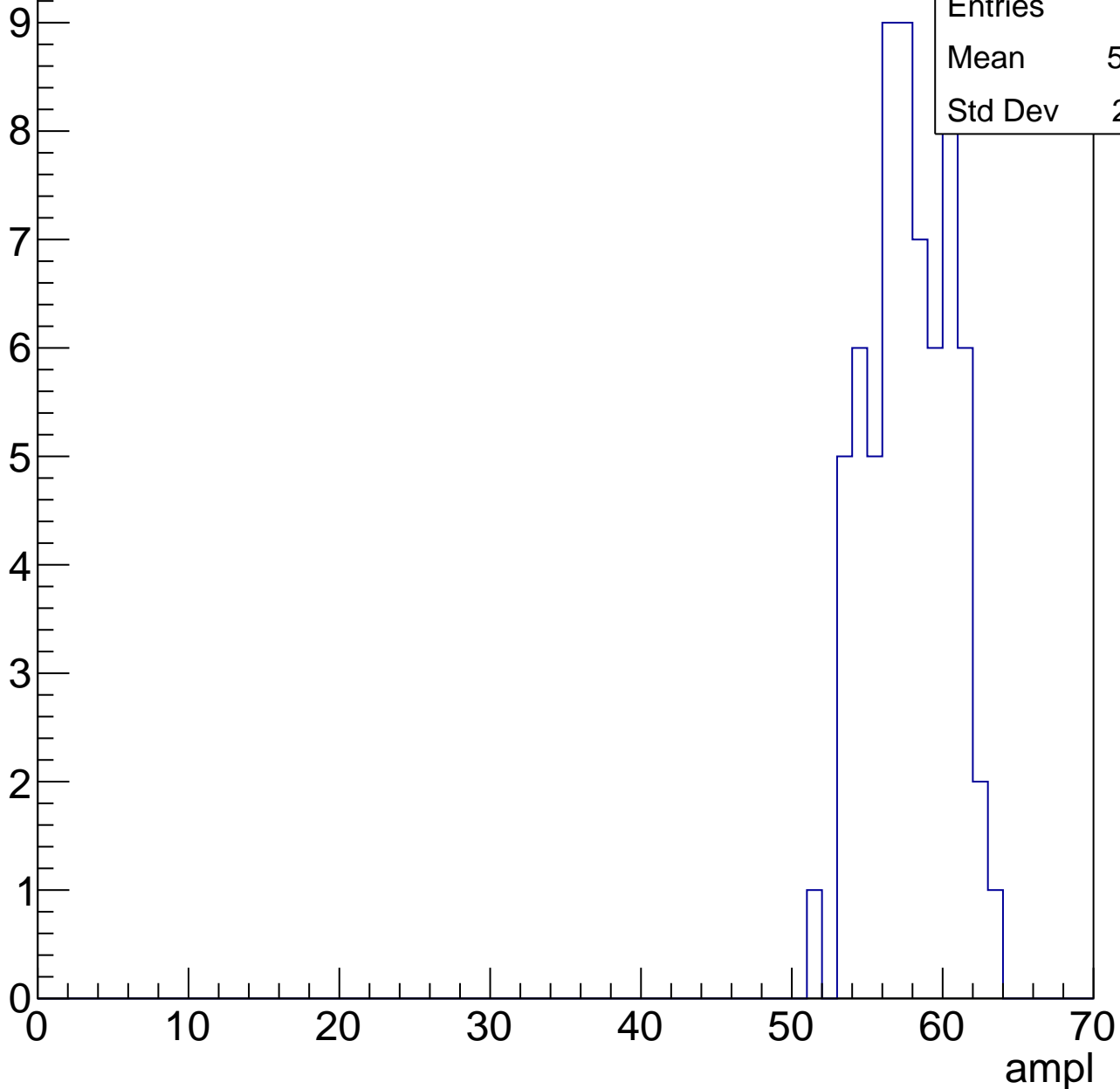


# B1L000S, U25-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

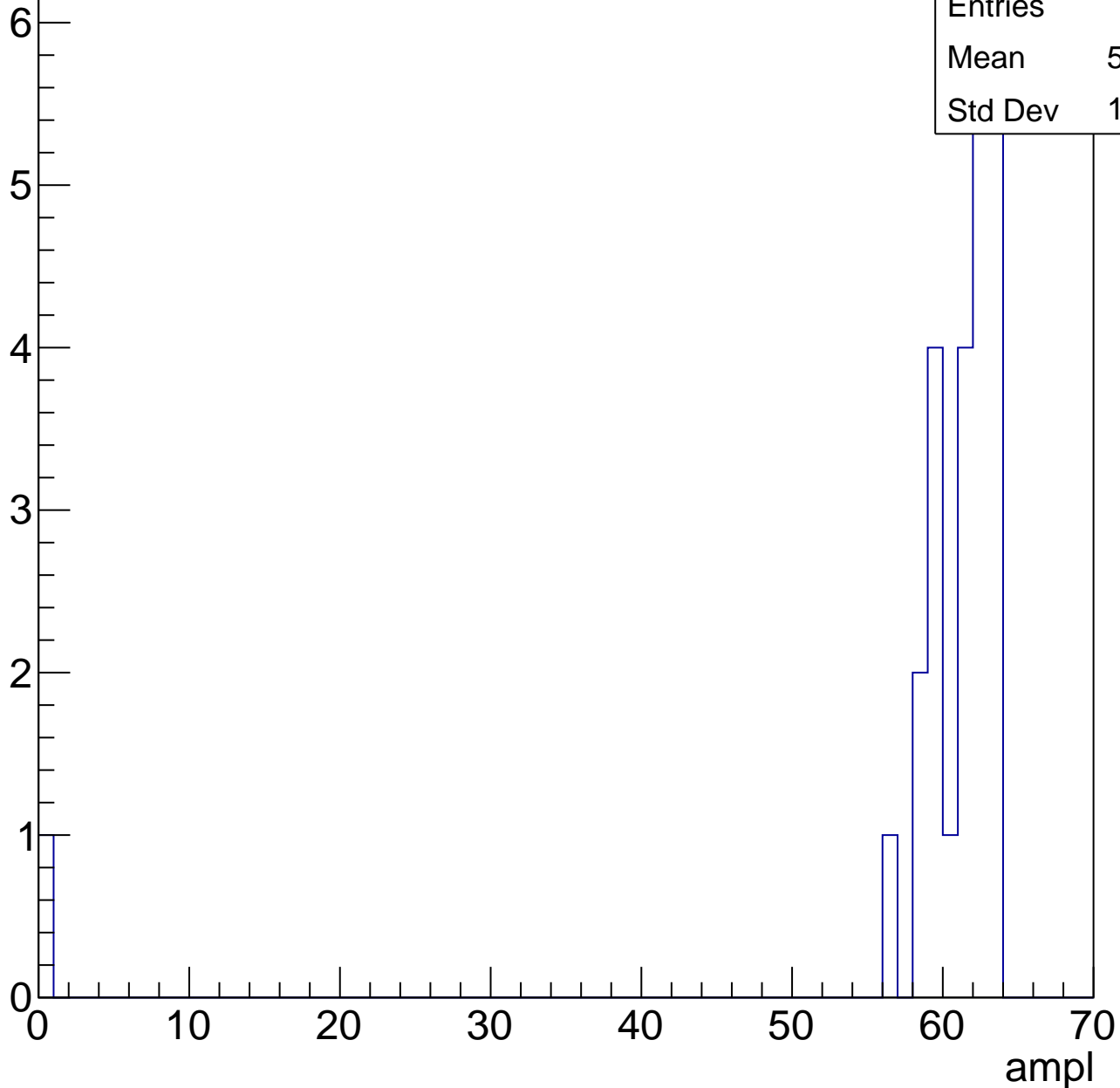
Entries	66
Mean	57.35
Std Dev	2.711



# B1L000S, U25-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch49, adc0

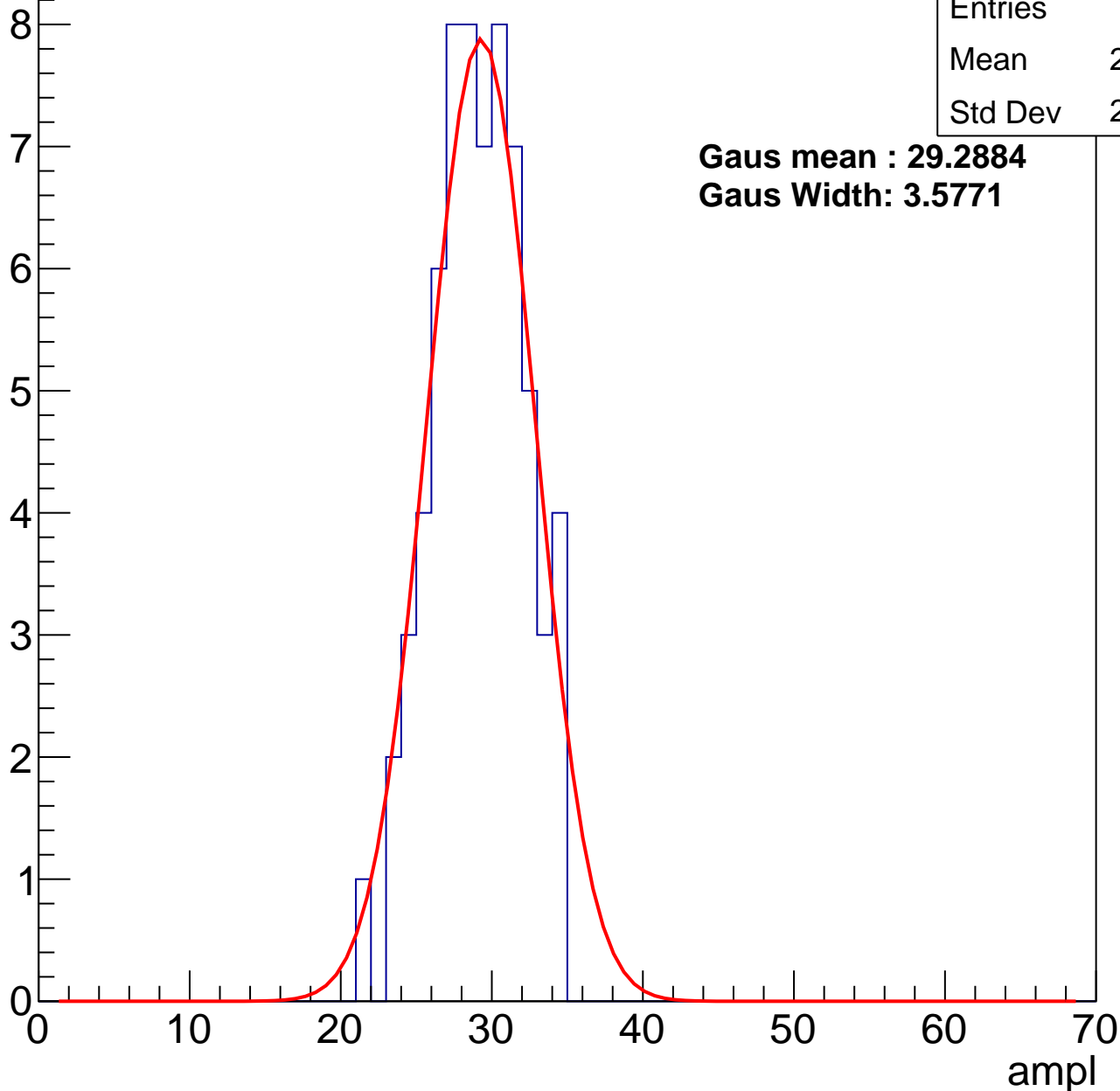
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	28.64
Std Dev	2.988

**Gaus mean : 29.2884**

**Gaus Width: 3.5771**



# B1L000S, U25-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	35.11
Std Dev	2.937

**Gaus mean : 35.3406**

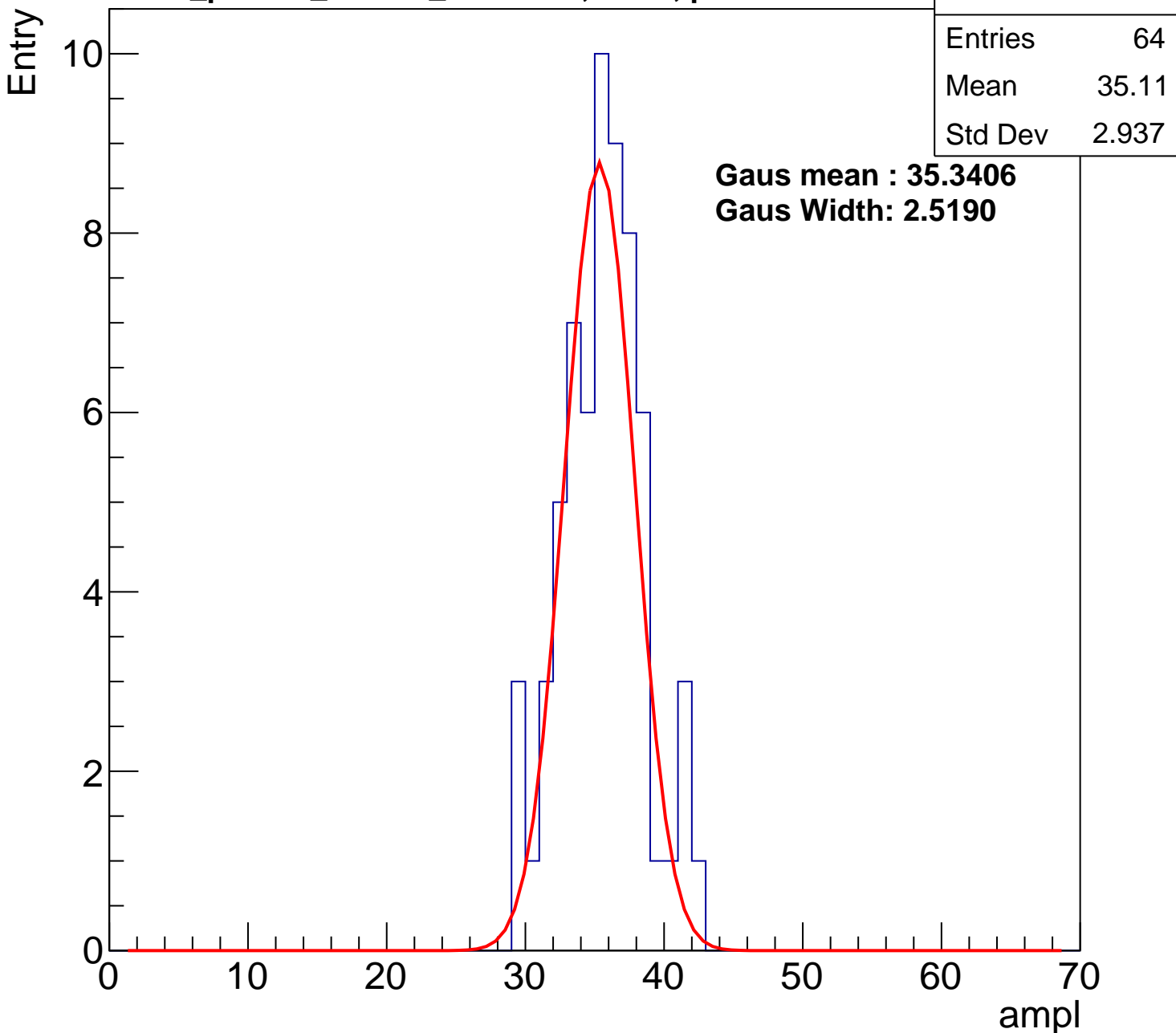
**Gaus Width: 2.5190**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch49, adc2

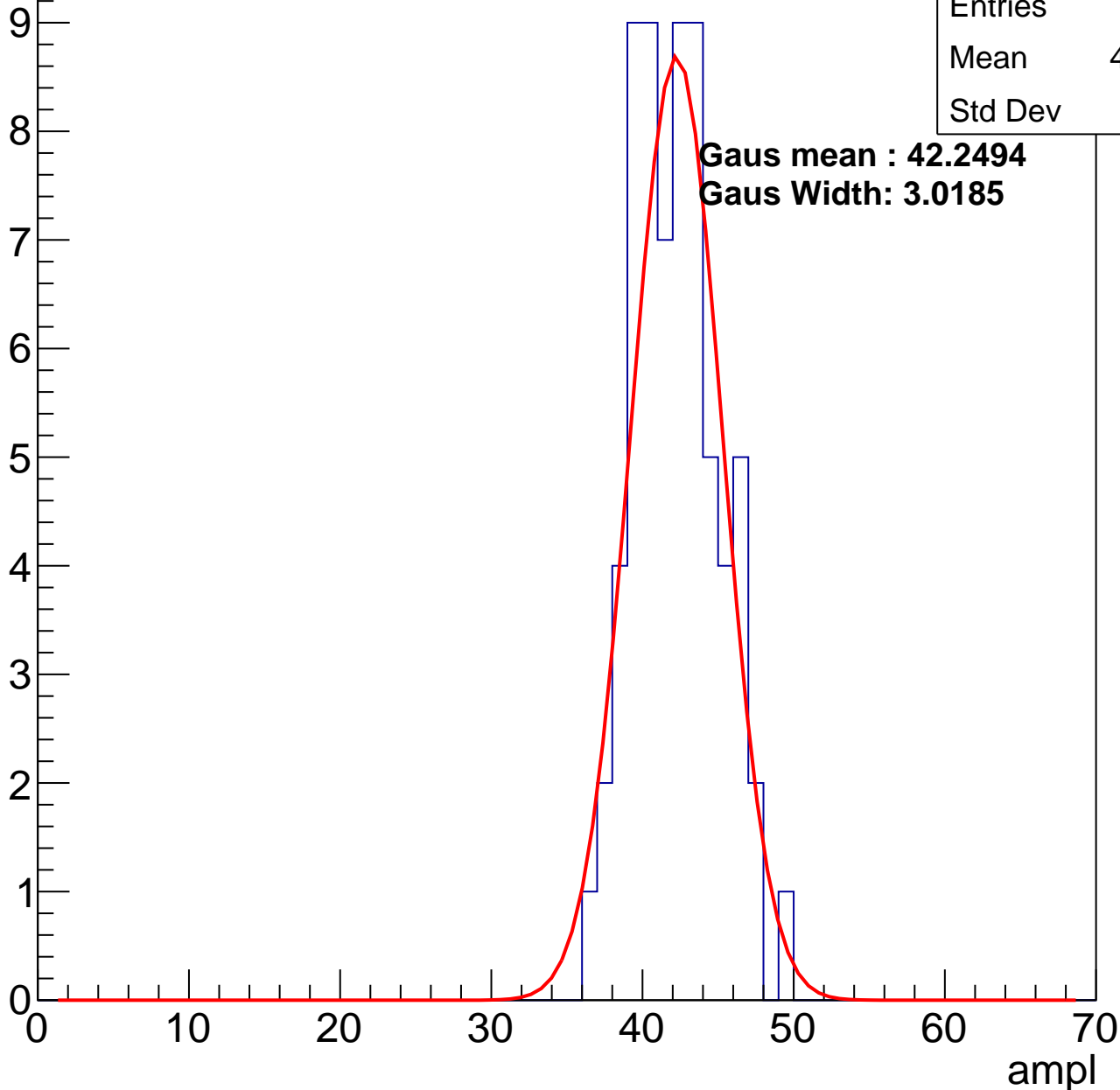
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.76
Std Dev	2.77

**Gaus mean : 42.2494**

**Gaus Width: 3.0185**

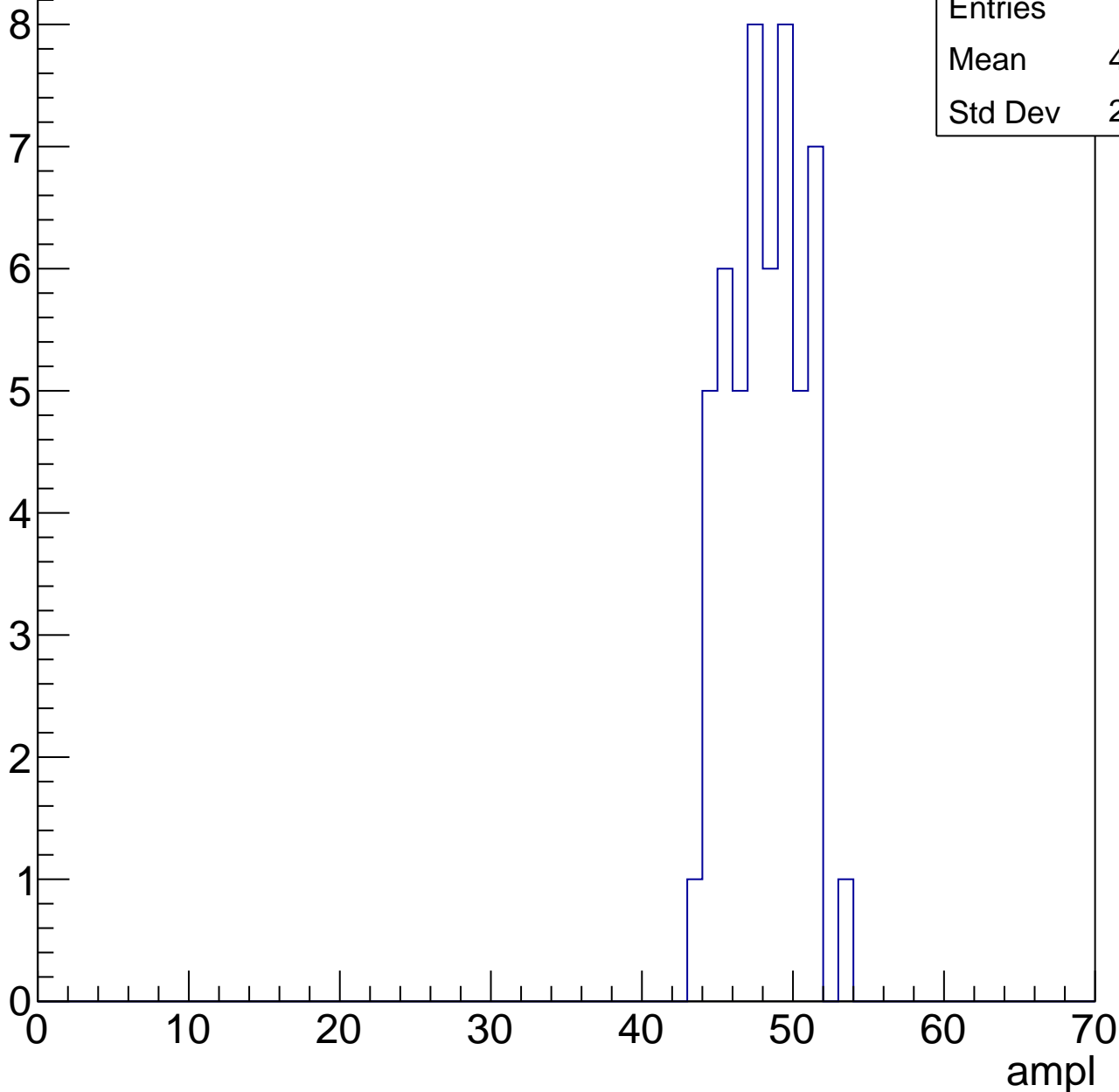


# B1L000S, U25-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	47.67
Std Dev	2.392

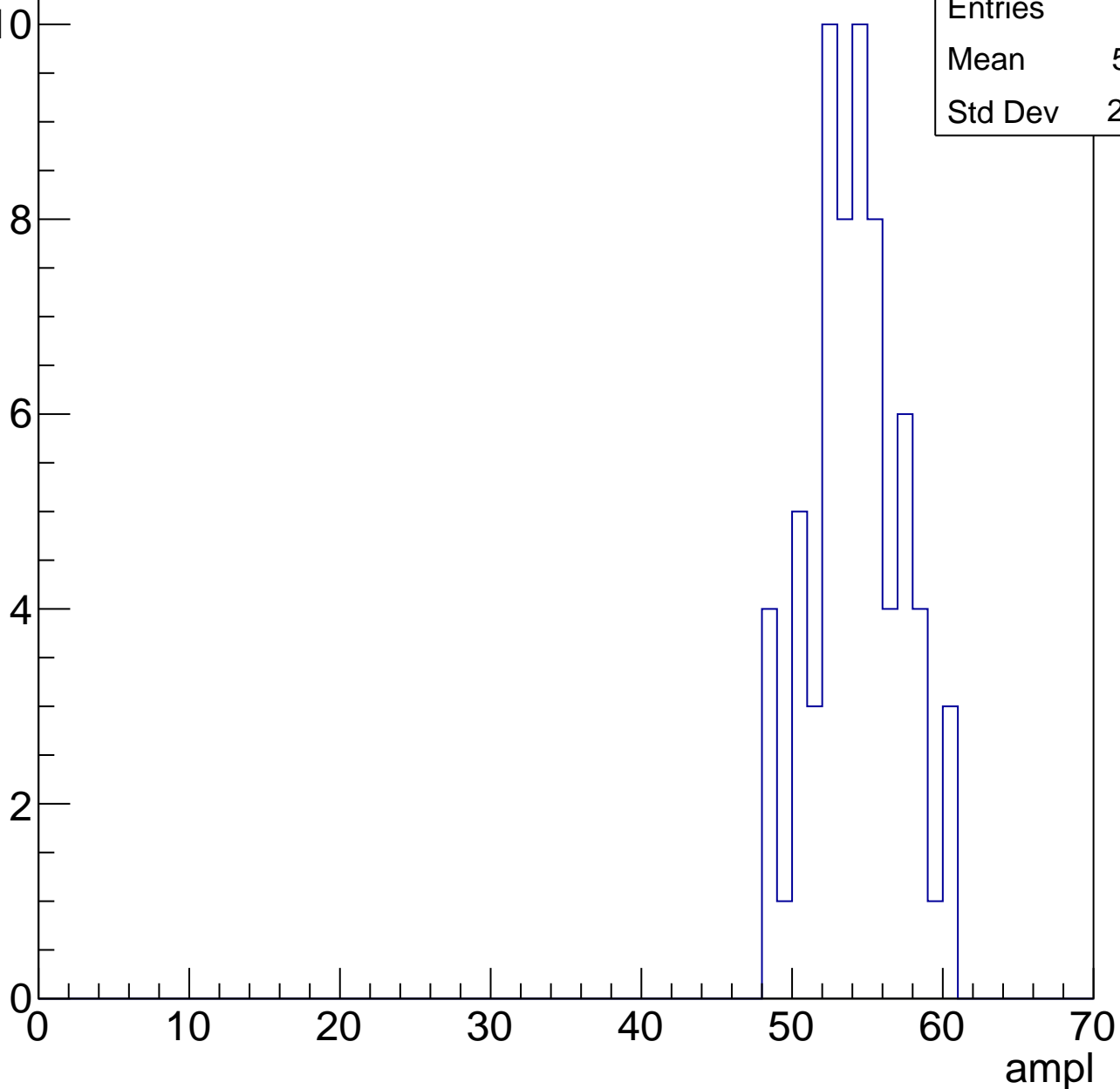


# B1L000S, U25-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

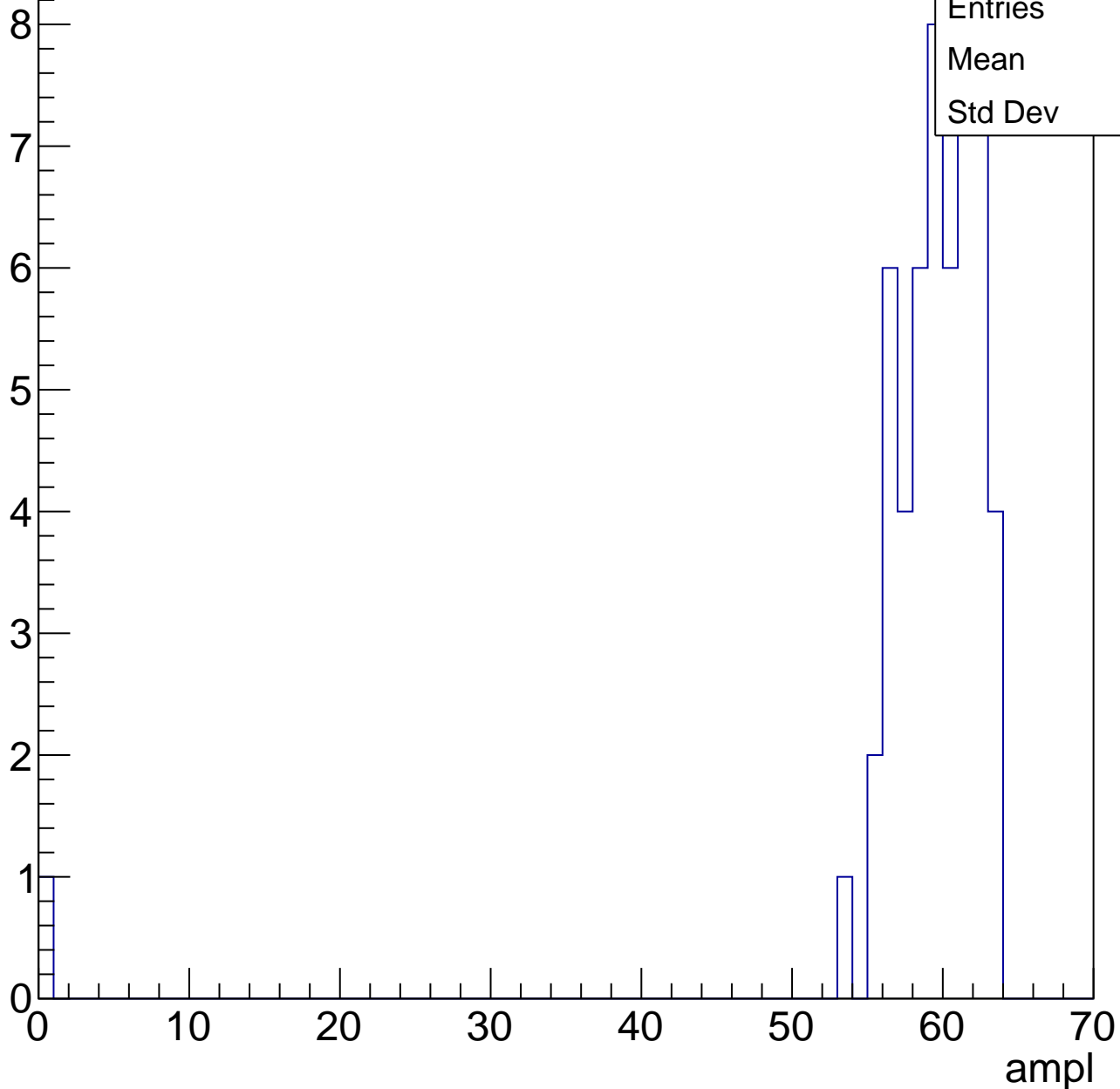
Entries	67
Mean	53.81
Std Dev	2.984



# B1L000S, U25-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

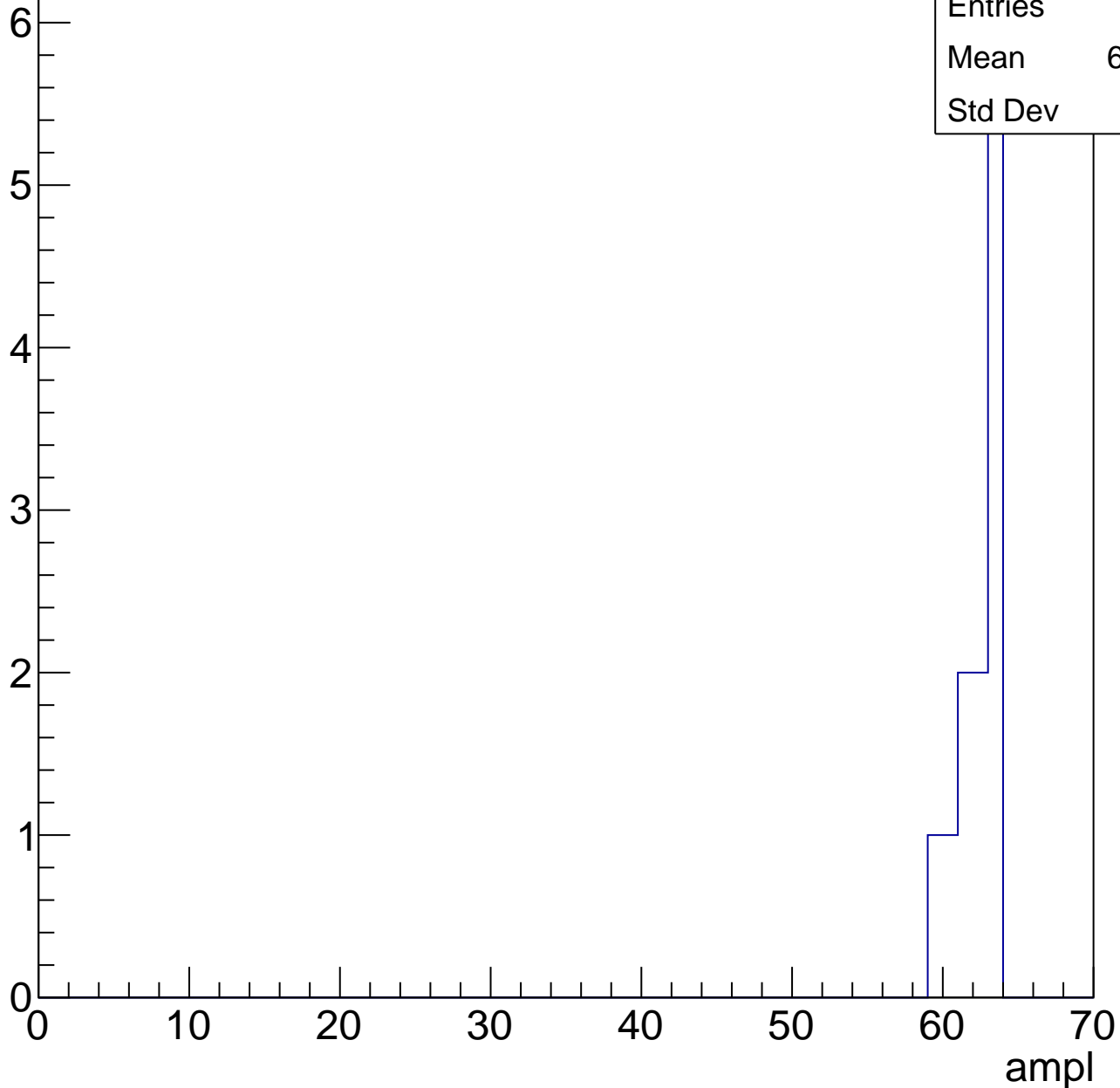


# B1L000S, U25-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	61.92
Std Dev	1.32





# B1L000S, U25-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch50, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	68
Mean	29.12
Std Dev	2.831

**Gaus mean : 29.5398**

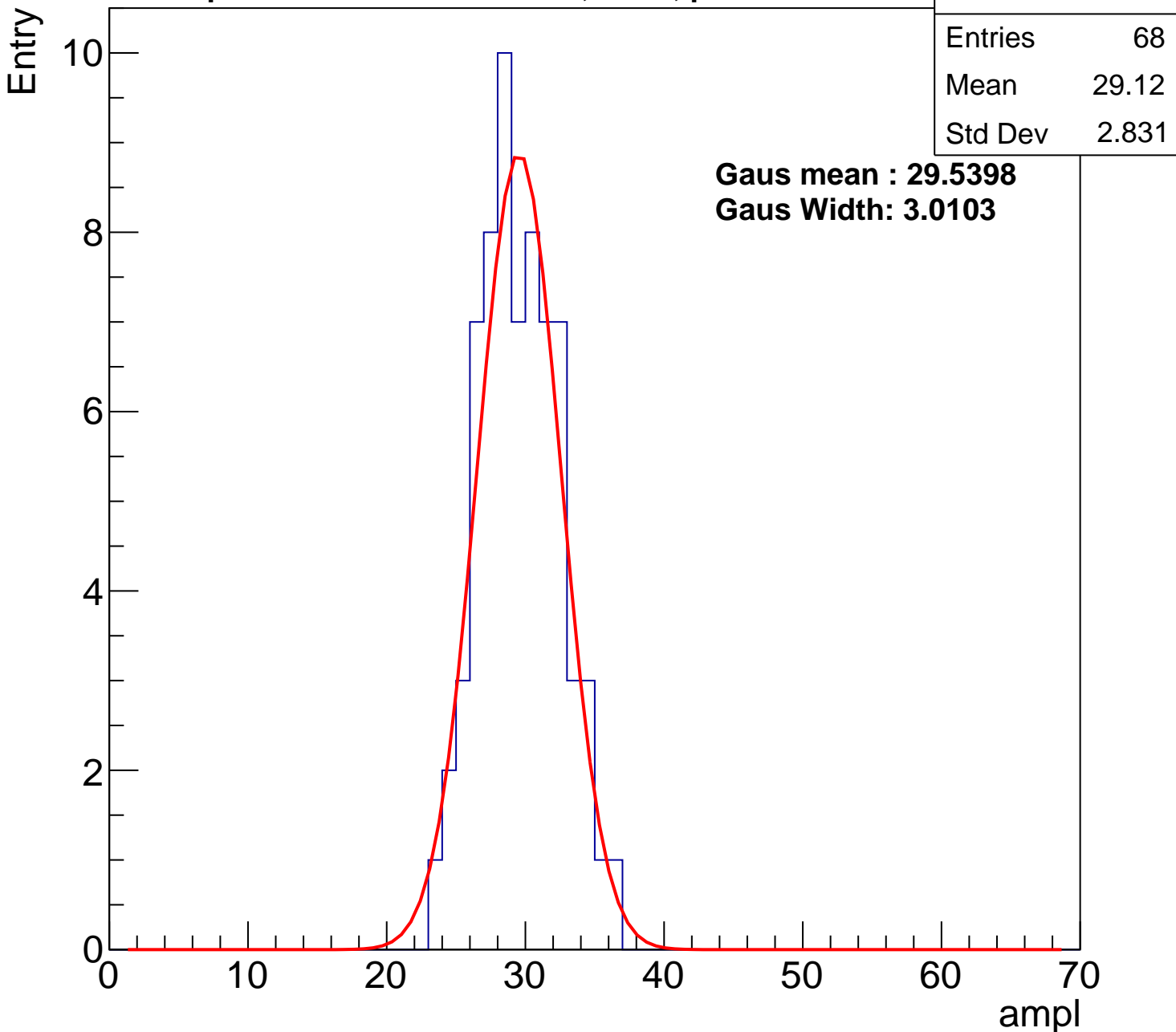
**Gaus Width: 3.0103**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

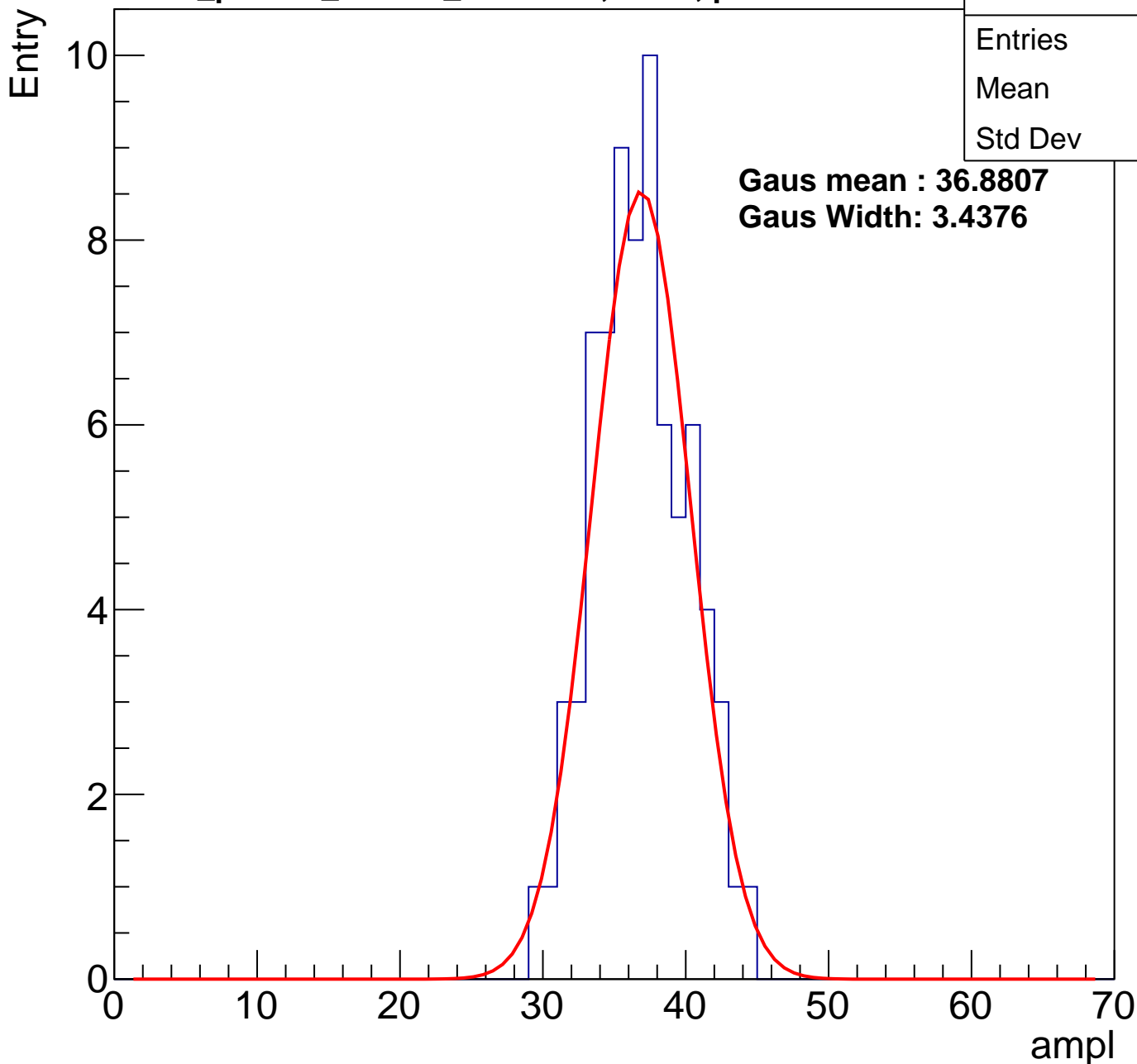


# B1L000S, U25-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	36.4
Std Dev	3.25

**Gaus mean : 36.8807**  
**Gaus Width: 3.4376**



# B1L000S, U25-ch50, adc2

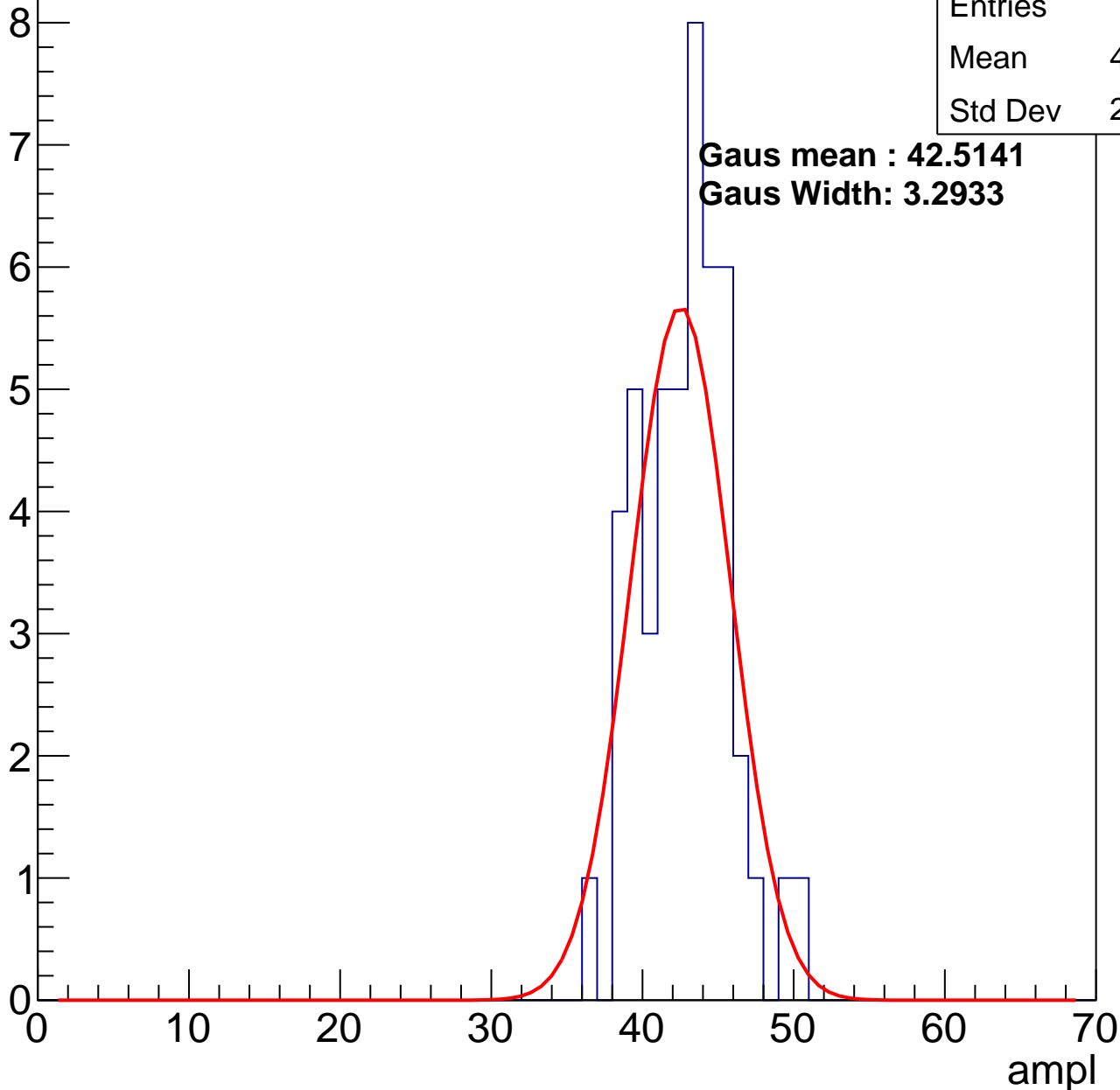
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	42.38
Std Dev	2.927

**Gaus mean : 42.5141**

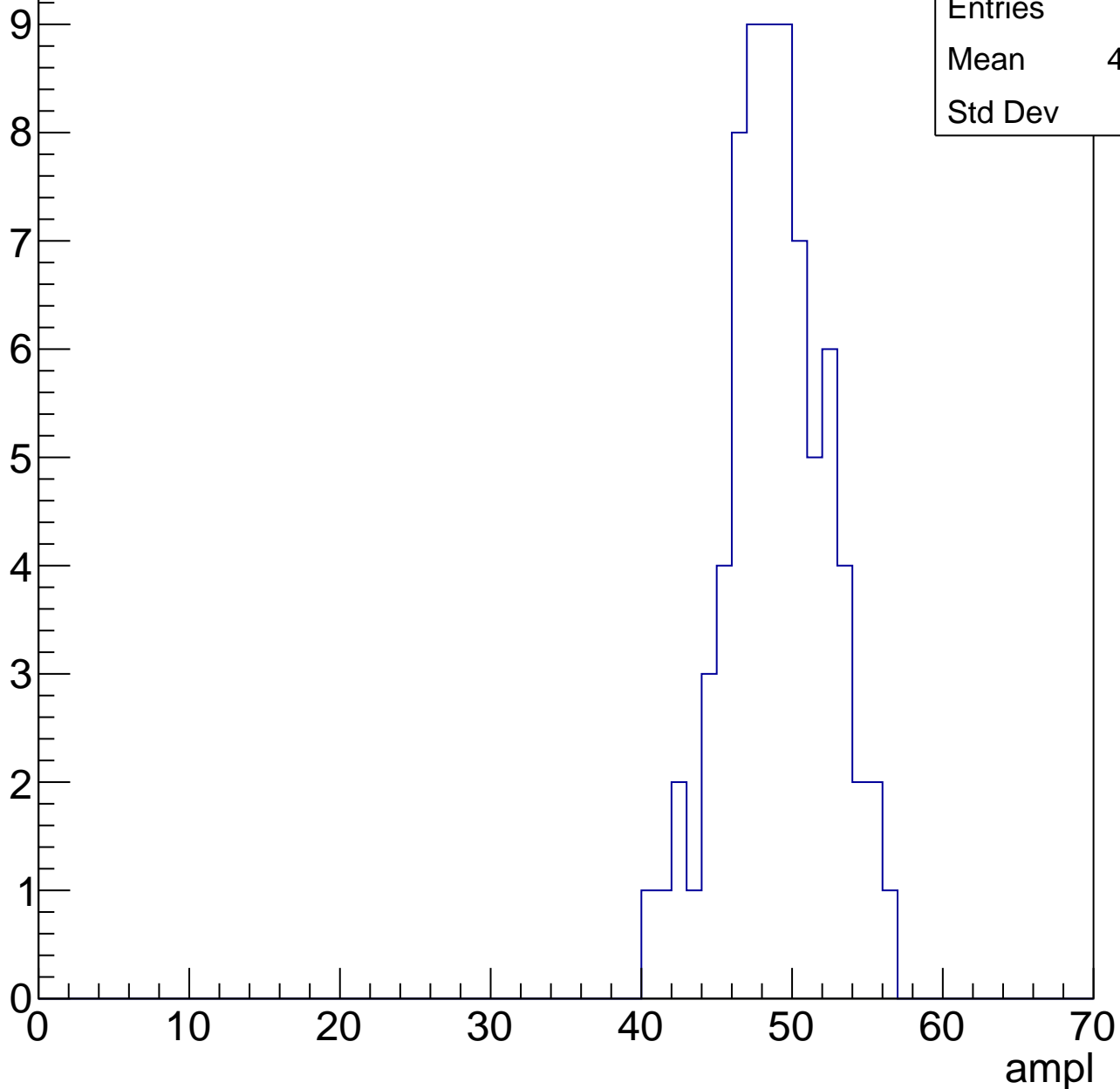
**Gaus Width: 3.2933**



# B1L000S, U25-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



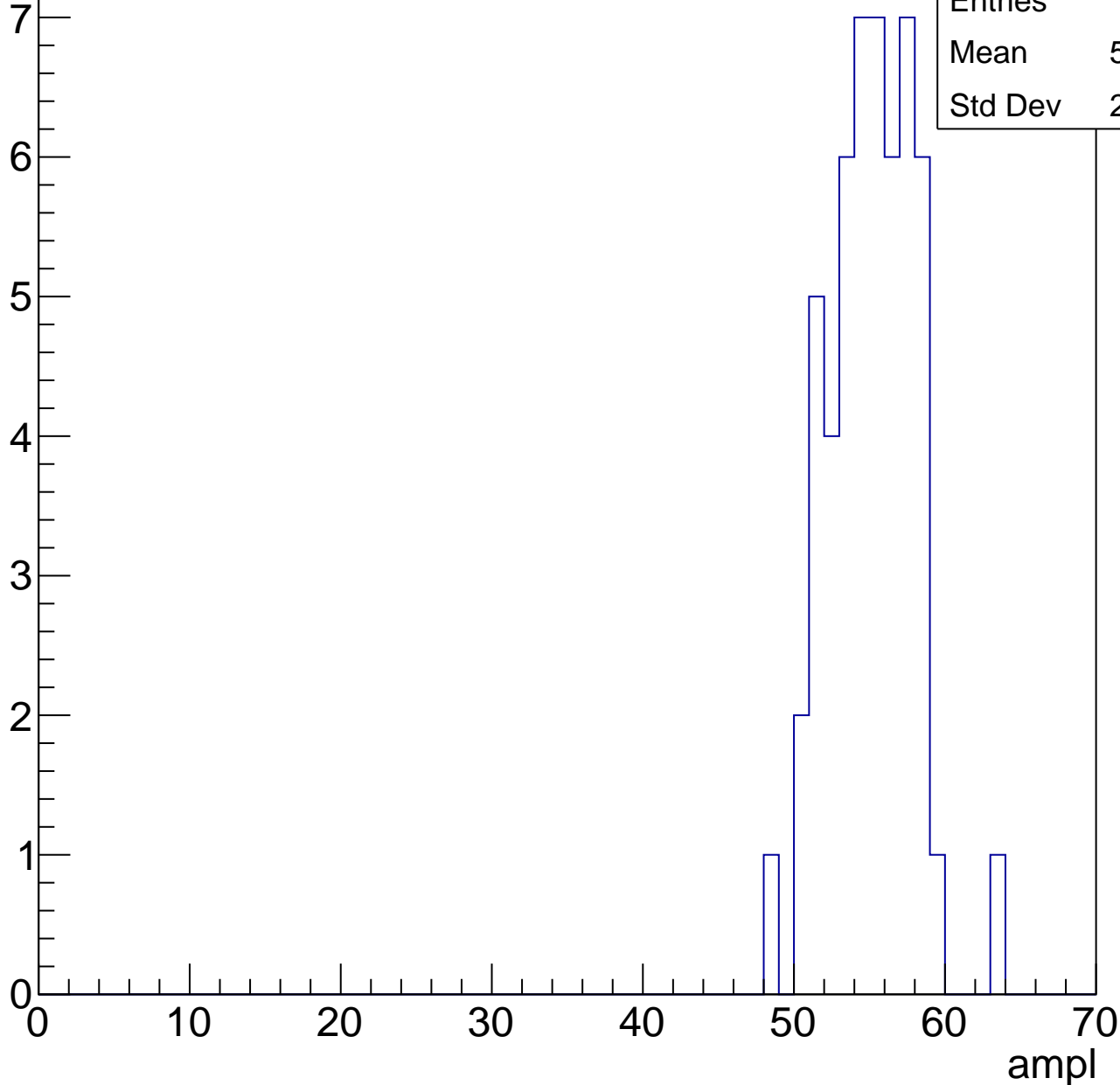
Entries	74
Mean	48.47
Std Dev	3.35

# B1L000S, U25-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	54.66
Std Dev	2.774



# B1L000S, U25-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

57

Mean

58.96

Std Dev

8.184

1

2

3

4

5

6

7

8

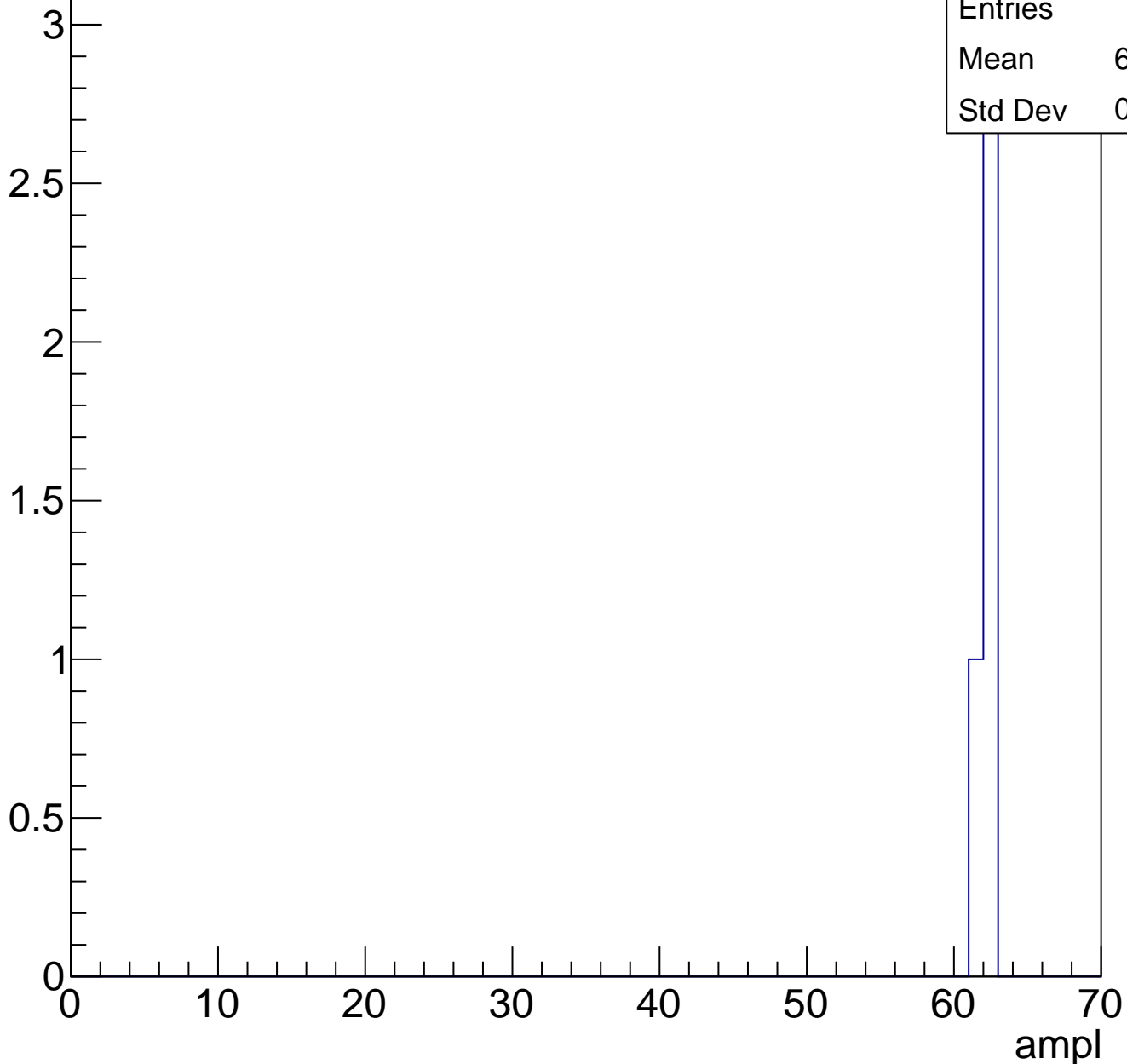
9

10

# B1L000S, U25-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	61.75
Std Dev	0.433



# B1L000S, U25-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U25-ch51, adc0

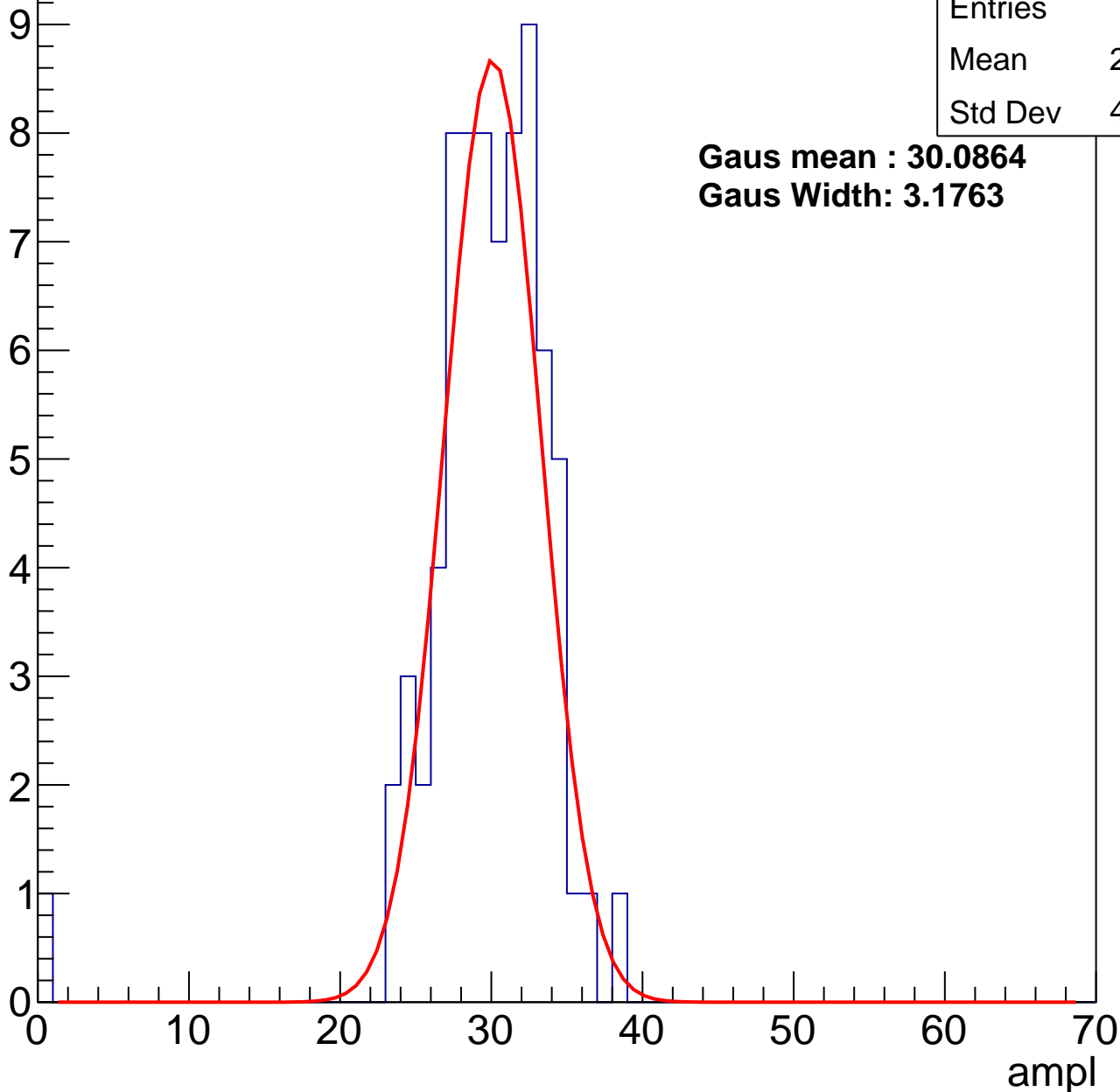
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	29.28
Std Dev	4.646

**Gaus mean : 30.0864**

**Gaus Width: 3.1763**



# B1L000S, U25-ch51, adc1

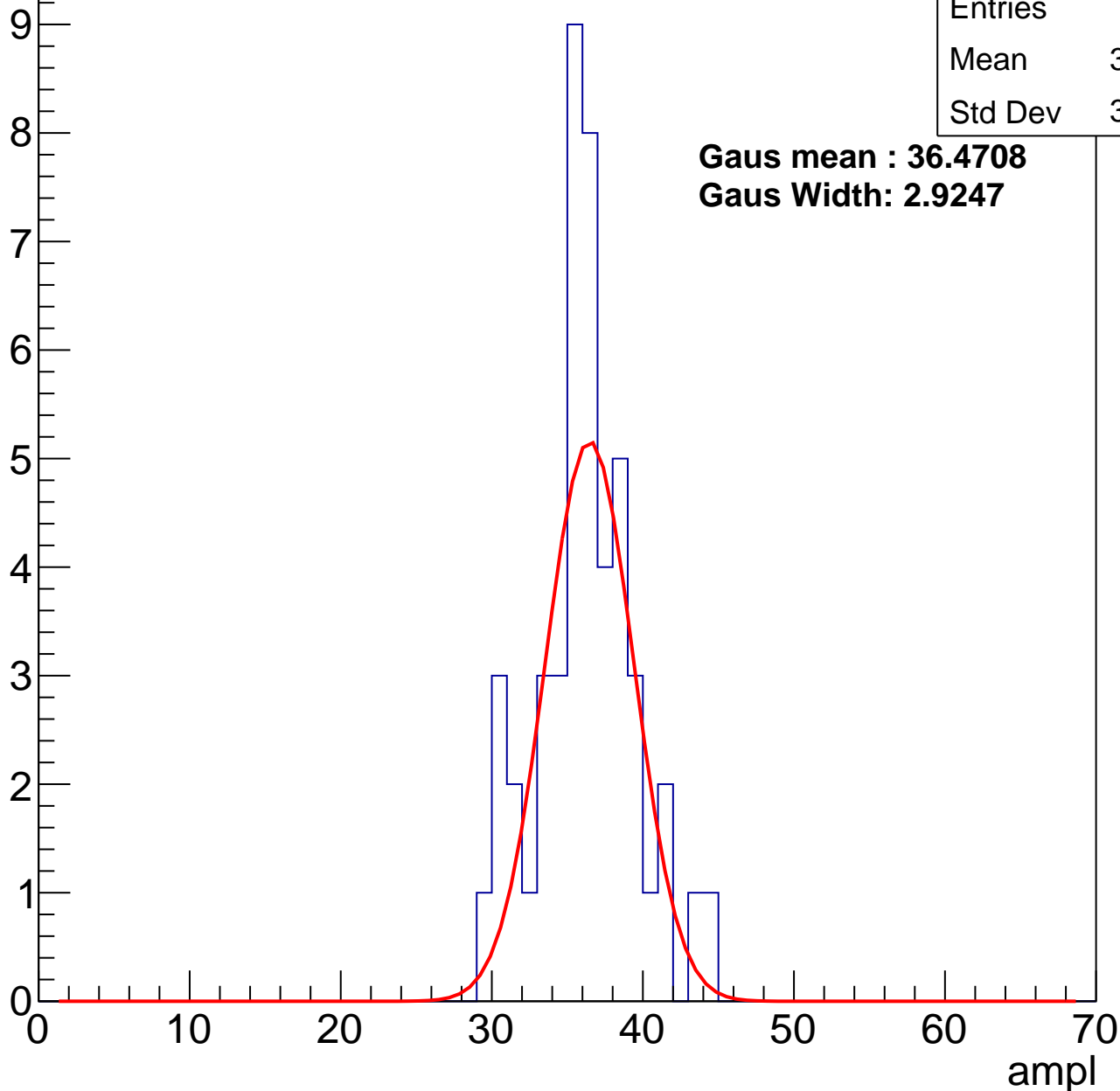
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	35.77
Std Dev	3.257

**Gaus mean : 36.4708**

**Gaus Width: 2.9247**



# B1L000S, U25-ch51, adc2

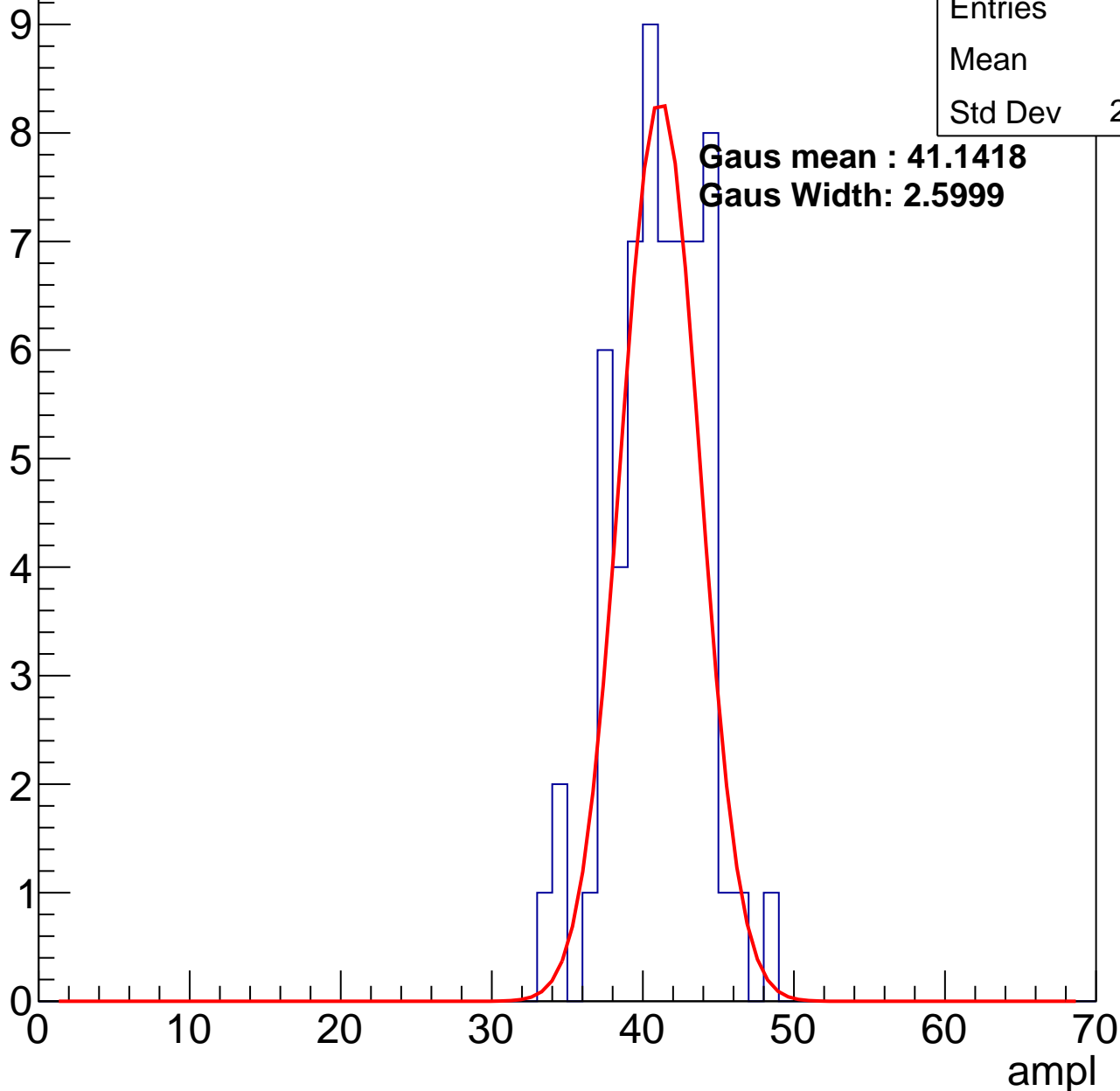
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.6
Std Dev	2.959

**Gaus mean : 41.1418**

**Gaus Width: 2.5999**

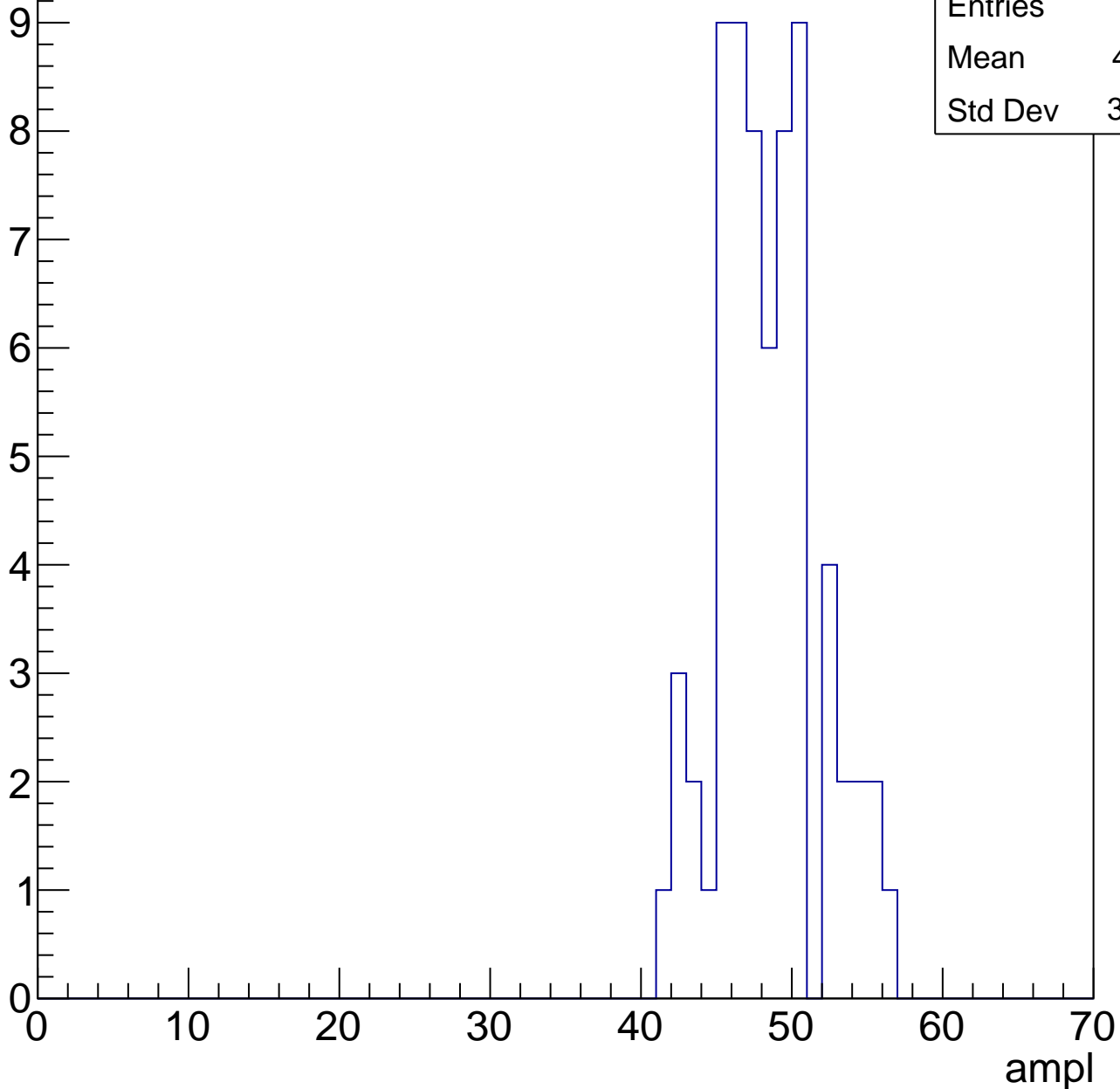


# B1L000S, U25-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

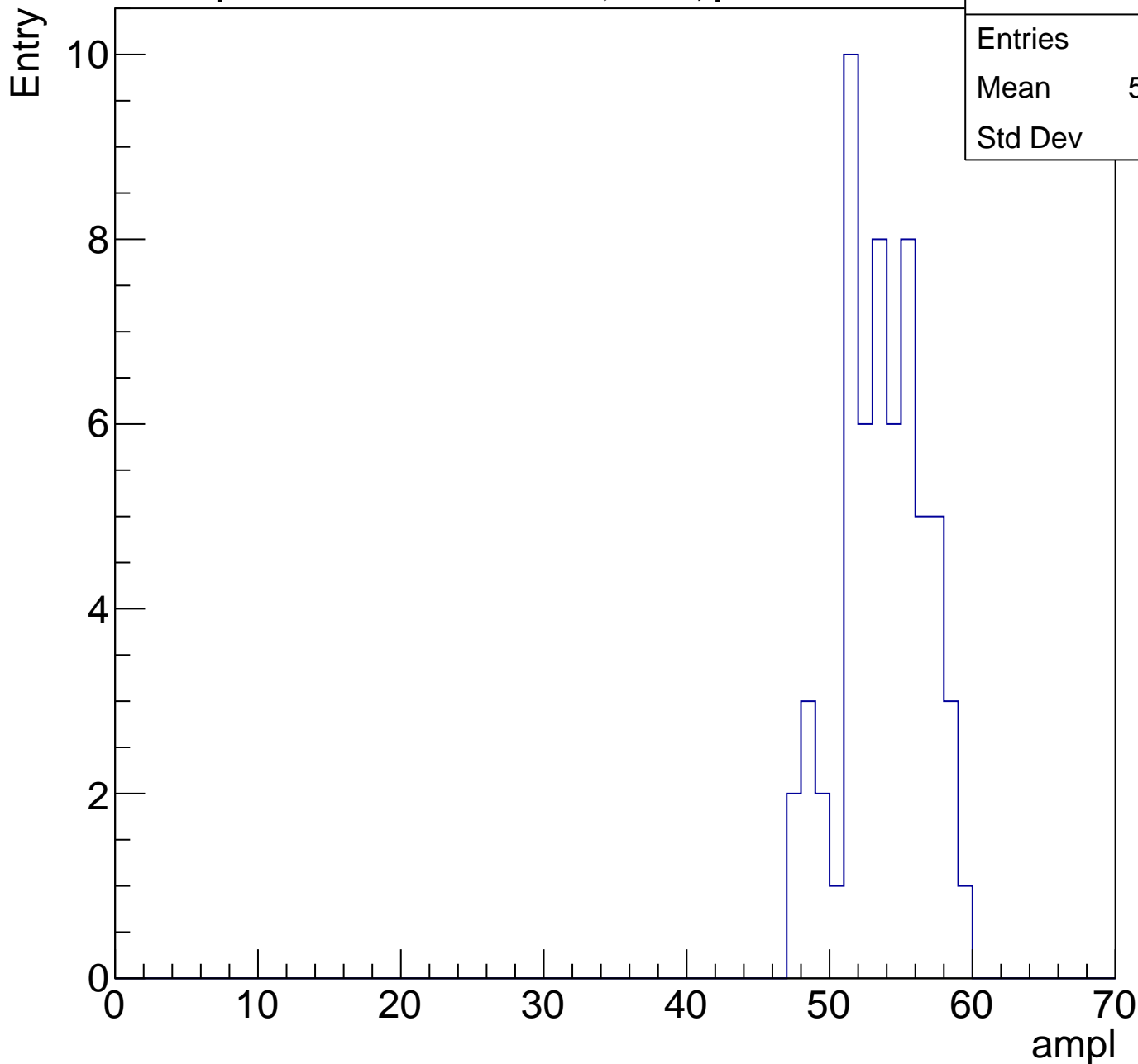
Entries	67
Mean	47.91
Std Dev	3.322



# B1L000S, U25-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	60
Mean	53.23
Std Dev	2.9

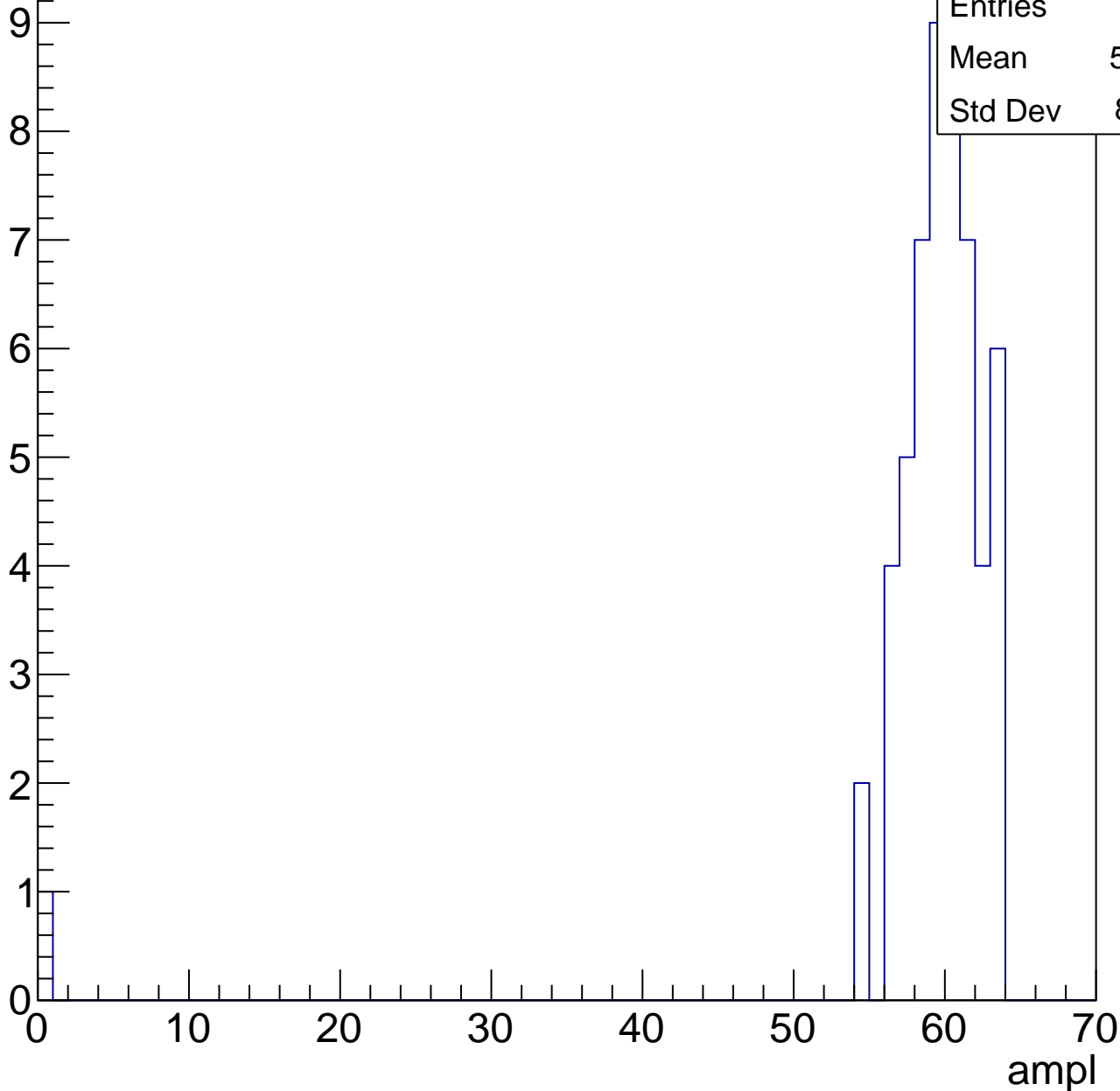


# B1L000S, U25-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	58.25
Std Dev	8.391

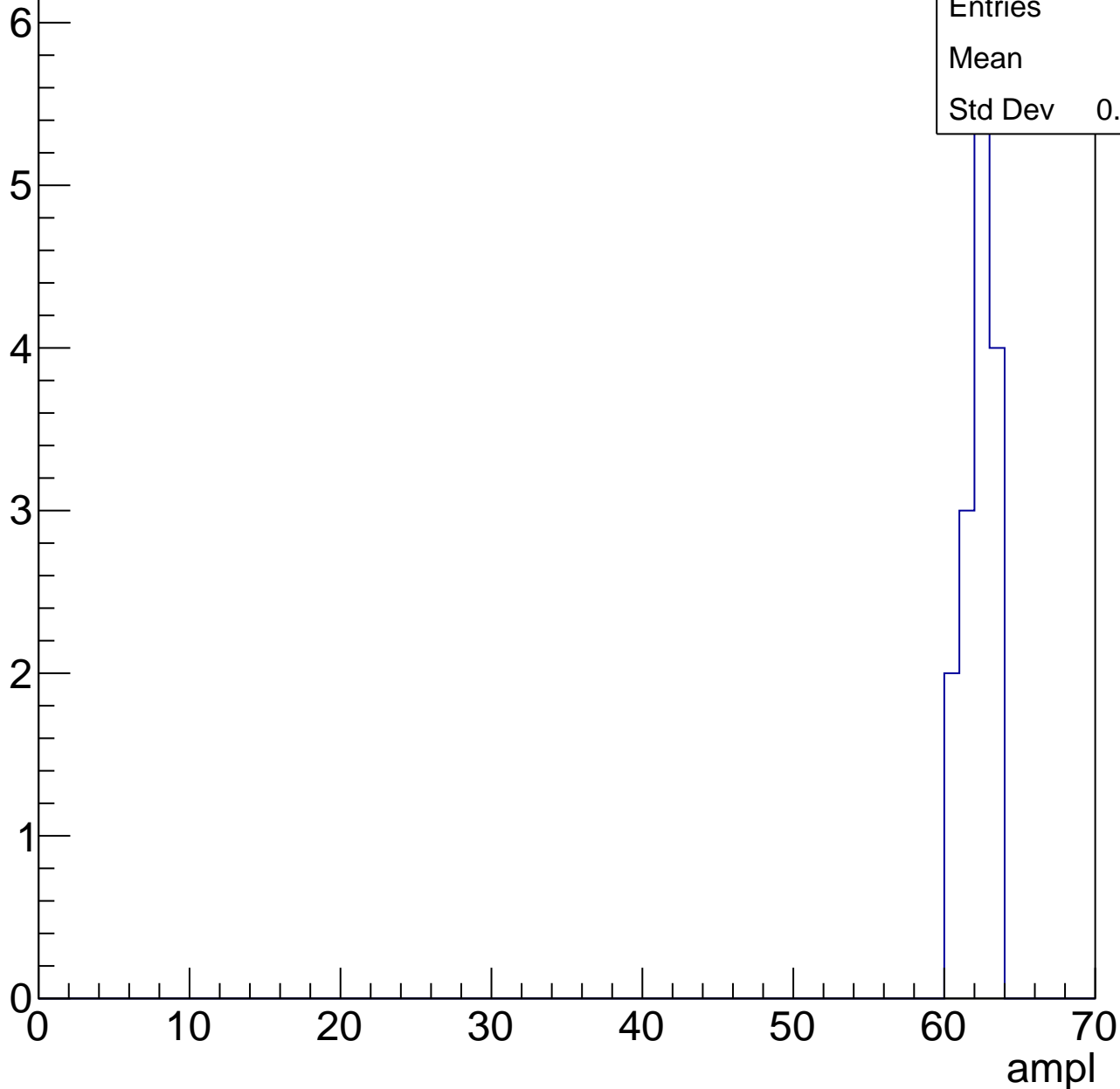


# B1L000S, U25-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.8
Std Dev	0.9798





# B1L000S, U25-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	71
Mean	27.82
Std Dev	4.422

**Gaus mean : 28.2430**

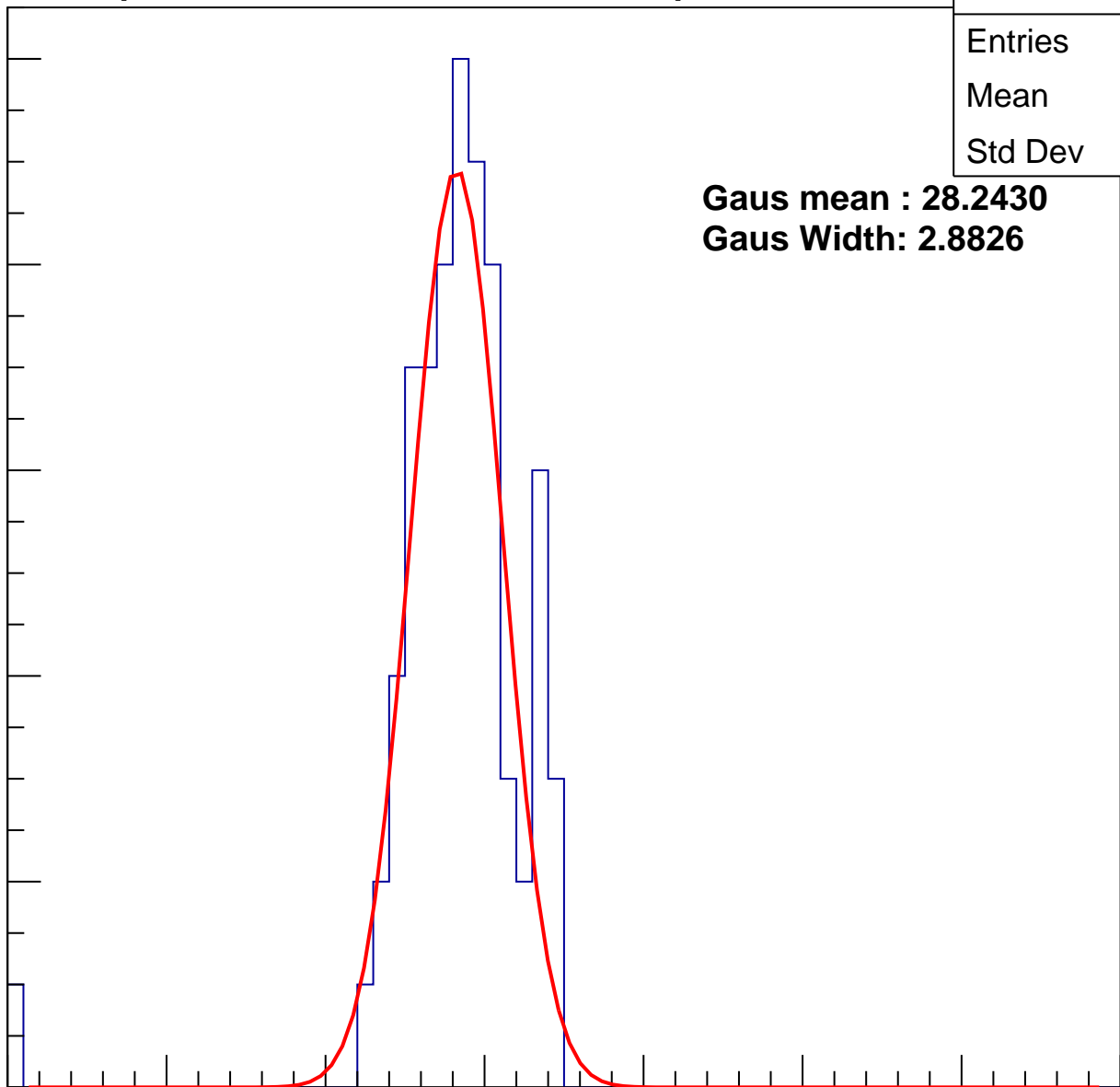
**Gaus Width: 2.8826**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch52, adc1

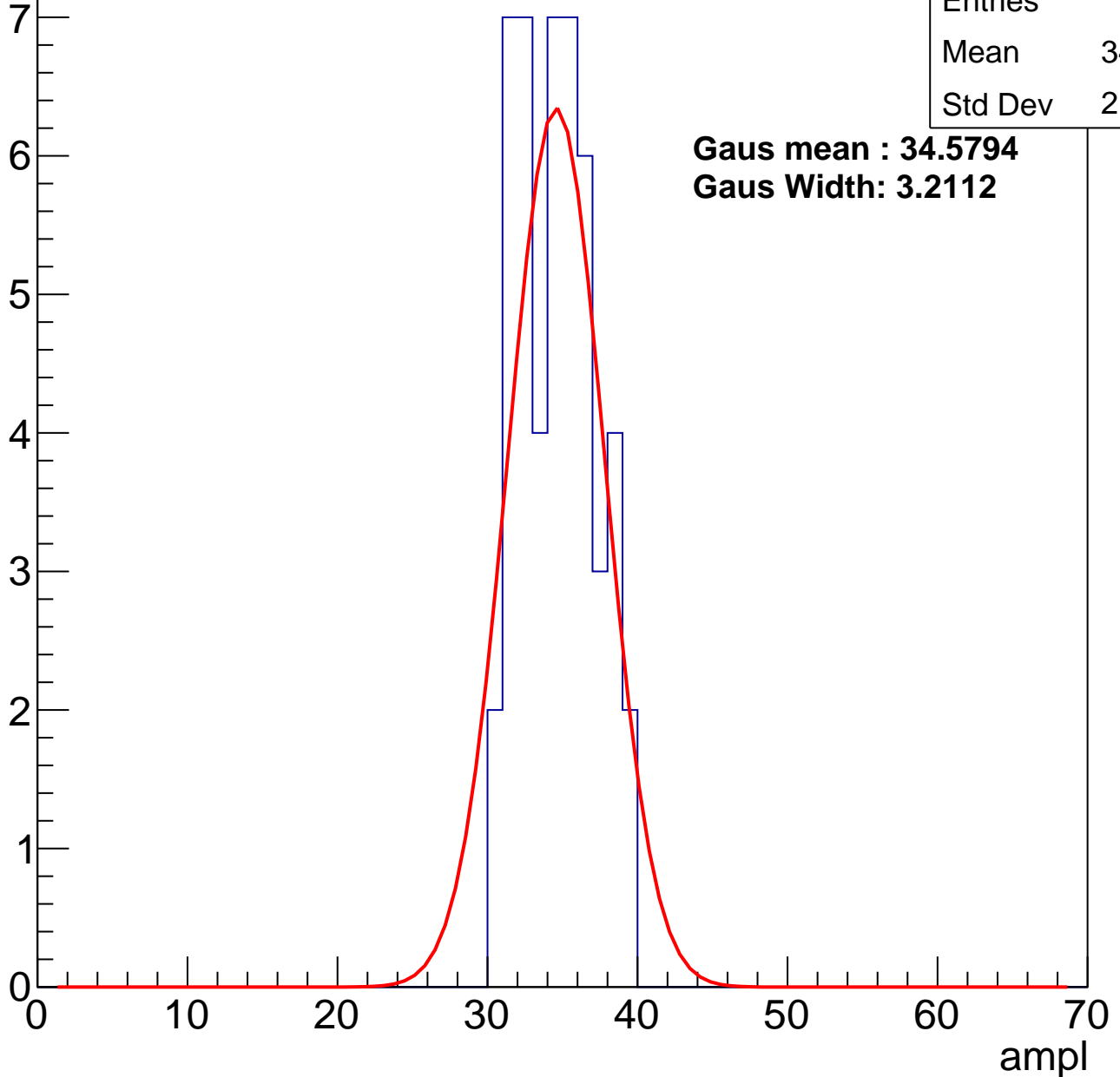
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	34.14
Std Dev	2.466

**Gaus mean : 34.5794**

**Gaus Width: 3.2112**



# B1L000S, U25-ch52, adc2

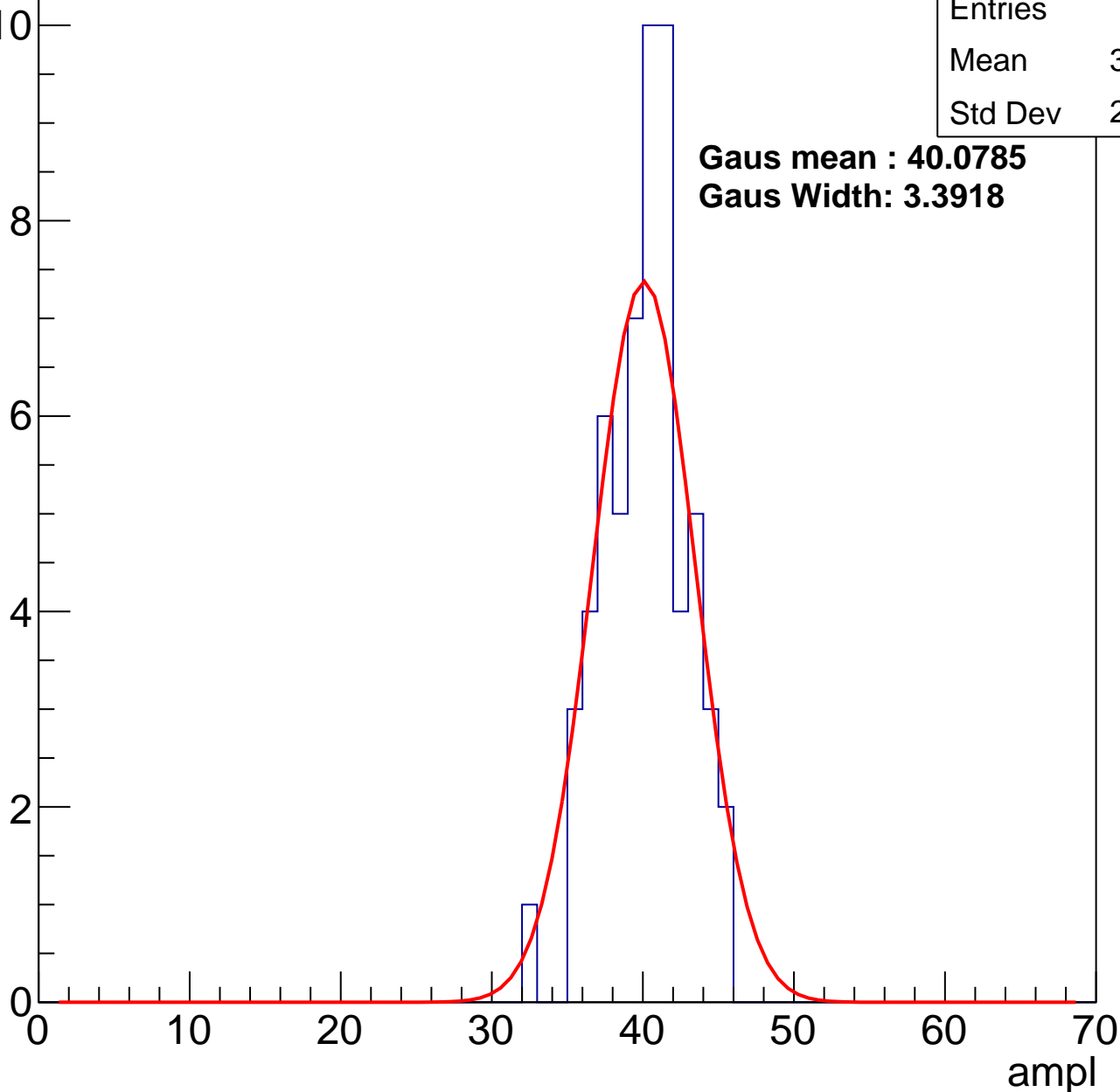
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	39.68
Std Dev	2.729

**Gaus mean : 40.0785**

**Gaus Width: 3.3918**



# B1L000S, U25-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

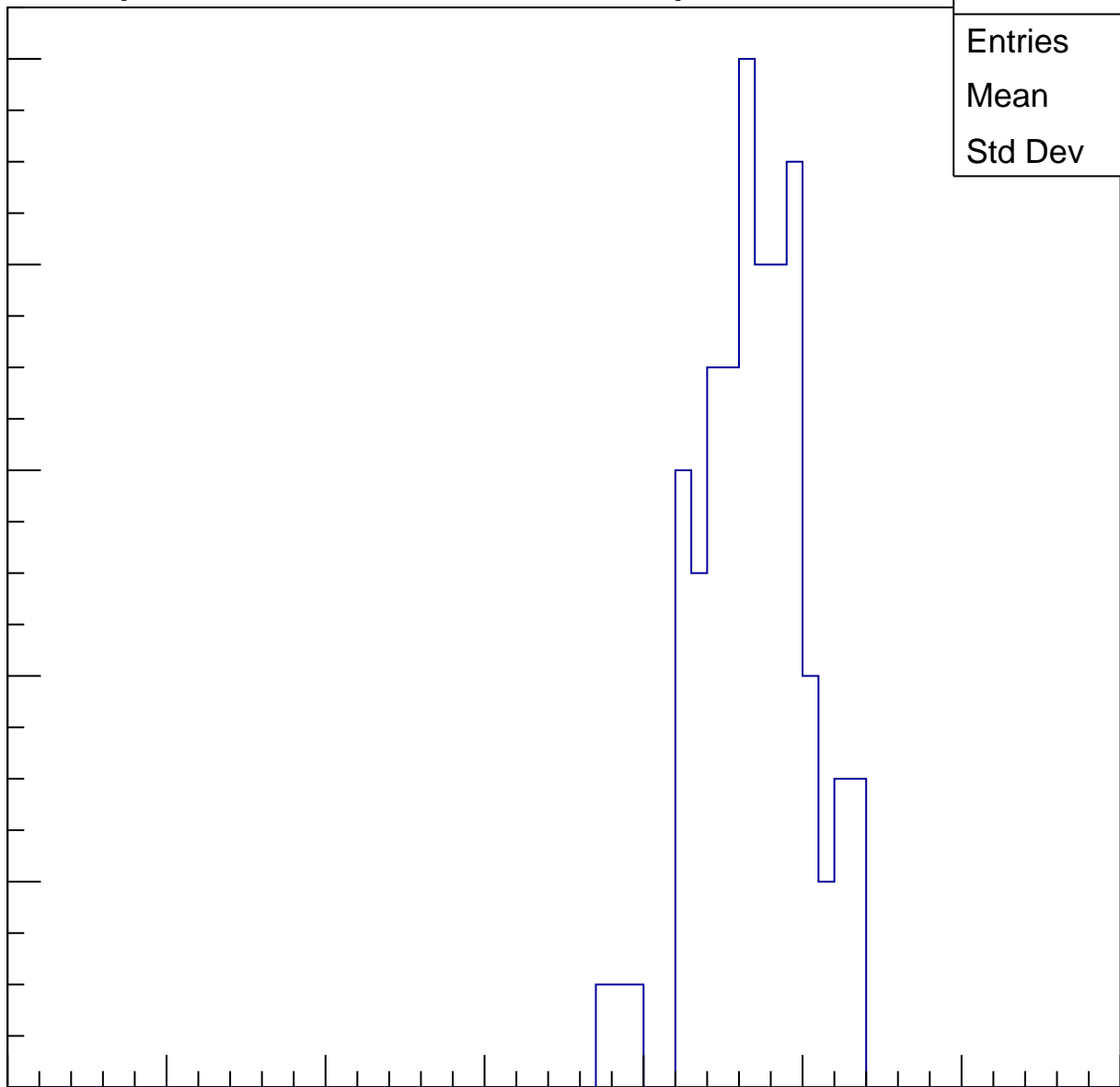
Entries	75
Mean	46.43
Std Dev	3.359

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

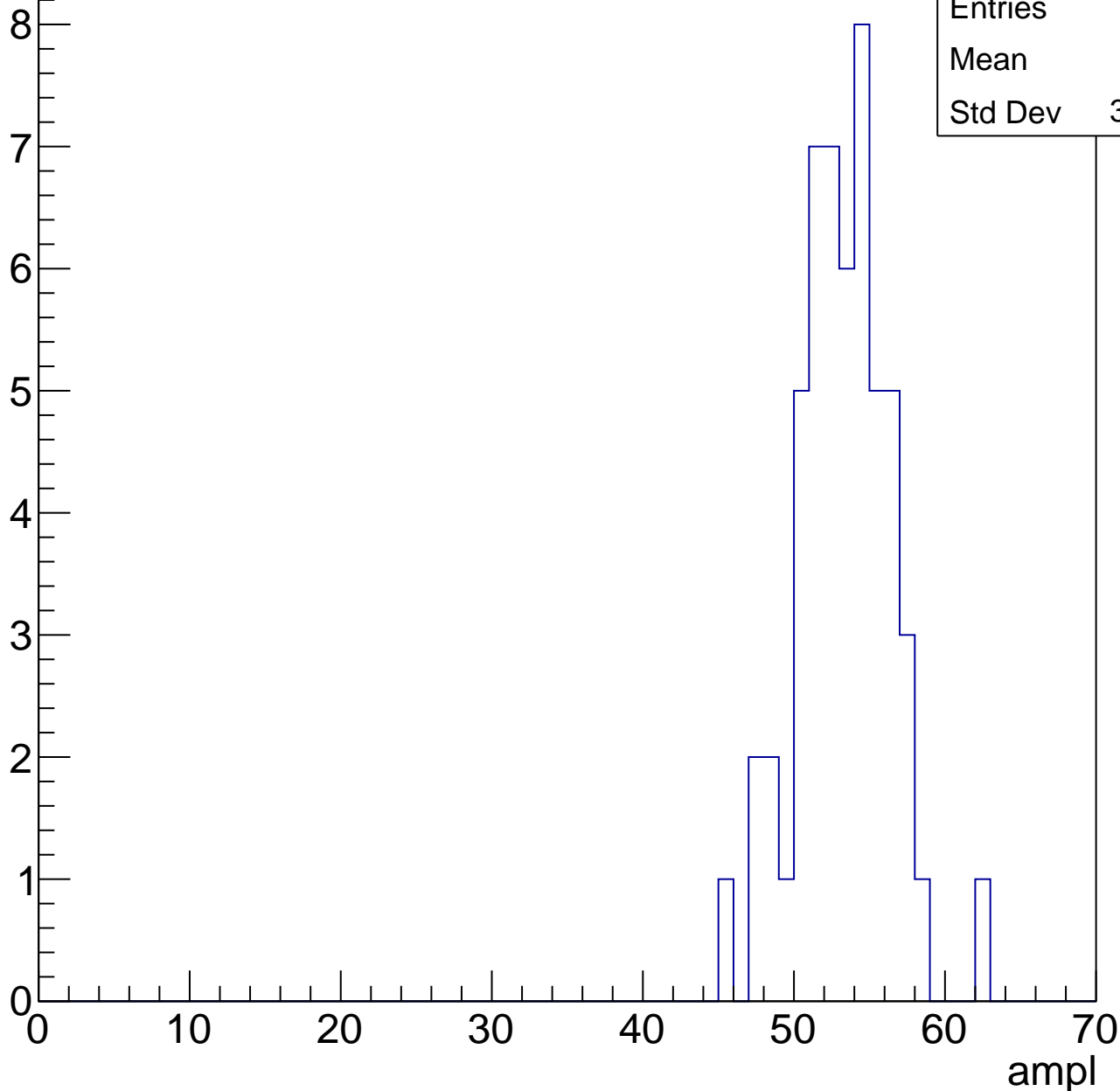


# B1L000S, U25-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	52.8
Std Dev	3.063

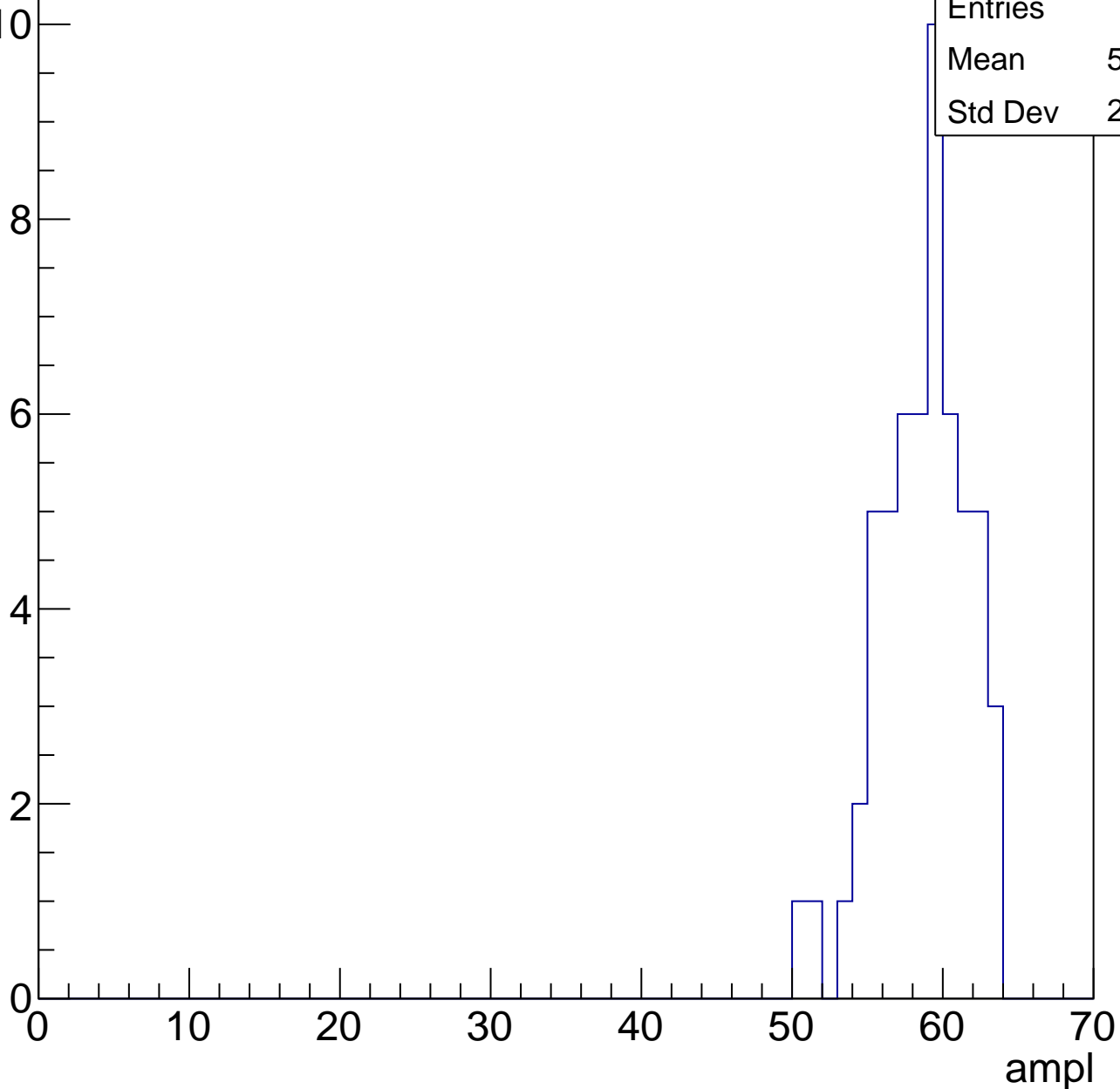


# B1L000S, U25-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	58.23
Std Dev	2.903

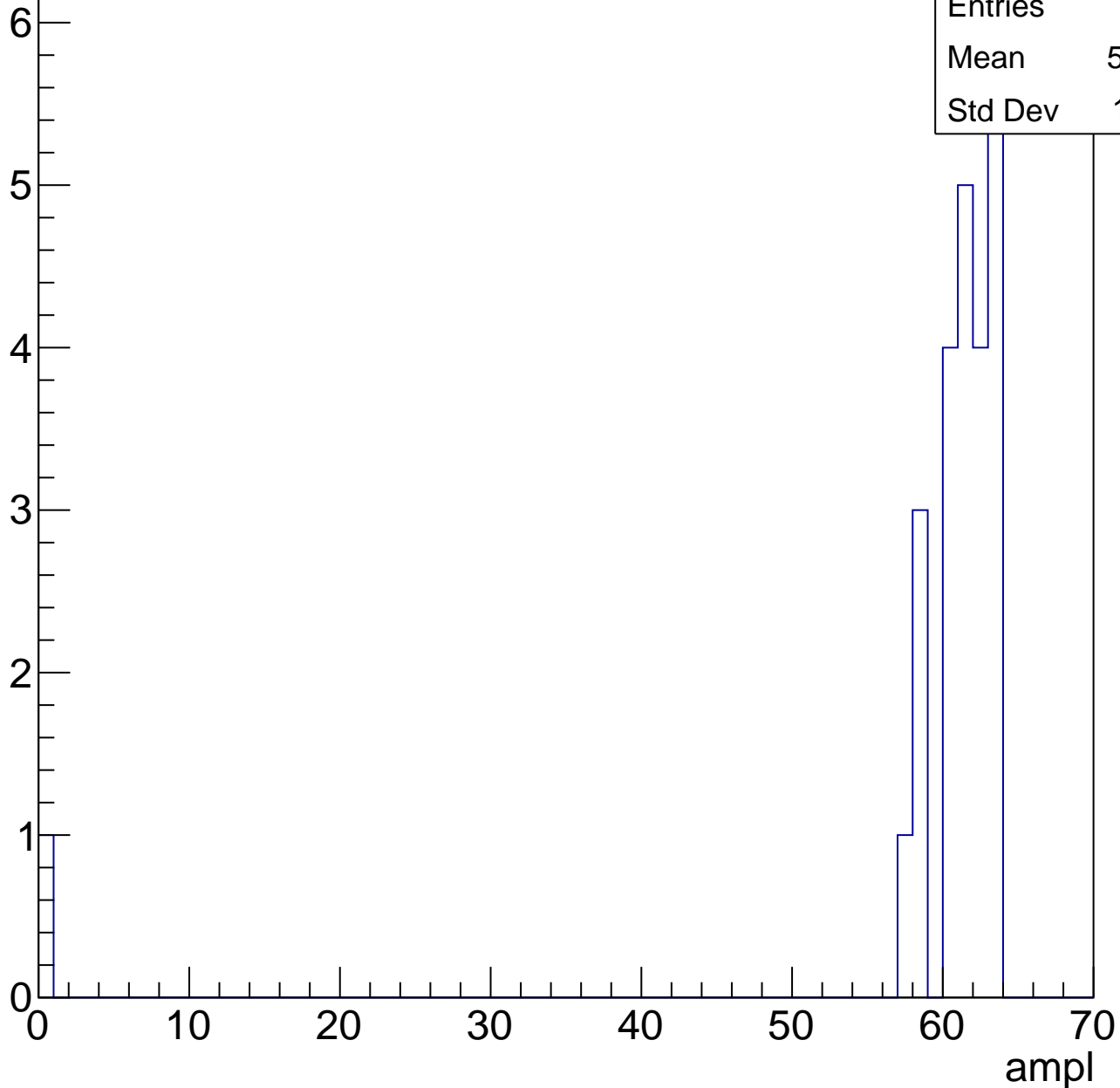


# B1L000S, U25-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.42
Std Dev	12.31

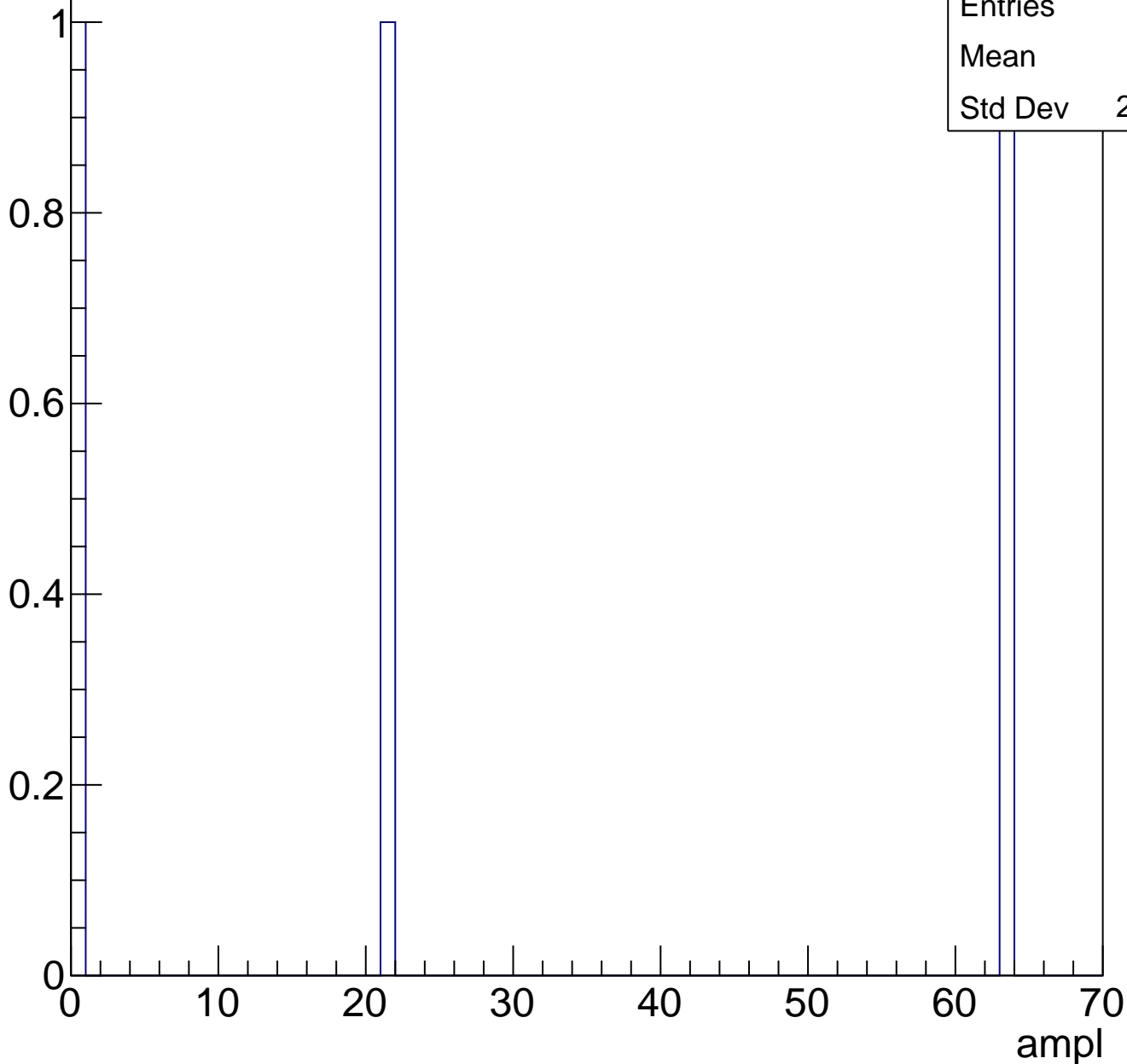




# B1L000S, U25-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L000S, U25-ch53, adc0

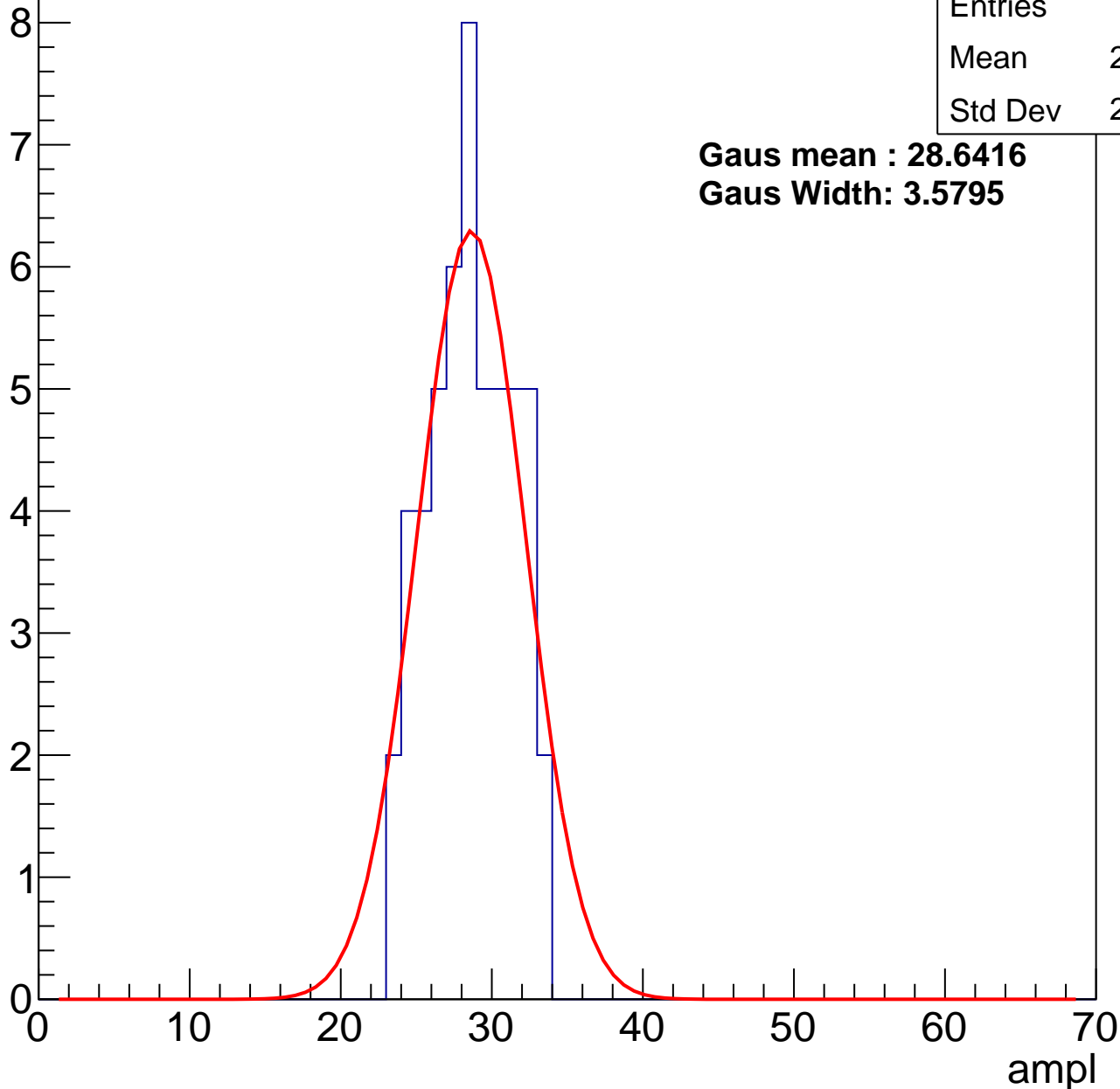
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	28.12
Std Dev	2.713

**Gaus mean : 28.6416**

**Gaus Width: 3.5795**



# B1L000S, U25-ch53, adc1

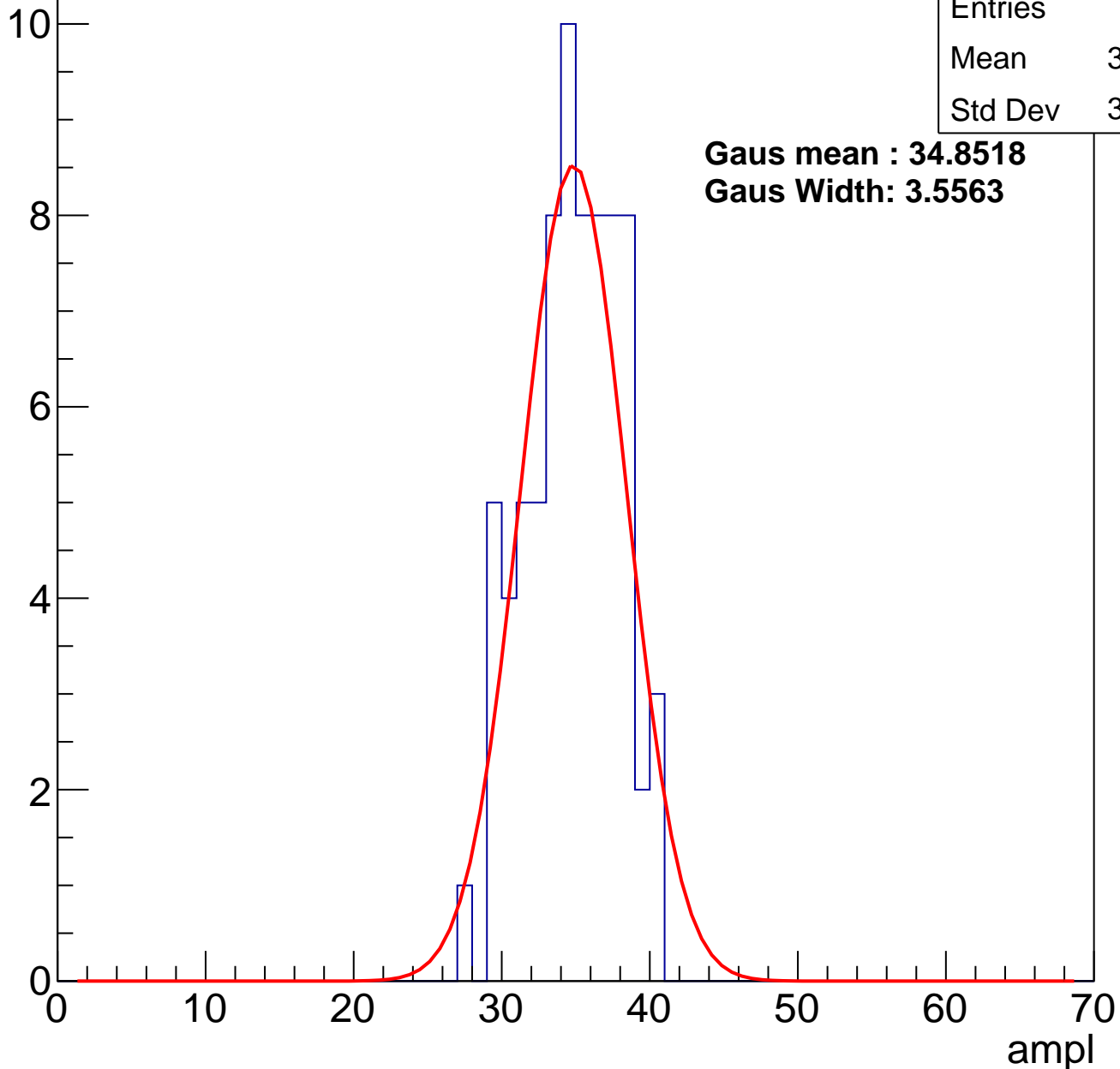
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	34.36
Std Dev	3.053

**Gaus mean : 34.8518**

**Gaus Width: 3.5563**

Entry



# B1L000S, U25-ch53, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	41.88
Std Dev	3.064

**Gaus mean : 42.5437**

**Gaus Width: 3.0813**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

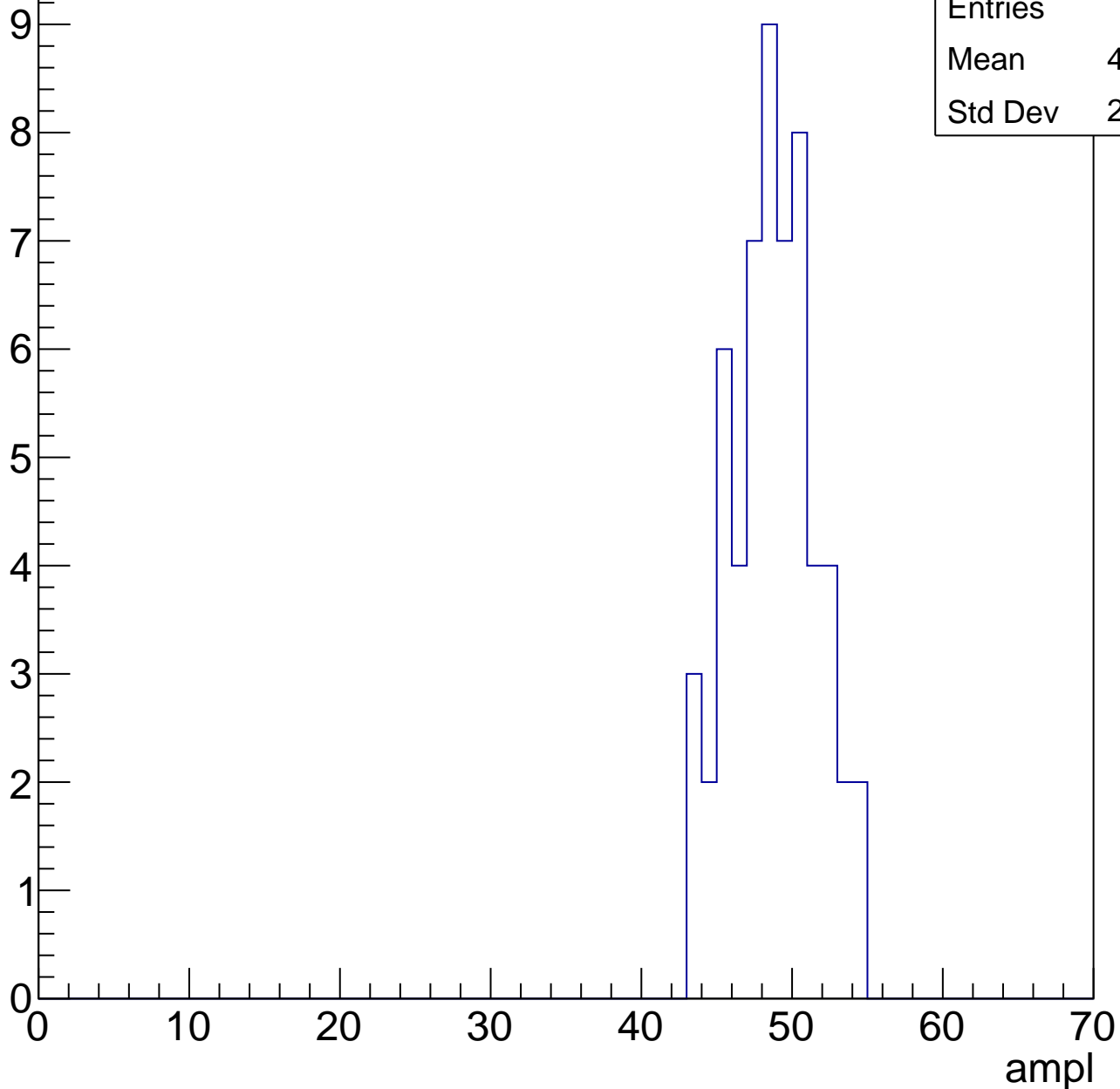
8

10

# B1L000S, U25-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

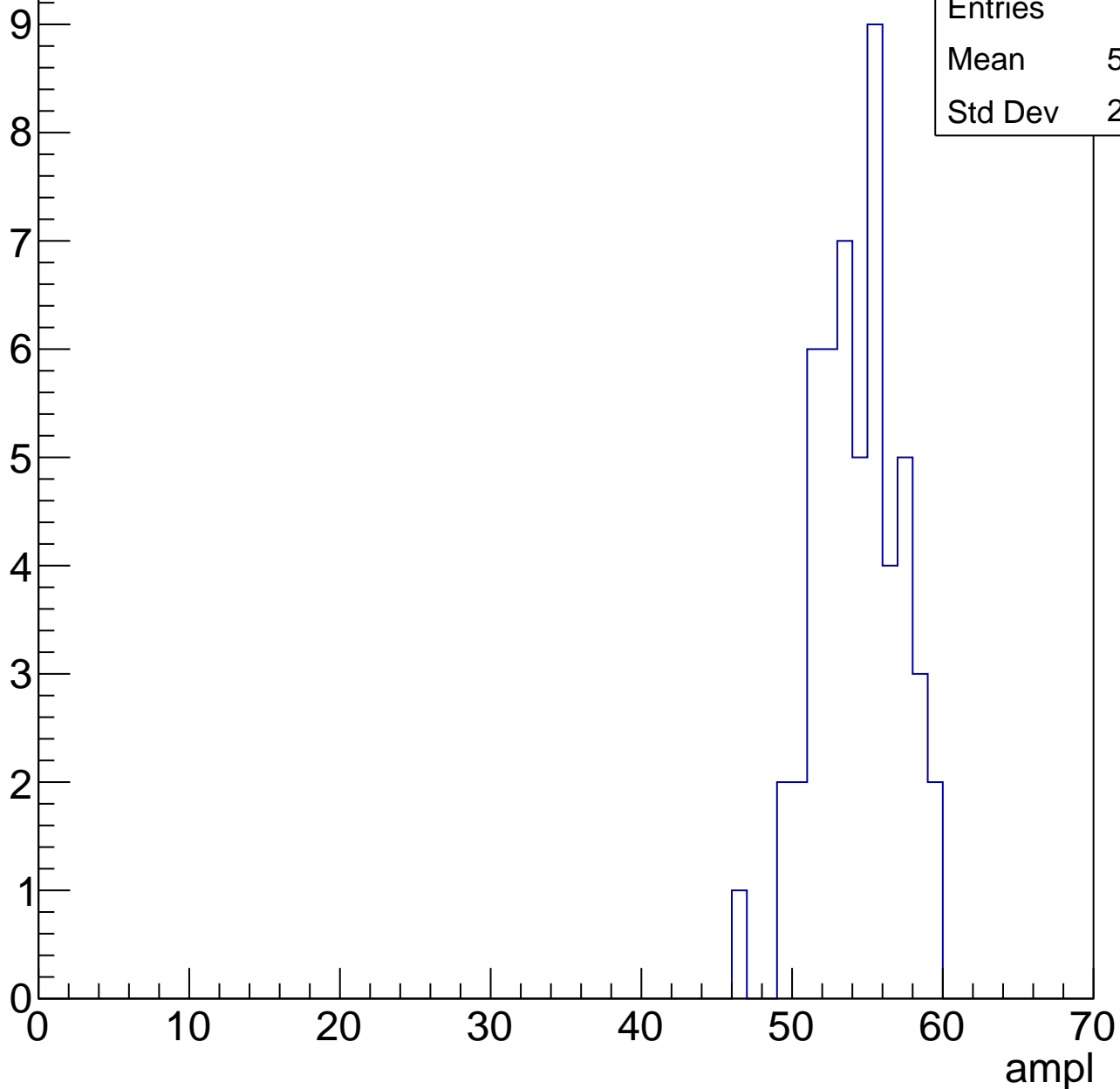


Entries	58
Mean	48.29
Std Dev	2.754

# B1L000S, U25-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



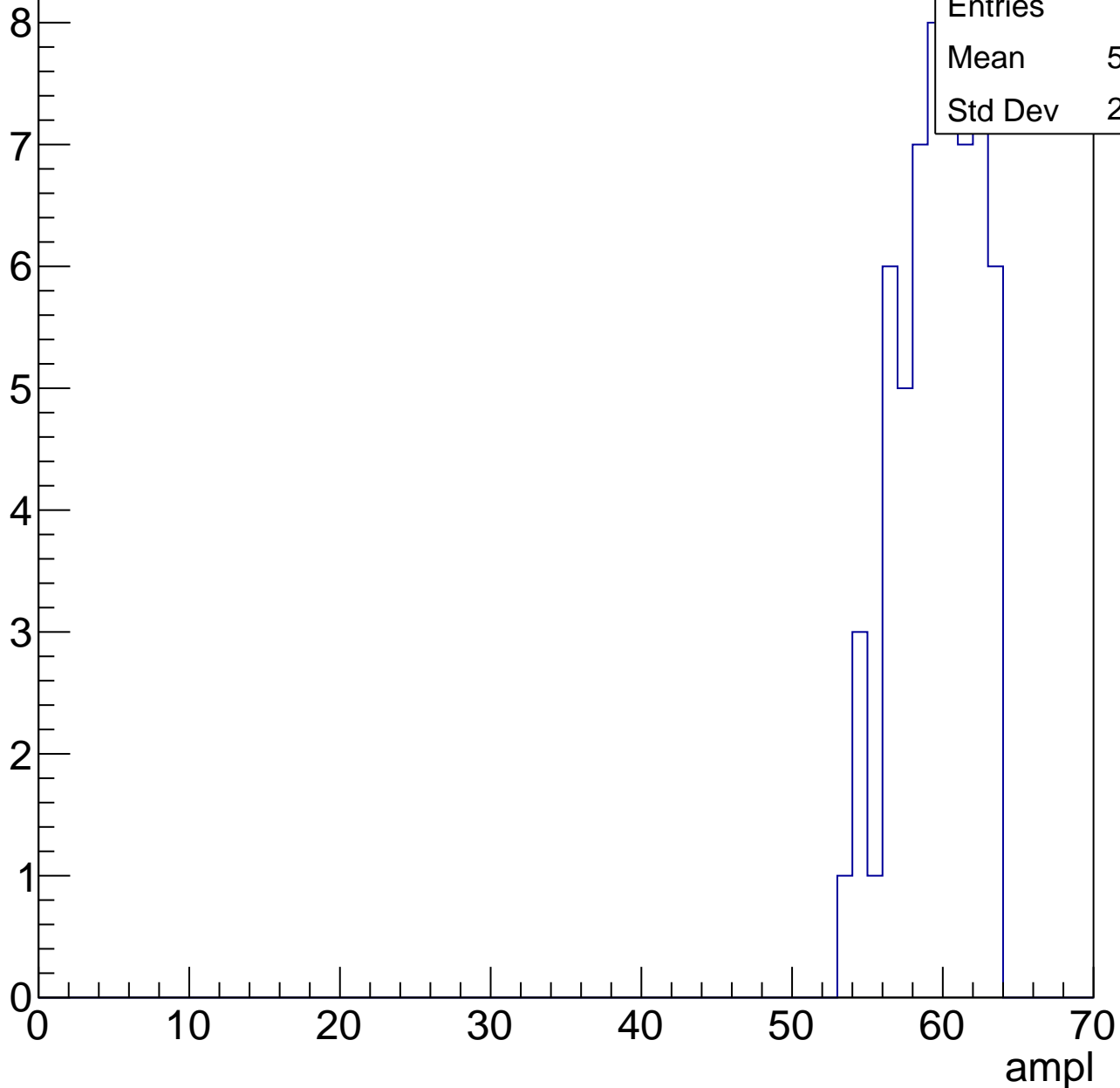
Entries	52
Mean	53.83
Std Dev	2.765

# B1L000S, U25-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

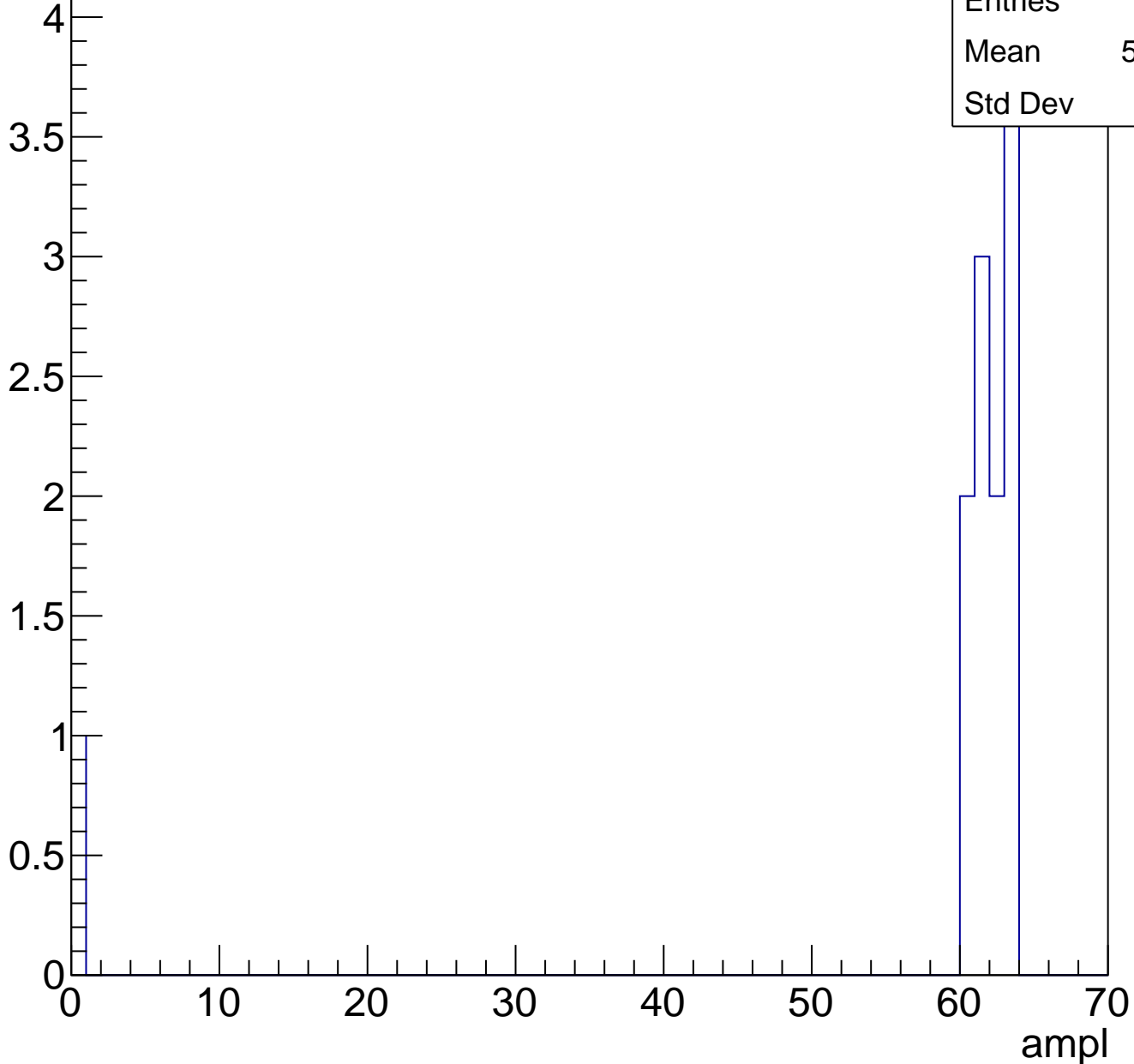
Entries	60
Mean	59.17
Std Dev	2.615



# B1L000S, U25-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch54, adc0

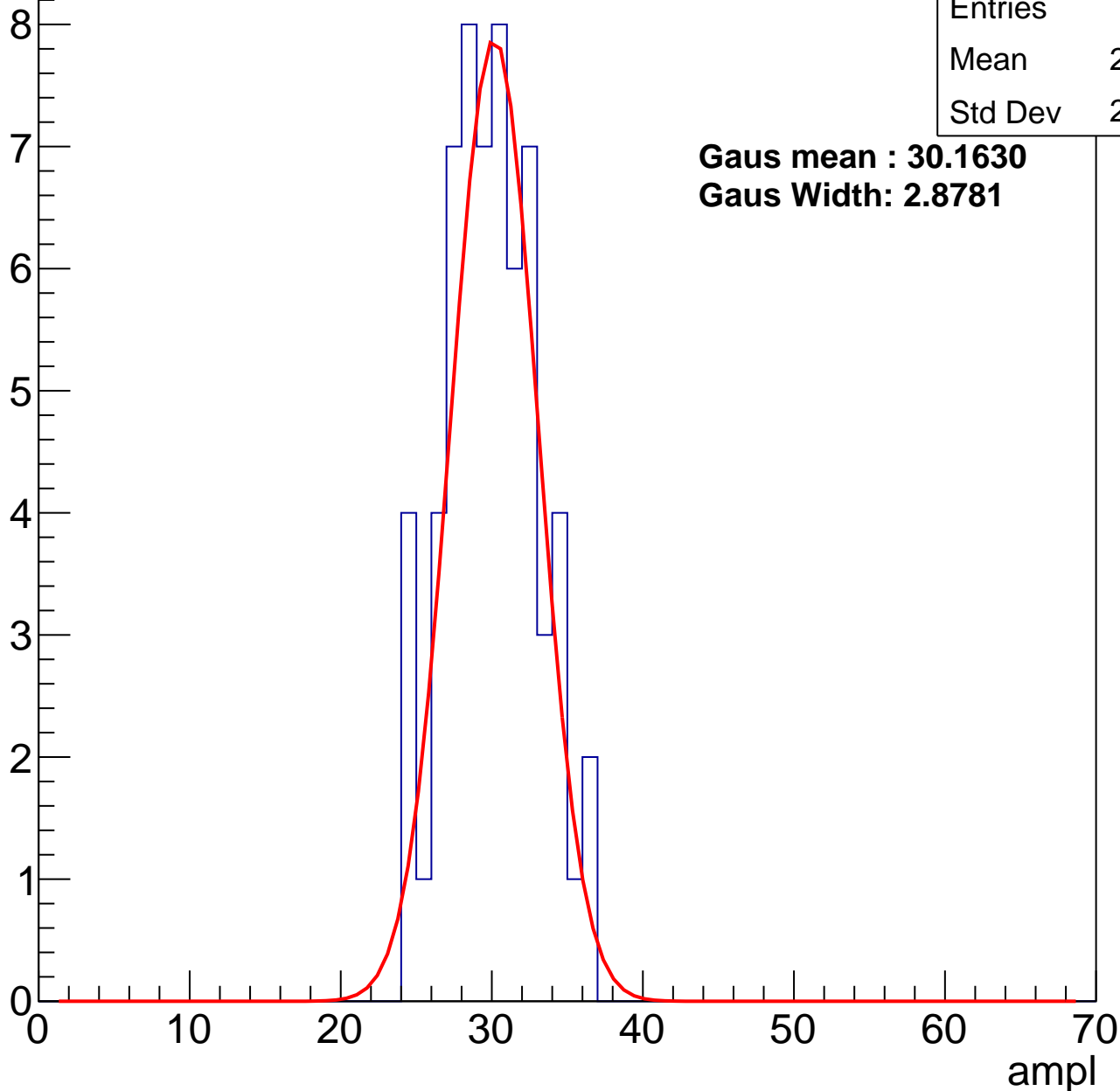
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	29.56
Std Dev	2.966

**Gaus mean : 30.1630**

**Gaus Width: 2.8781**



# B1L000S, U25-ch54, adc1

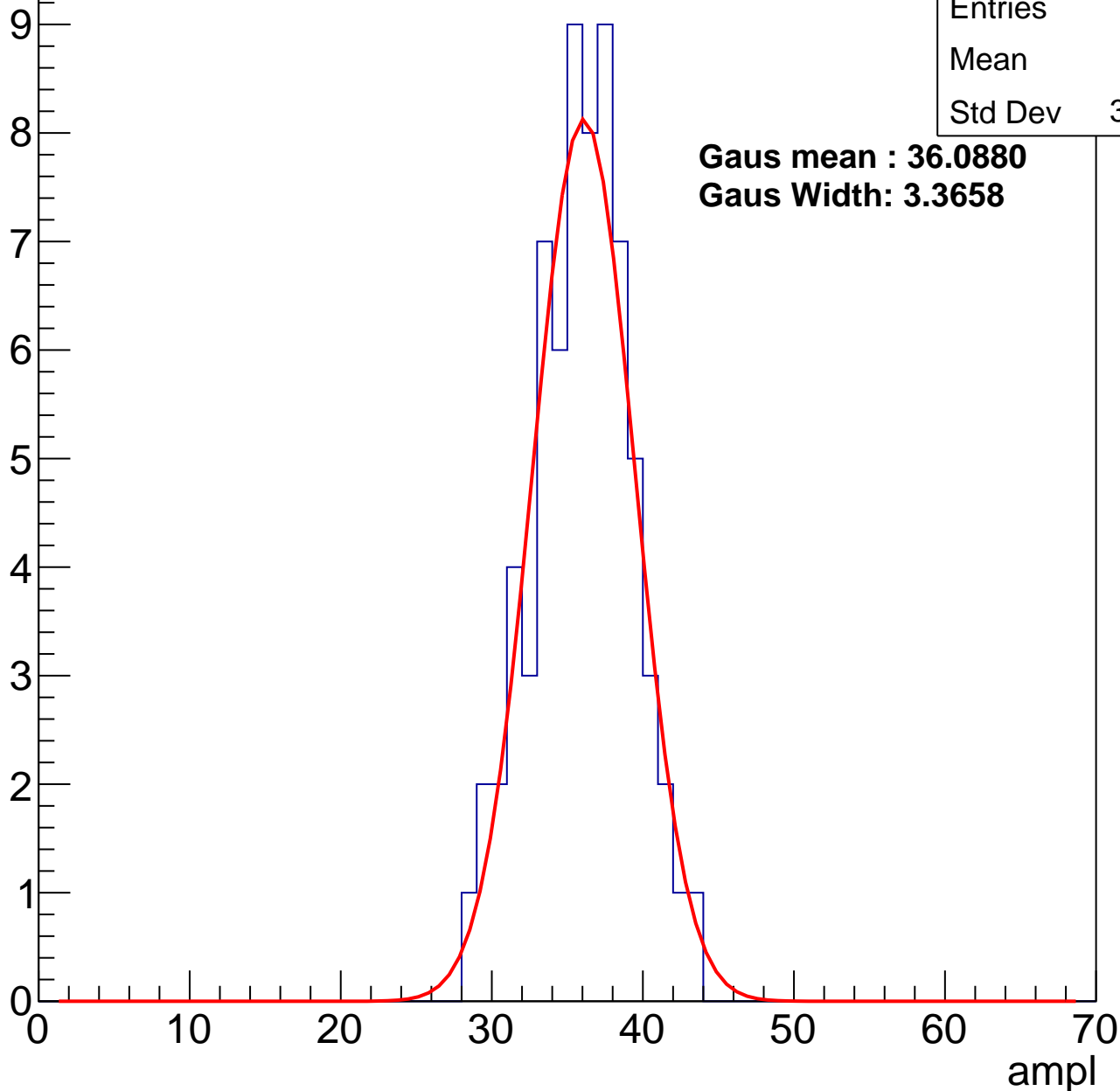
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	35.5
Std Dev	3.219

**Gaus mean : 36.0880**

**Gaus Width: 3.3658**



# B1L000S, U25-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

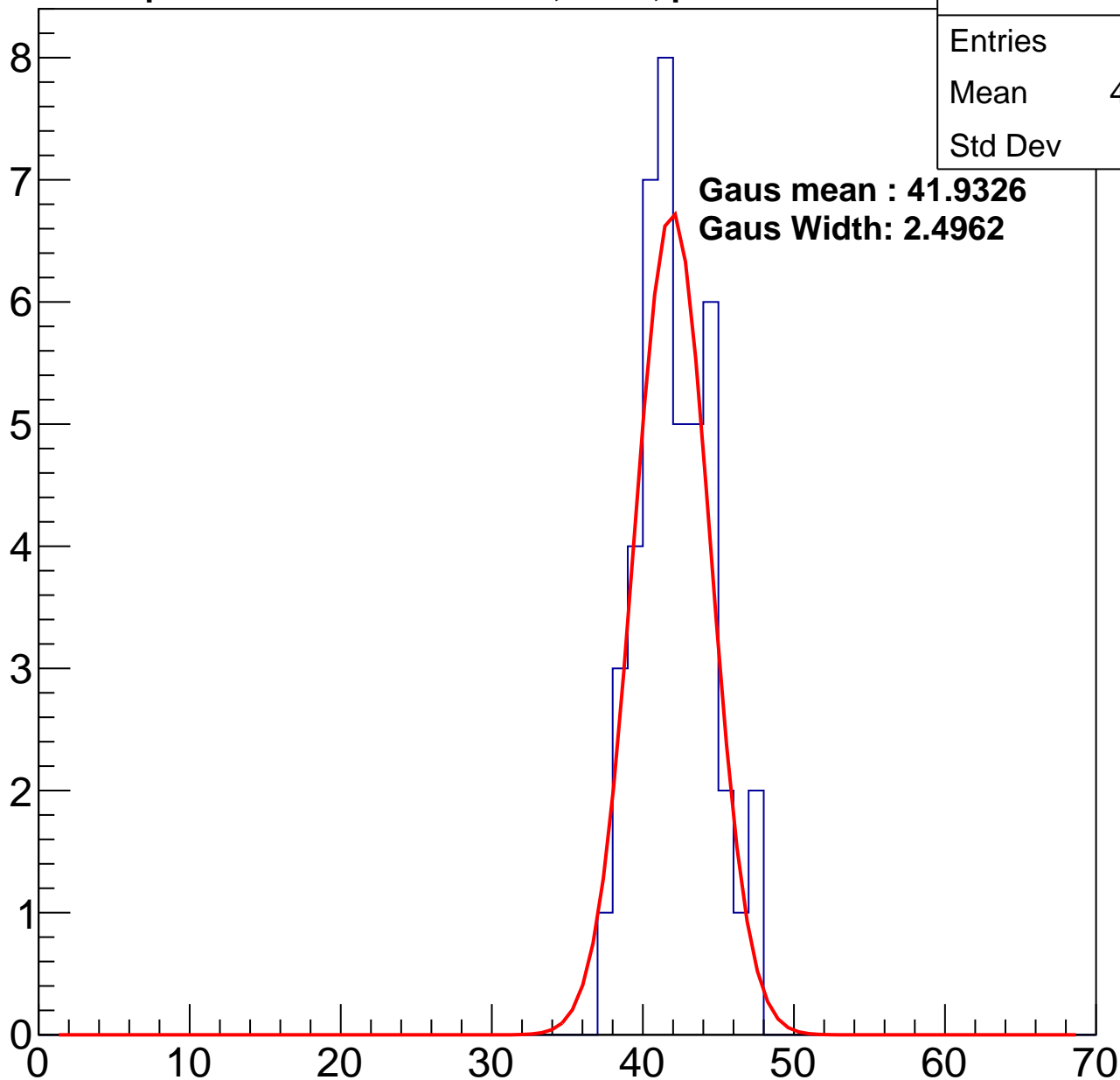
Entries	44
Mean	41.68
Std Dev	2.4

**Gaus mean : 41.9326**

**Gaus Width: 2.4962**

0 10 20 30 40 50 60 70

ampl

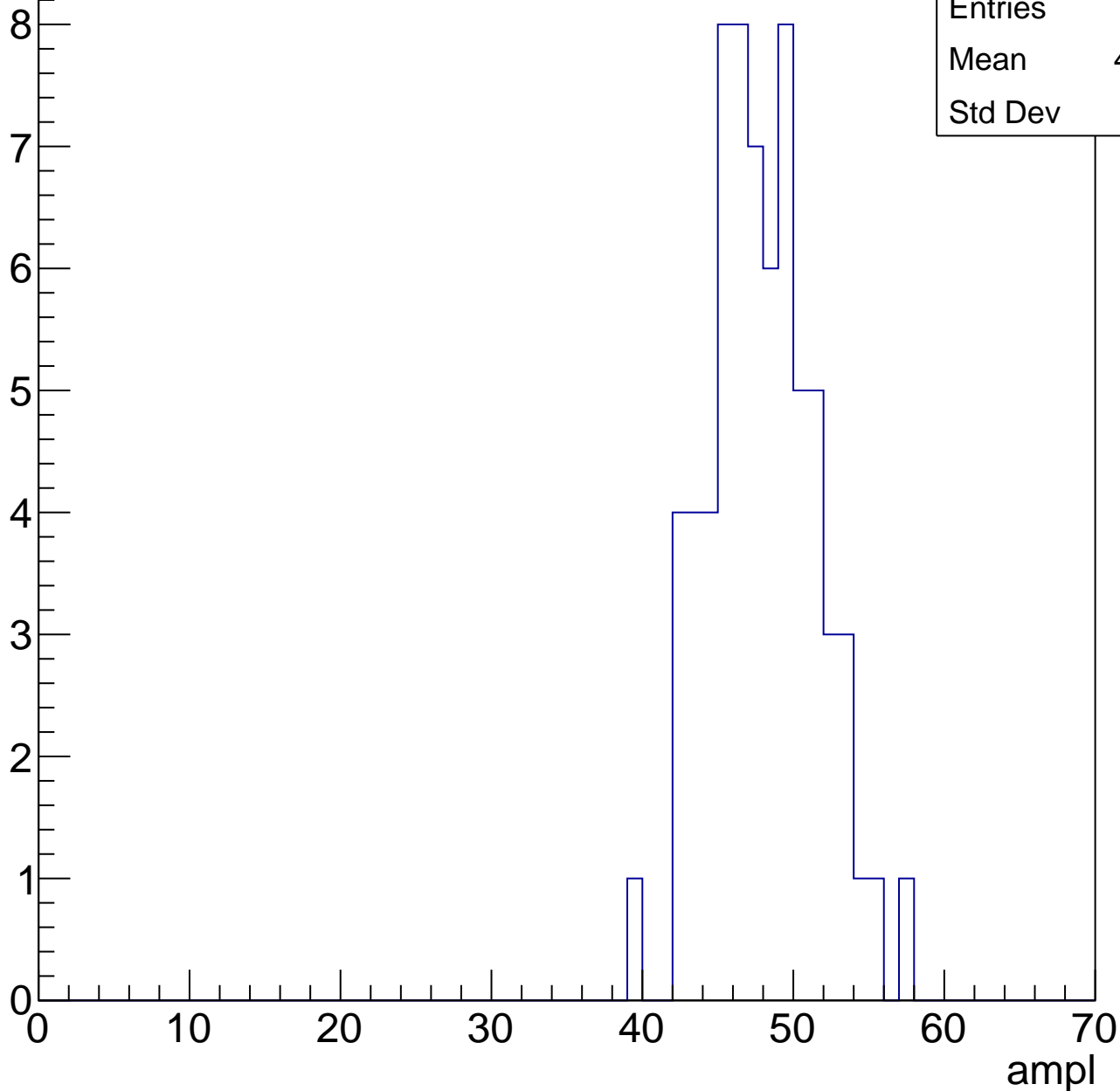


# B1L000S, U25-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

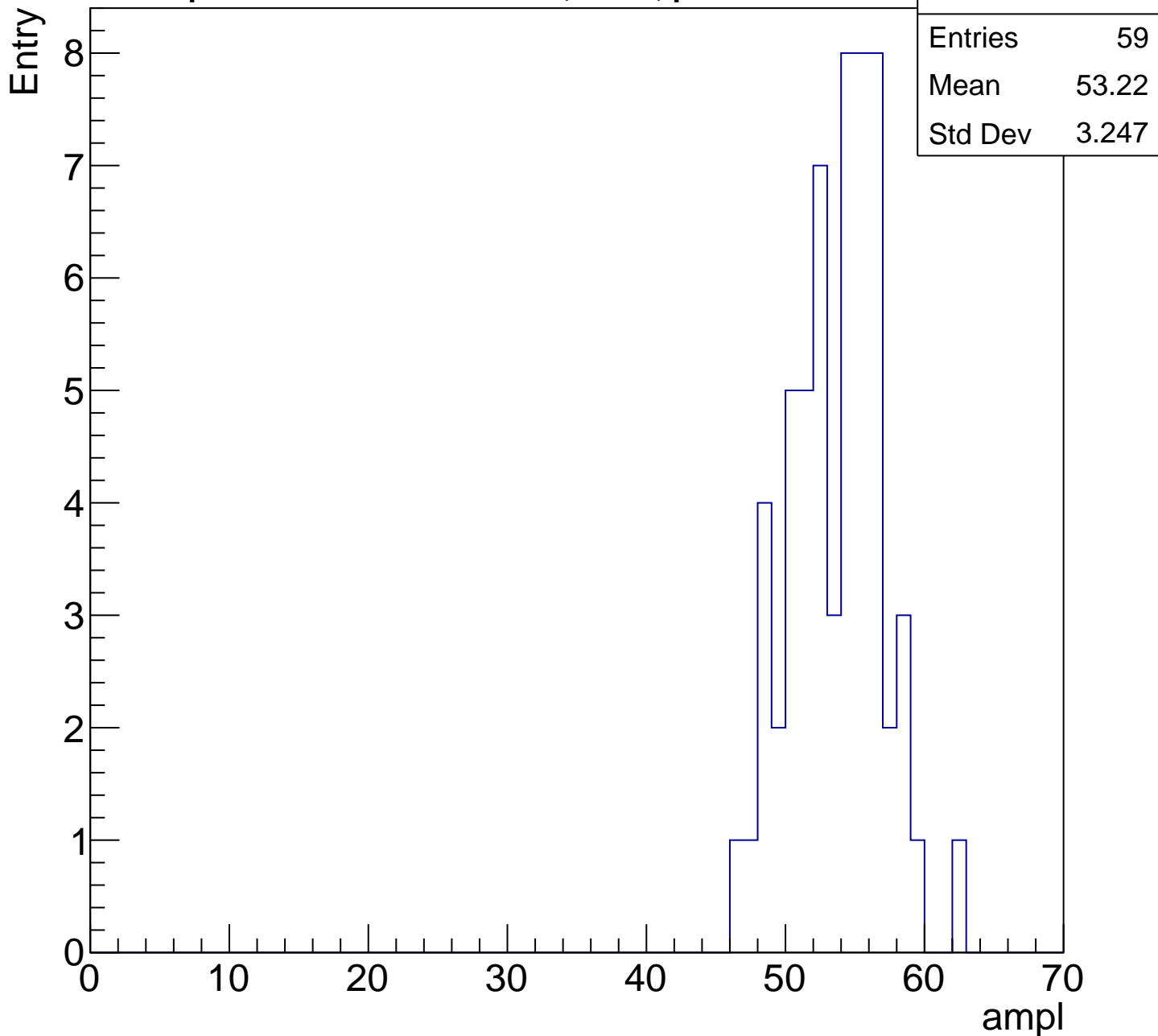
Entry

Entries	69
Mean	47.51
Std Dev	3.5



# B1L000S, U25-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

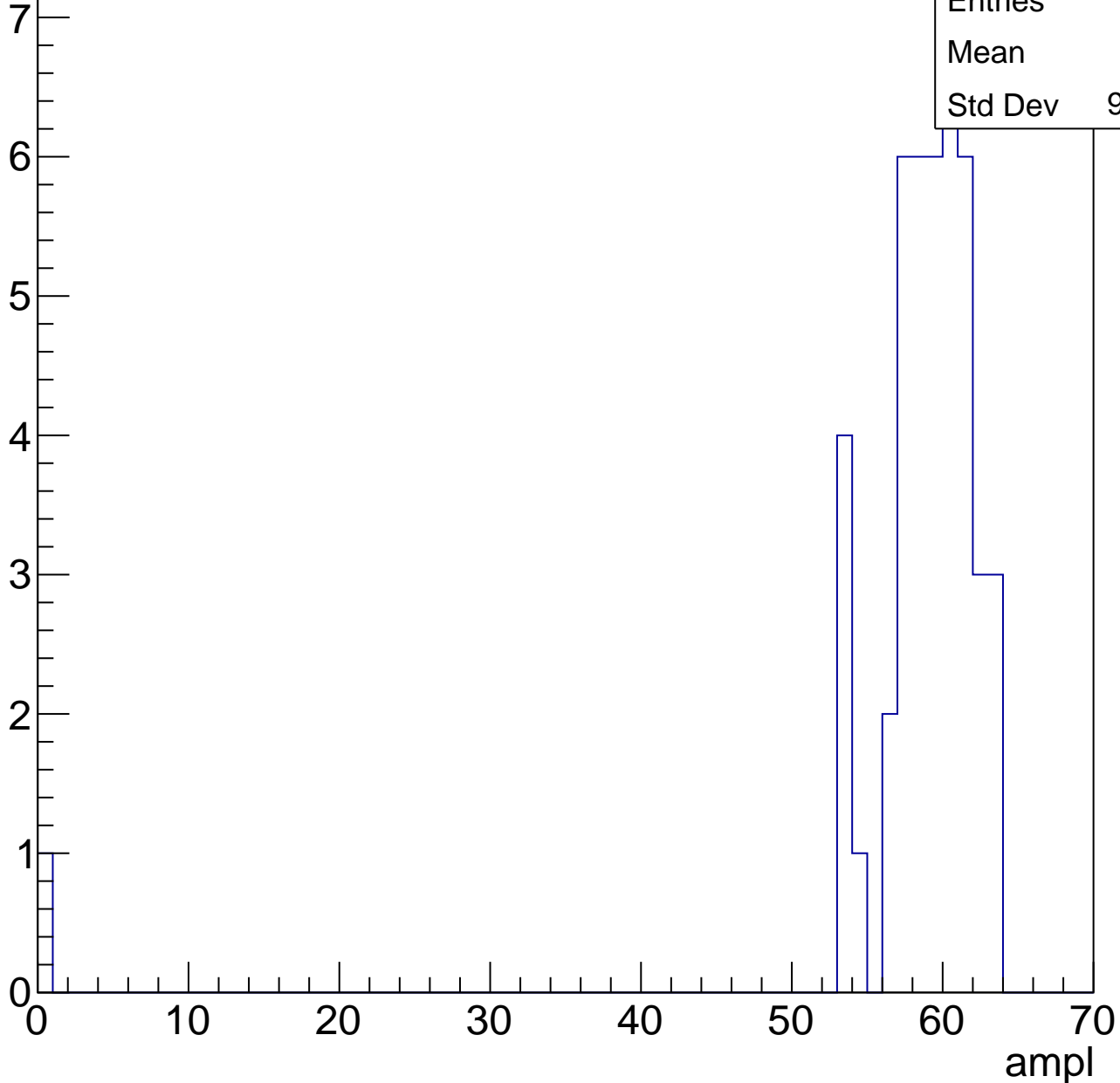


# B1L000S, U25-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	57.4
Std Dev	9.054

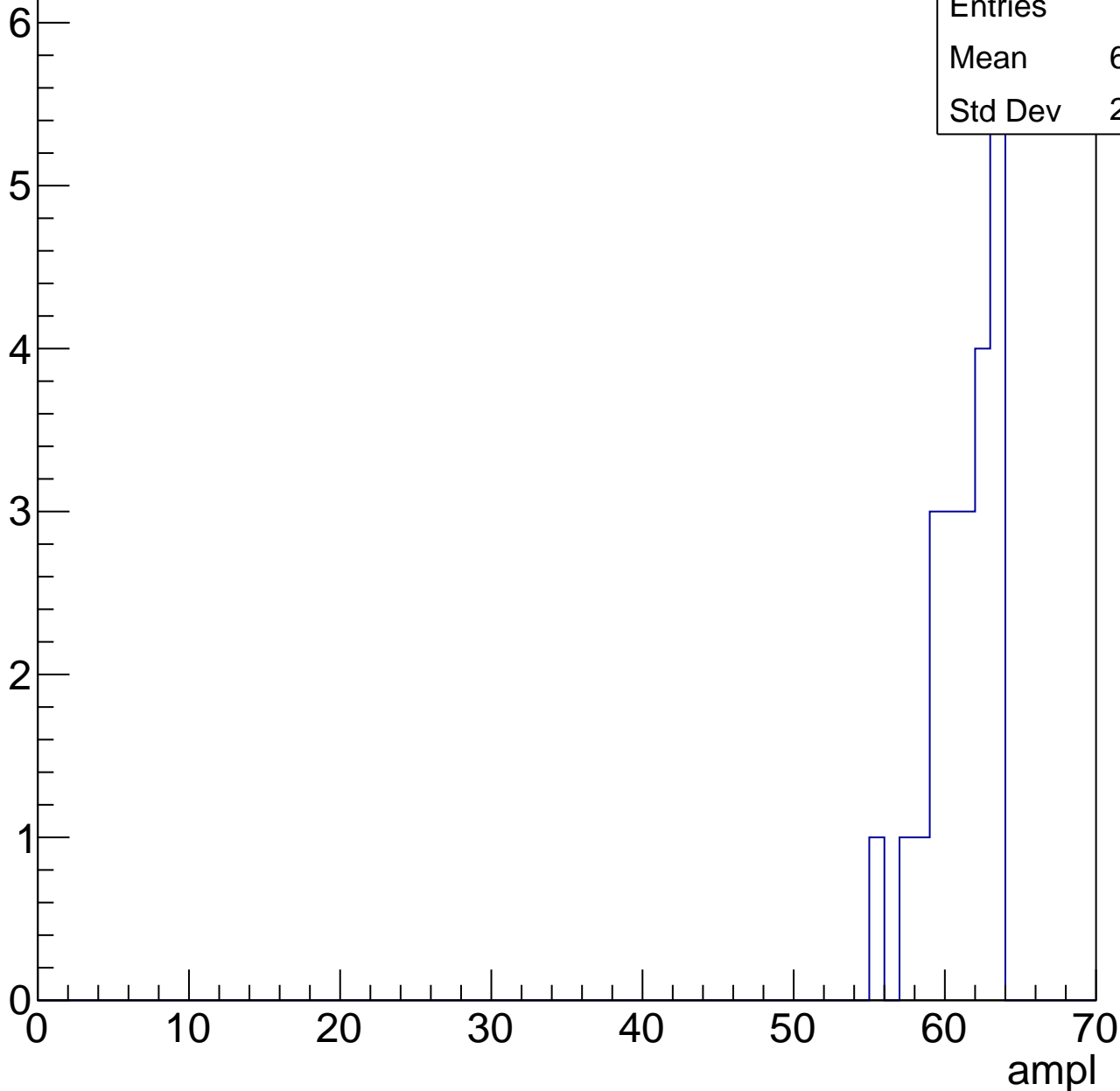


# B1L000S, U25-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	60.73
Std Dev	2.157

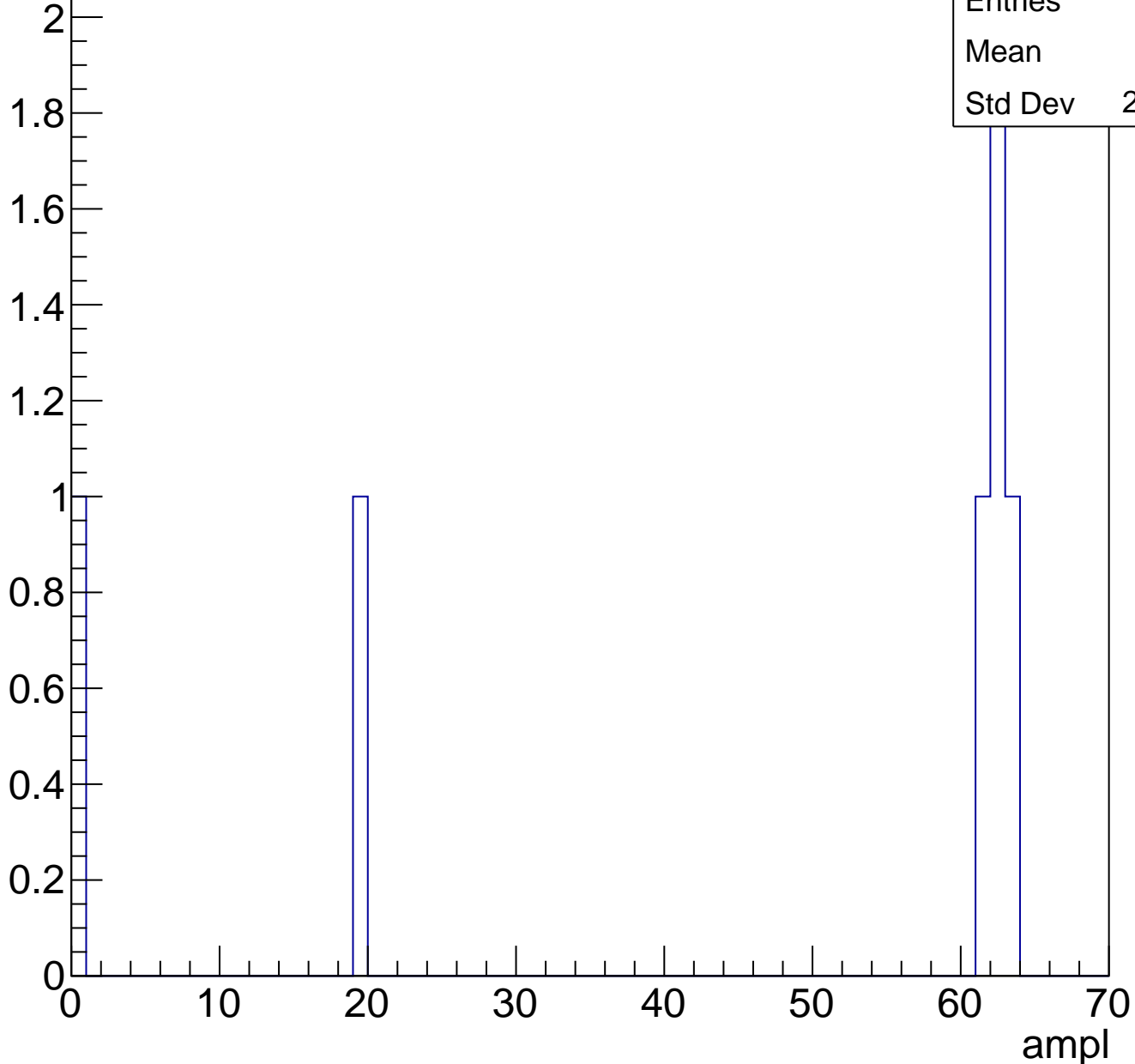




# B1L000S, U25-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	6
Mean	44.5
Std Dev	25.36

# B1L000S, U25-ch55, adc0

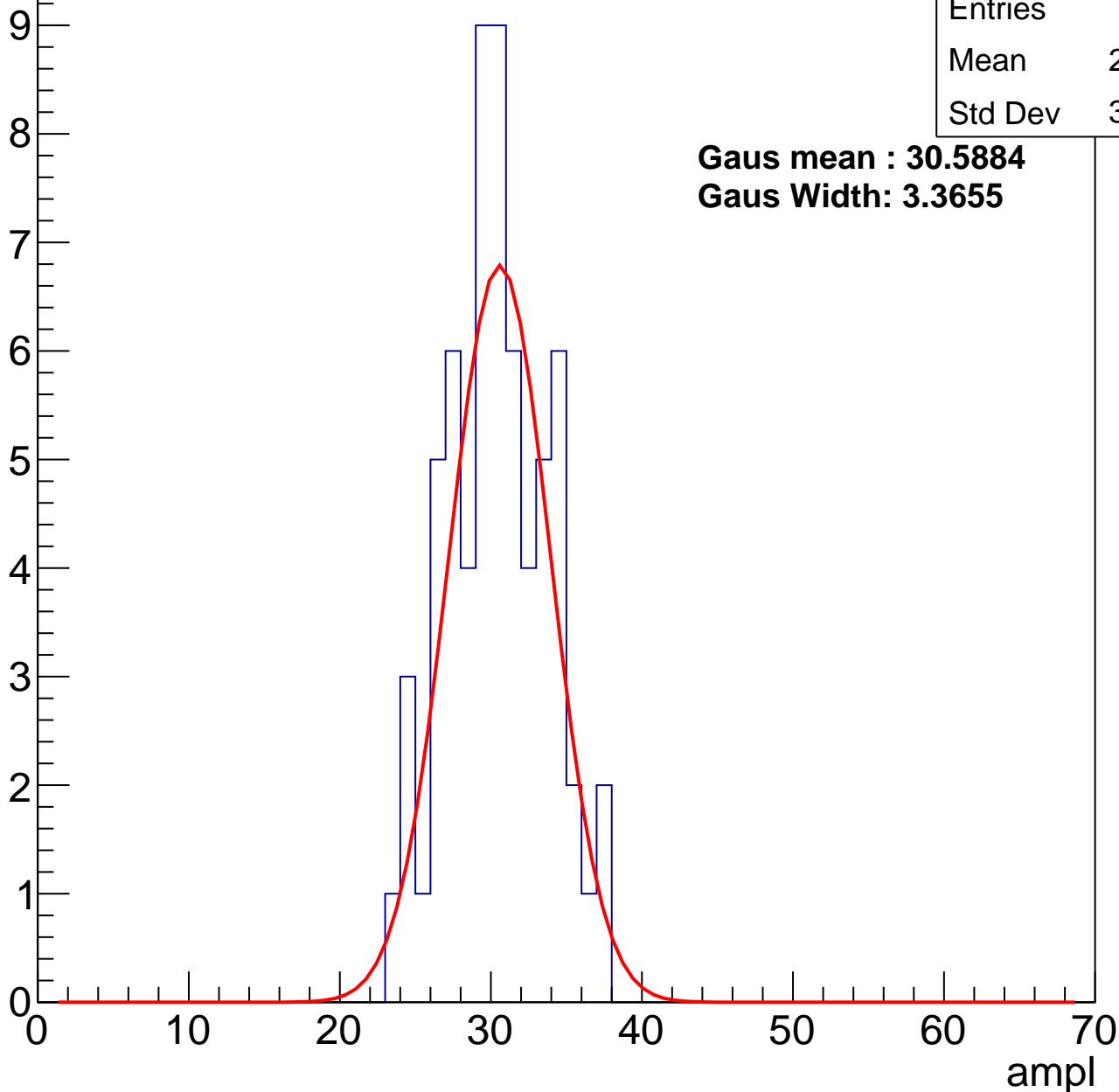
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.97
Std Dev	3.279

**Gaus mean : 30.5884**

**Gaus Width: 3.3655**



# B1L000S, U25-ch55, adc1

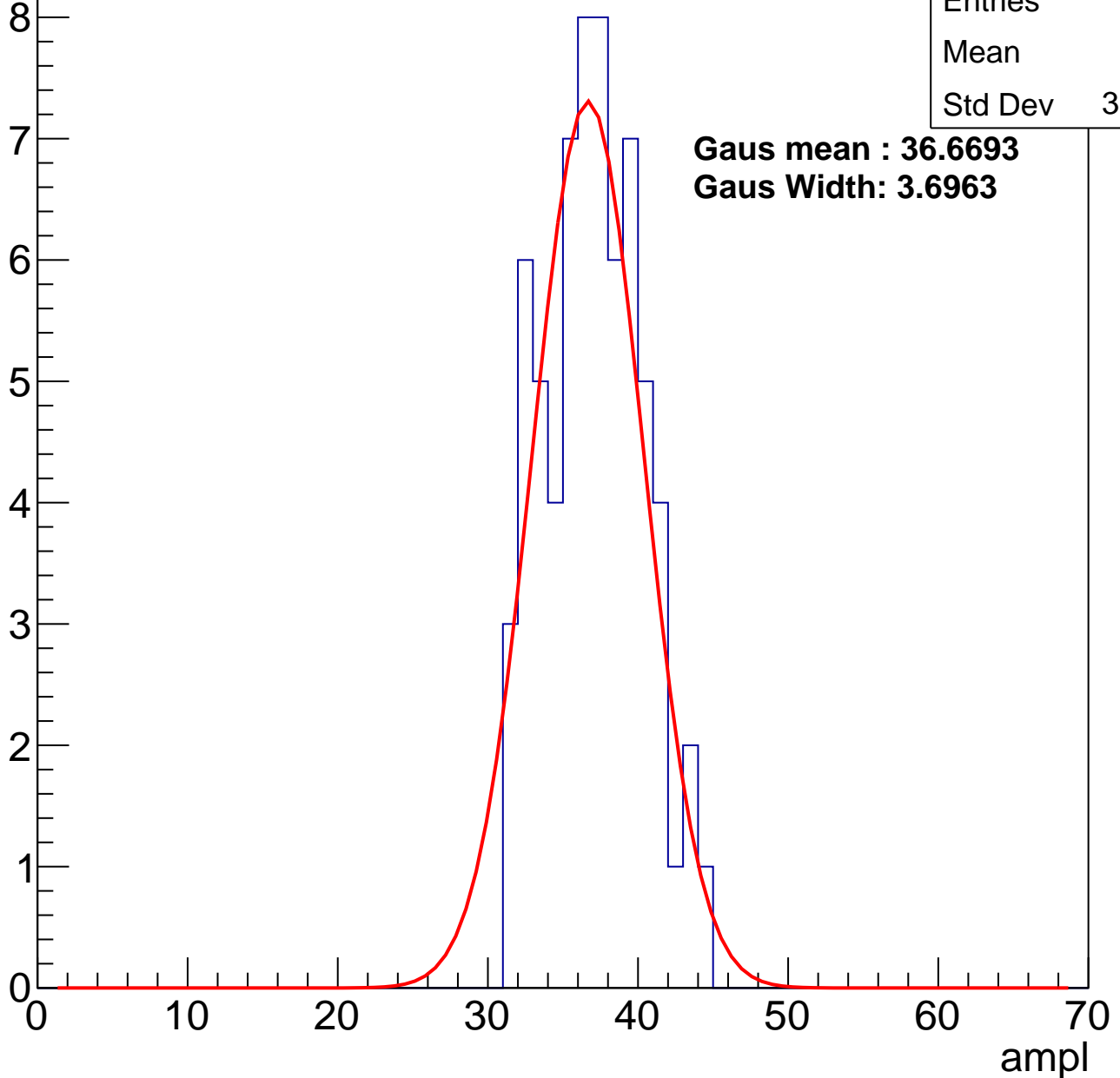
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.6
Std Dev	3.205

**Gaus mean : 36.6693**

**Gaus Width: 3.6963**



# B1L000S, U25-ch55, adc2

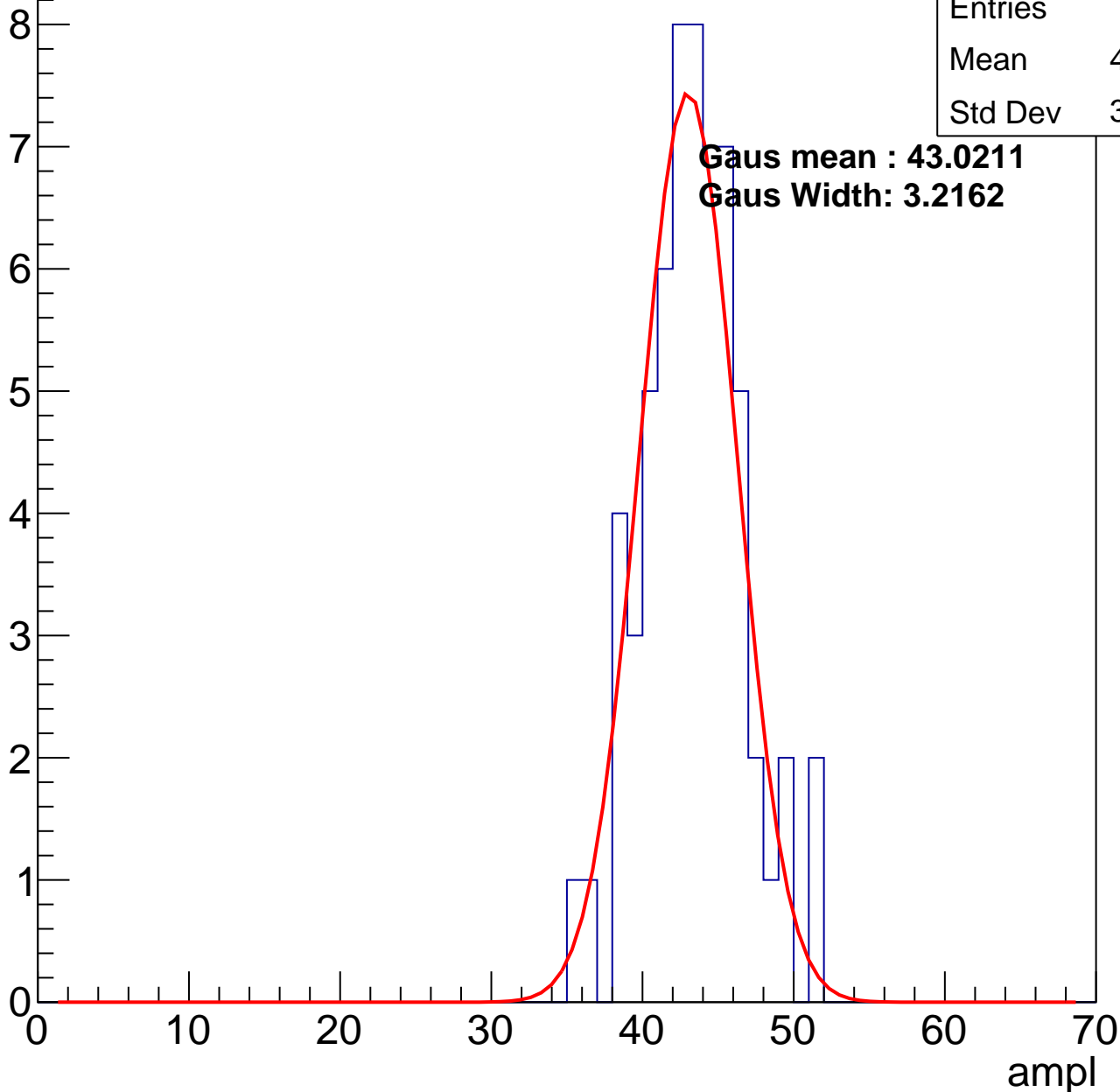
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	42.92
Std Dev	3.299

**Gaus mean : 43.0211**

**Gaus Width: 3.2162**

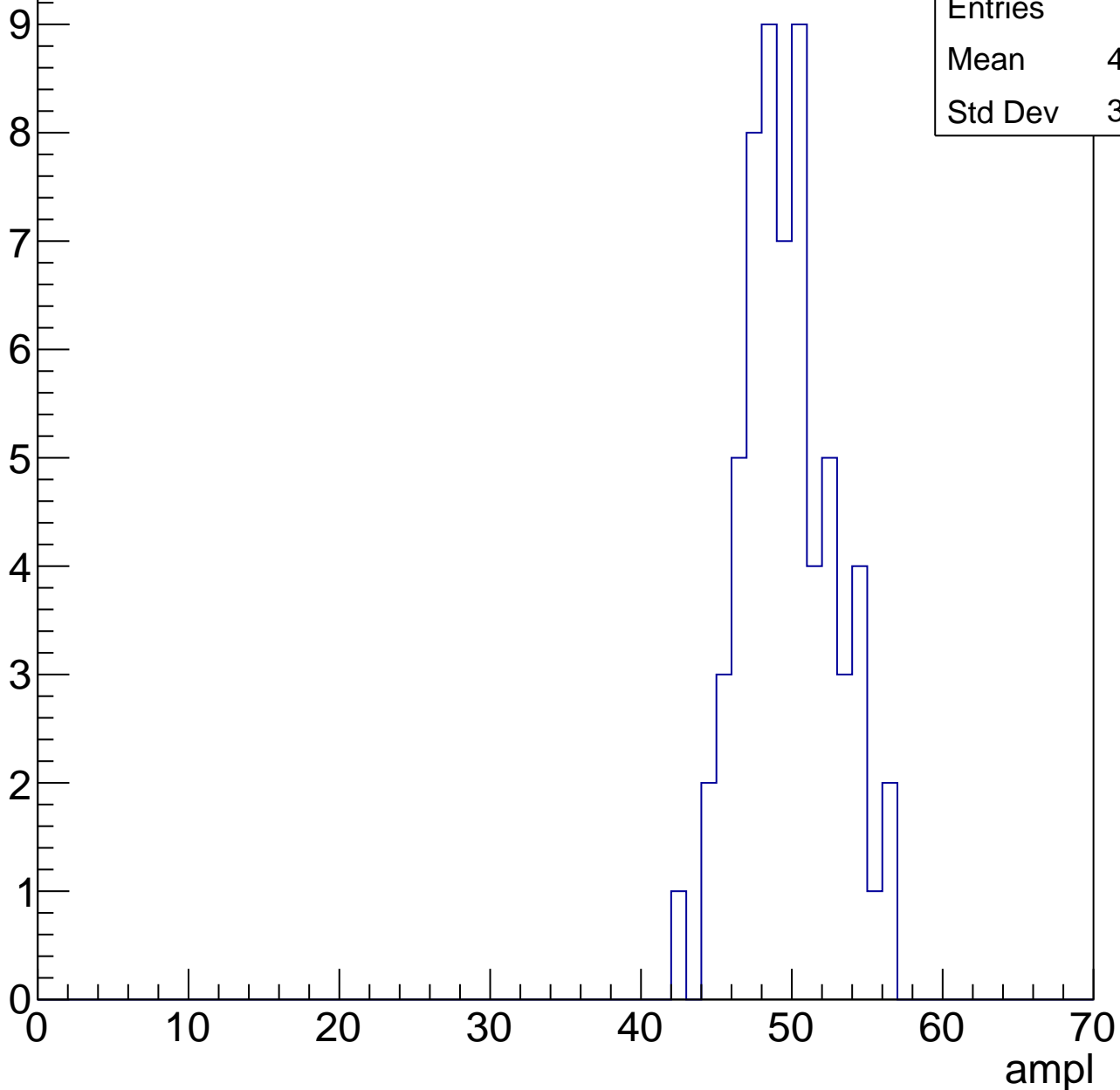


# B1L000S, U25-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

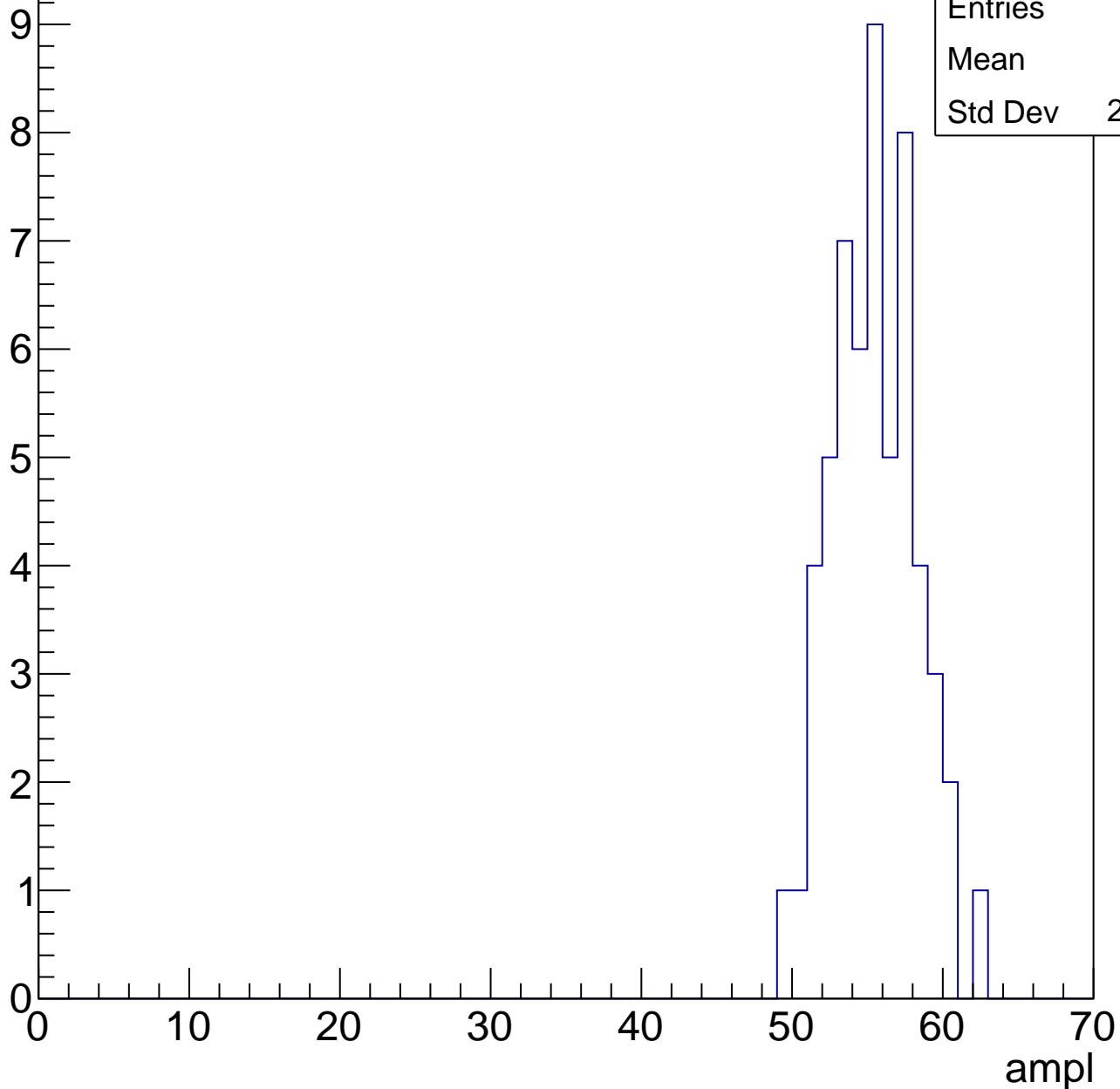
Entries	63
Mean	49.24
Std Dev	3.038



# B1L000S, U25-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	60.16
Std Dev	2.033

ampl

0 10 20 30 40 50 60 70

3

4

5

6

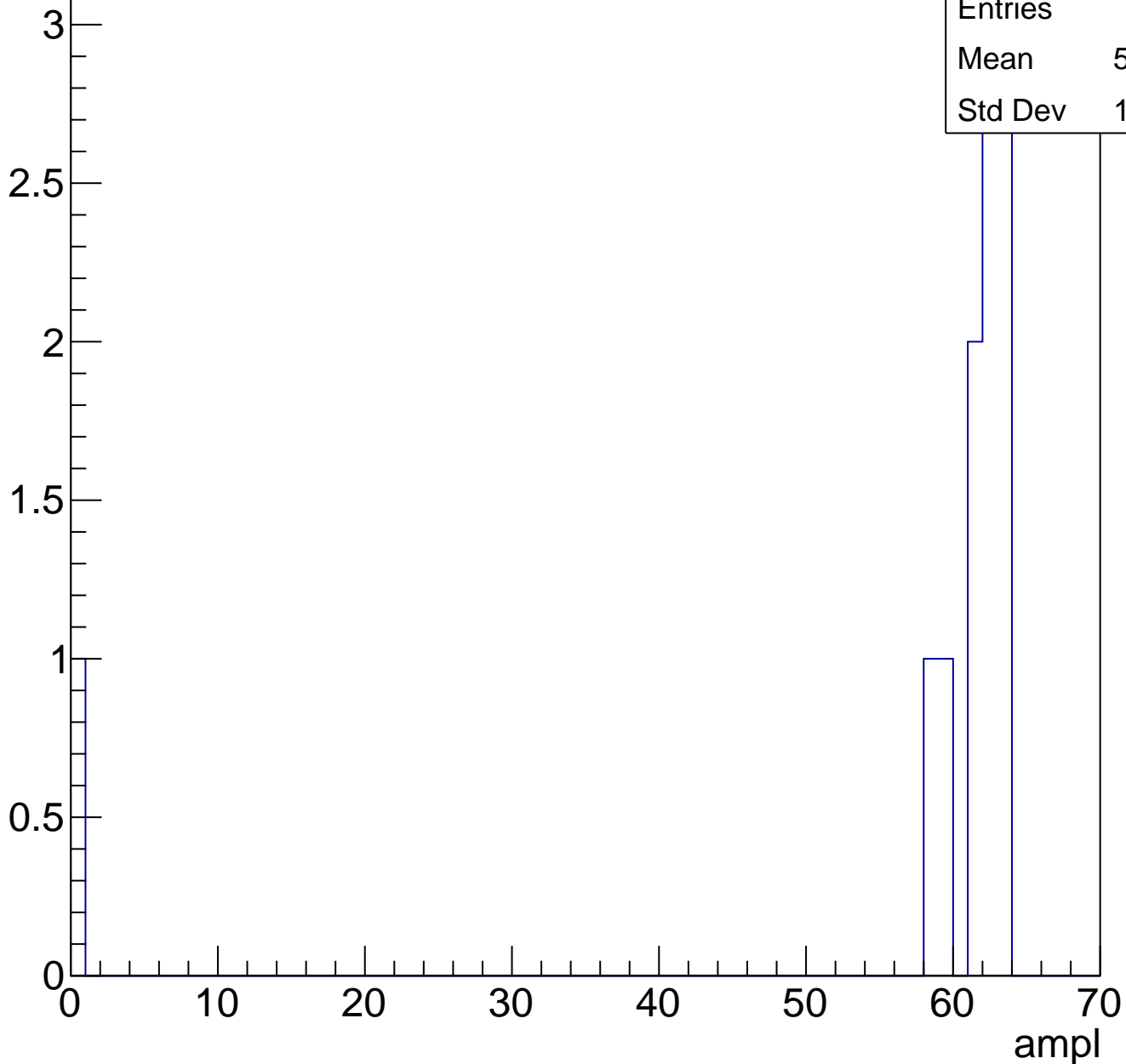
7

8

# B1L000S, U25-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch56, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	28.1
Std Dev	2.967

**Gaus mean : 28.5932**

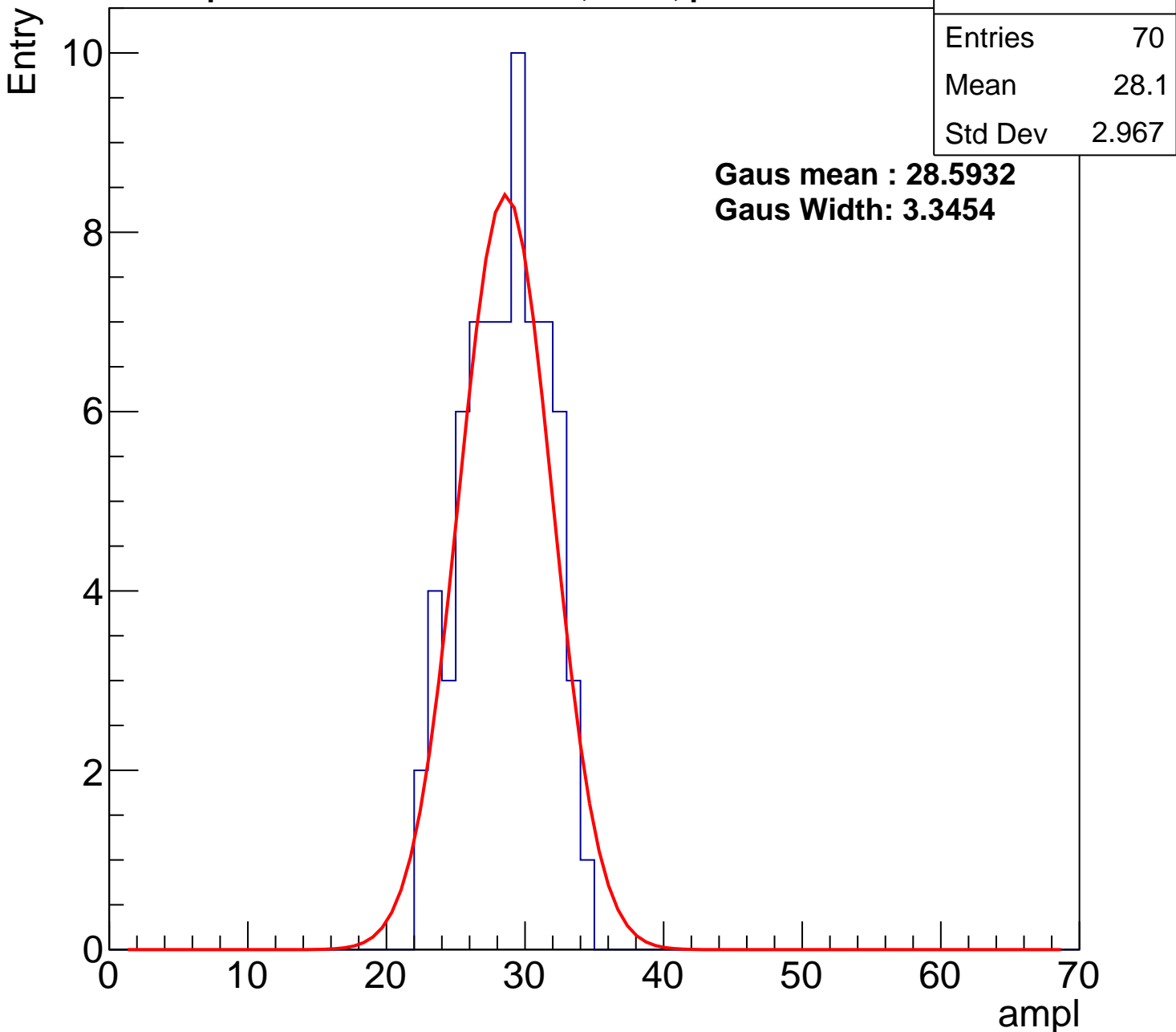
**Gaus Width: 3.3454**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



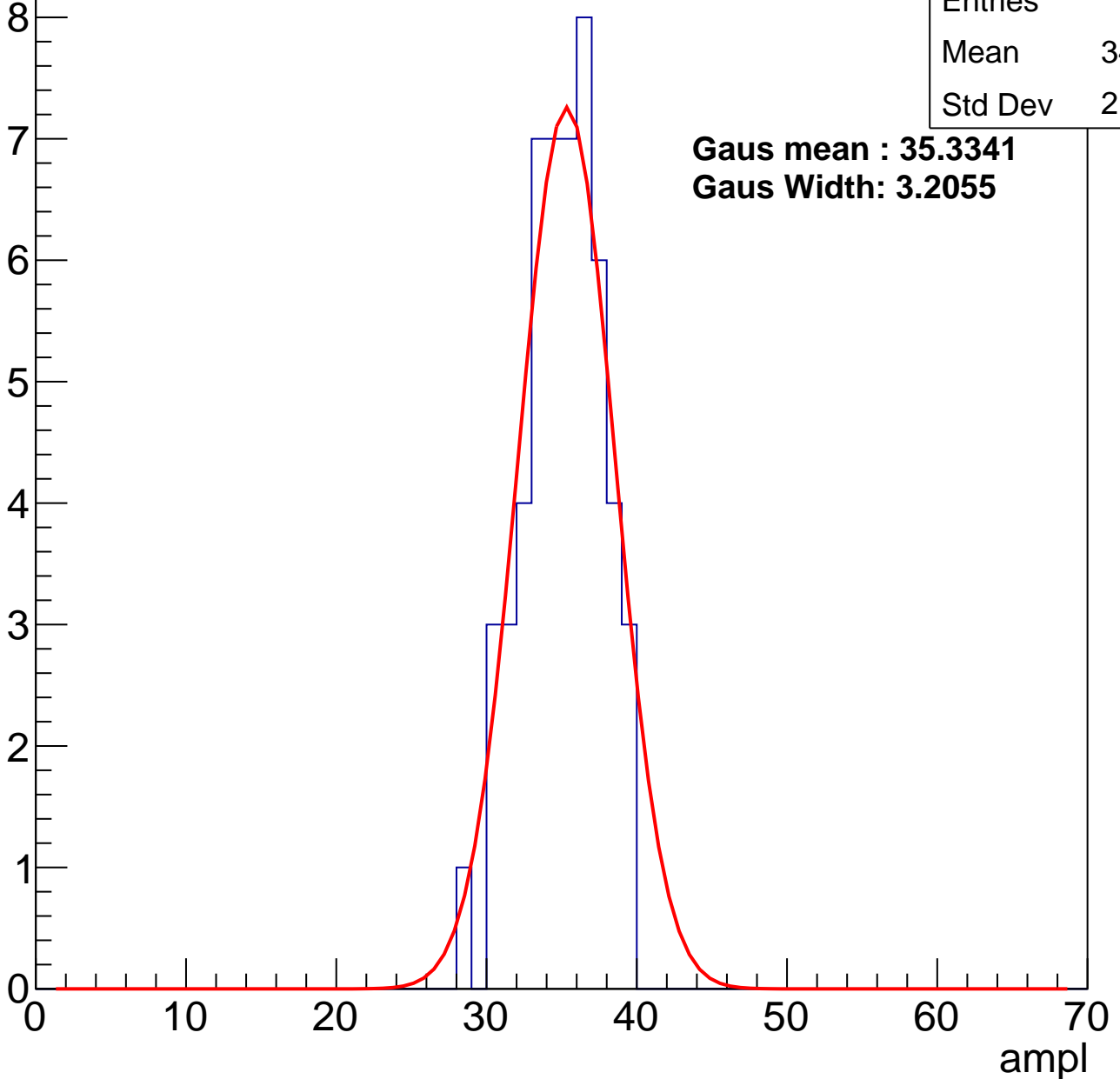
# B1L000S, U25-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	34.57
Std Dev	2.566

**Gaus mean : 35.3341**  
**Gaus Width: 3.2055**



# B1L000S, U25-ch56, adc2

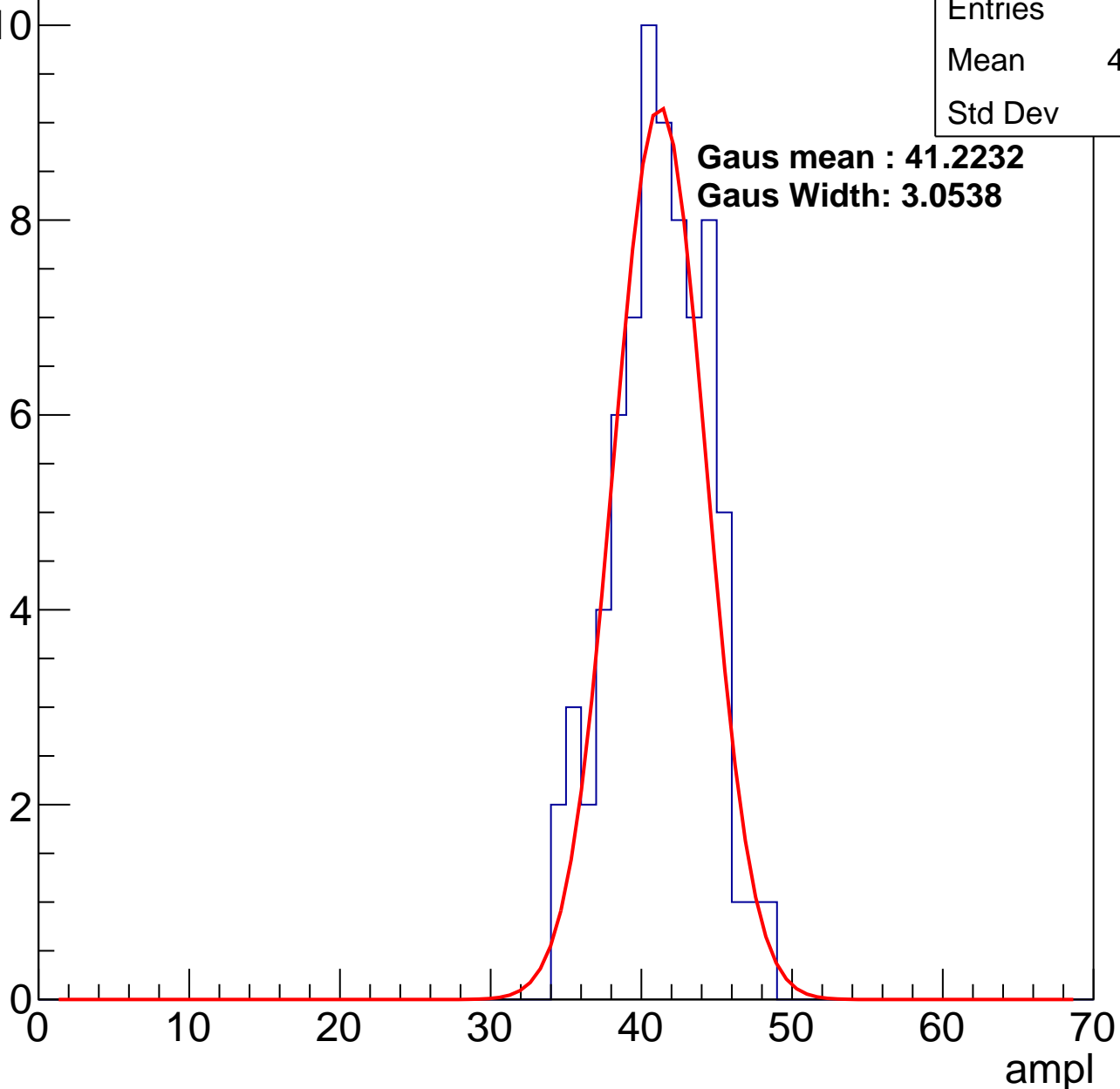
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	40.78
Std Dev	3.09

**Gaus mean : 41.2232**

**Gaus Width: 3.0538**

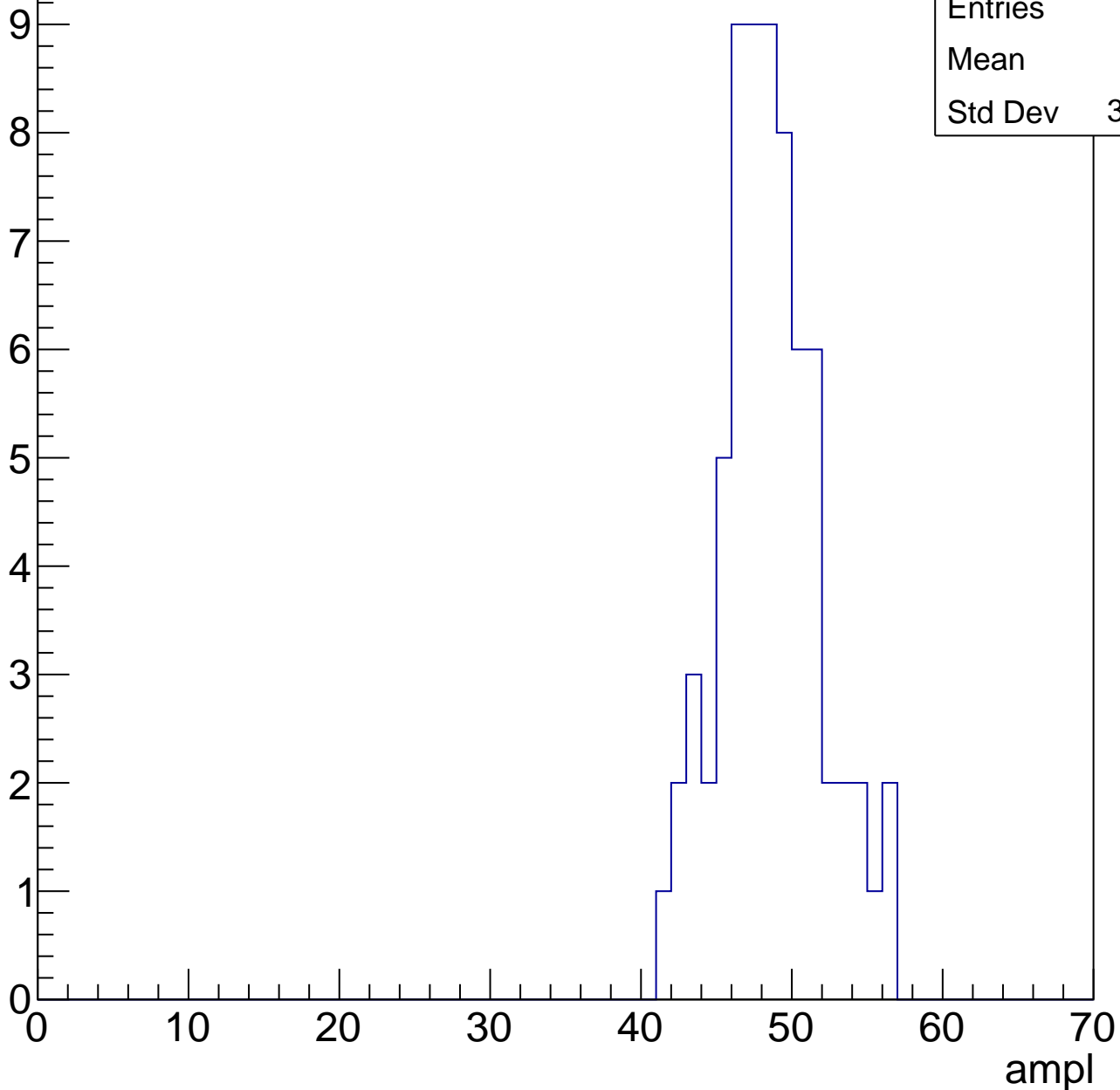


# B1L000S, U25-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.1
Std Dev	3.262

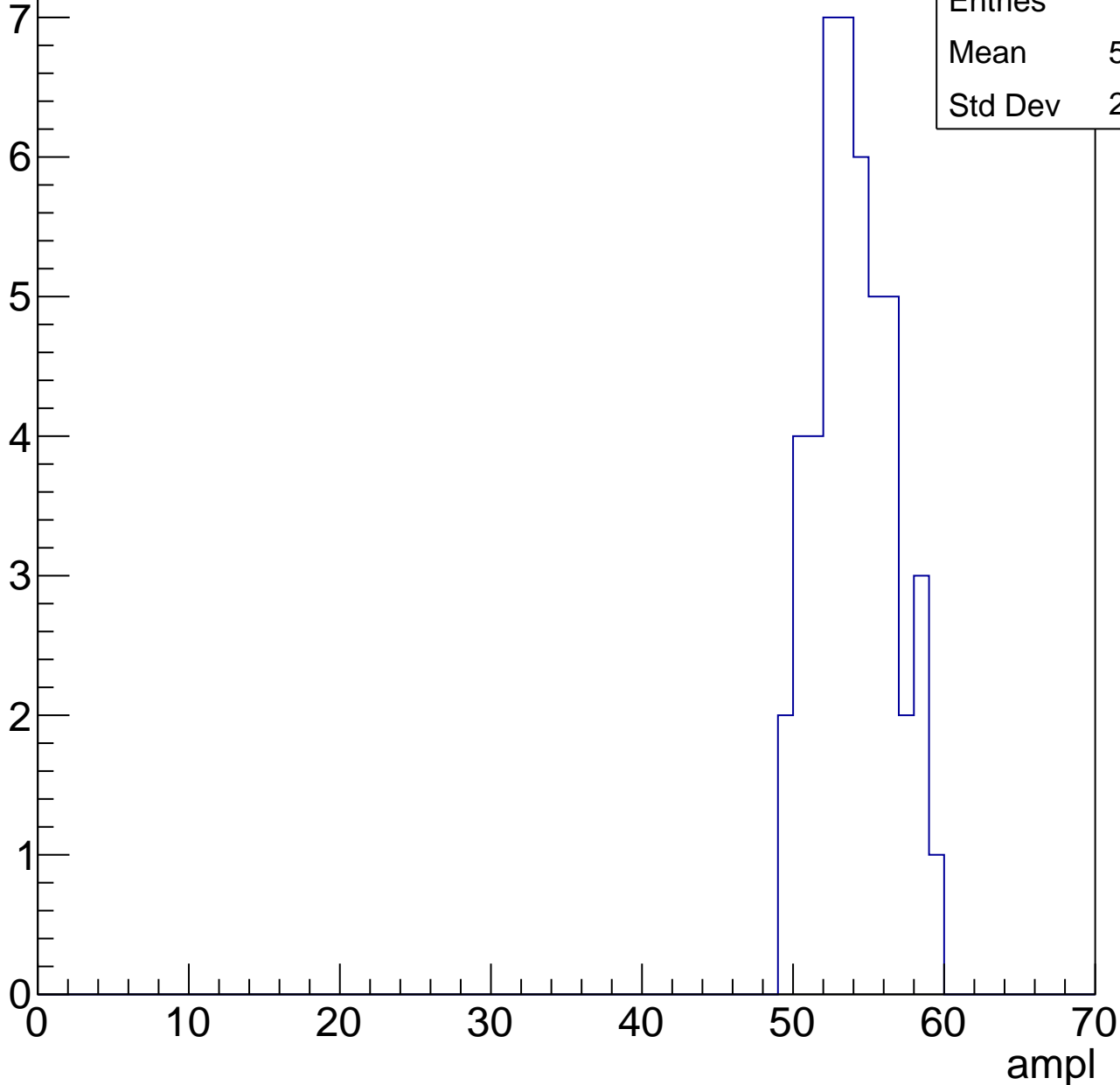


# B1L000S, U25-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	53.54
Std Dev	2.517

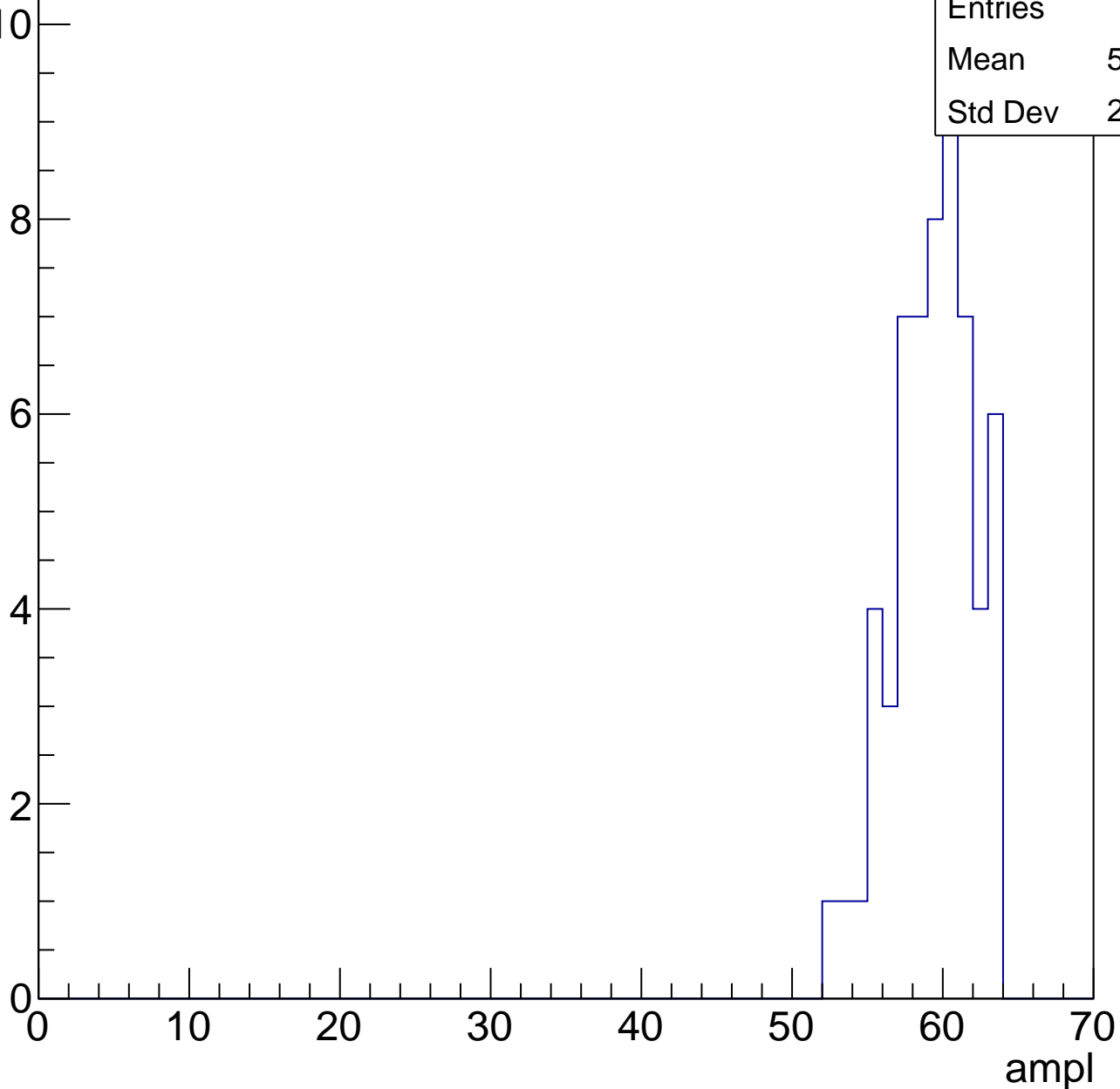


# B1L000S, U25-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	58.93
Std Dev	2.622

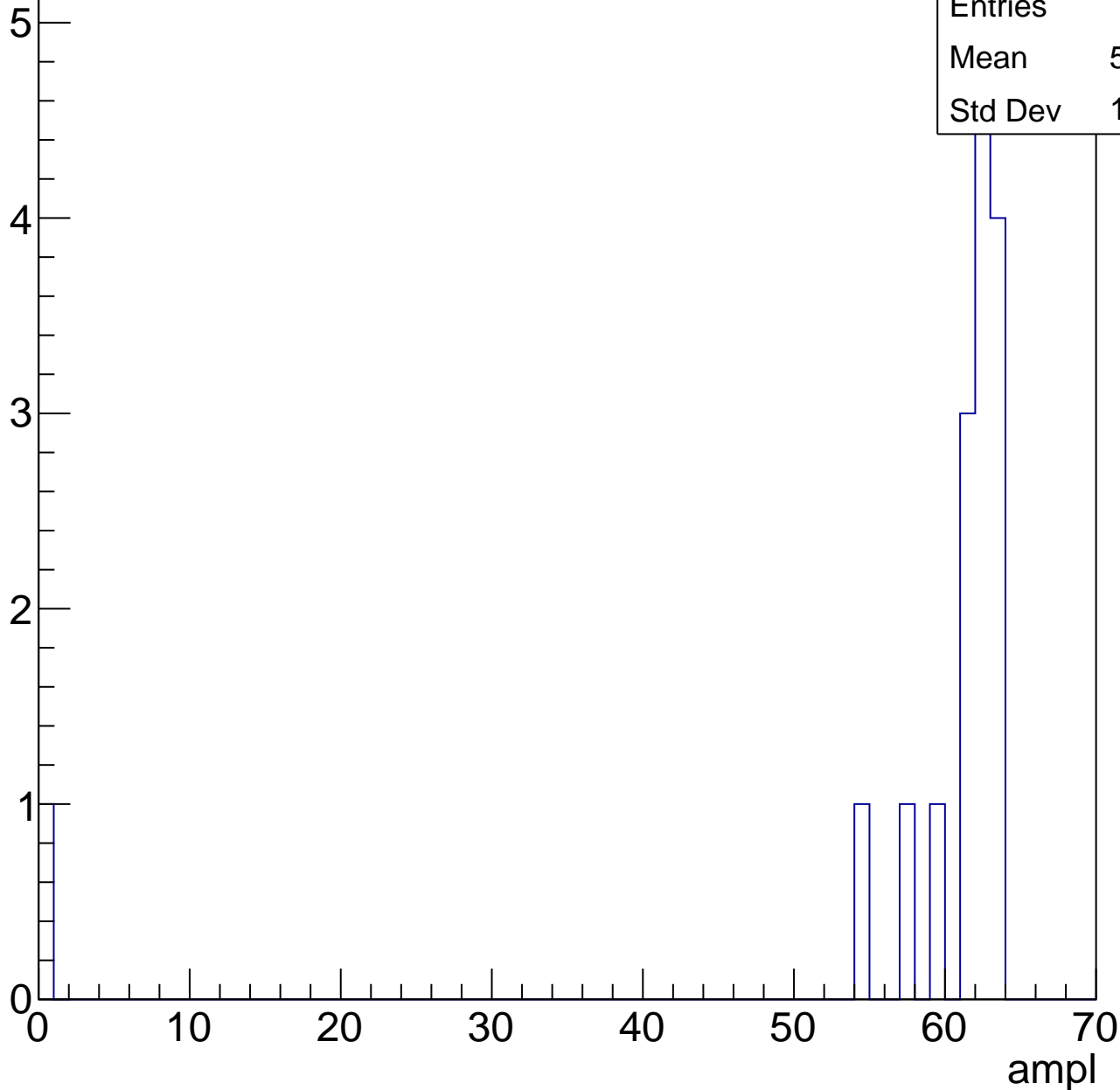


# B1L000S, U25-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	57.19
Std Dev	14.96

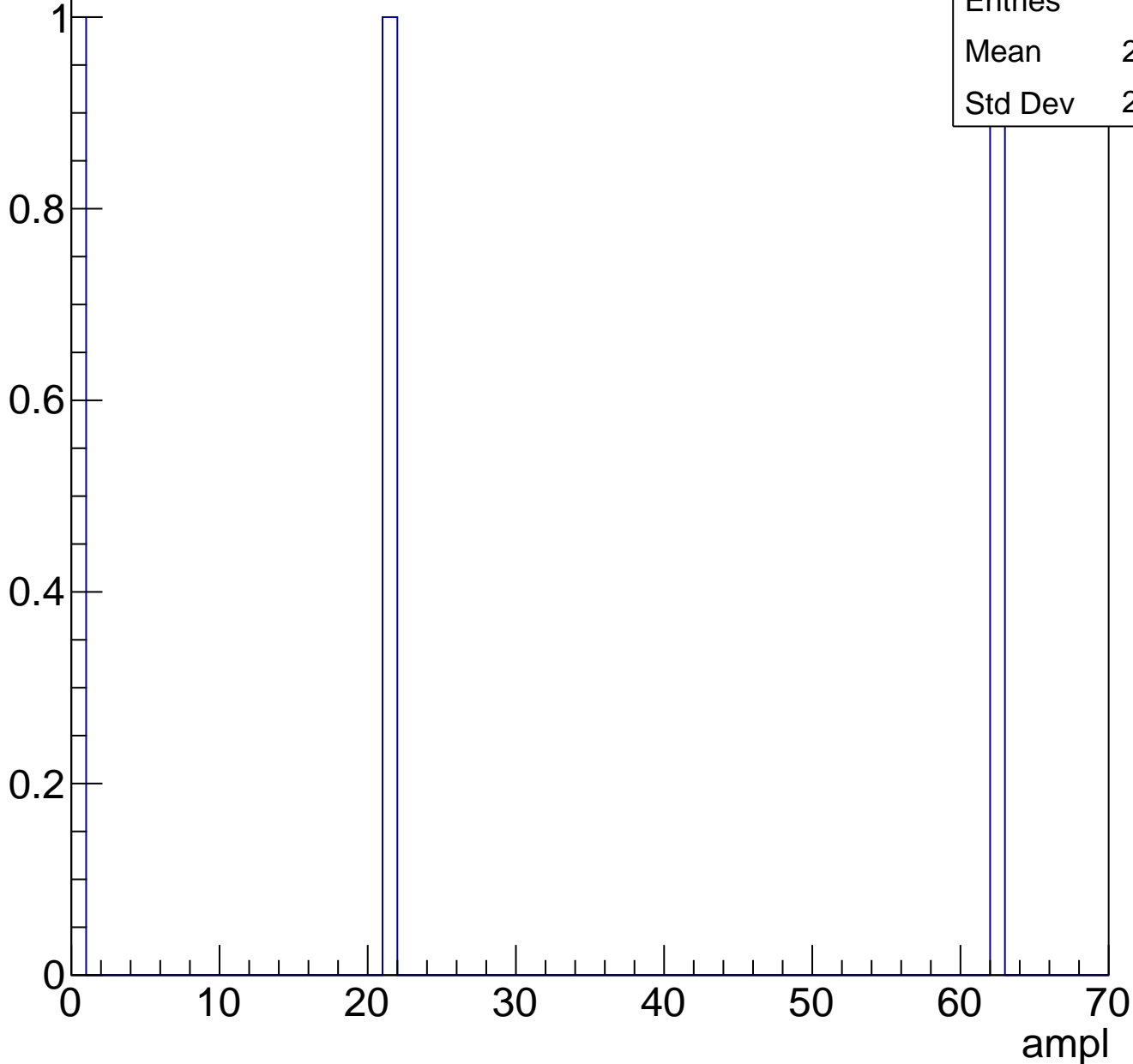




# B1L000S, U25-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L000S, U25-ch57, adc0

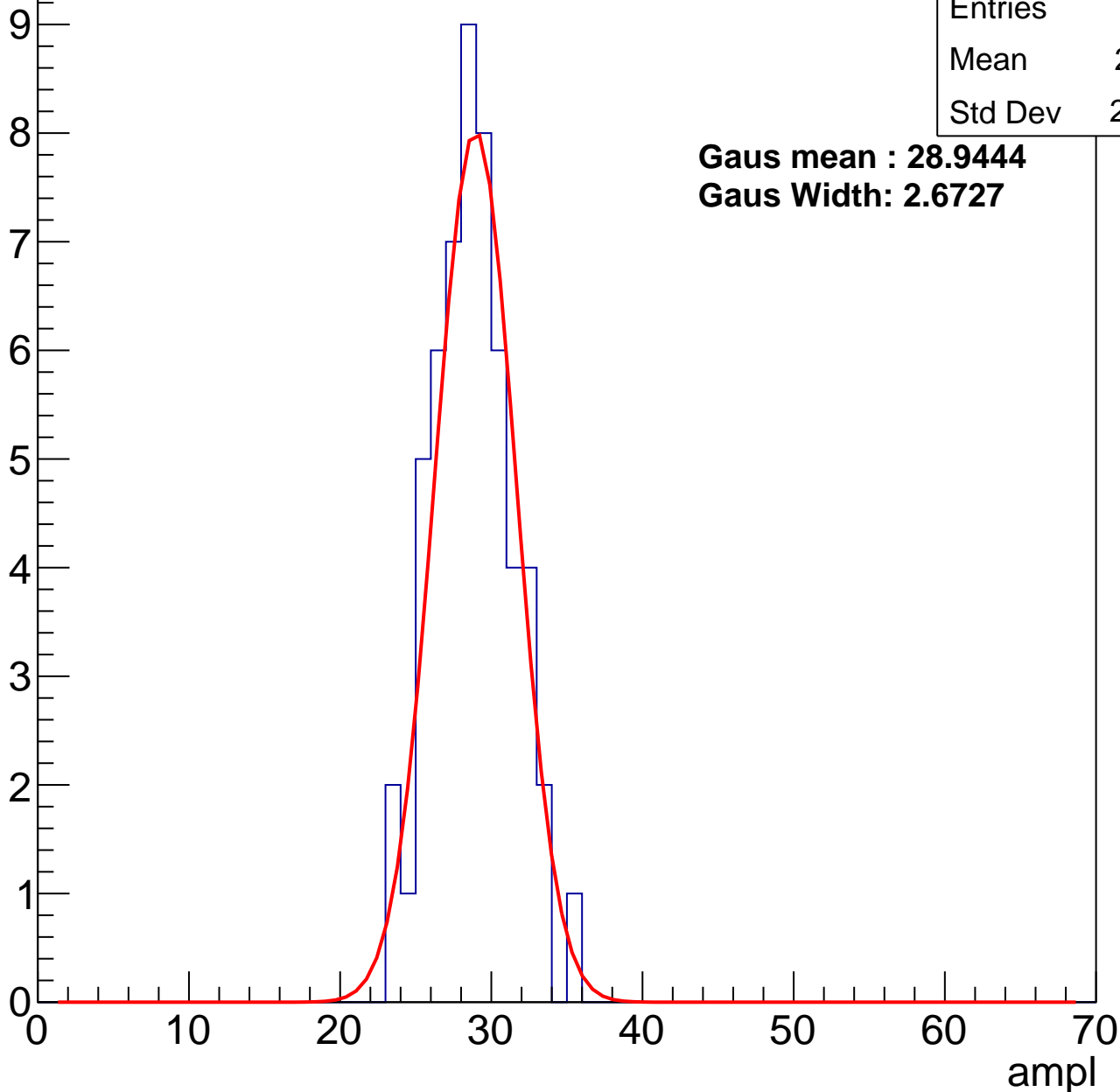
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.31
Std Dev	2.586

**Gaus mean : 28.9444**

**Gaus Width: 2.6727**



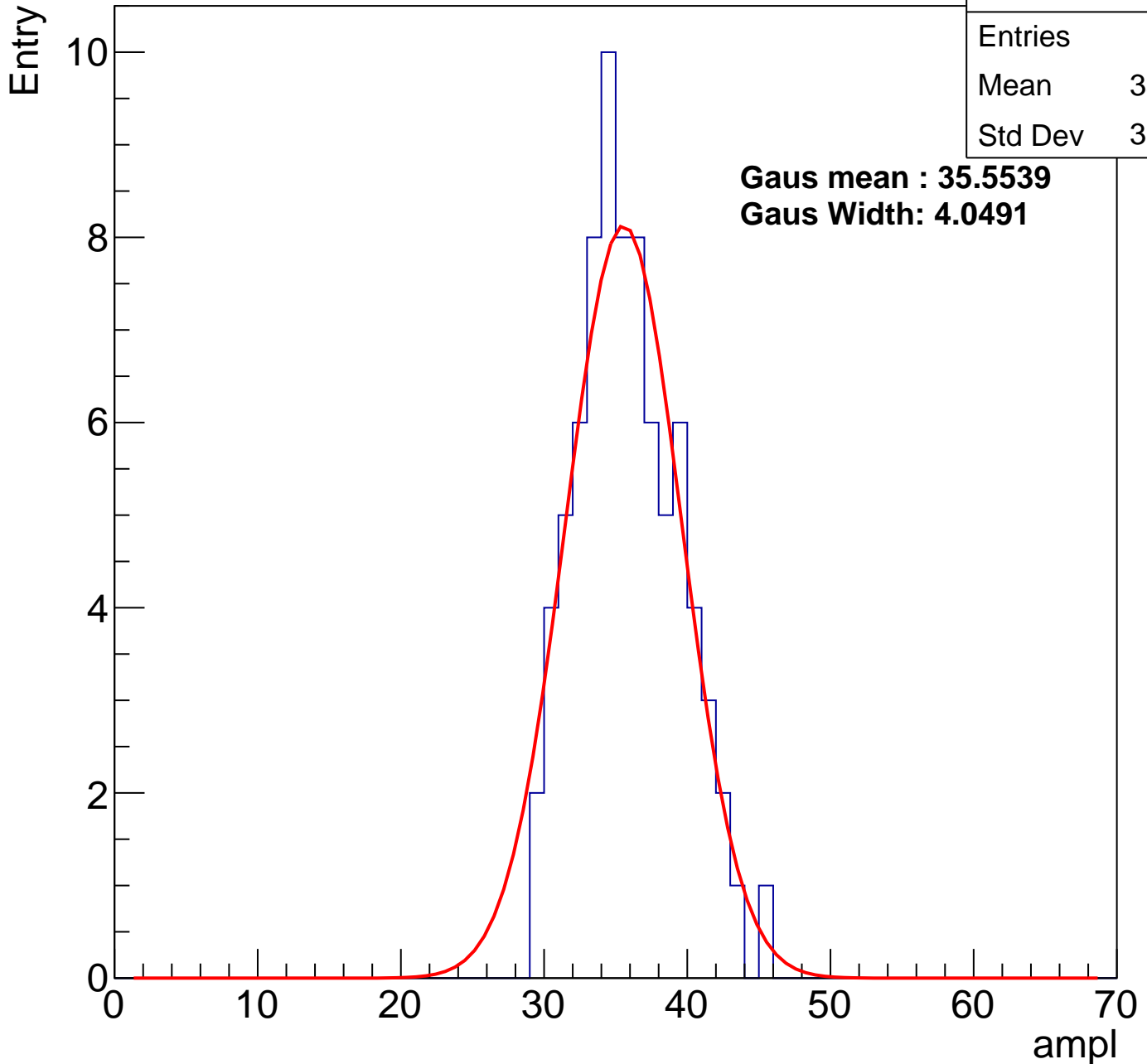
# B1L000S, U25-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	79
Mean	35.42
Std Dev	3.517

**Gaus mean : 35.5539**

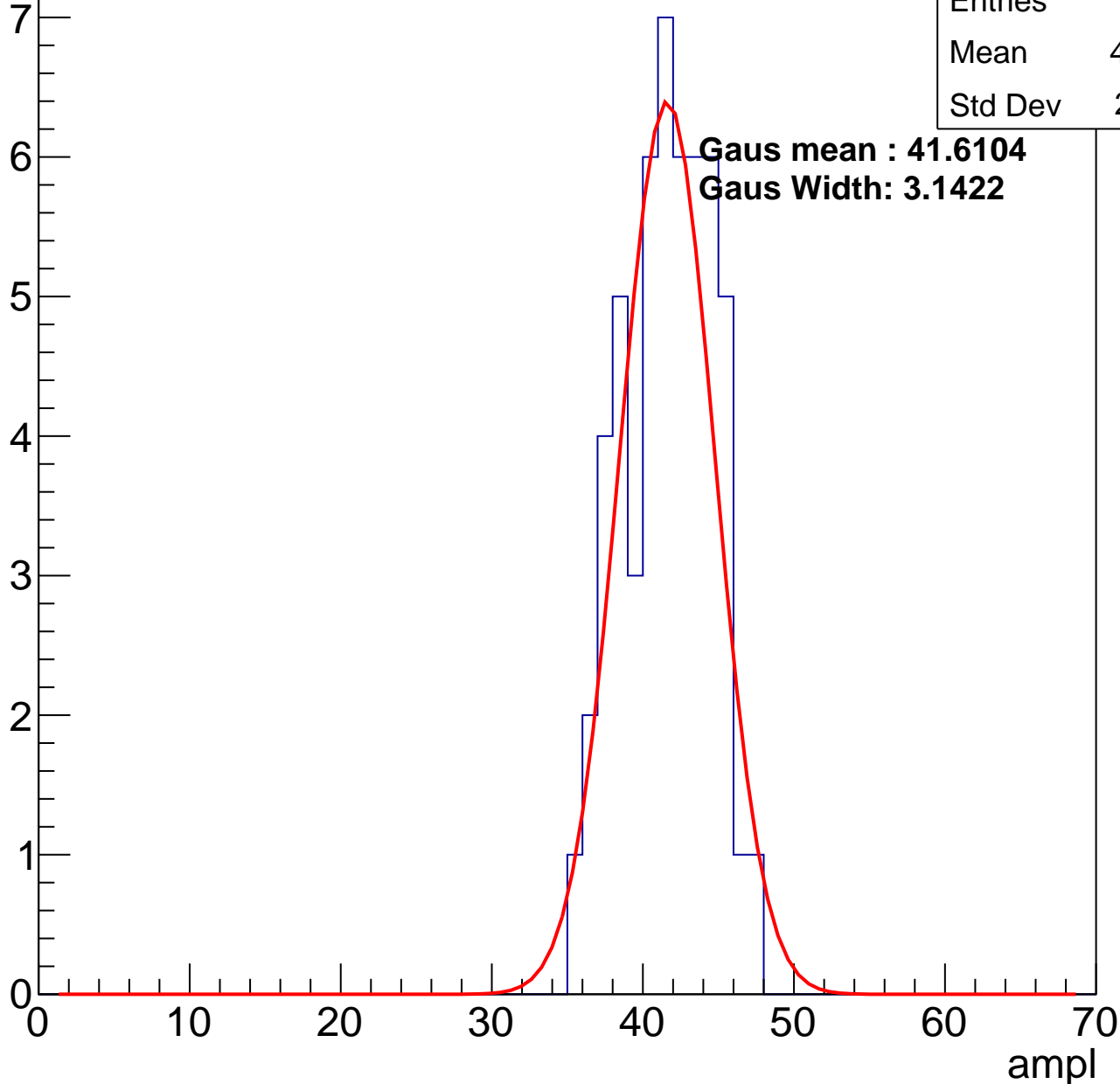
**Gaus Width: 4.0491**



# B1L000S, U25-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

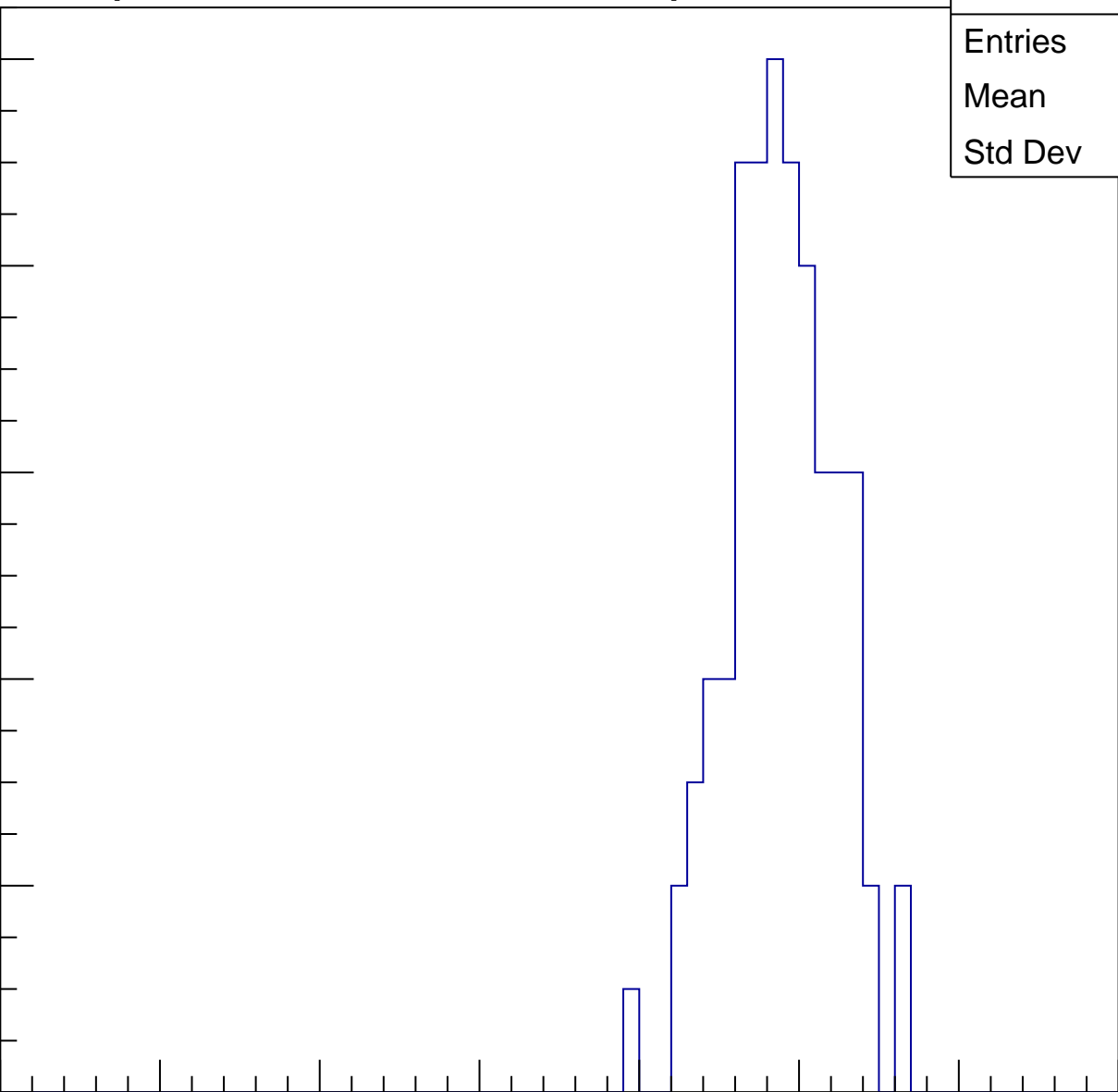
Entries	81
Mean	48.42
Std Dev	3.333

Entry

10  
8  
6  
4  
2  
0

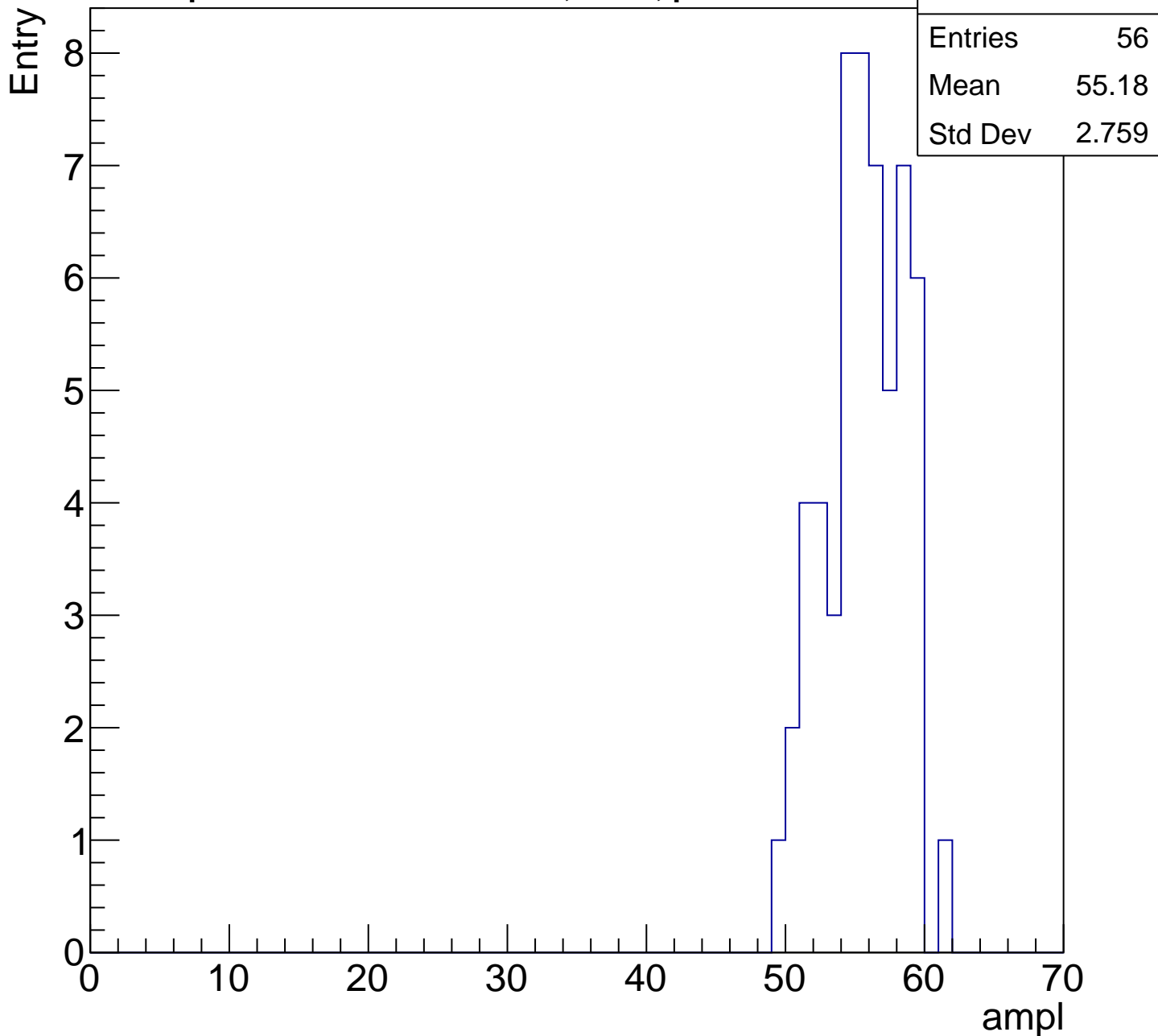
0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch57, adc5

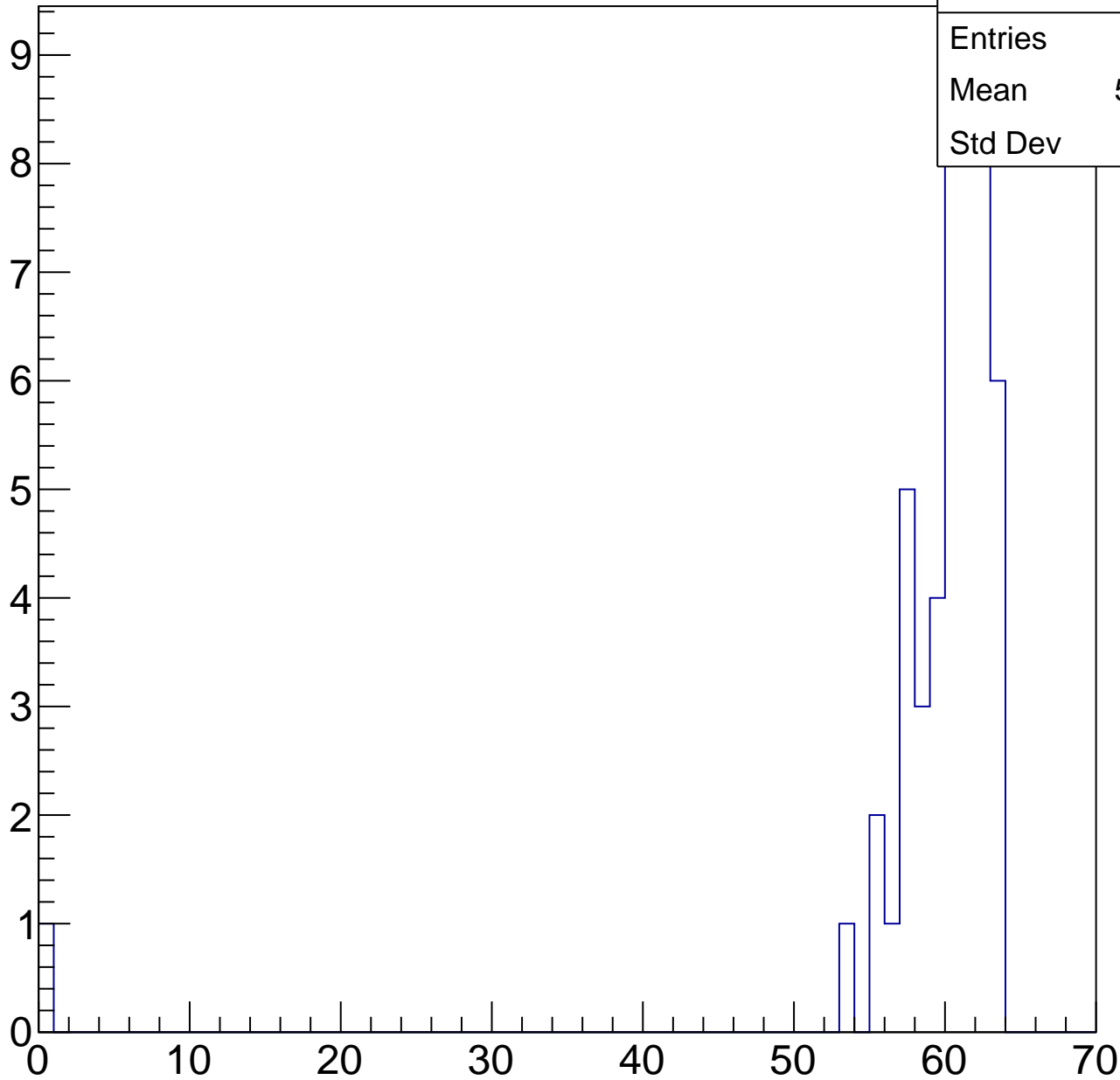
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	58.71
Std Dev	8.89

ampl



# B1L000S, U25-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

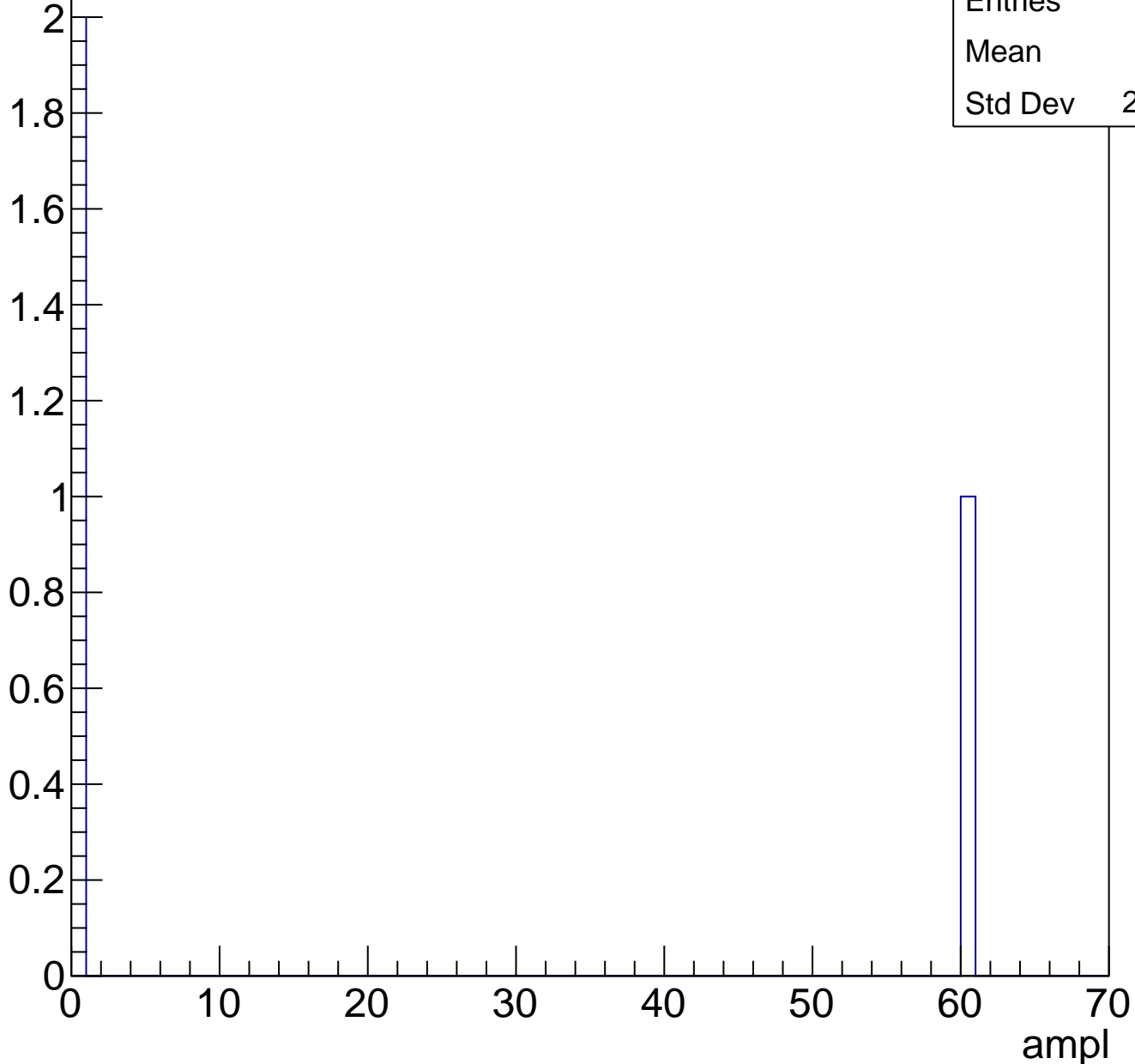




# B1L000S, U25-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	20
Std Dev	28.28

# B1L000S, U25-ch58, adc0

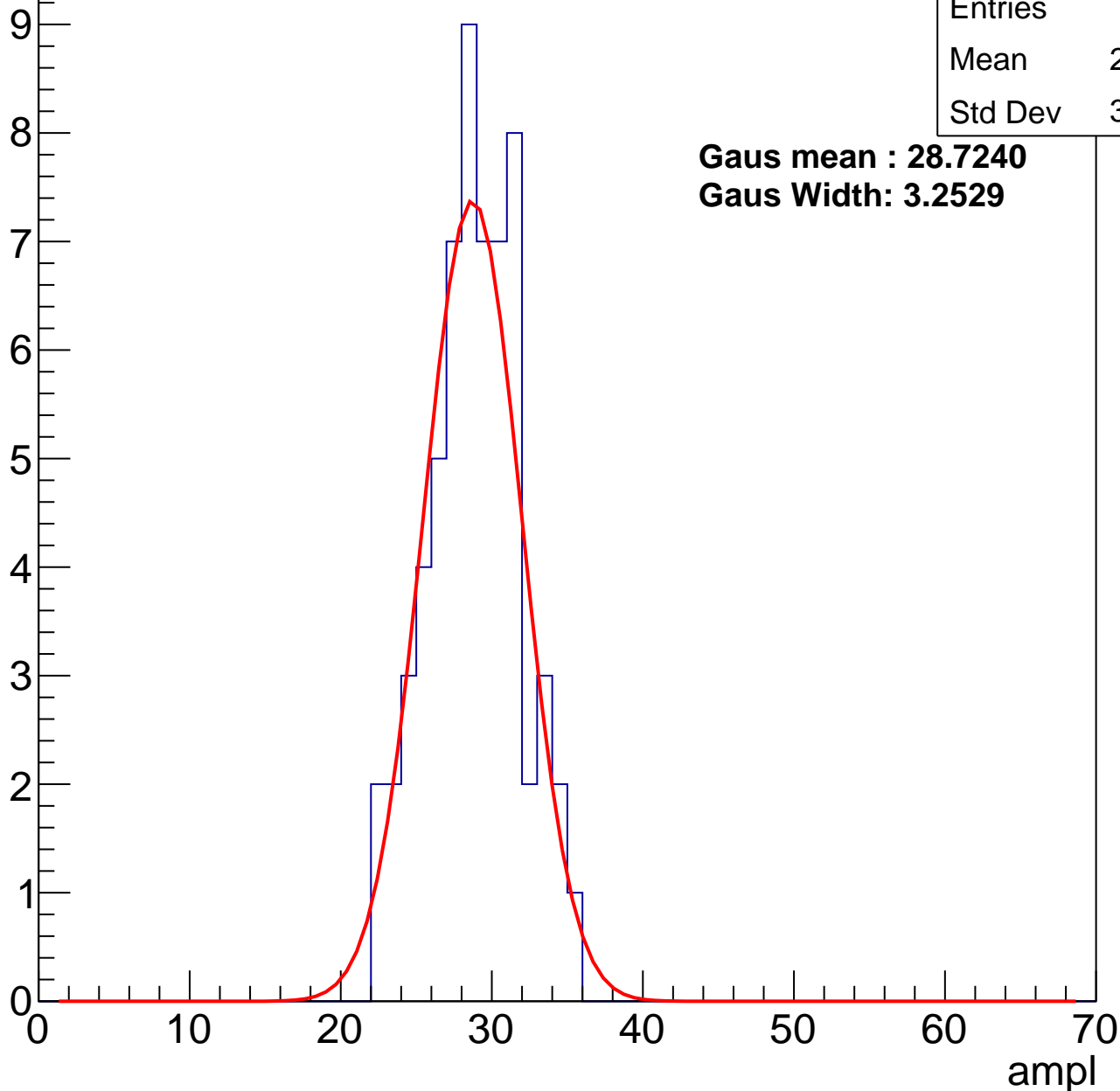
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.39
Std Dev	3.002

**Gaus mean : 28.7240**

**Gaus Width: 3.2529**



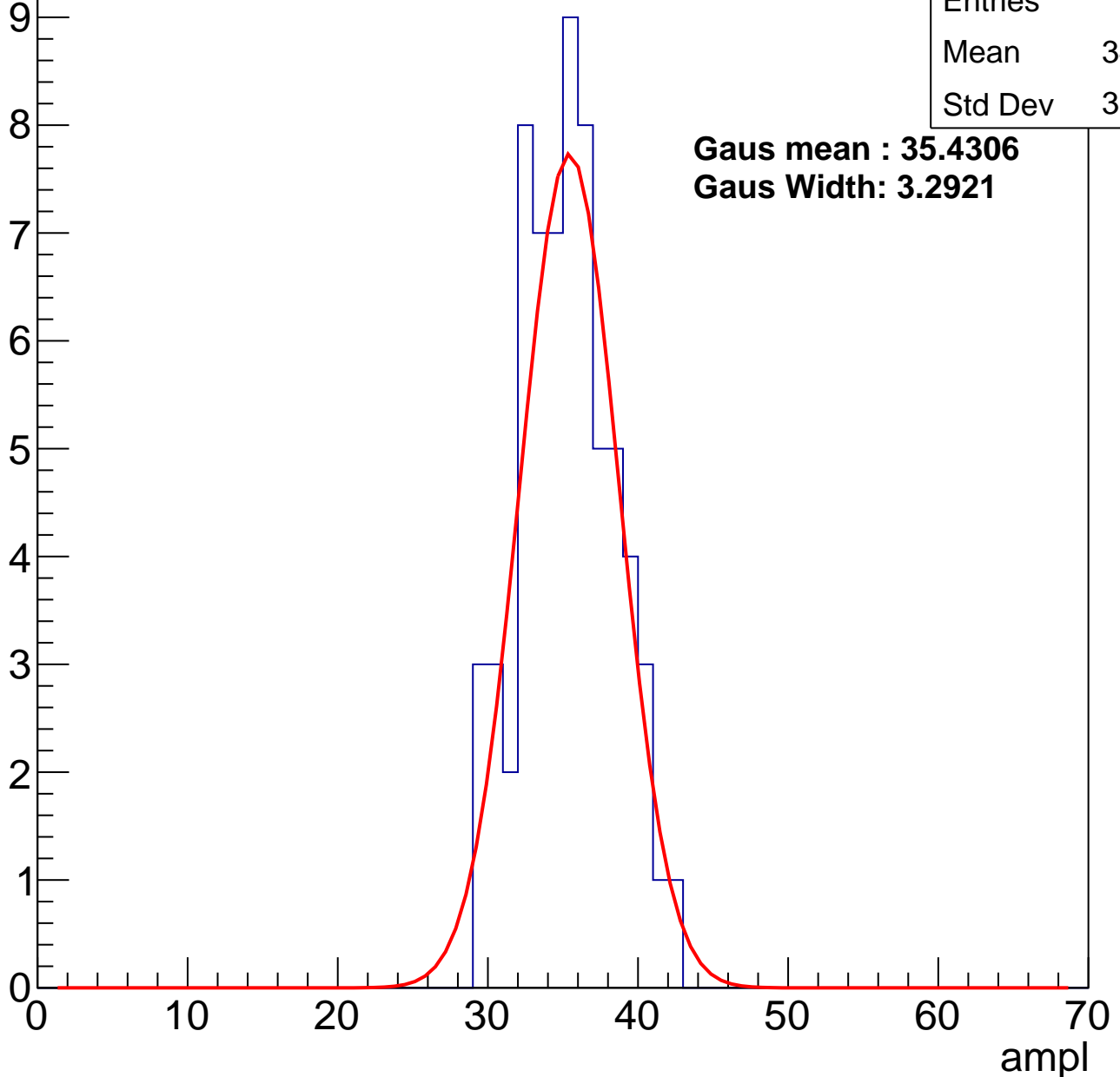
# B1L000S, U25-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.86
Std Dev	3.059

**Gaus mean : 35.4306**  
**Gaus Width: 3.2921**



# B1L000S, U25-ch58, adc2

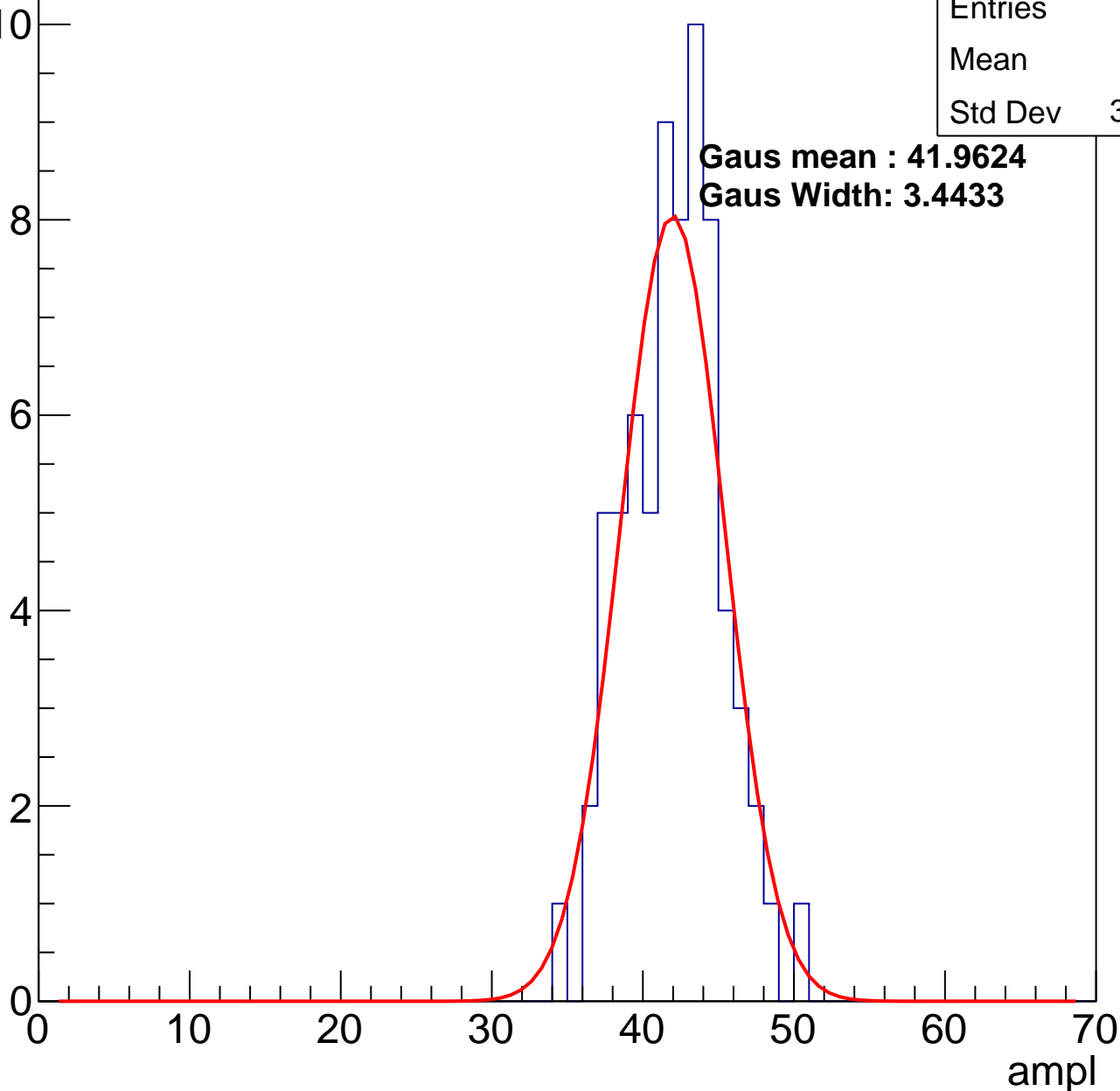
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	41.6
Std Dev	3.137

**Gaus mean : 41.9624**

**Gaus Width: 3.4433**

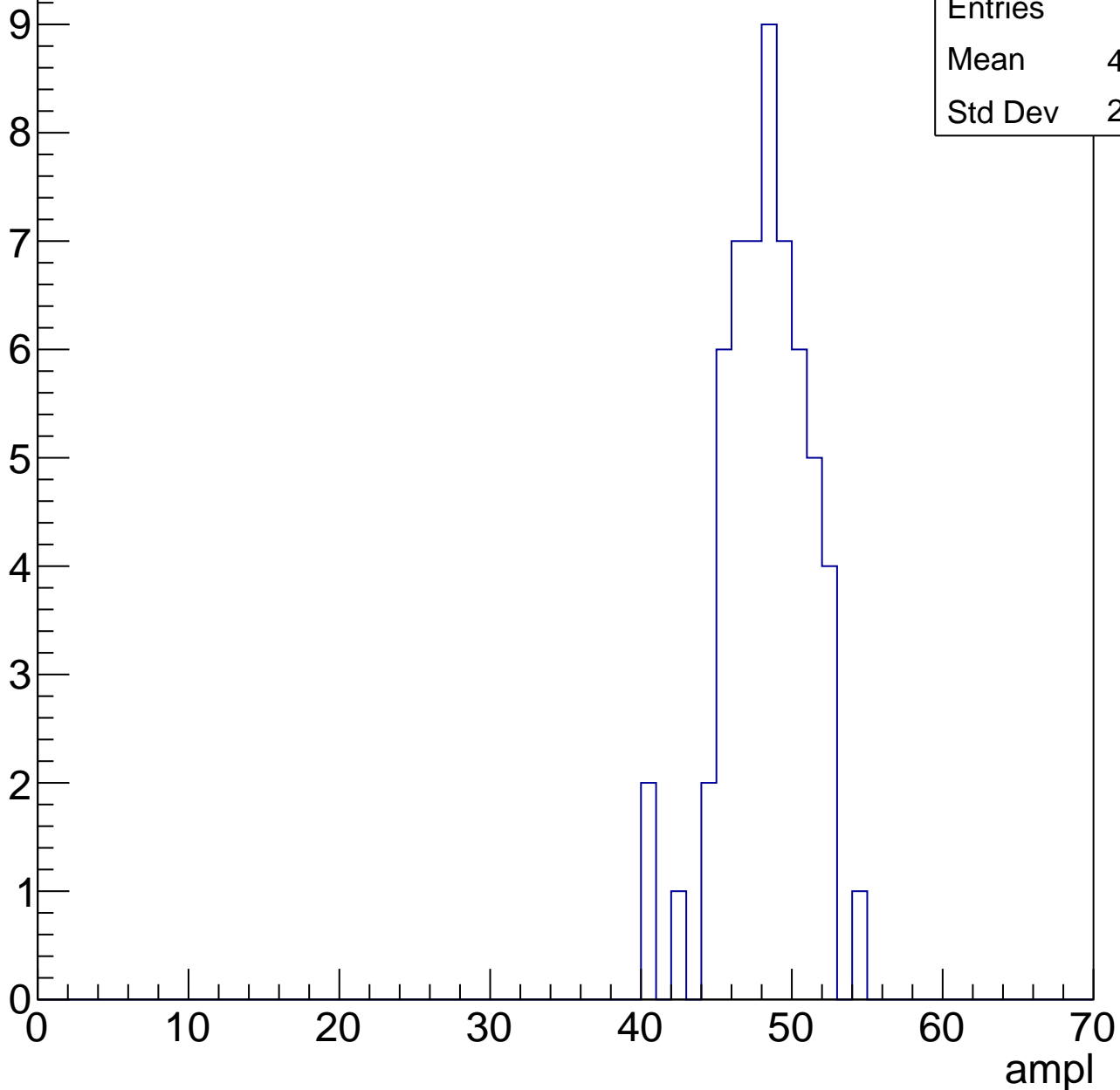


# B1L000S, U25-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.77
Std Dev	2.835

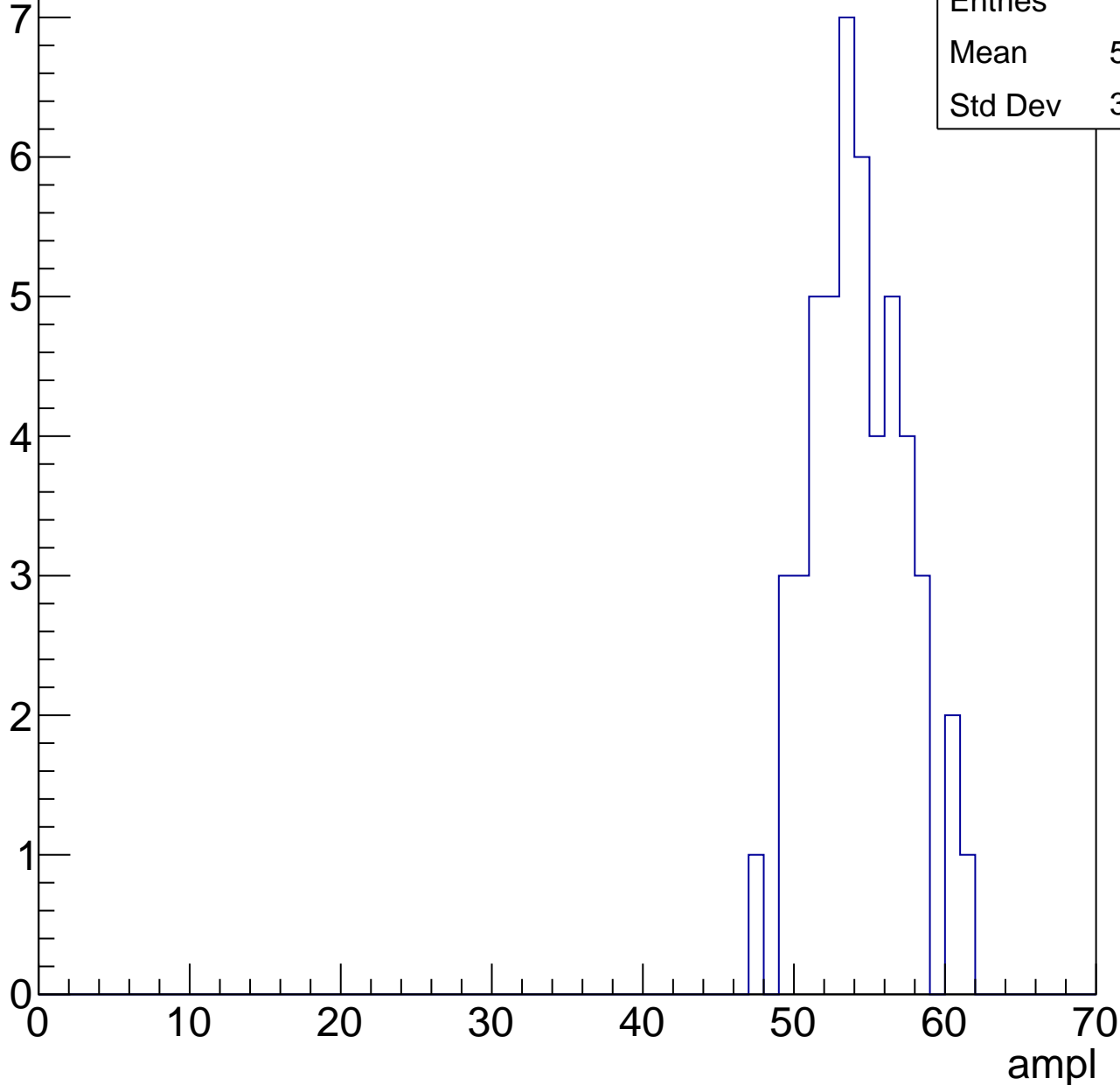


# B1L000S, U25-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	53.82
Std Dev	3.102

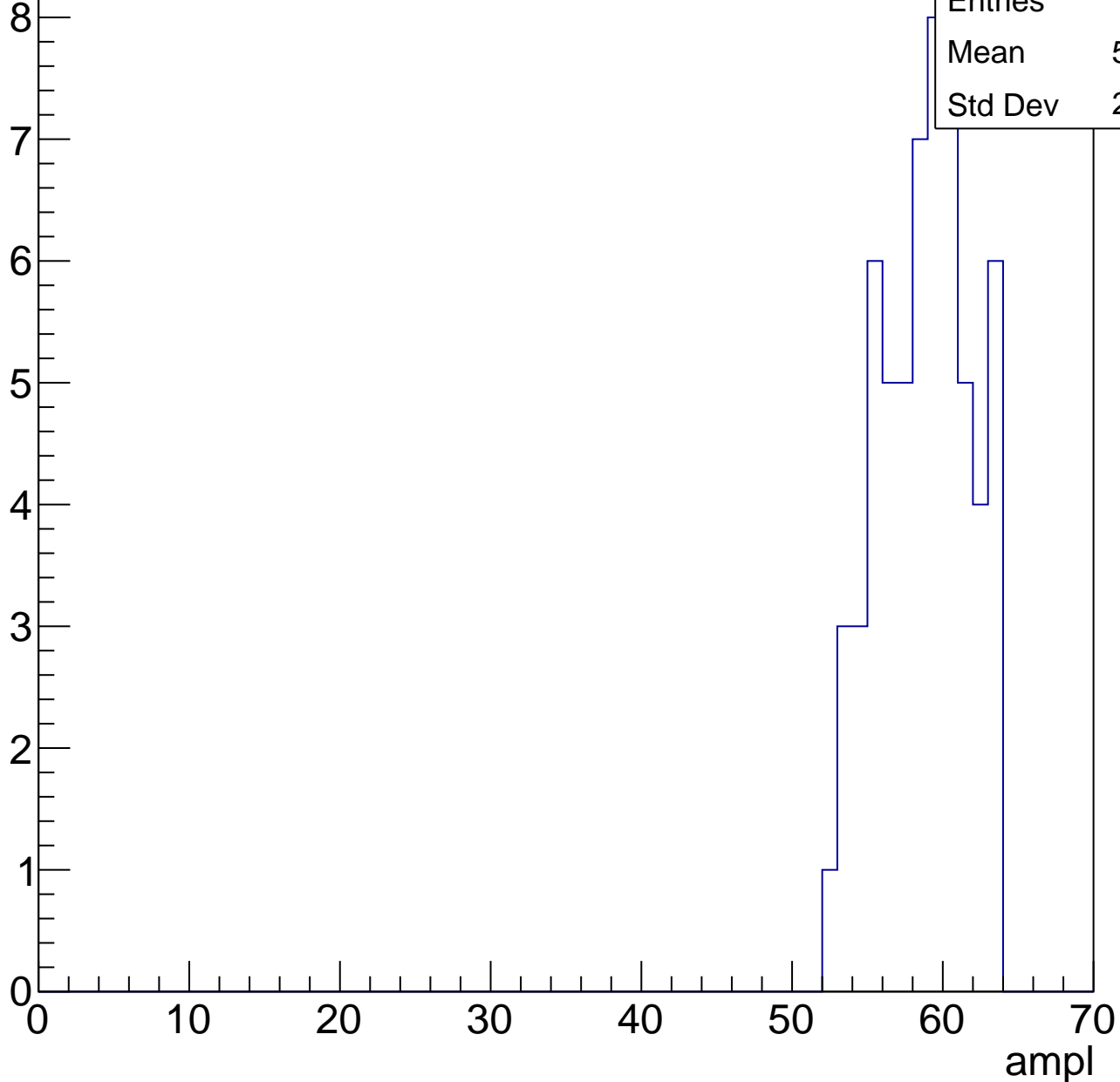


# B1L000S, U25-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	58.31
Std Dev	2.951

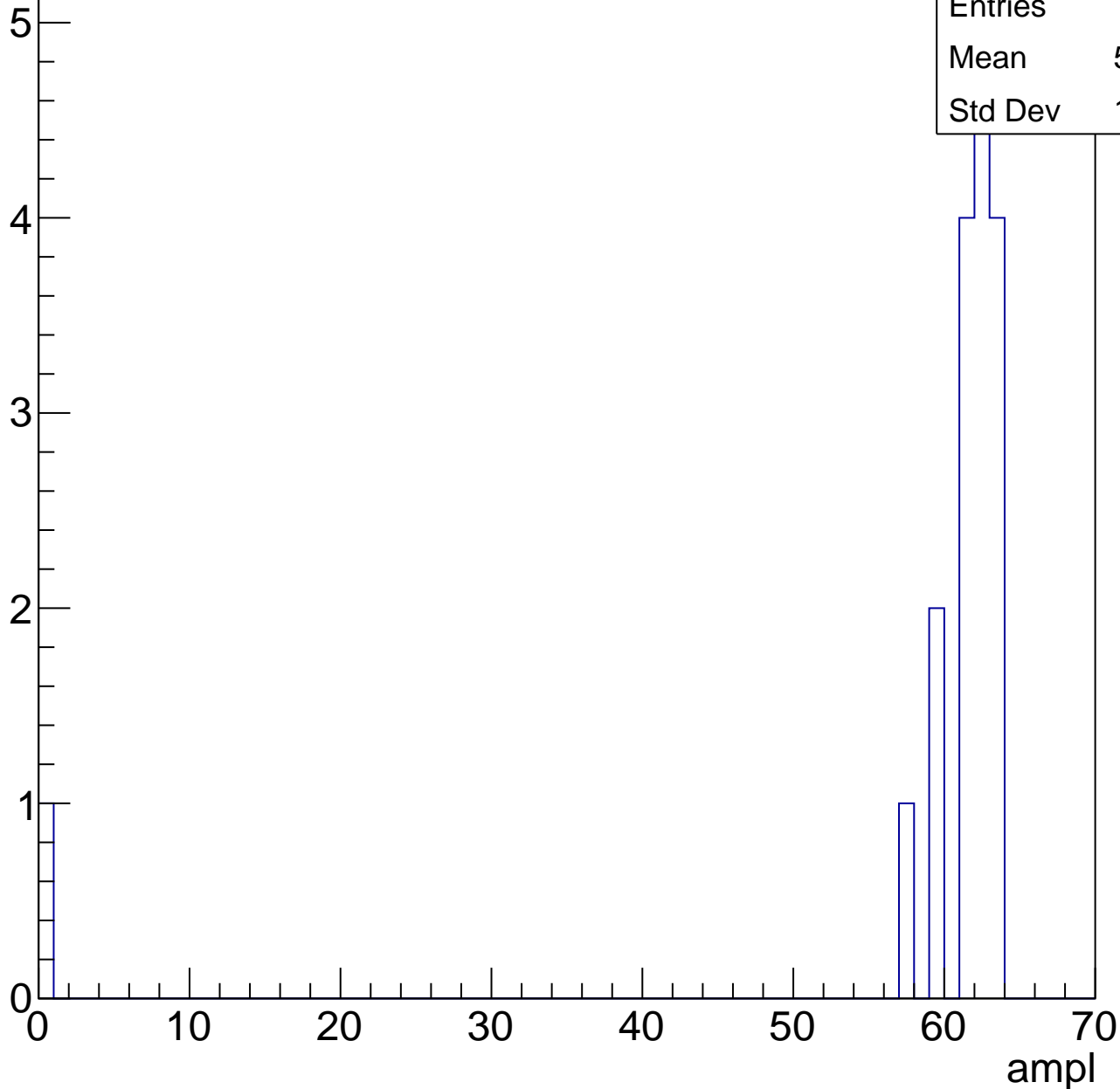


# B1L000S, U25-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.71
Std Dev	14.51





# B1L000S, U25-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch59, adc0

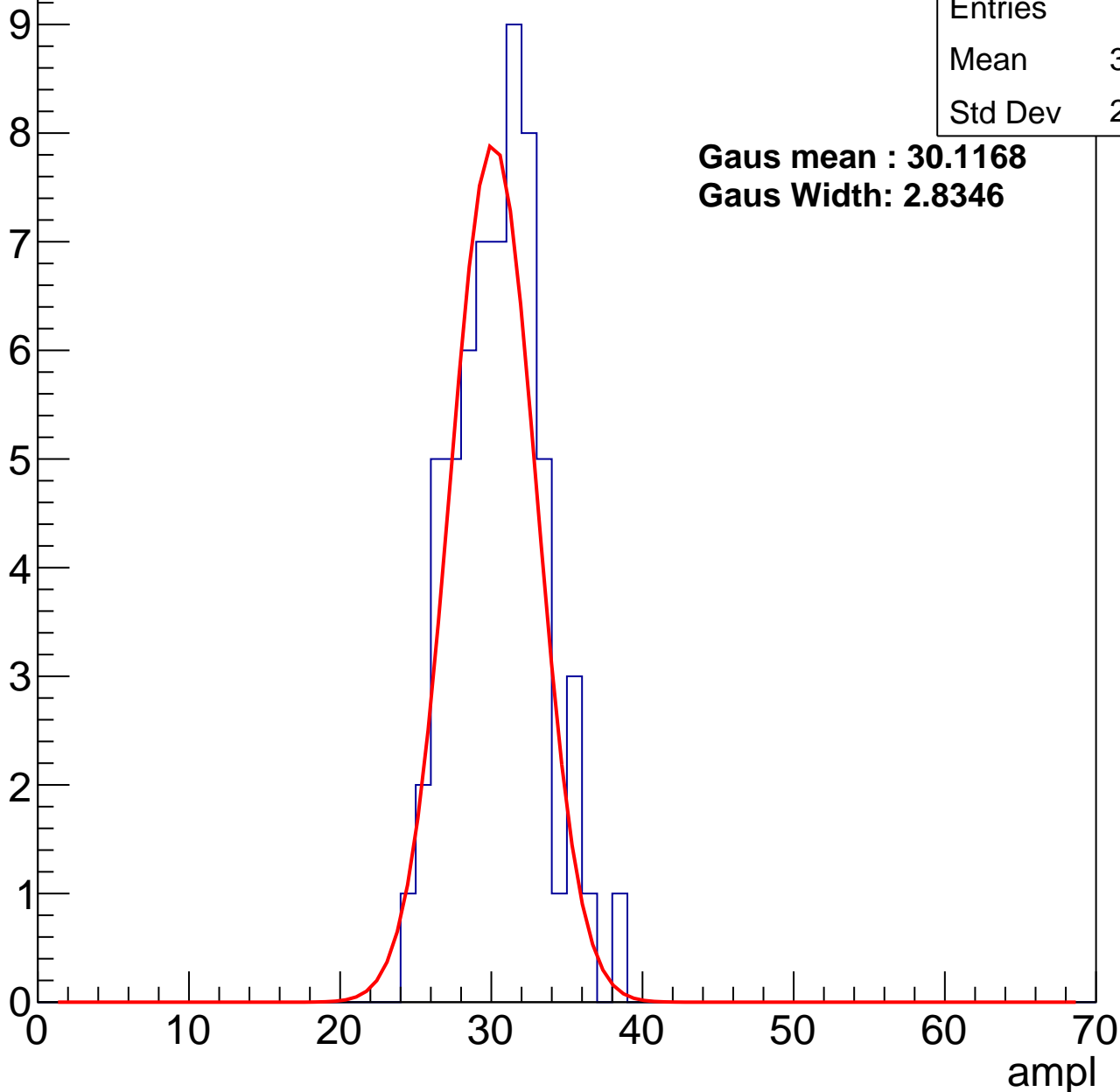
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	30.05
Std Dev	2.916

**Gaus mean : 30.1168**

**Gaus Width: 2.8346**



# B1L000S, U25-ch59, adc1

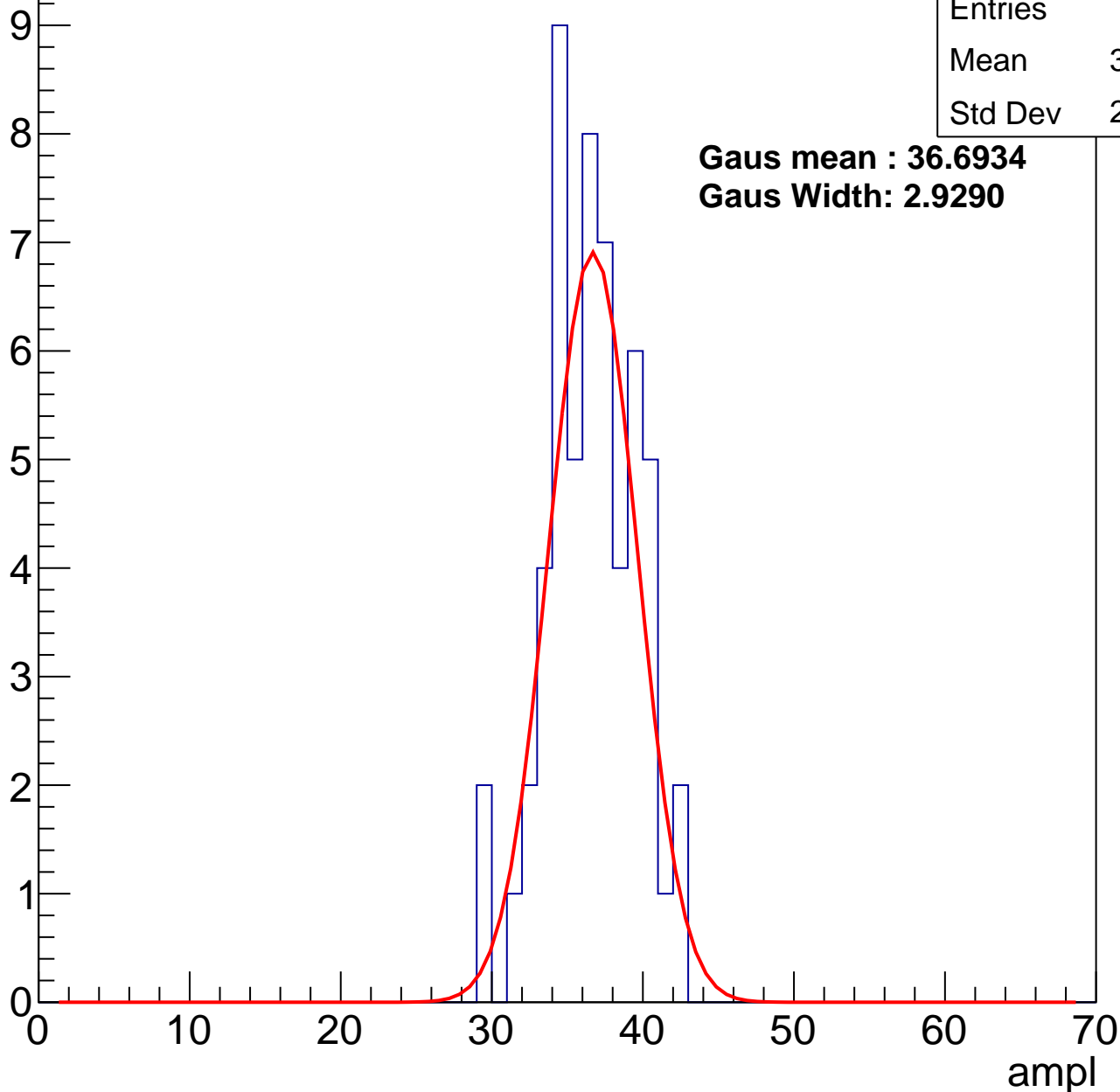
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	36.14
Std Dev	2.942

**Gaus mean : 36.6934**

**Gaus Width: 2.9290**



# B1L000S, U25-ch59, adc2

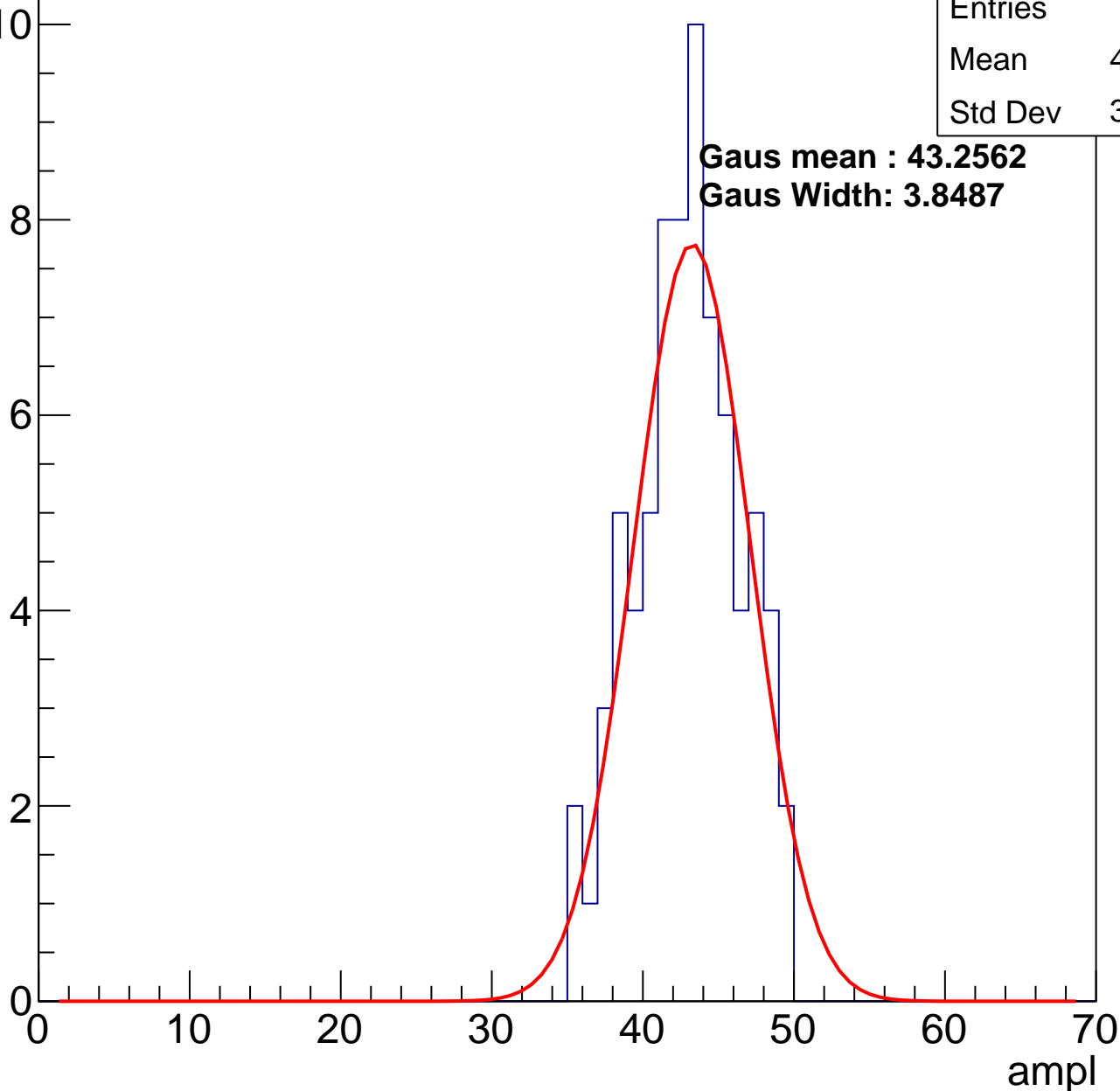
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	42.49
Std Dev	3.406

**Gaus mean : 43.2562**

**Gaus Width: 3.8487**

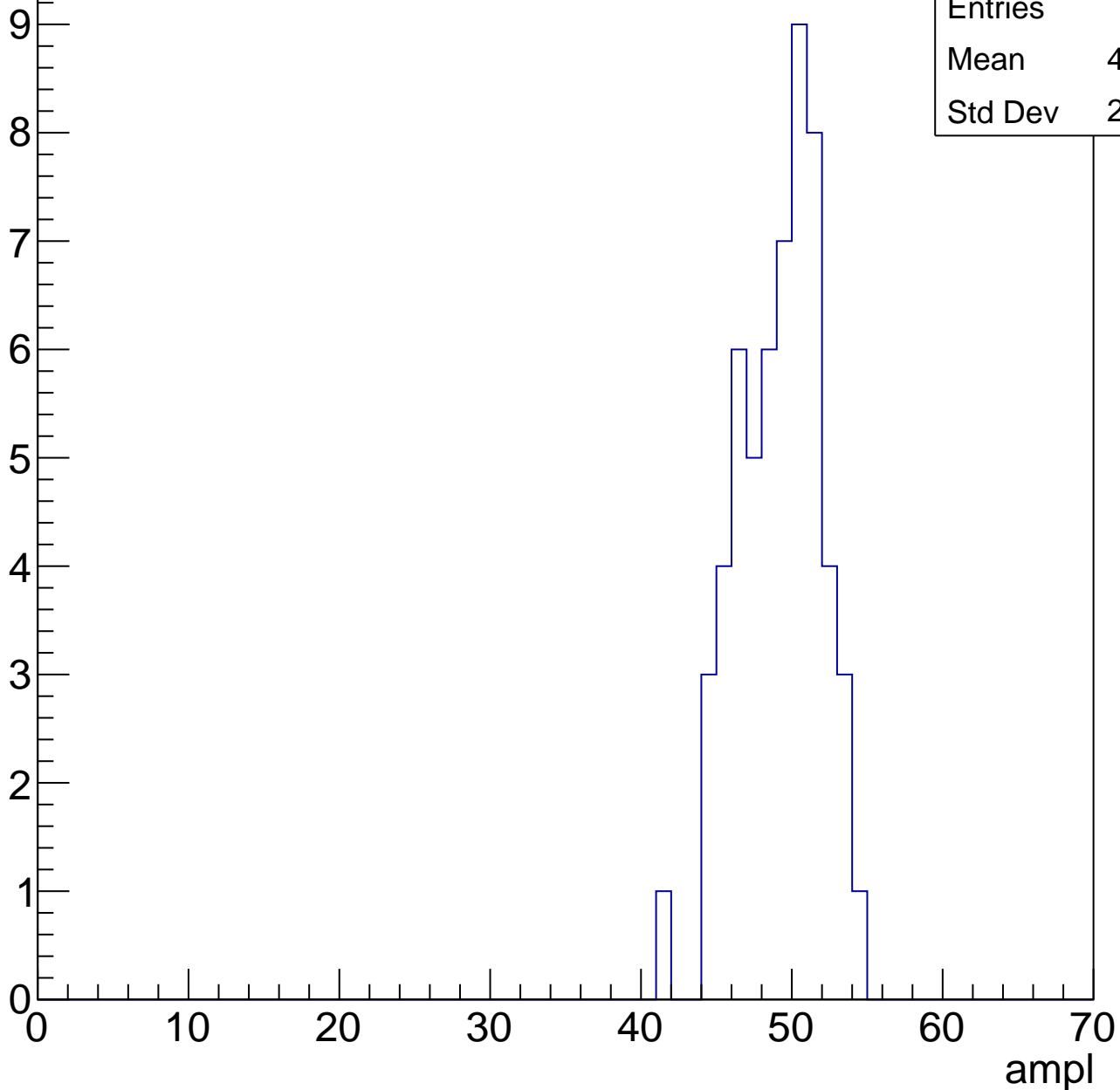


# B1L000S, U25-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

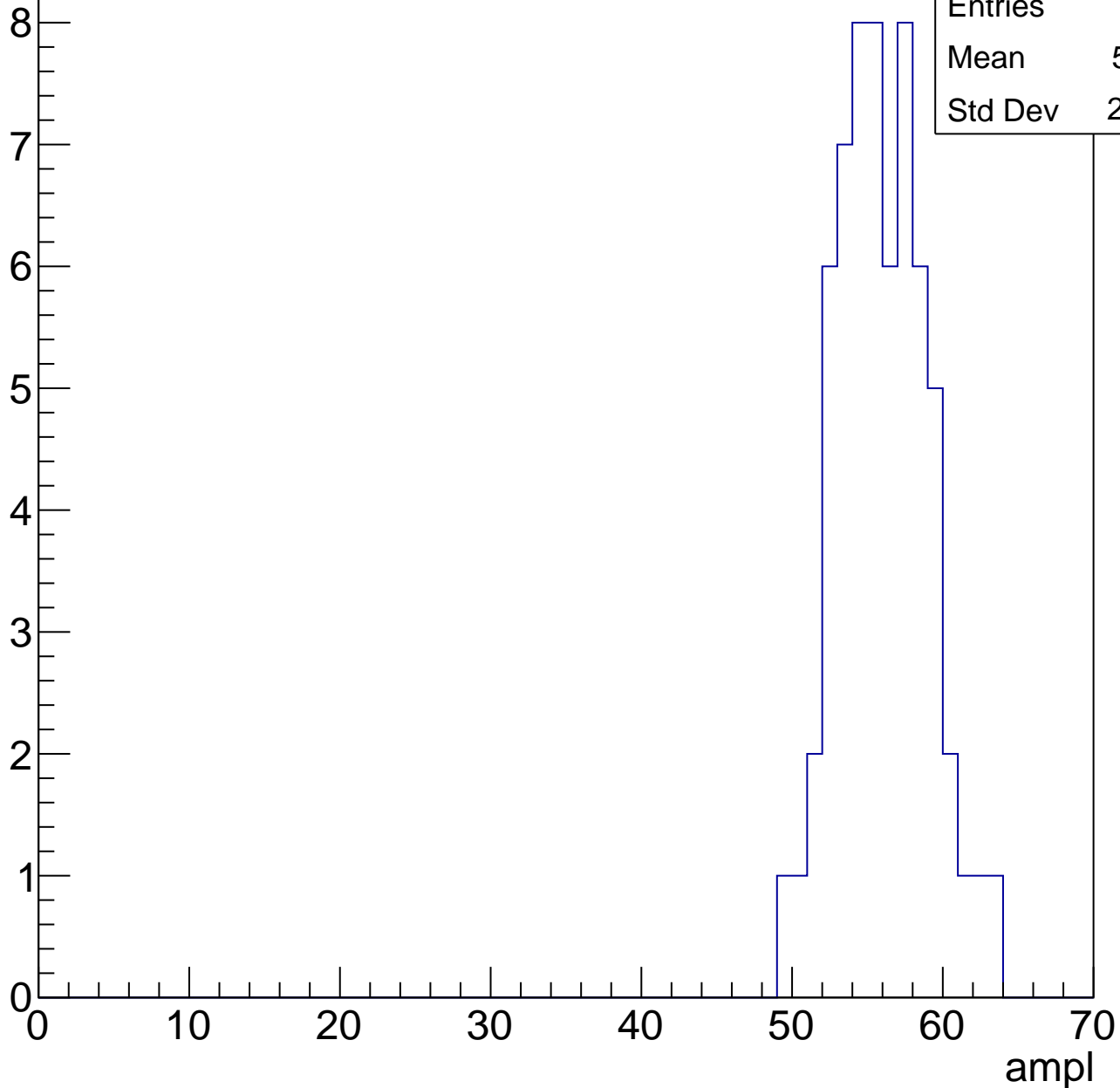
Entries	57
Mean	48.67
Std Dev	2.736



# B1L000S, U25-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	63
Mean	55.51
Std Dev	2.922

# B1L000S, U25-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

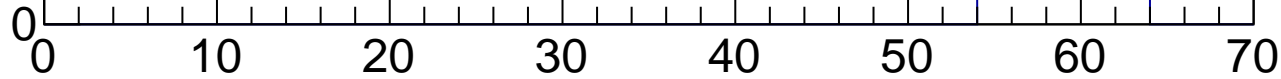
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	59.52
Std Dev	2.393

ampl

0 10 20 30 40 50 60 70

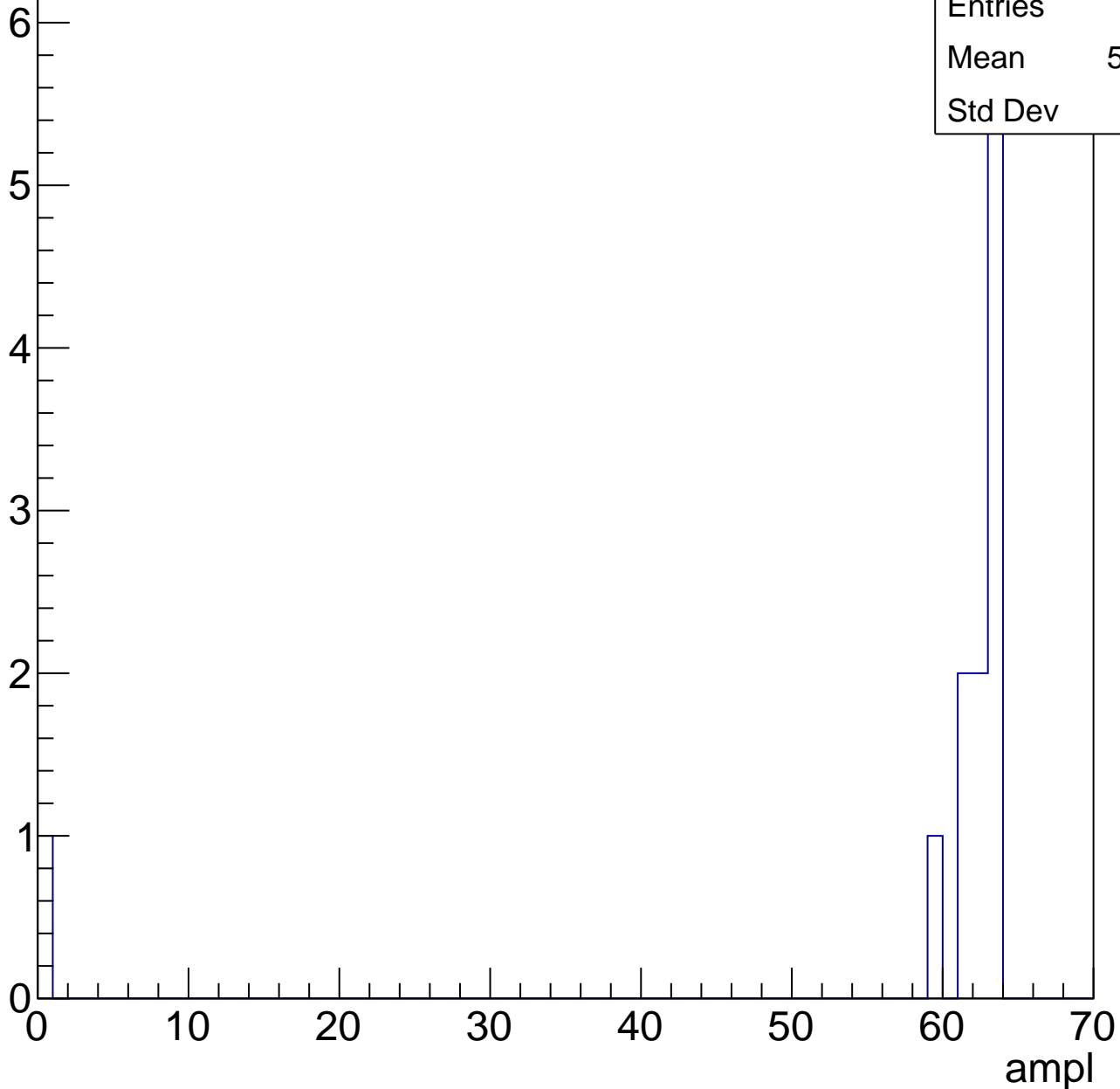


# B1L000S, U25-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	56.92
Std Dev	17.2





# B1L000S, U25-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	29.69
Std Dev	3.316

**Gaus mean : 30.6045**

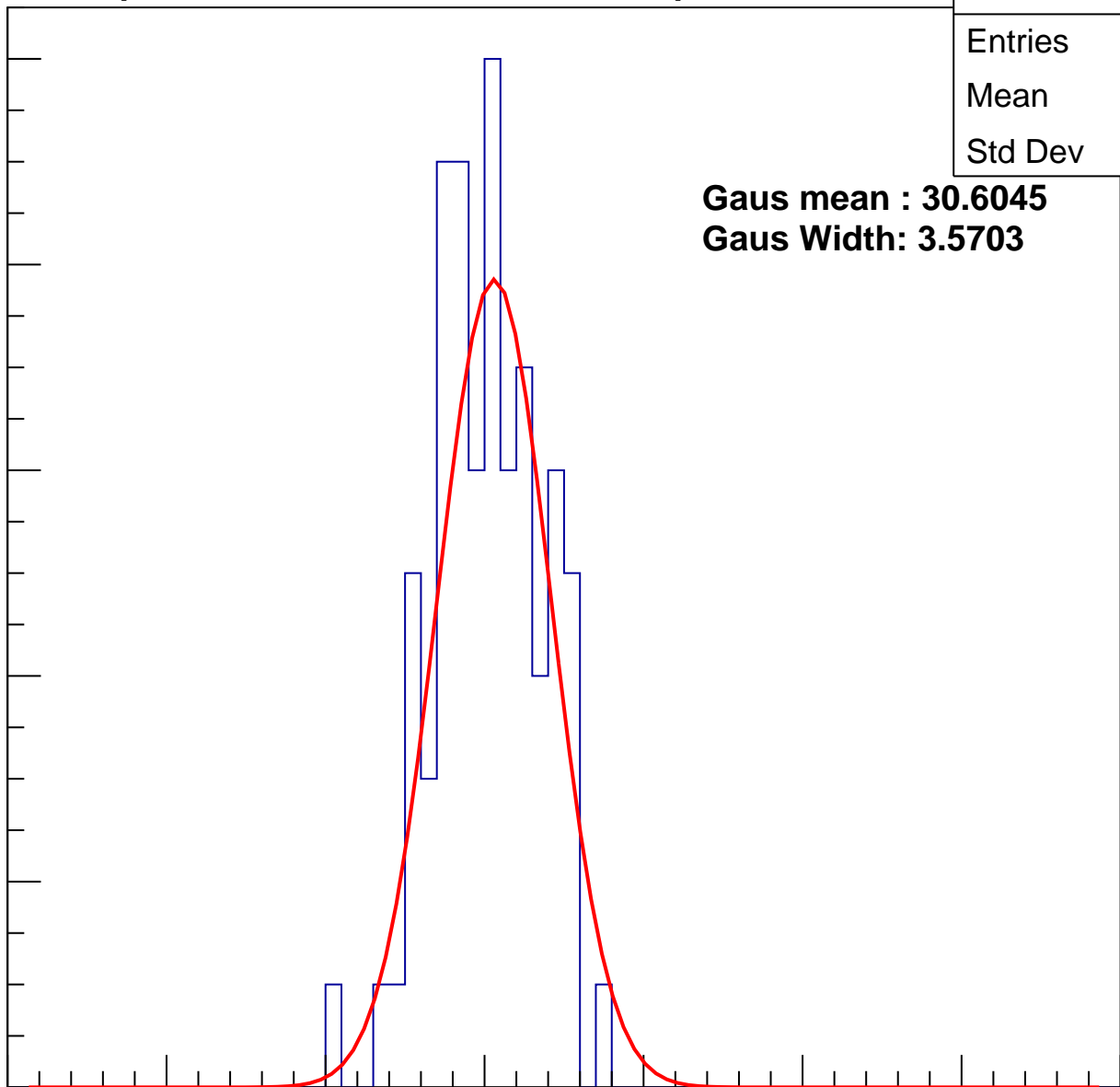
**Gaus Width: 3.5703**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch60, adc1

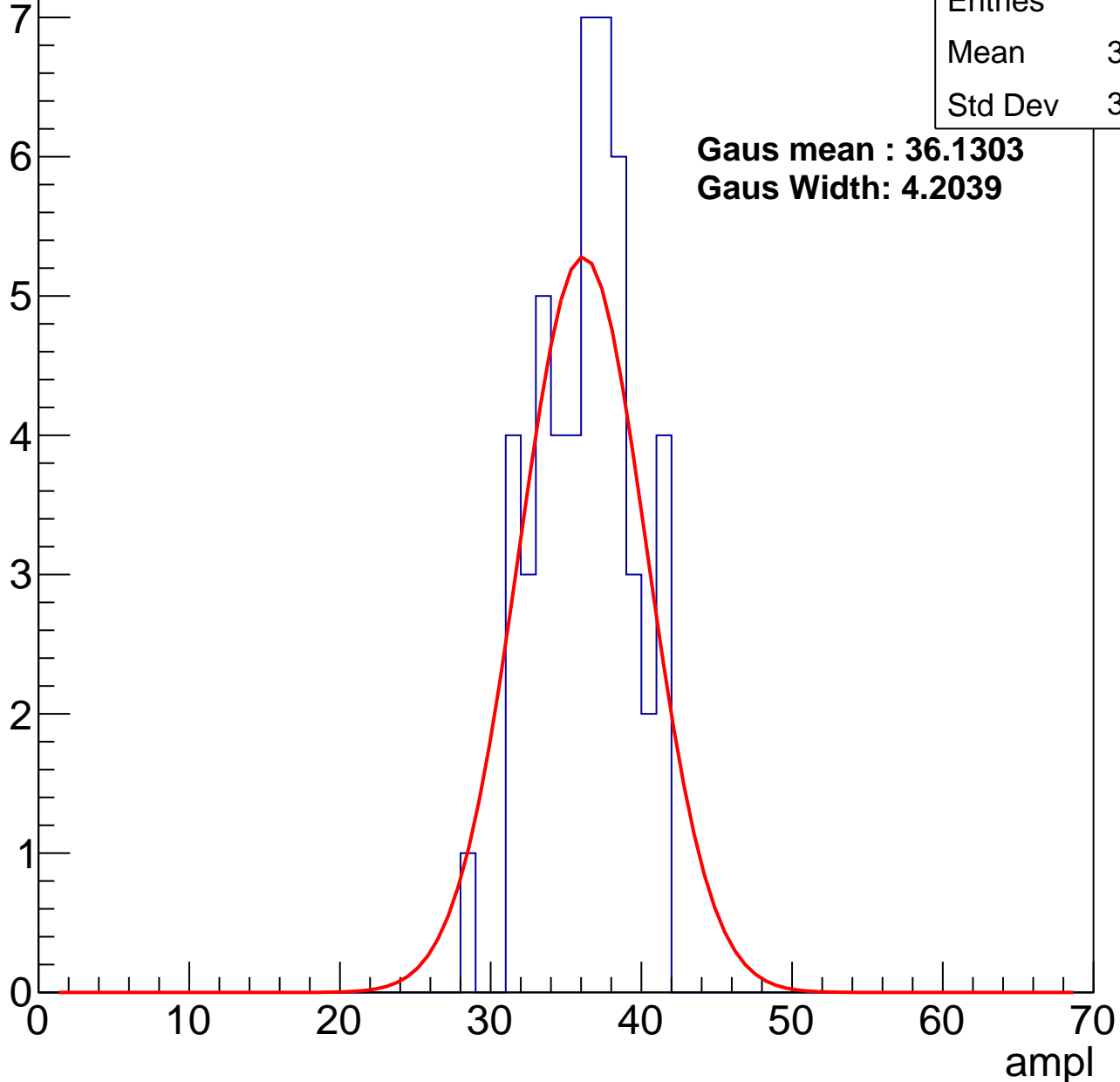
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	35.78
Std Dev	3.048

**Gaus mean : 36.1303**

**Gaus Width: 4.2039**



# B1L000S, U25-ch60, adc2

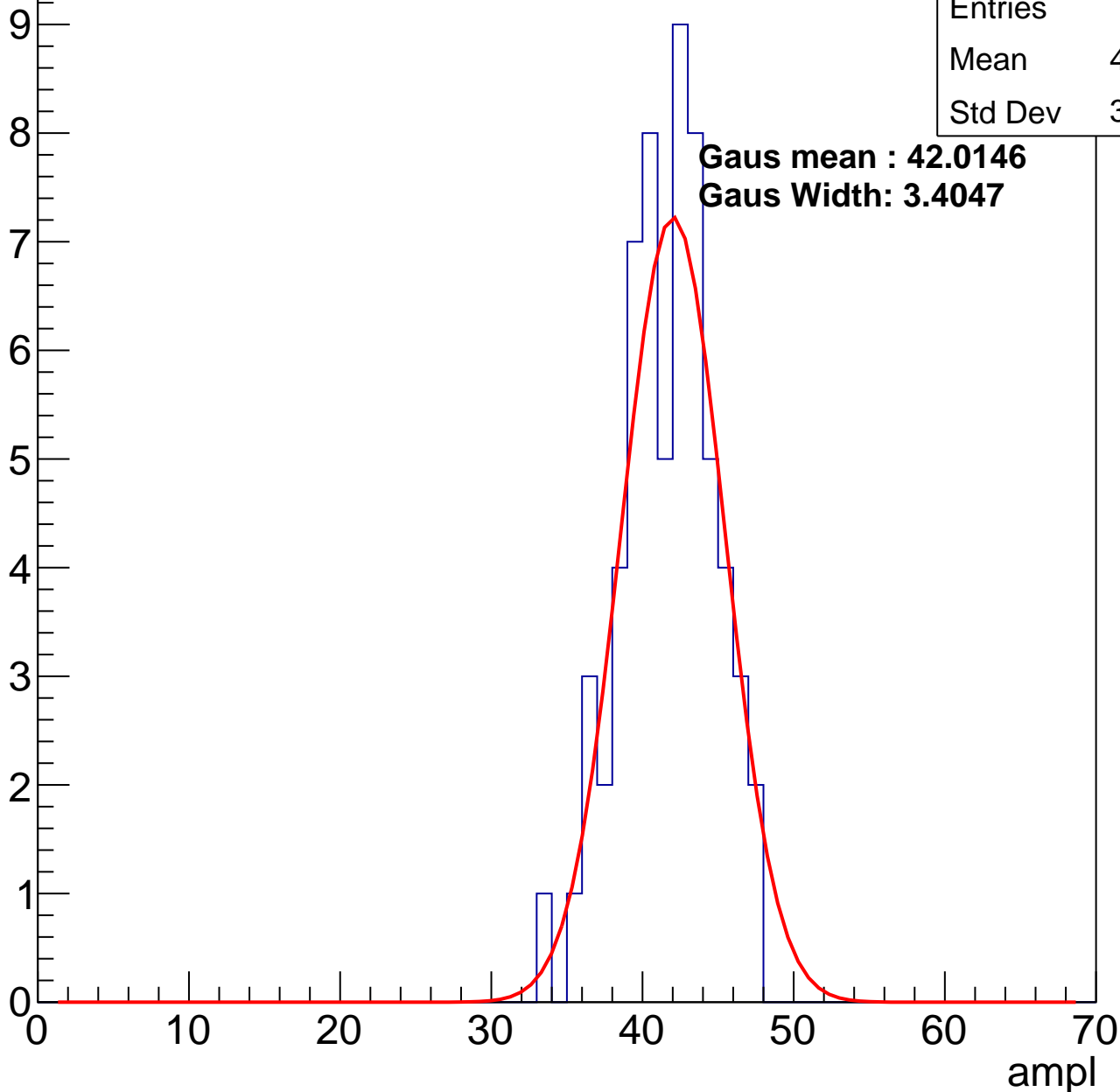
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	41.19
Std Dev	3.042

**Gaus mean : 42.0146**

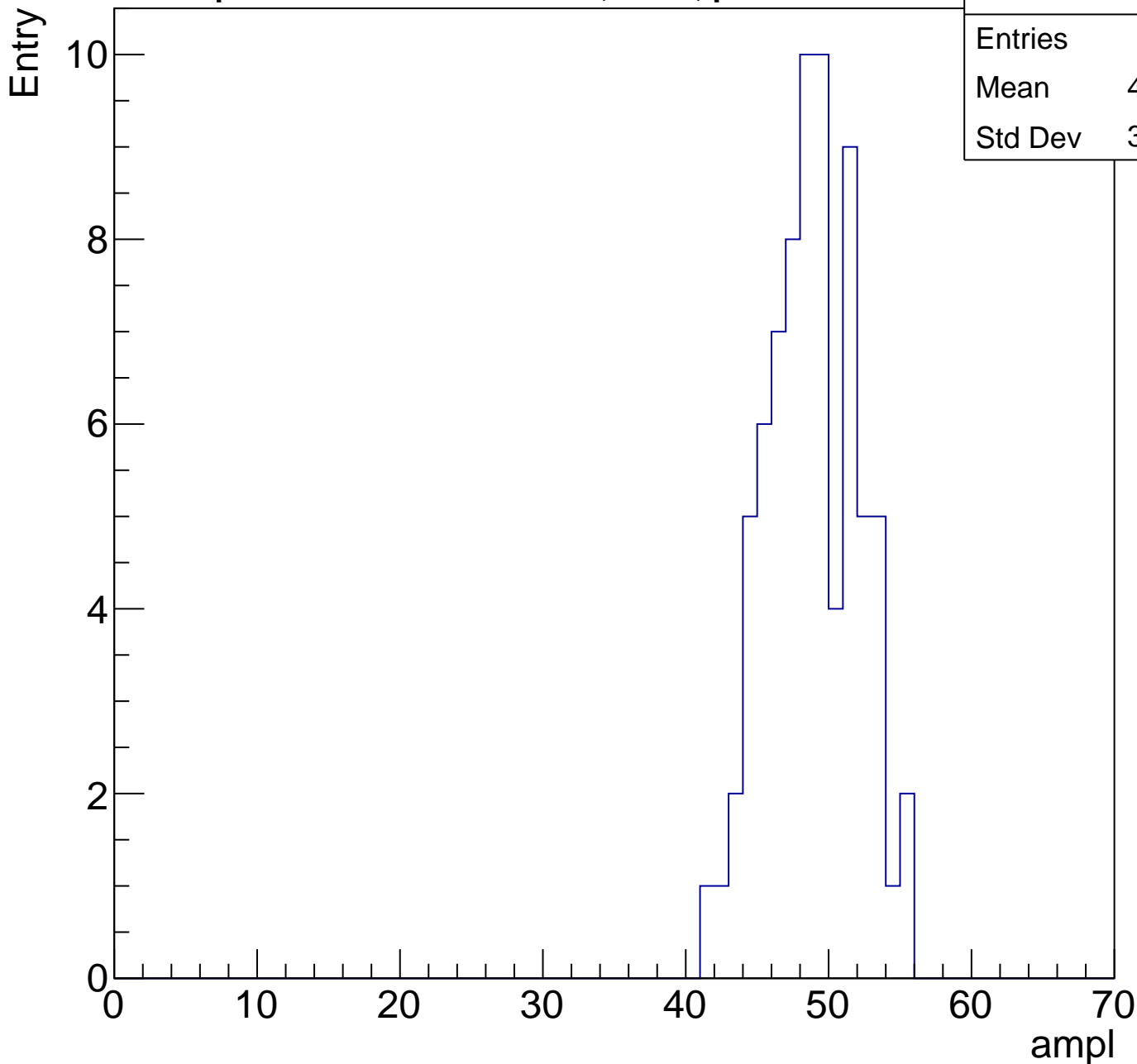
**Gaus Width: 3.4047**



# B1L000S, U25-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	76
Mean	48.36
Std Dev	3.123

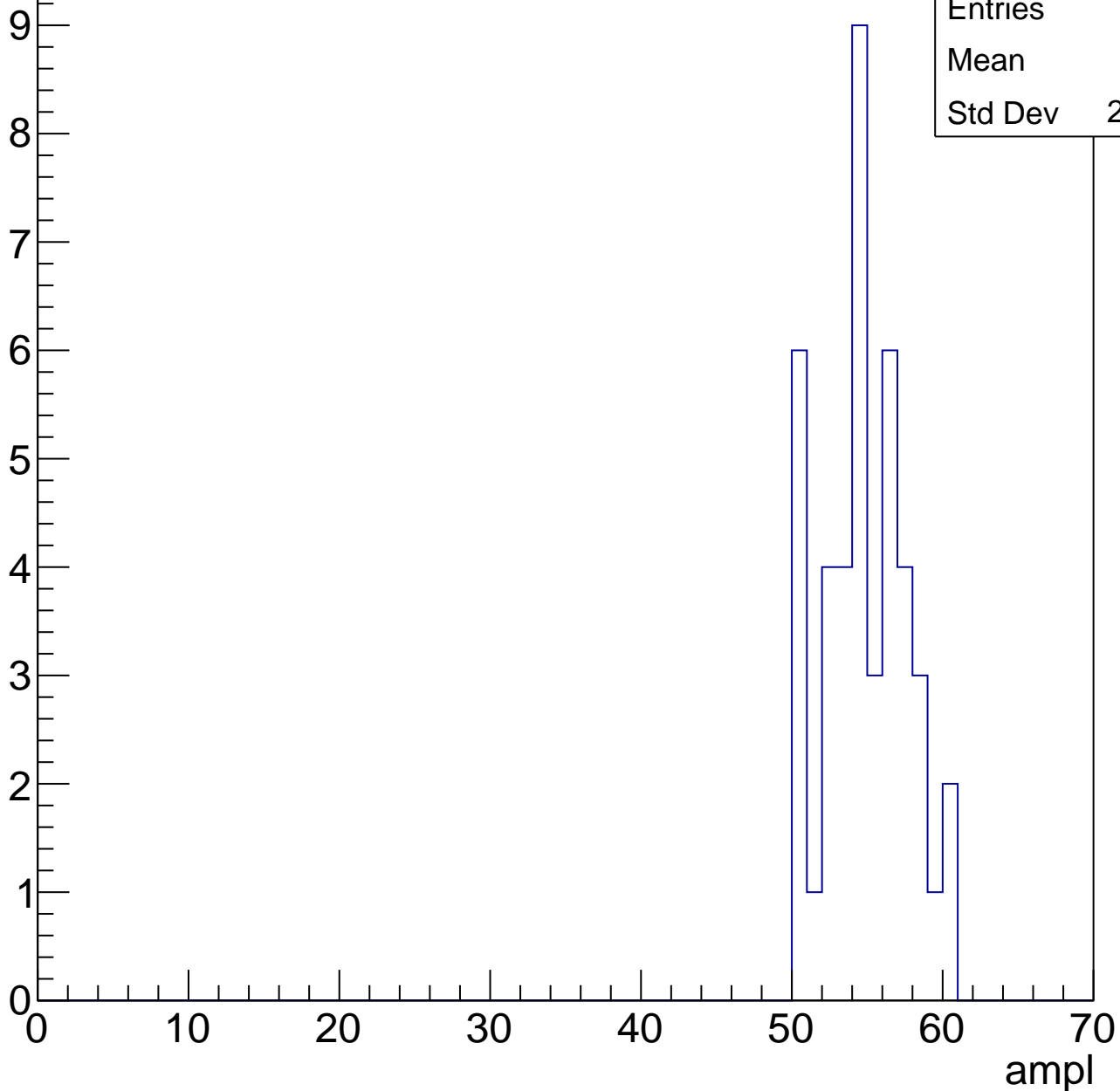


# B1L000S, U25-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

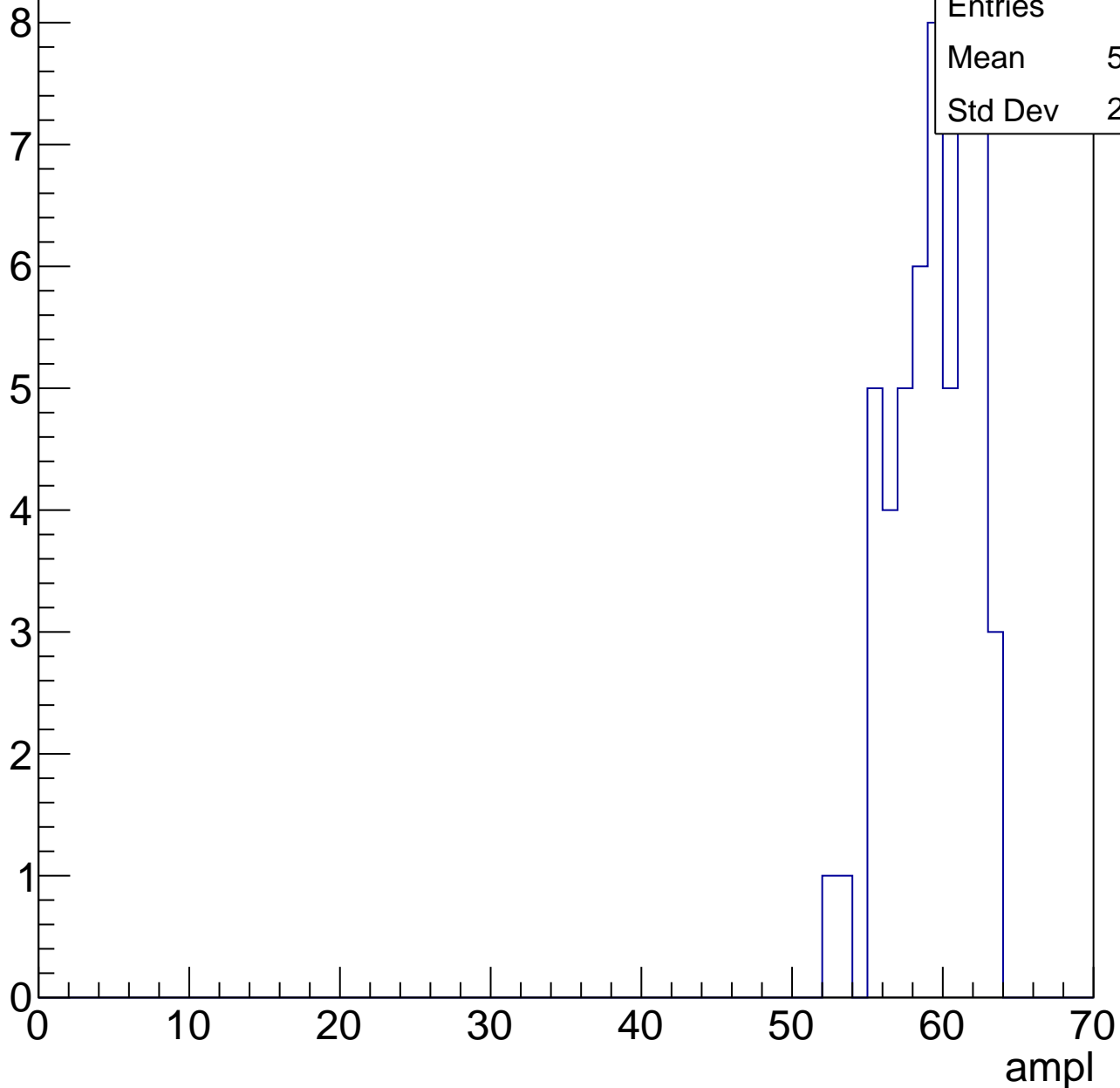
Entries	43
Mean	54.4
Std Dev	2.755



# B1L000S, U25-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

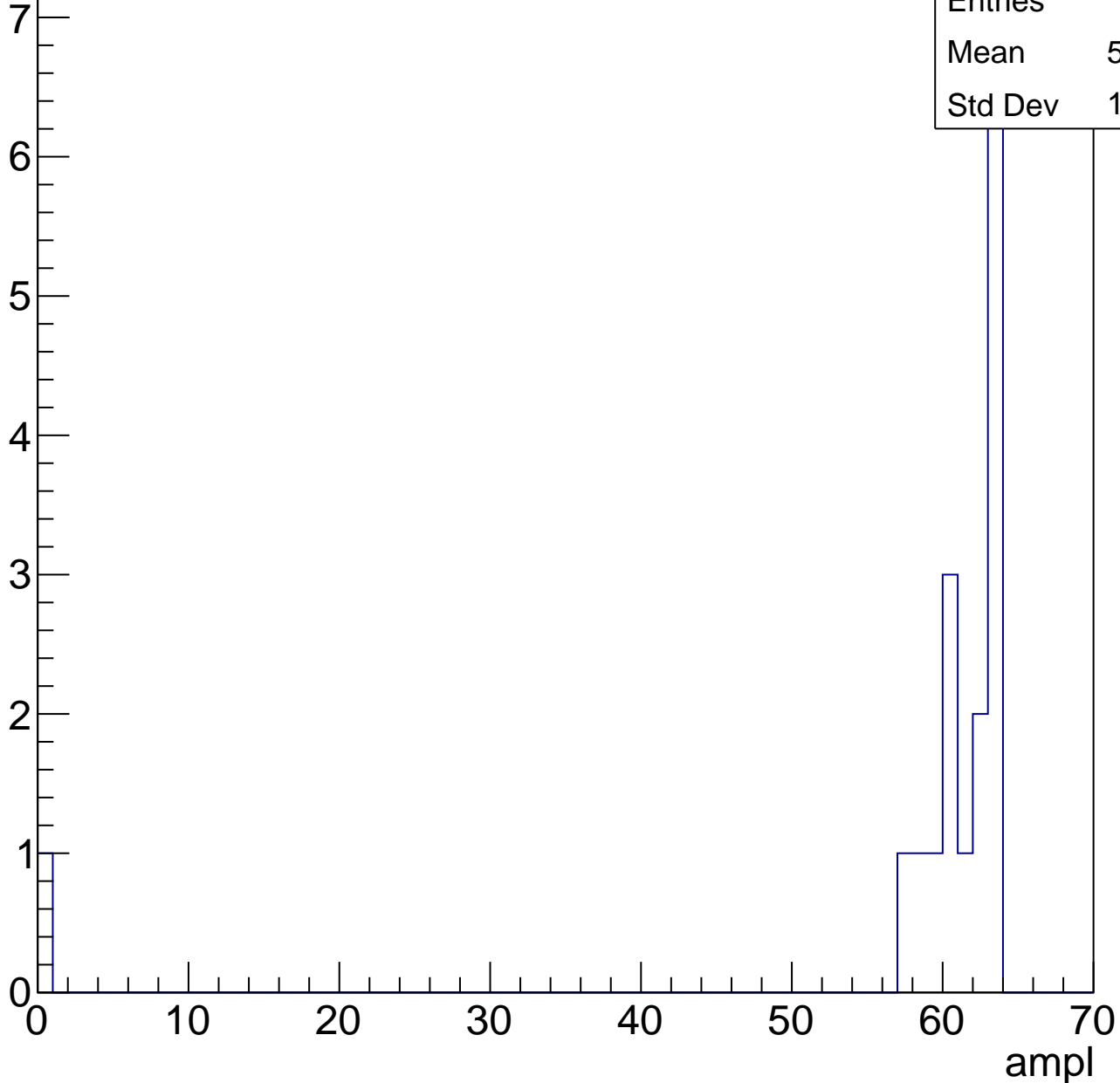


# B1L000S, U25-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	57.65
Std Dev	14.54

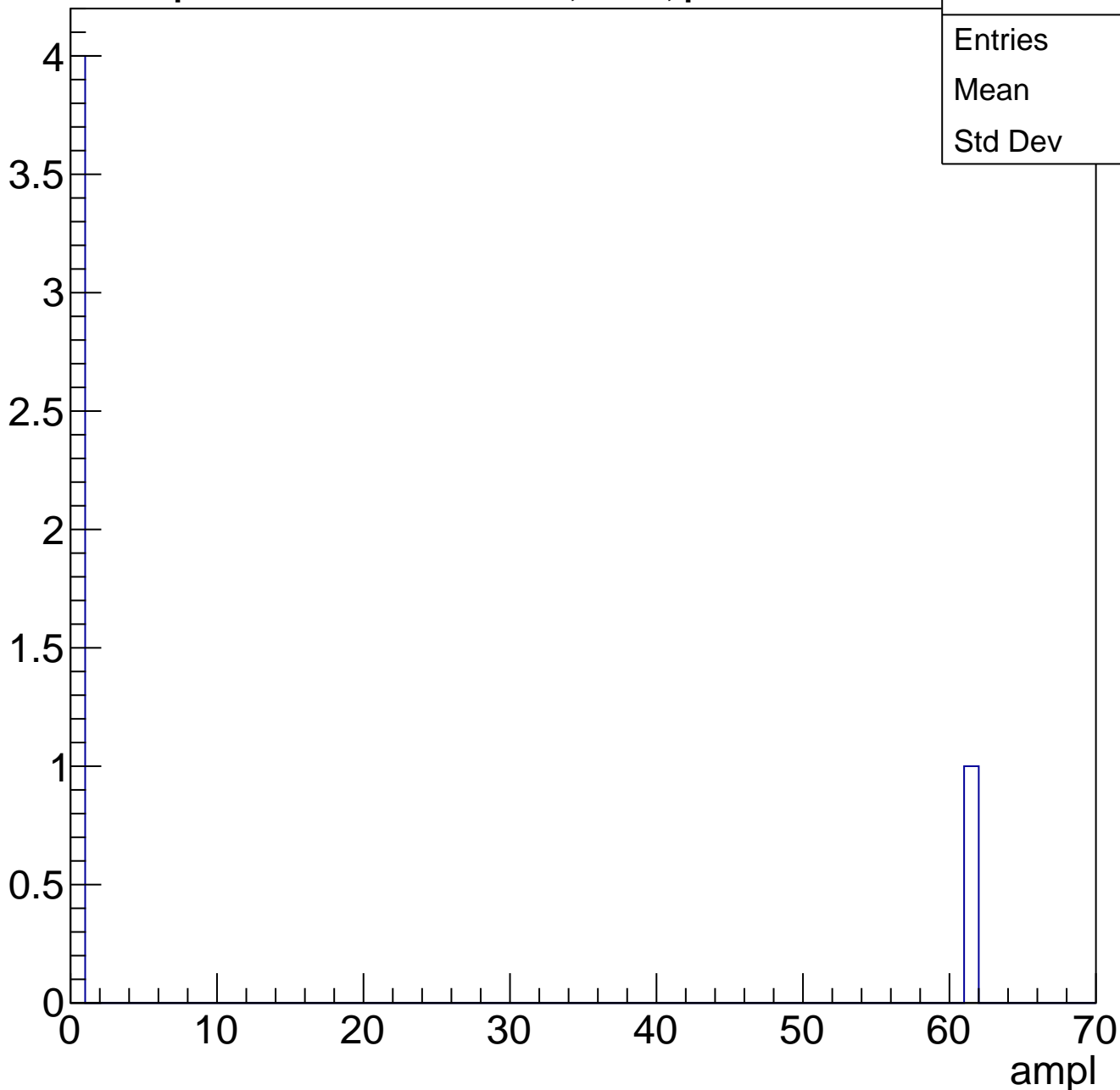




# B1L000S, U25-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	12.2
Std Dev	24.4

# B1L000S, U25-ch61, adc0

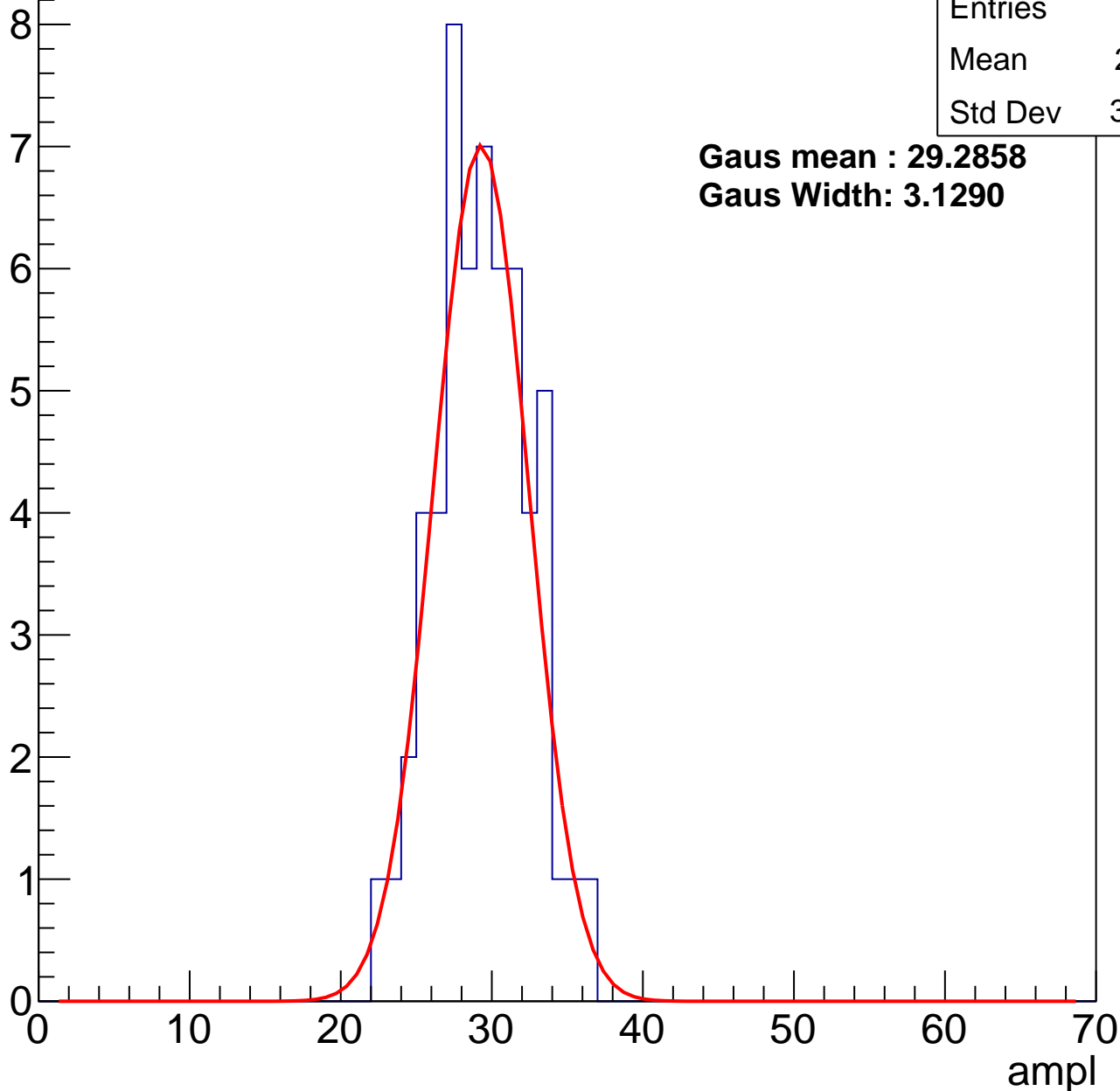
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.91
Std Dev	3.045

**Gaus mean : 29.2858**

**Gaus Width: 3.1290**



# B1L000S, U25-ch61, adc1

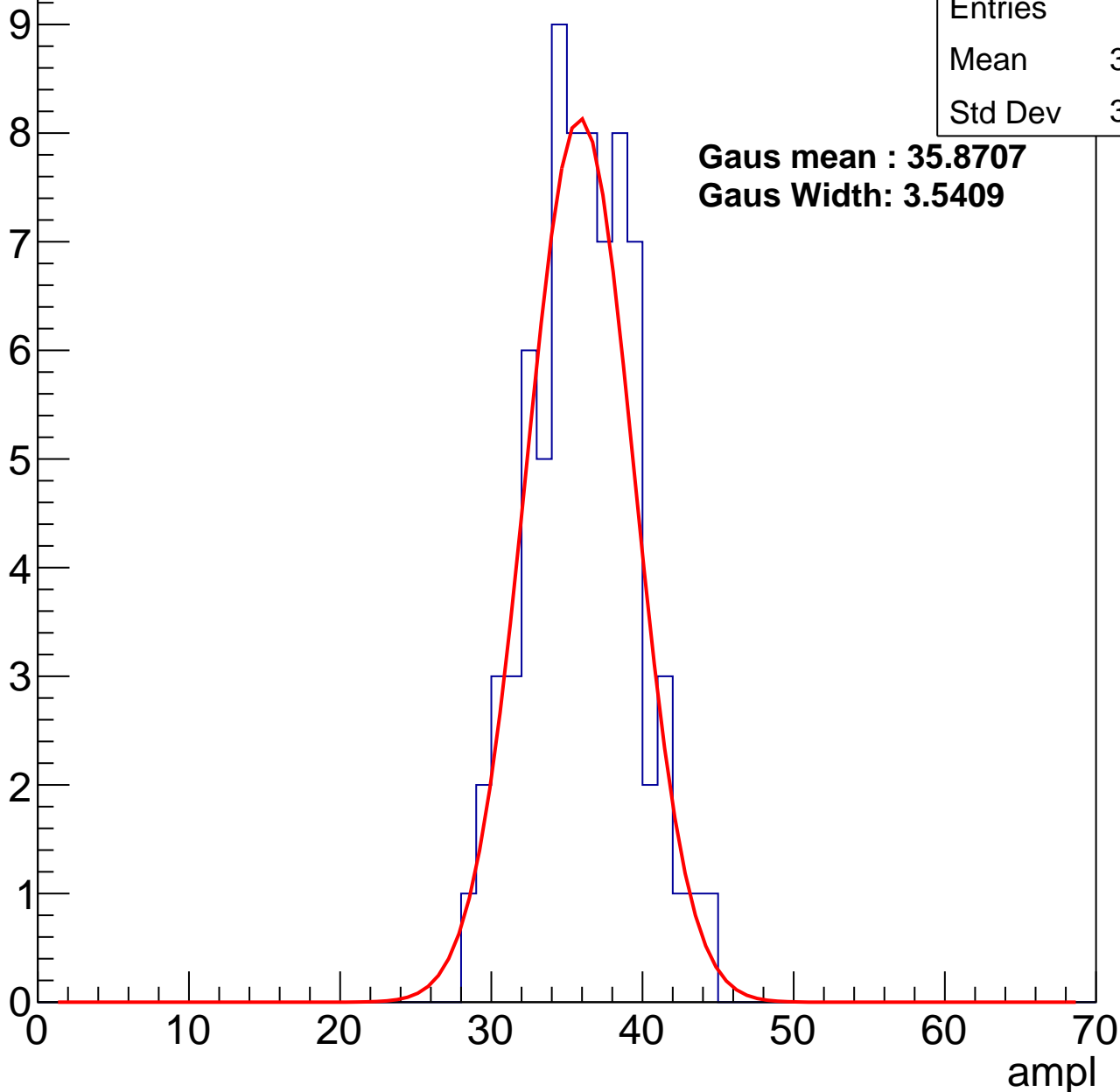
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	35.57
Std Dev	3.414

**Gaus mean : 35.8707**

**Gaus Width: 3.5409**



# B1L000S, U25-ch61, adc2

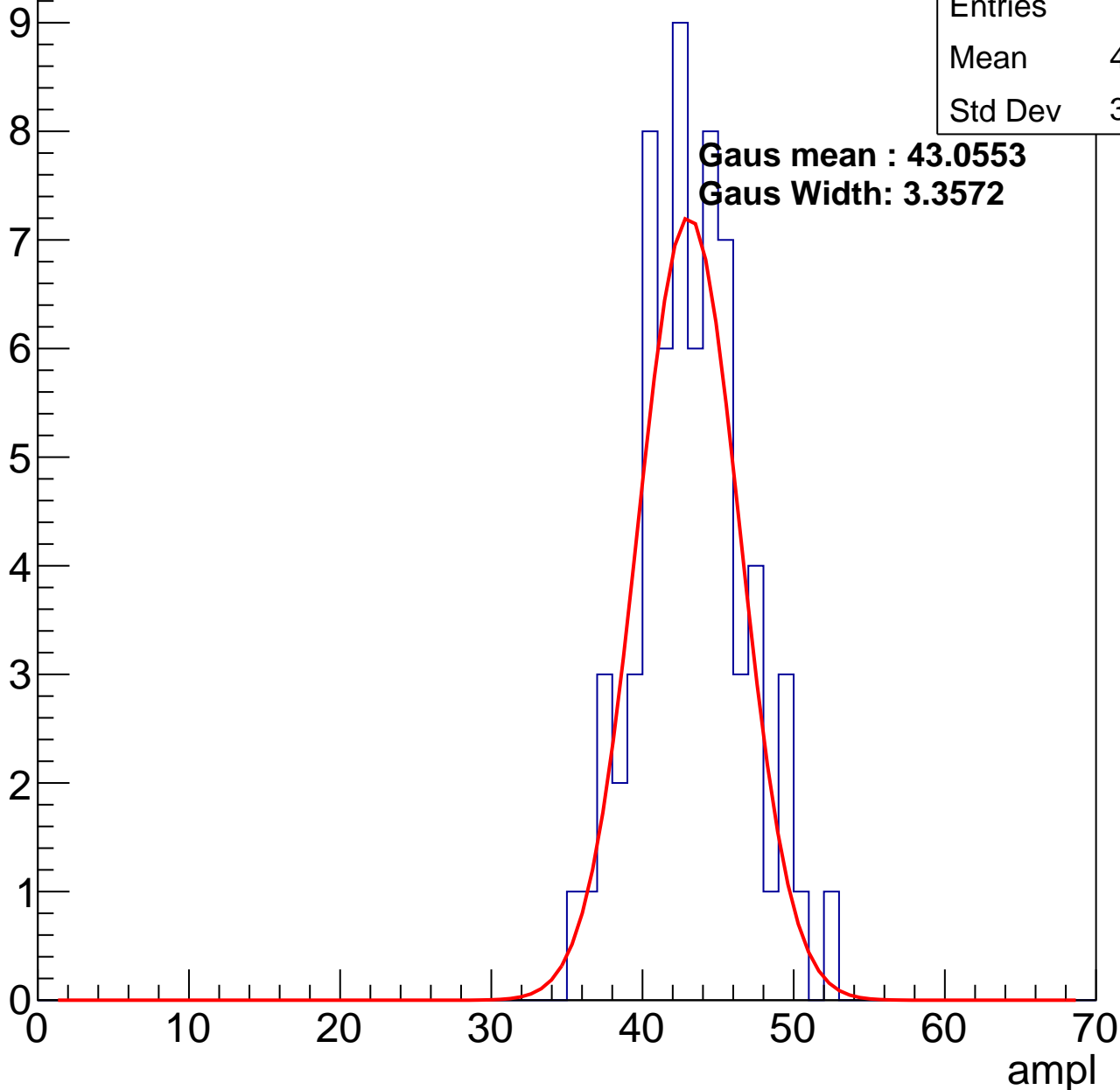
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	42.79
Std Dev	3.479

**Gaus mean : 43.0553**

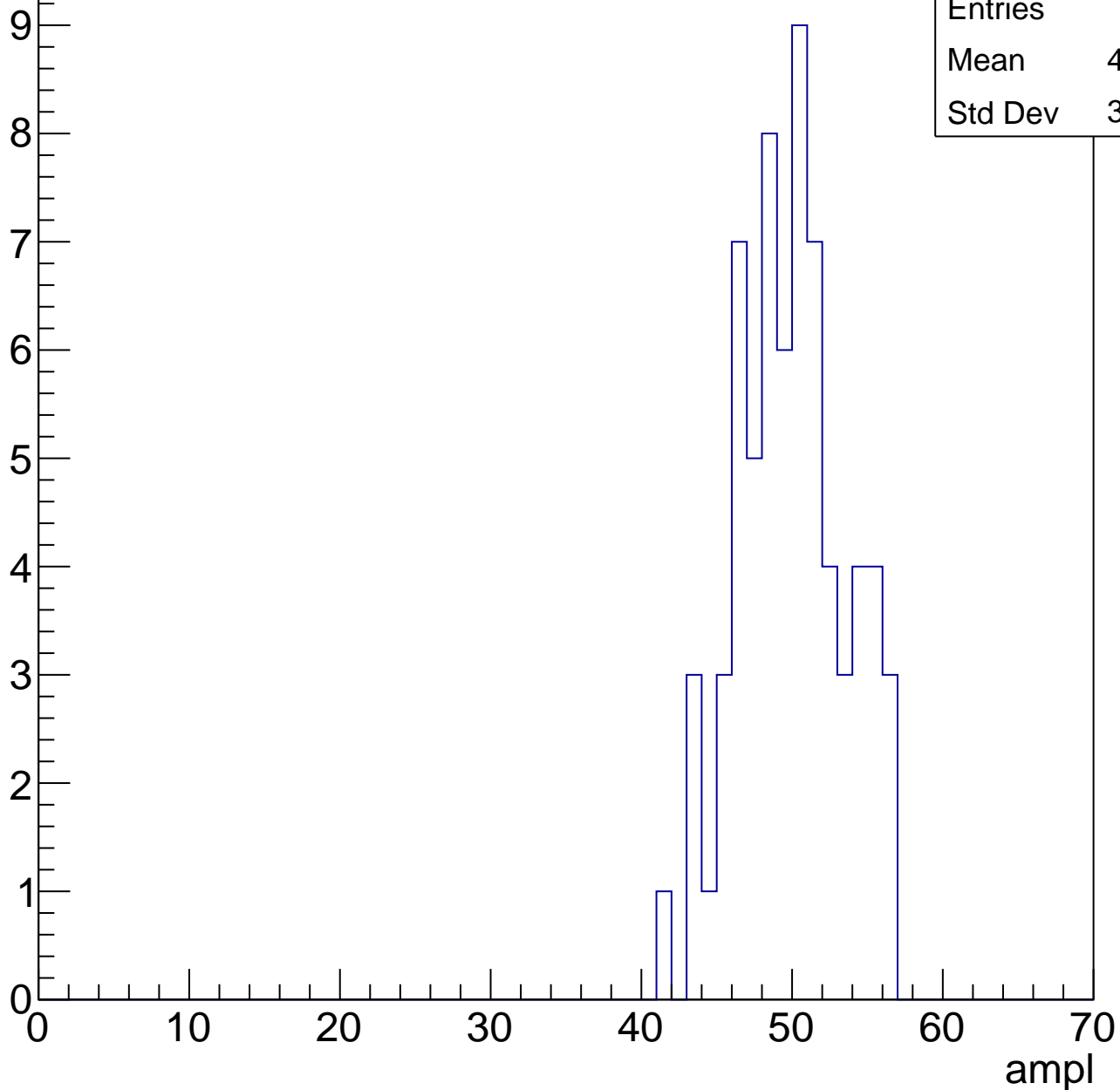
**Gaus Width: 3.3572**



# B1L000S, U25-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

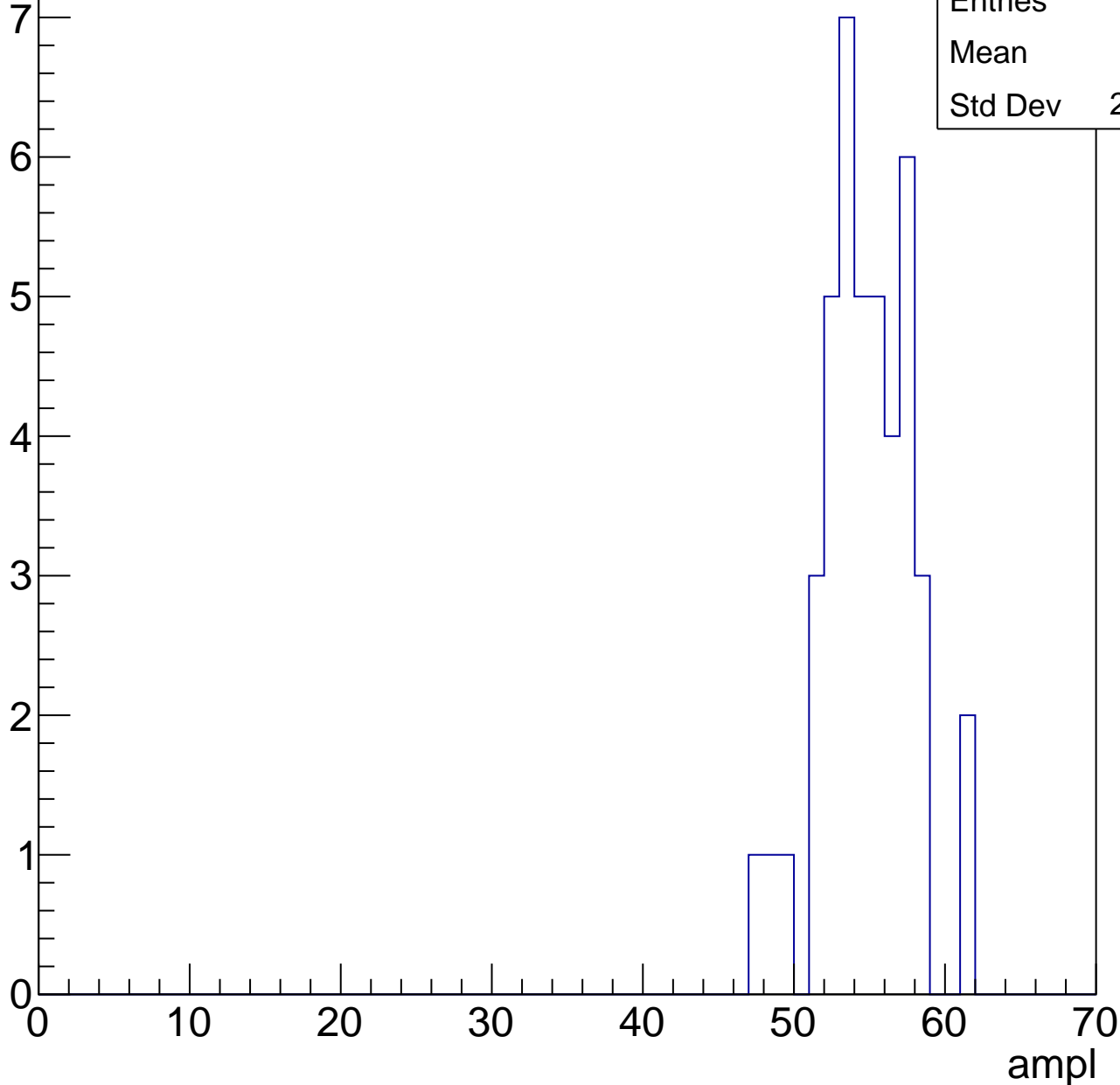


# B1L000S, U25-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	54.3
Std Dev	2.977

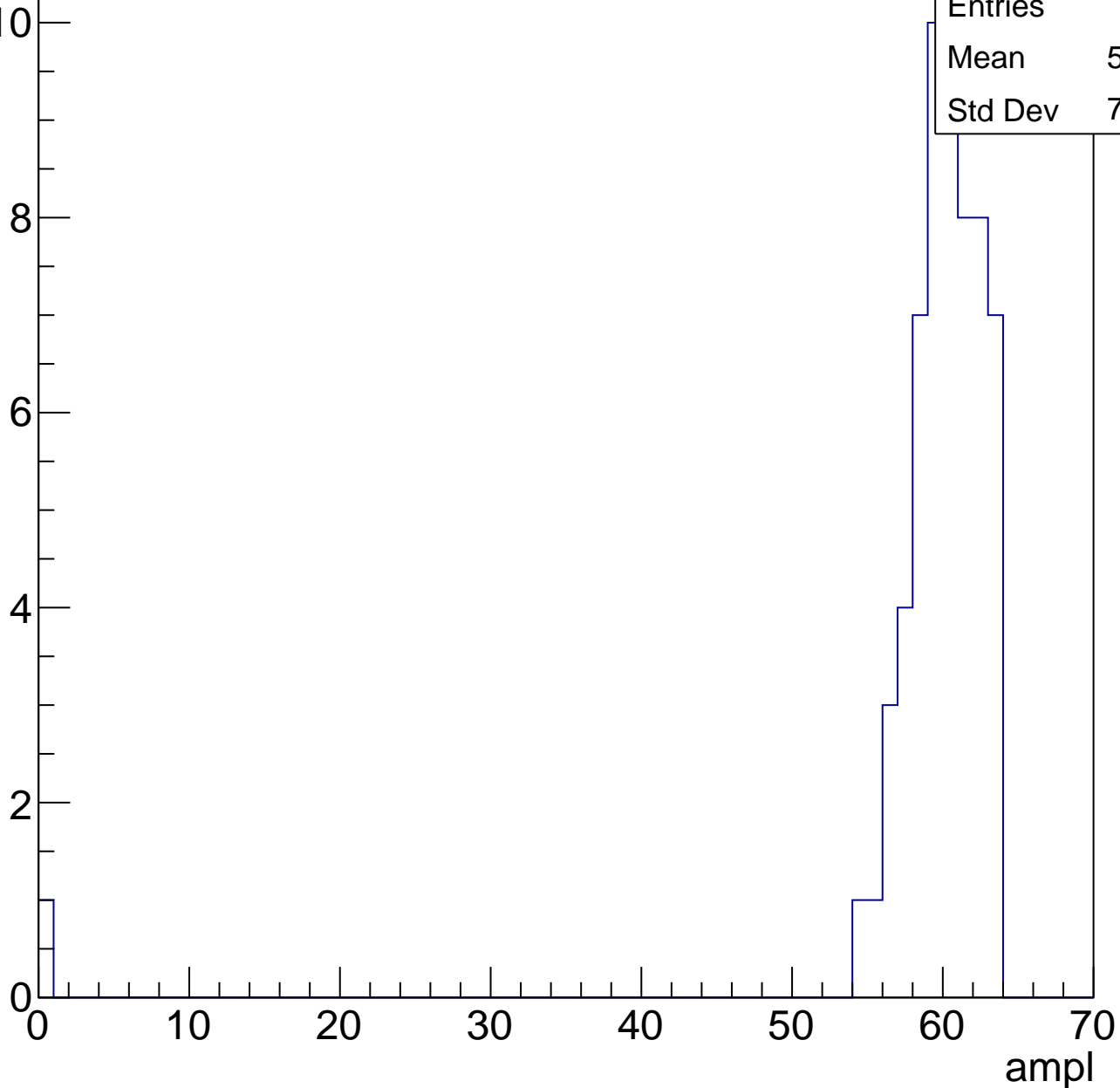


# B1L000S, U25-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.77
Std Dev	7.953



# B1L000S, U25-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch62, adc0

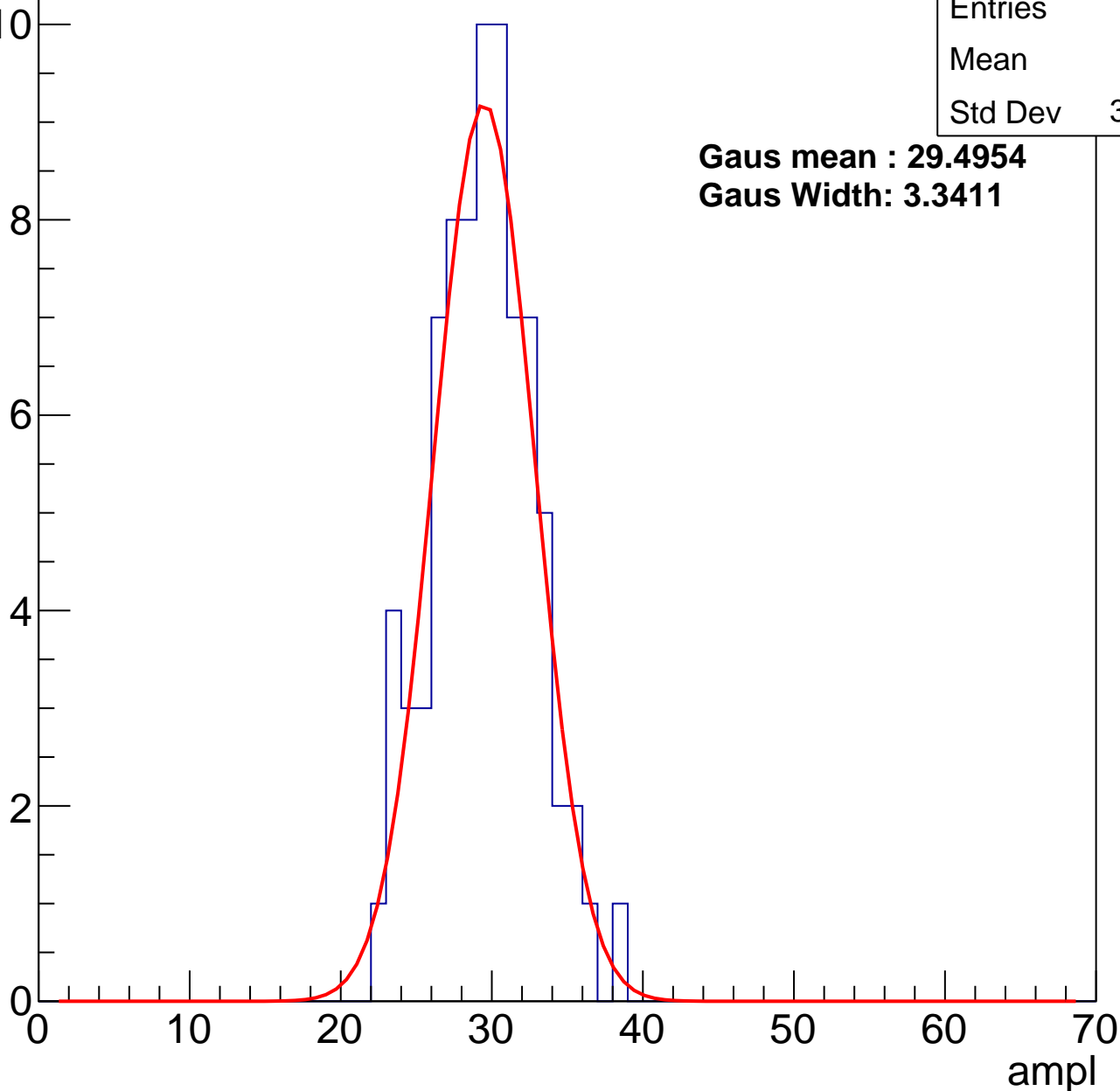
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	29
Std Dev	3.284

**Gaus mean : 29.4954**

**Gaus Width: 3.3411**



# B1L000S, U25-ch62, adc1

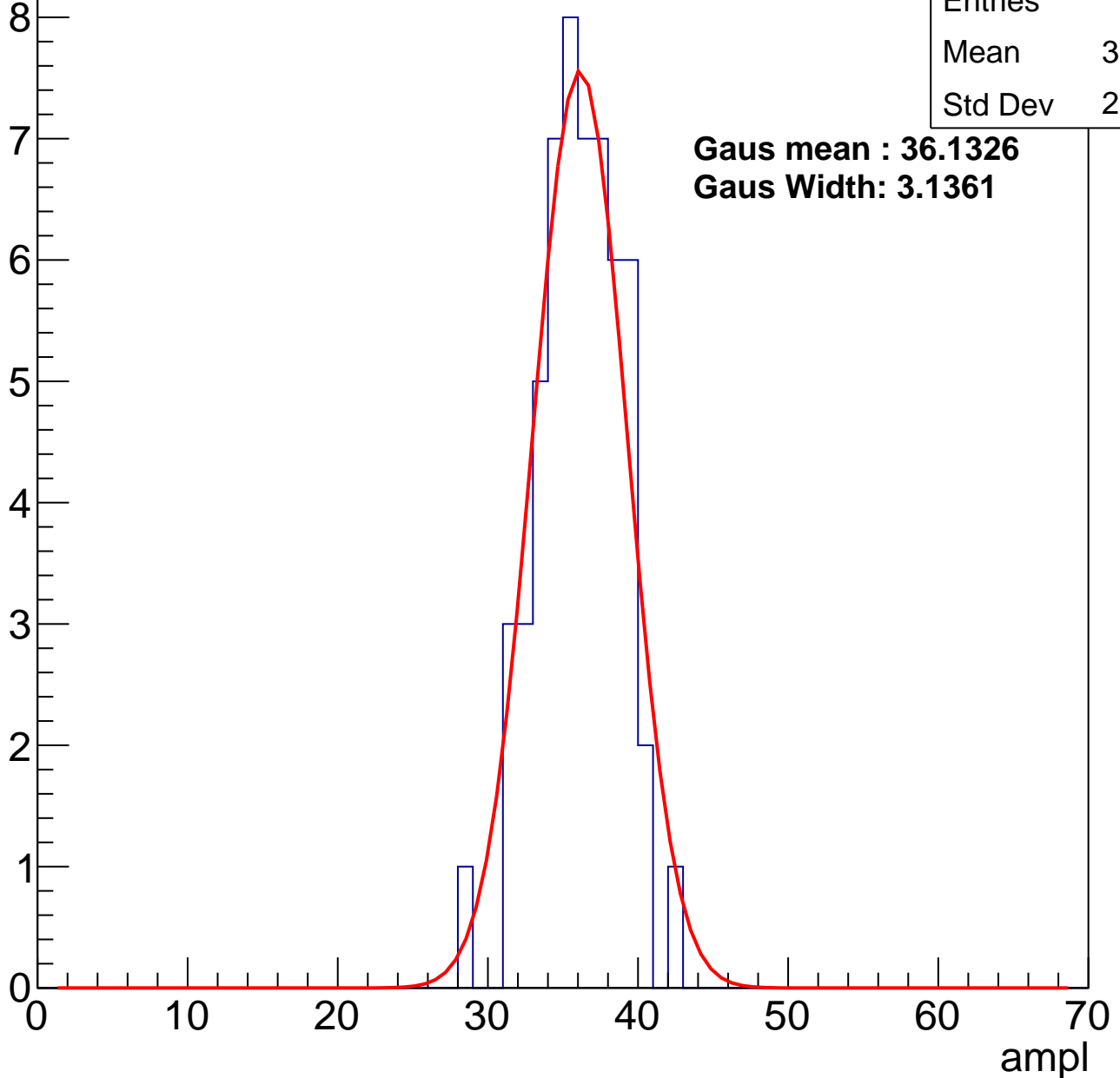
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.62
Std Dev	2.716

**Gaus mean : 36.1326**

**Gaus Width: 3.1361**



# B1L000S, U25-ch62, adc2

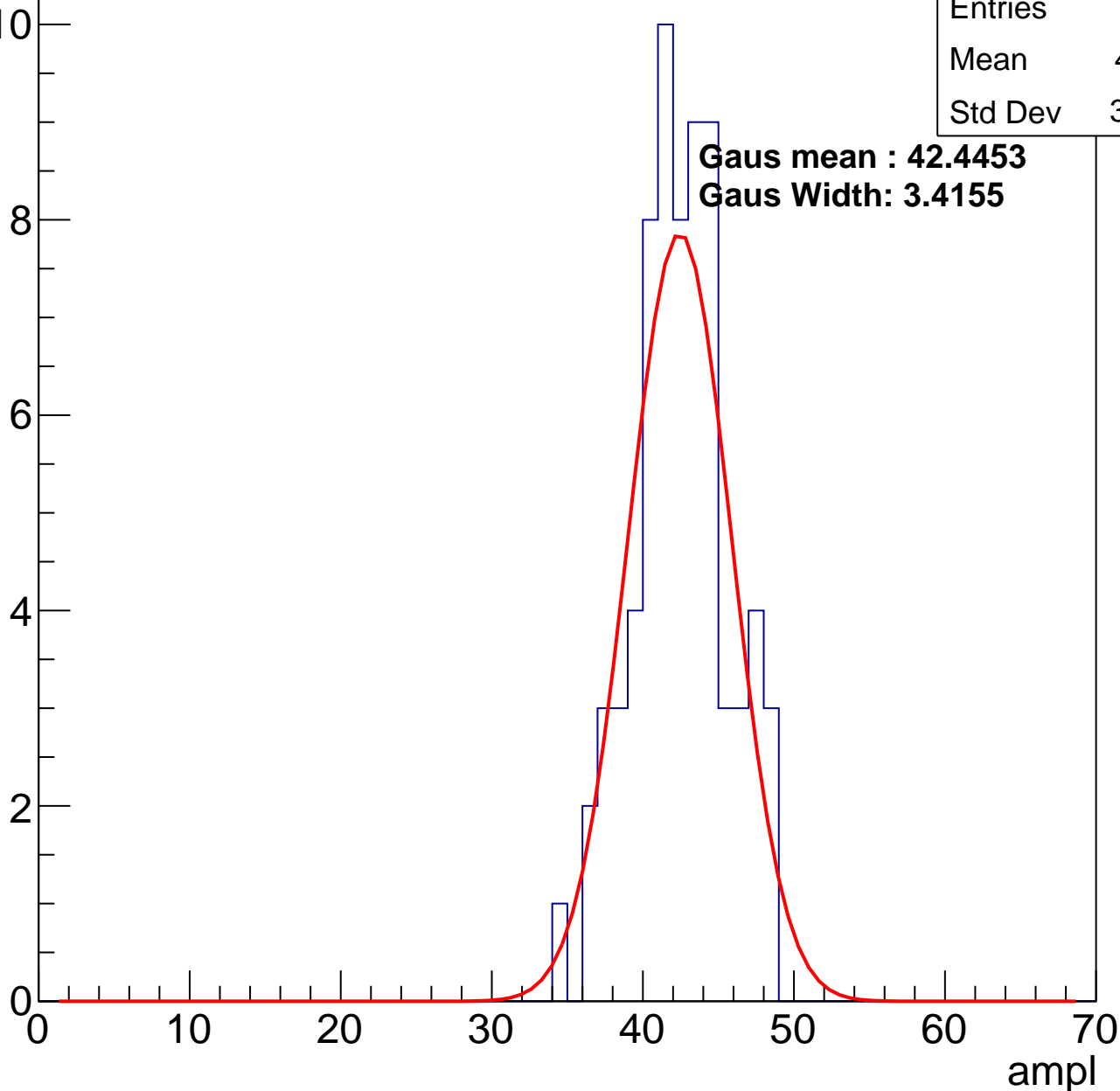
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.01
Std Dev	3.082

**Gaus mean : 42.4453**

**Gaus Width: 3.4155**

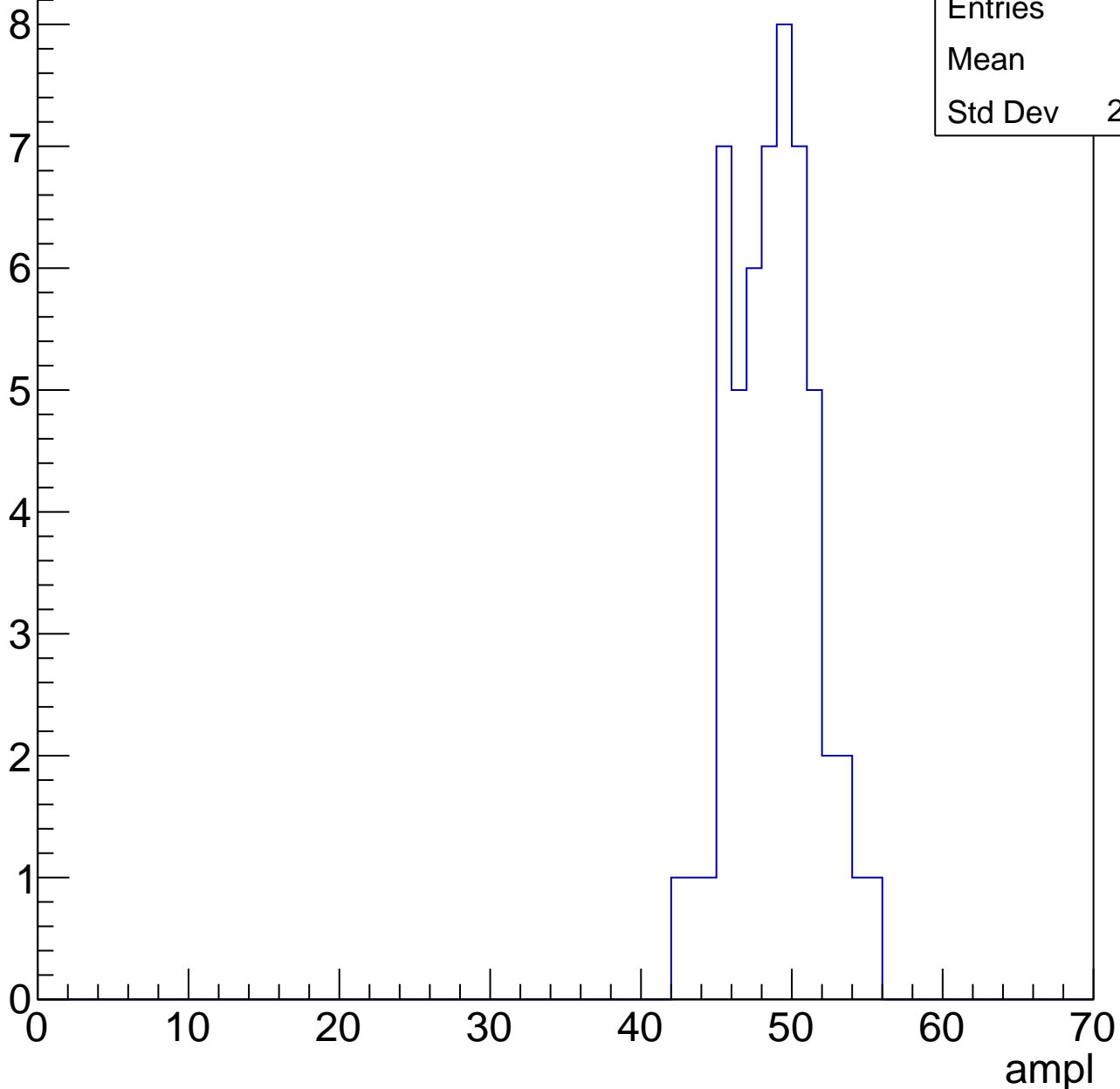


# B1L000S, U25-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

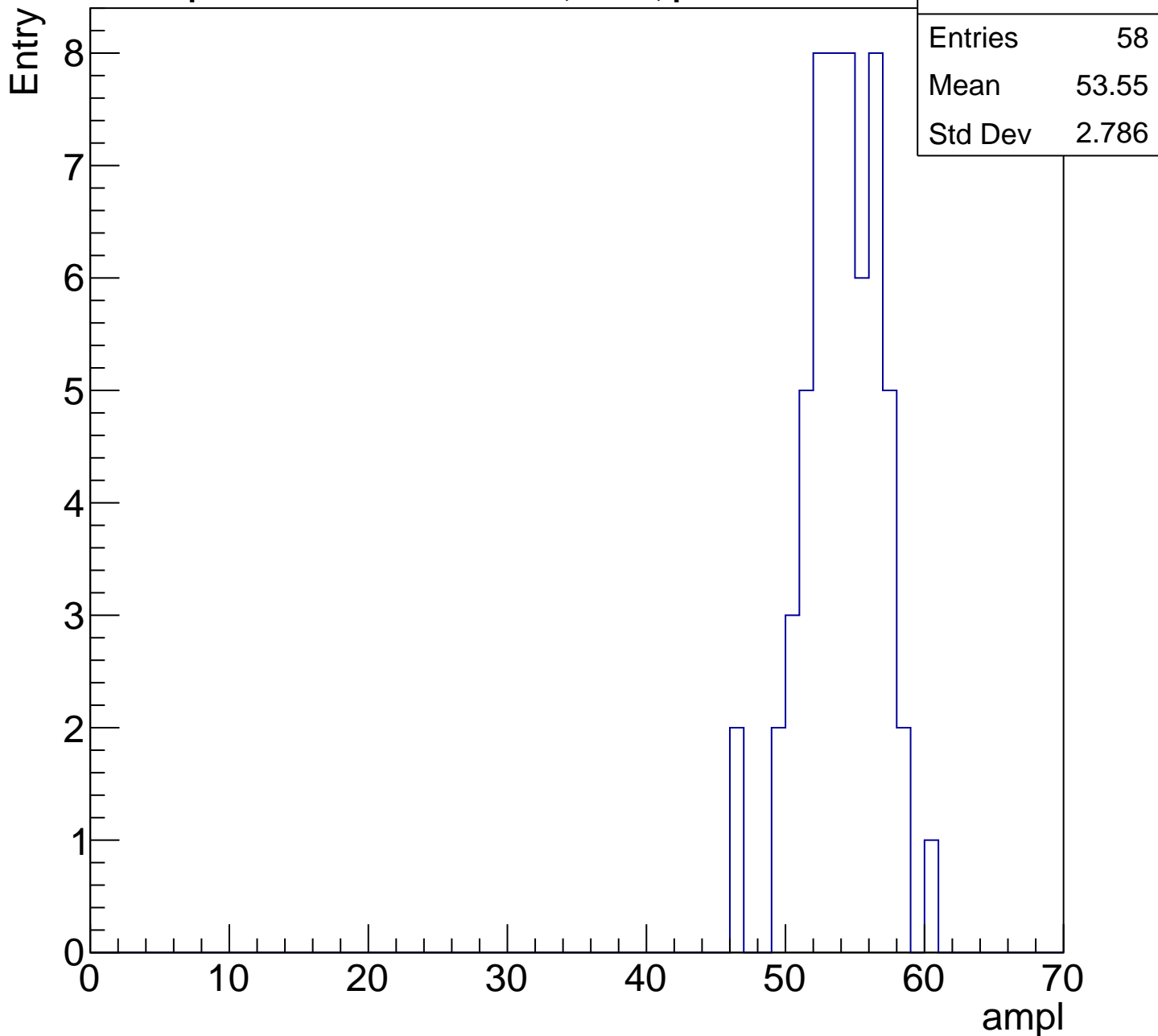
Entry

Entries	54
Mean	48.3
Std Dev	2.753



# B1L000S, U25-ch62, adc4

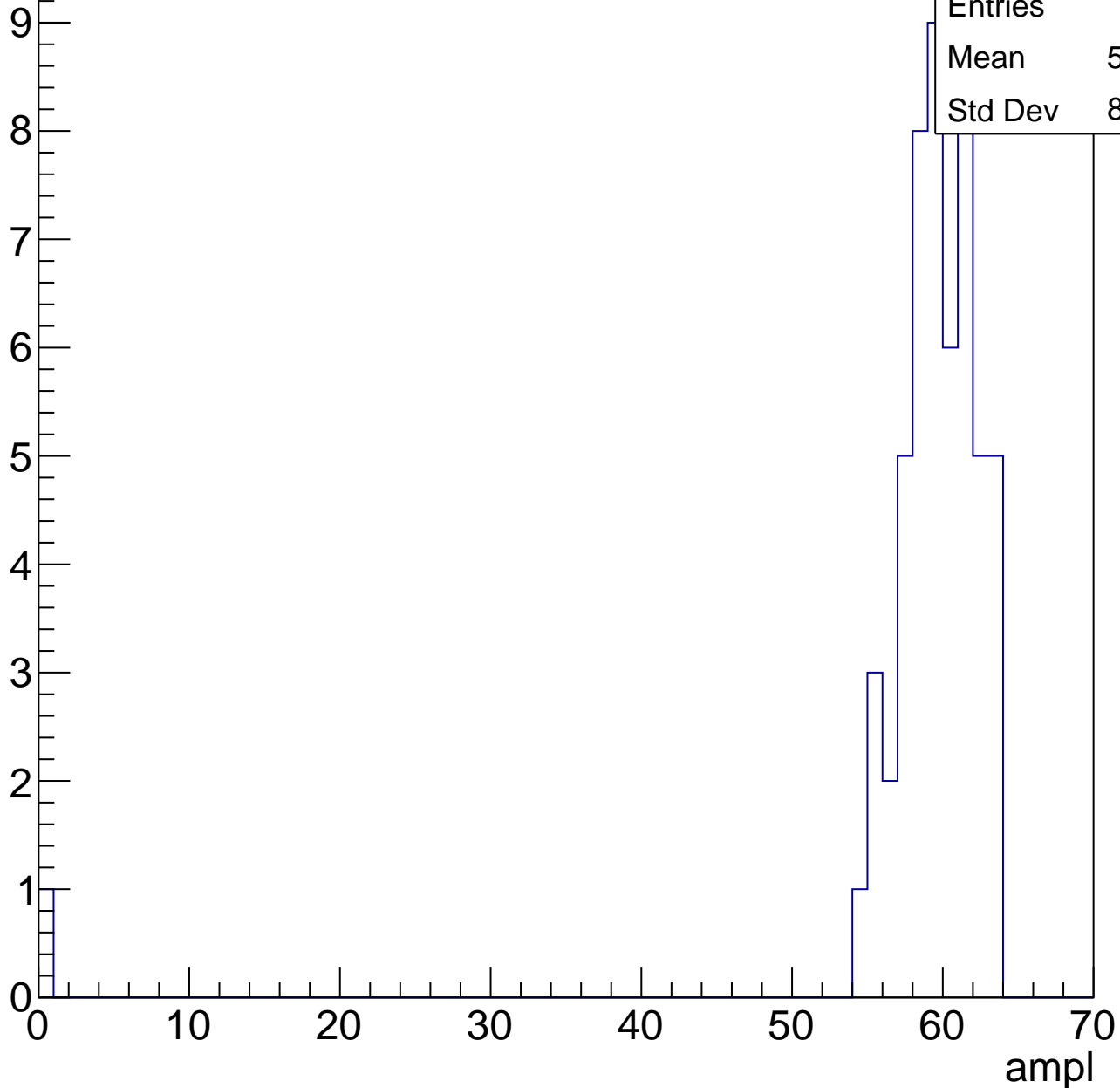
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

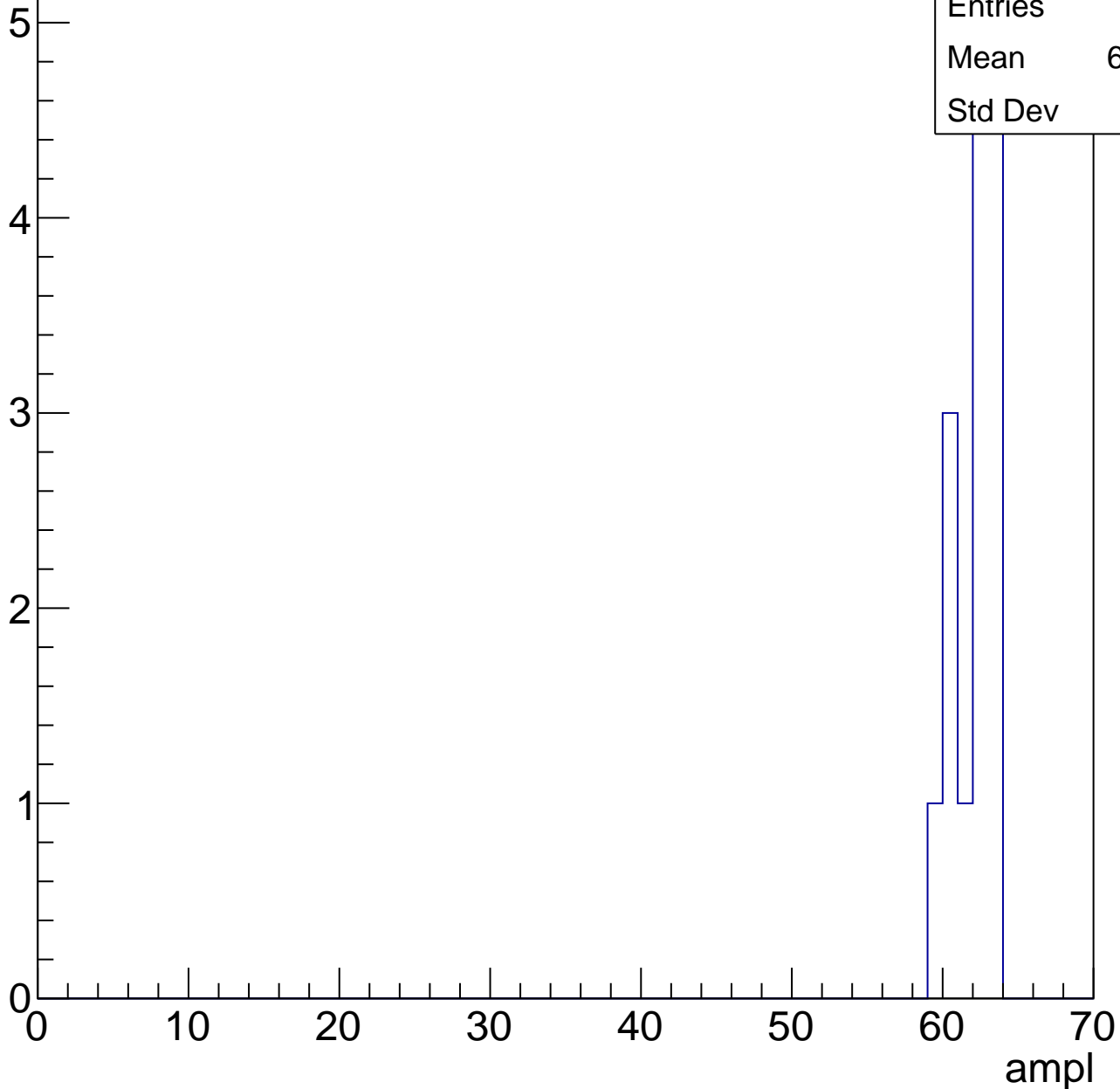


# B1L000S, U25-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.67
Std Dev	1.3





# B1L000S, U25-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L000S, U25-ch63, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	83
Mean	29.48
Std Dev	6.465

**Gaus mean : 31.2391**

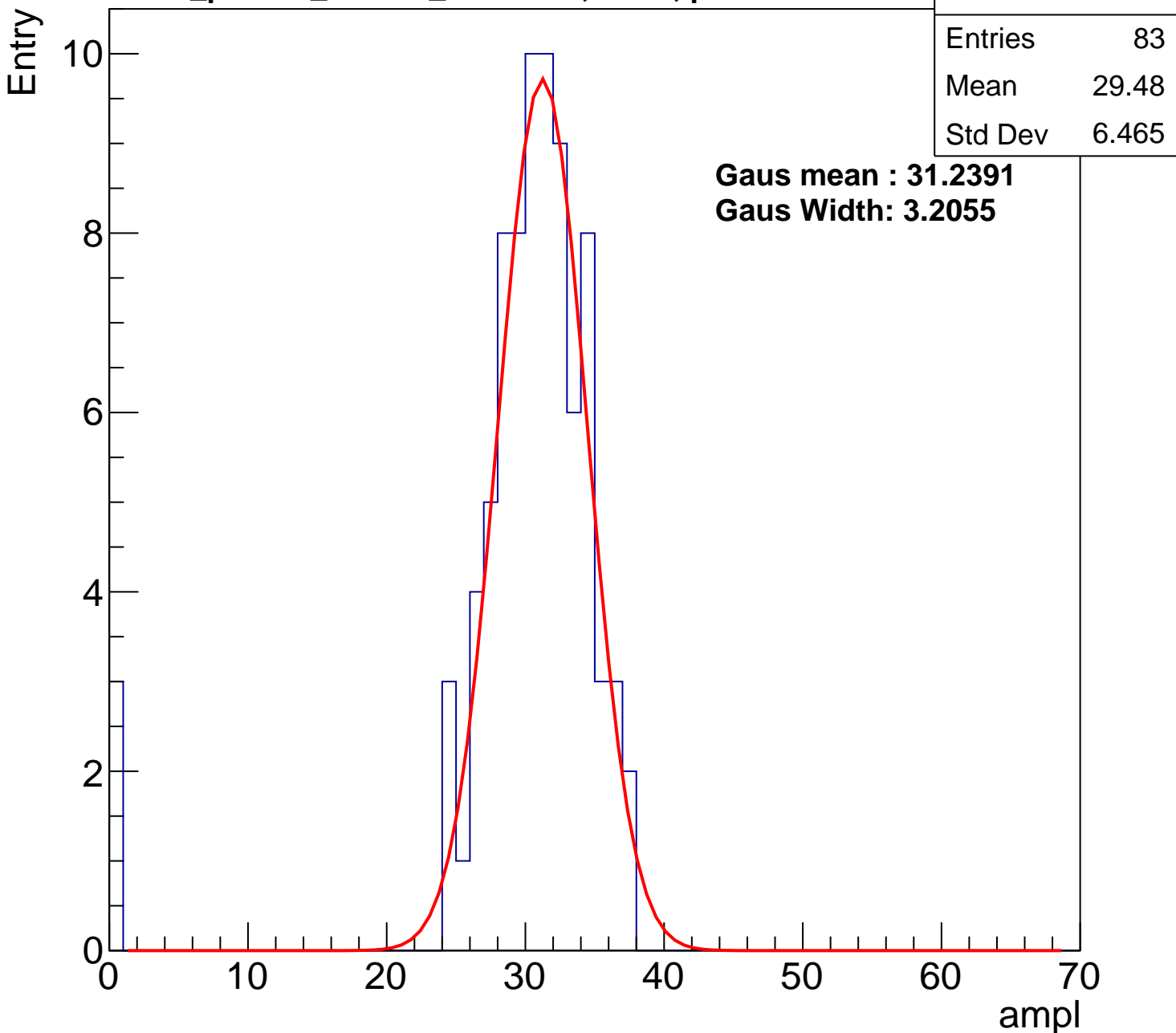
**Gaus Width: 3.2055**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch63, adc1

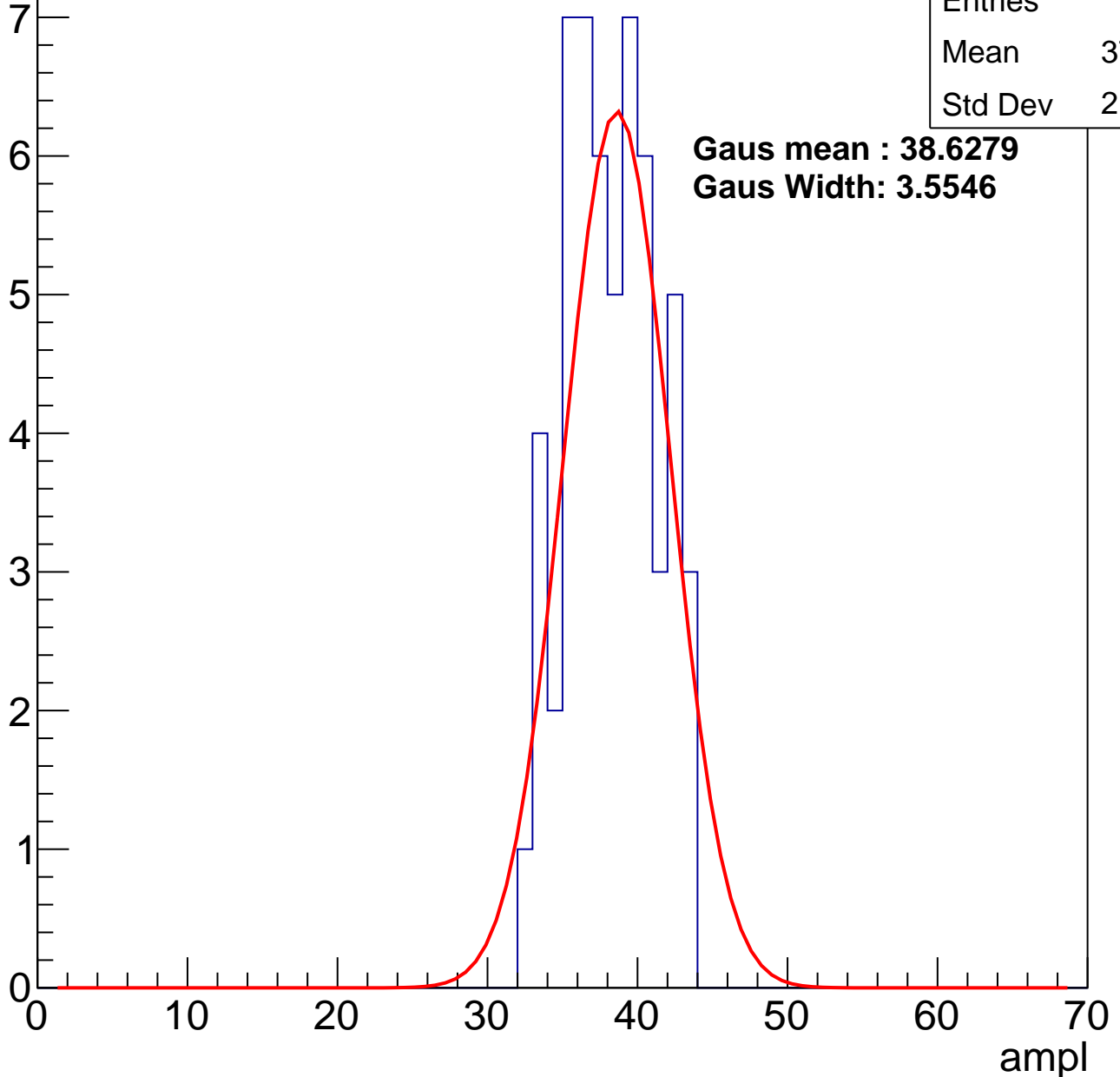
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	37.79
Std Dev	2.914

**Gaus mean : 38.6279**

**Gaus Width: 3.5546**



# B1L000S, U25-ch63, adc2

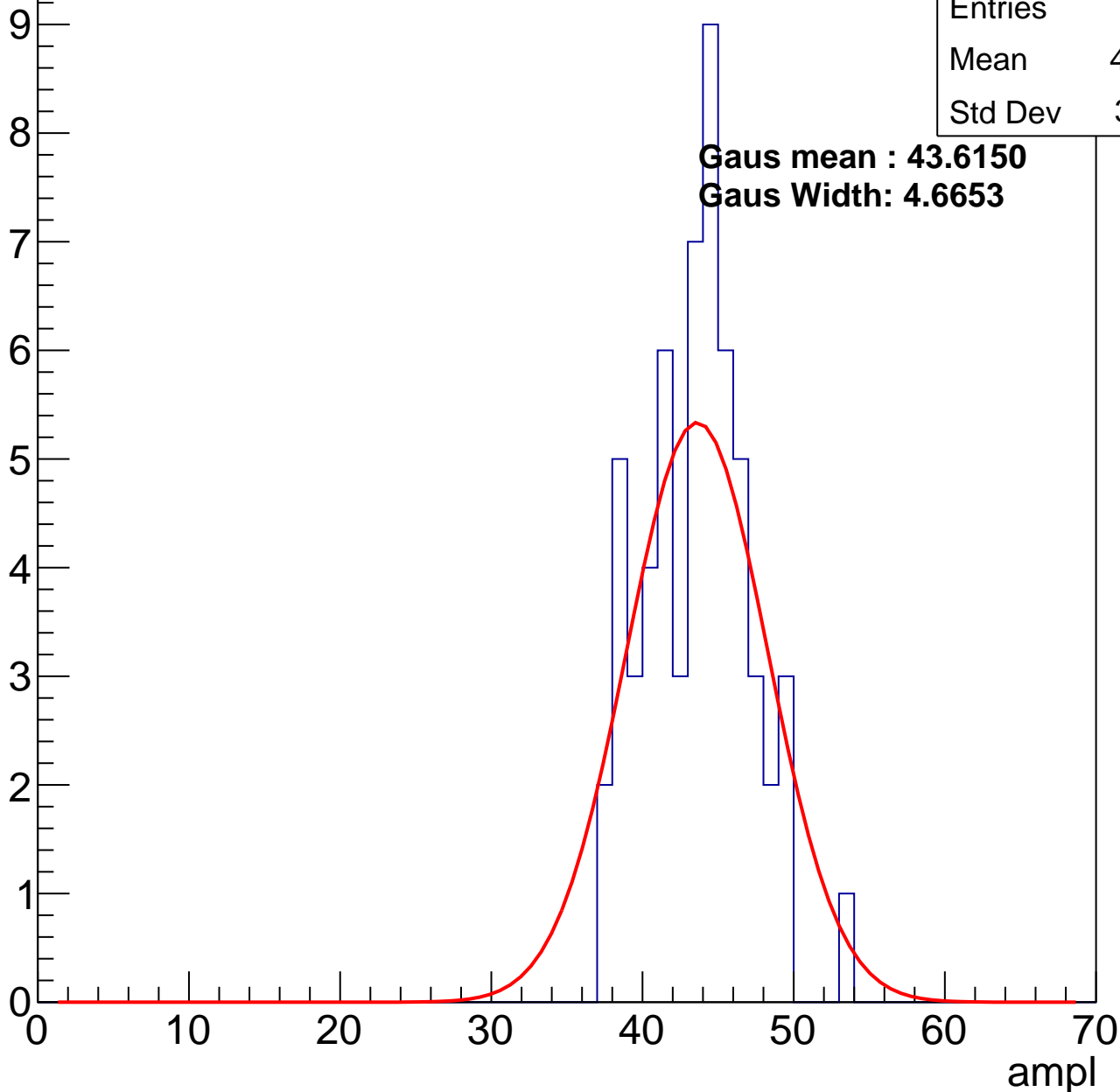
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	43.17
Std Dev	3.421

**Gaus mean : 43.6150**

**Gaus Width: 4.6653**



# B1L000S, U25-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

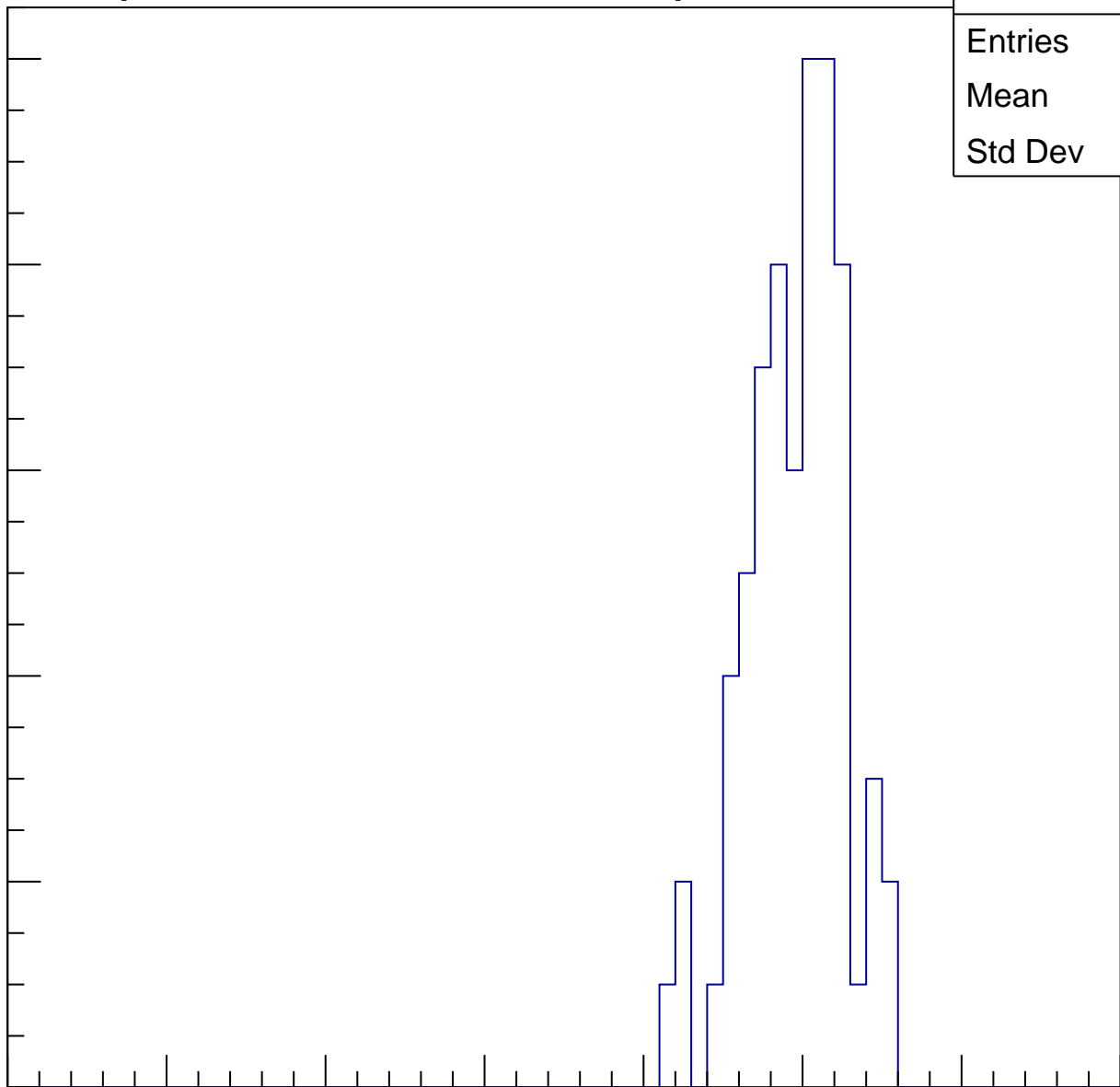
Entries	68
Mean	49.07
Std Dev	3.011

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

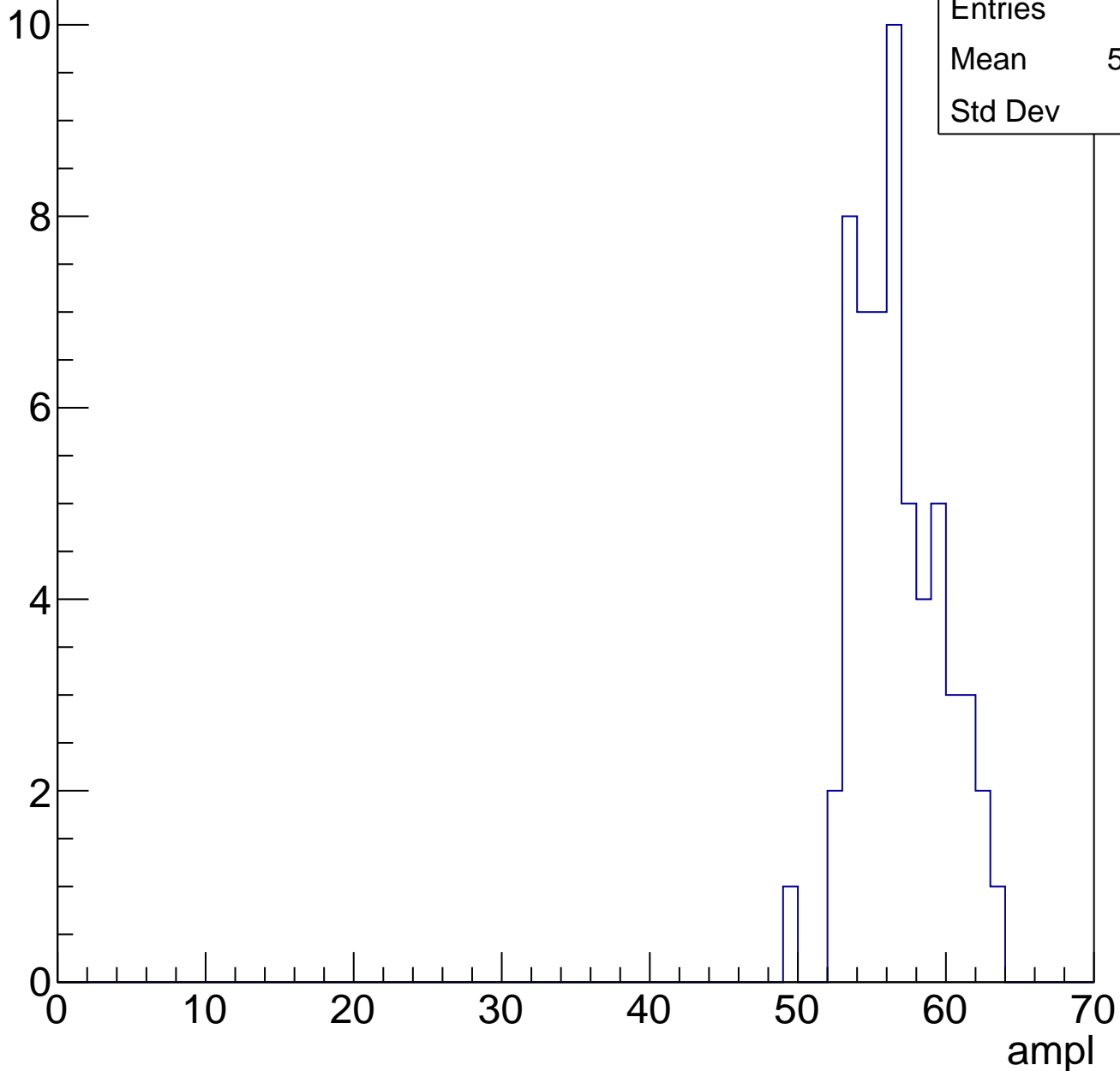


# B1L000S, U25-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	56.24
Std Dev	2.92

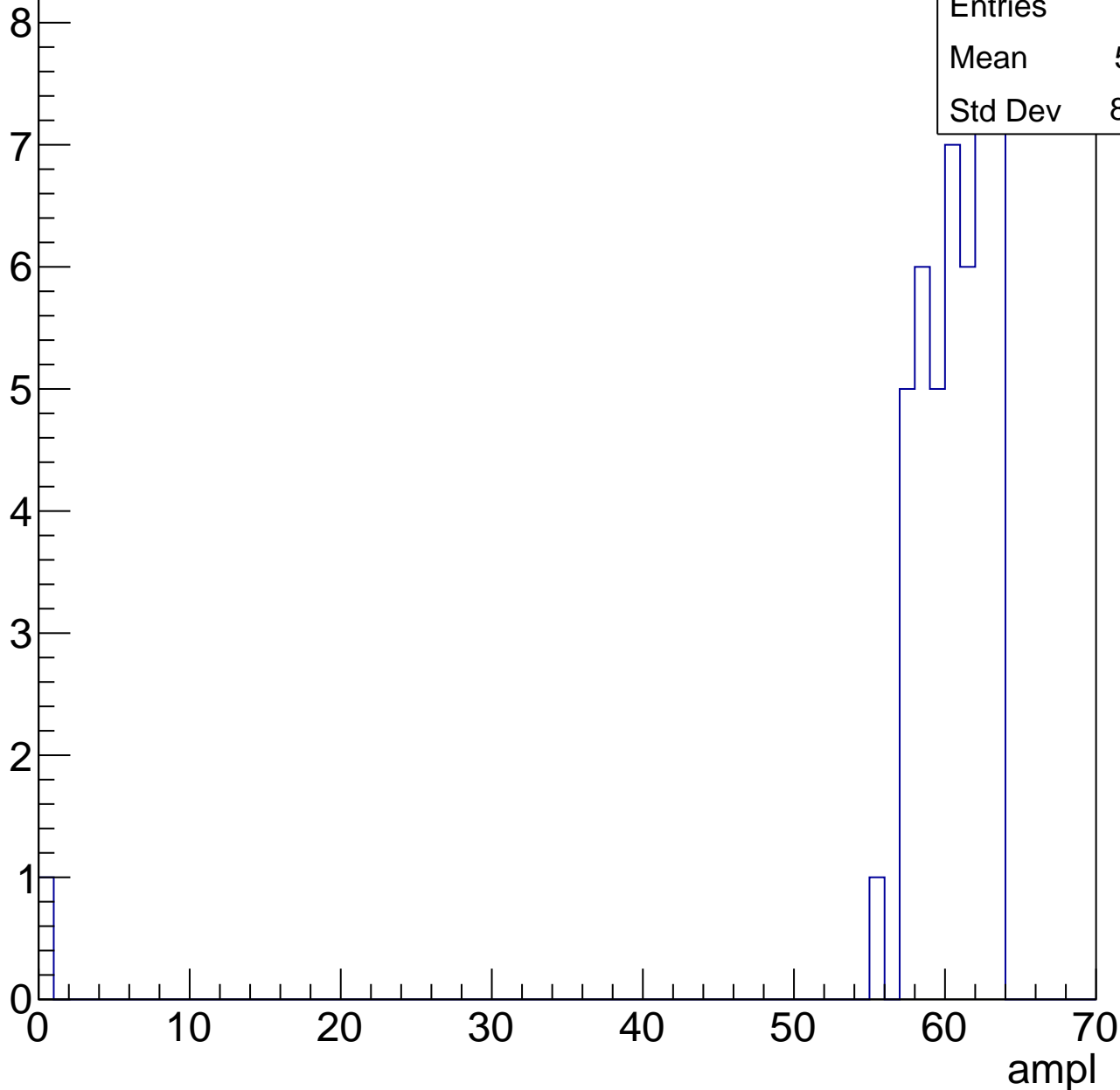


# B1L000S, U25-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	58.91
Std Dev	8.937



# B1L000S, U25-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

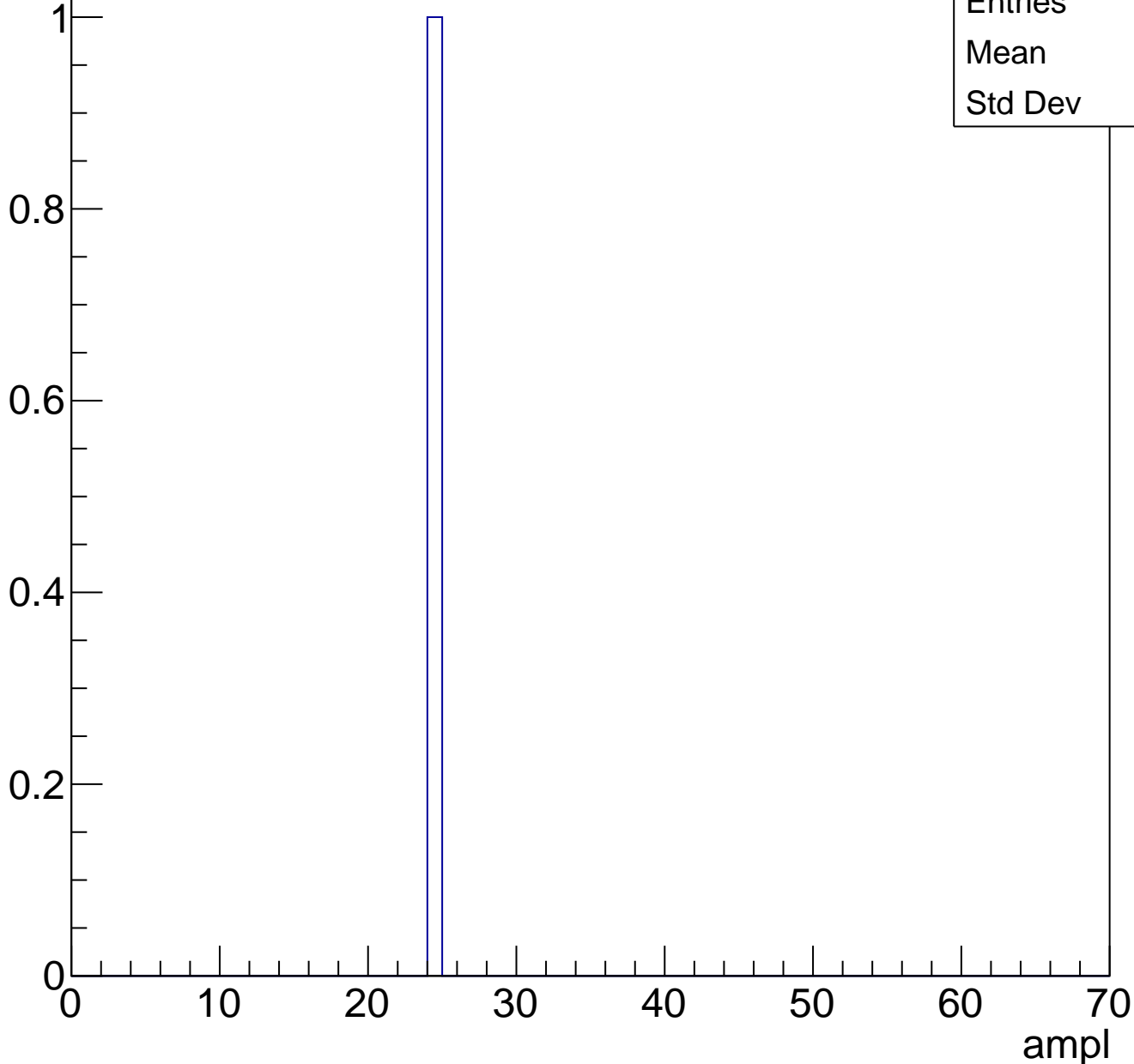




# B1L000S, U25-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	24
Std Dev	0

# B1L000S, U25-ch64, adc0

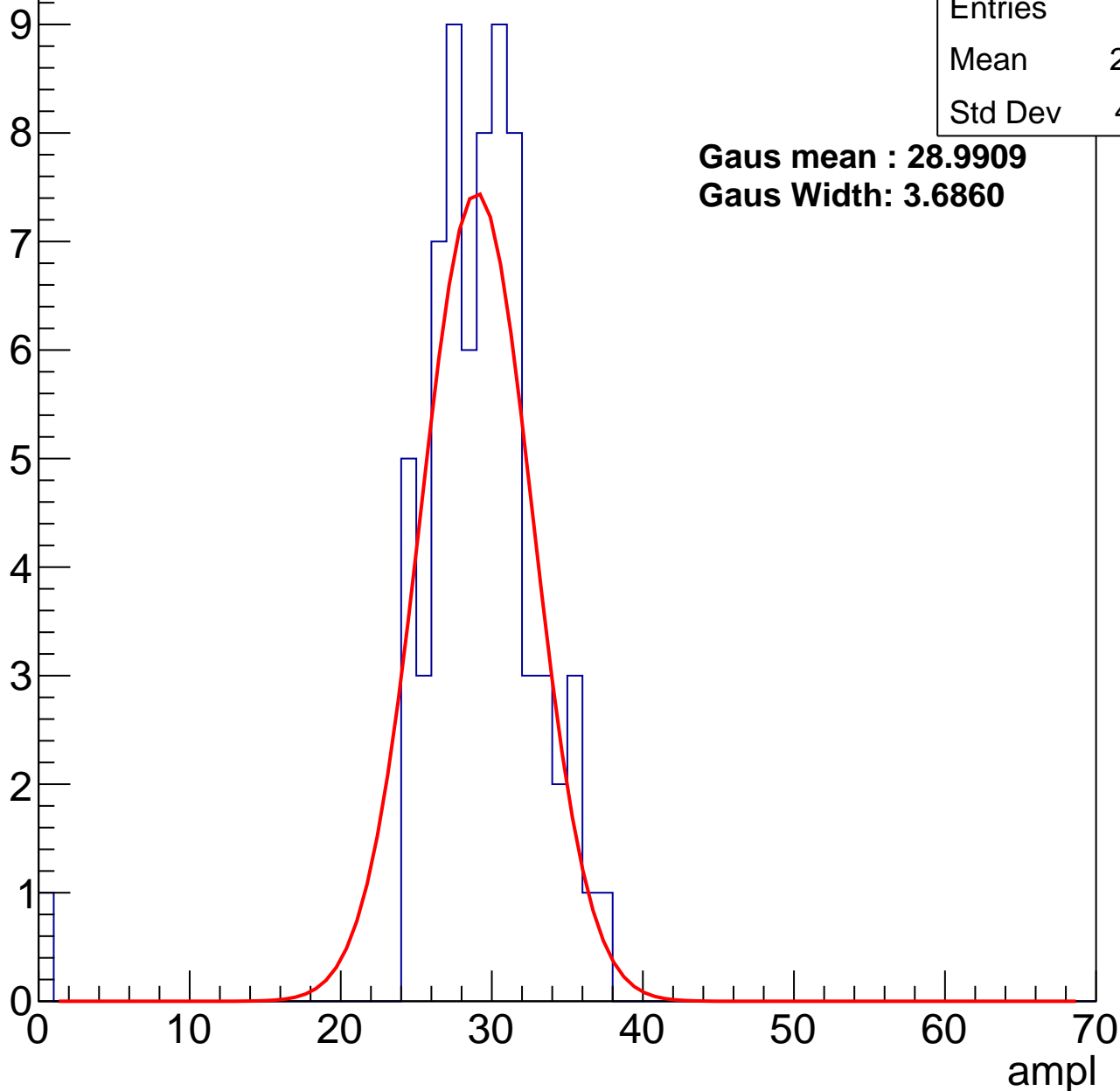
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.68
Std Dev	4.661

**Gaus mean : 28.9909**

**Gaus Width: 3.6860**



# B1L000S, U25-ch64, adc1

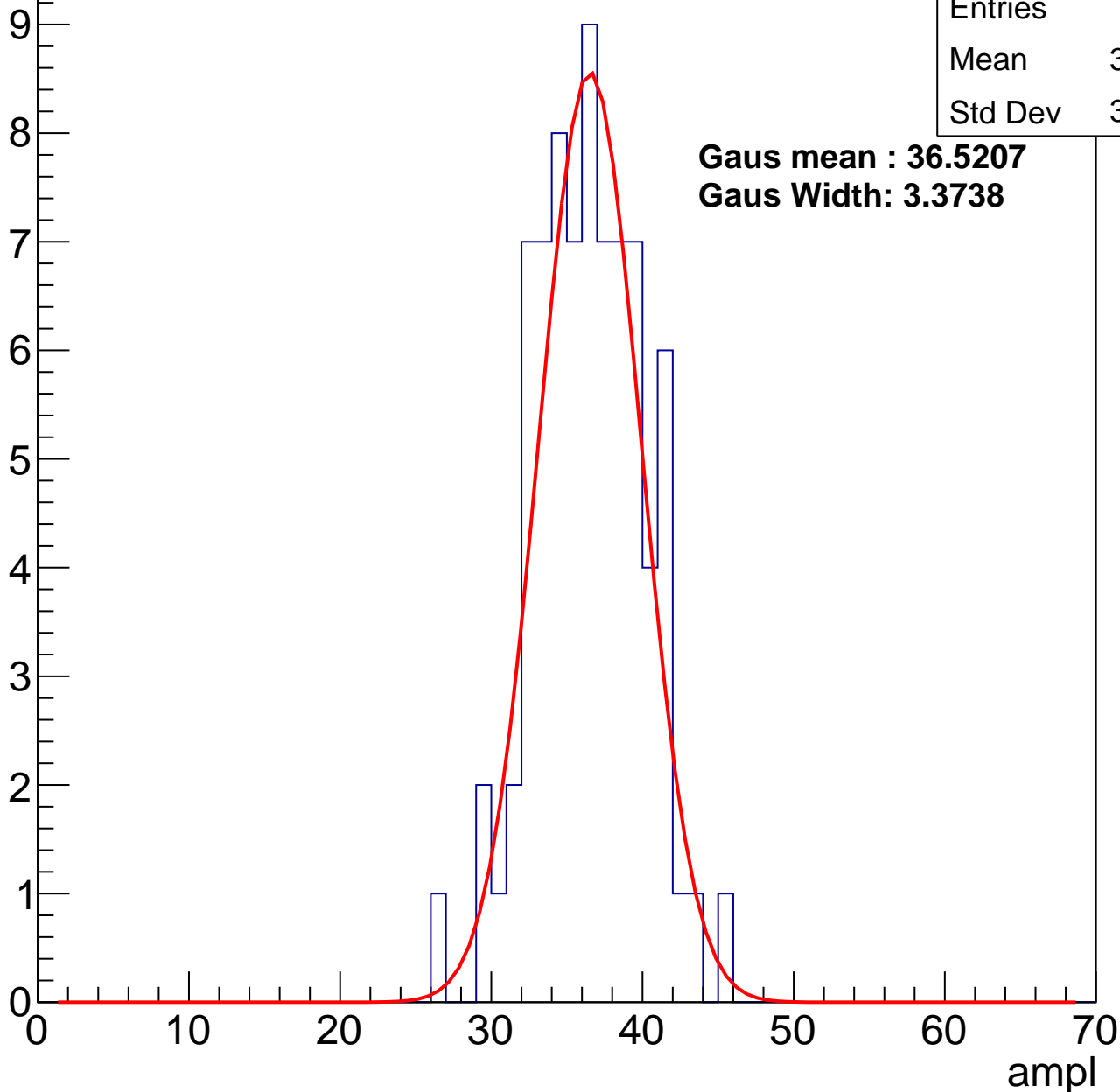
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	35.97
Std Dev	3.537

**Gaus mean : 36.5207**

**Gaus Width: 3.3738**



# B1L000S, U25-ch64, adc2

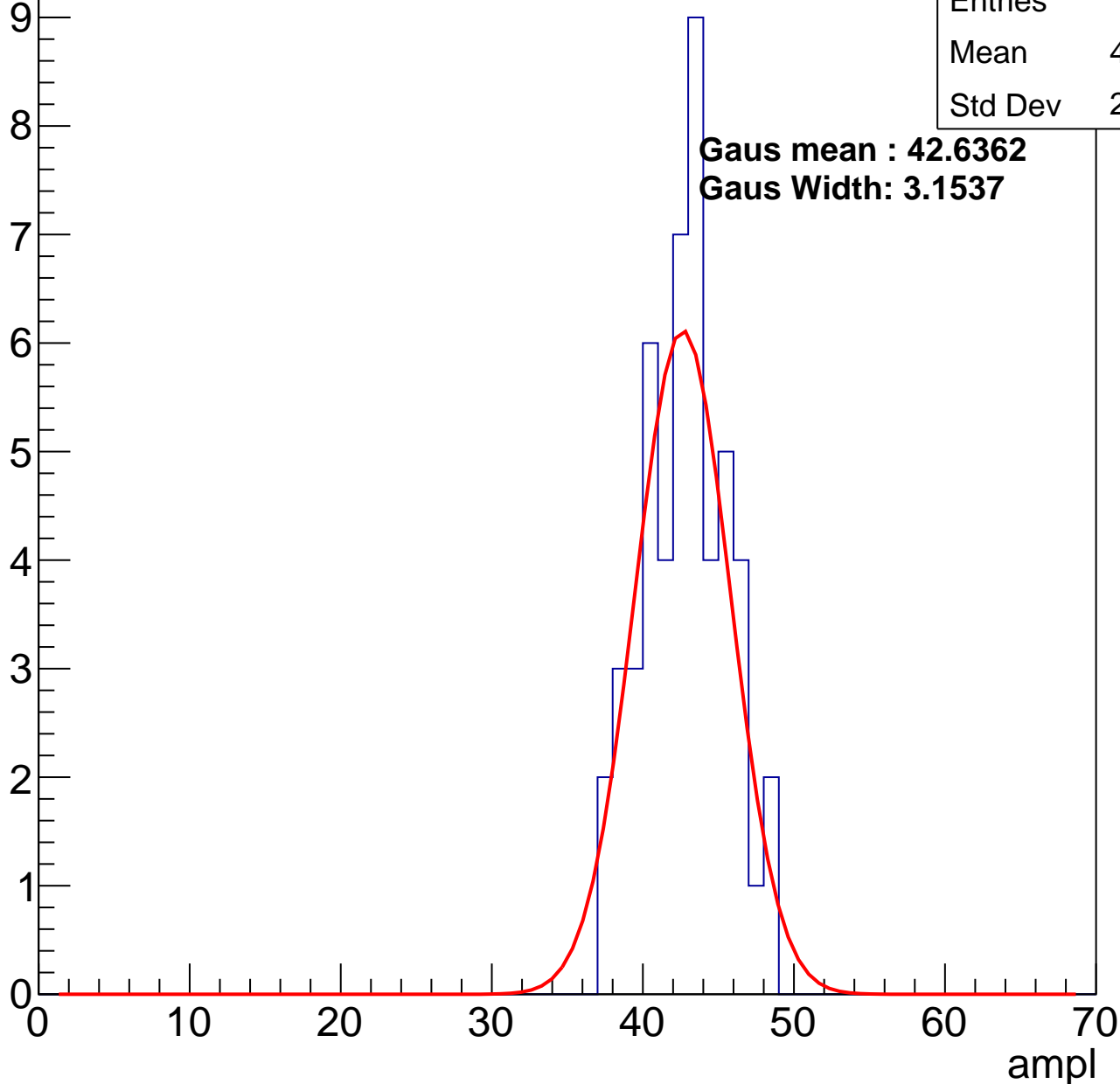
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	42.36
Std Dev	2.748

**Gaus mean : 42.6362**

**Gaus Width: 3.1537**

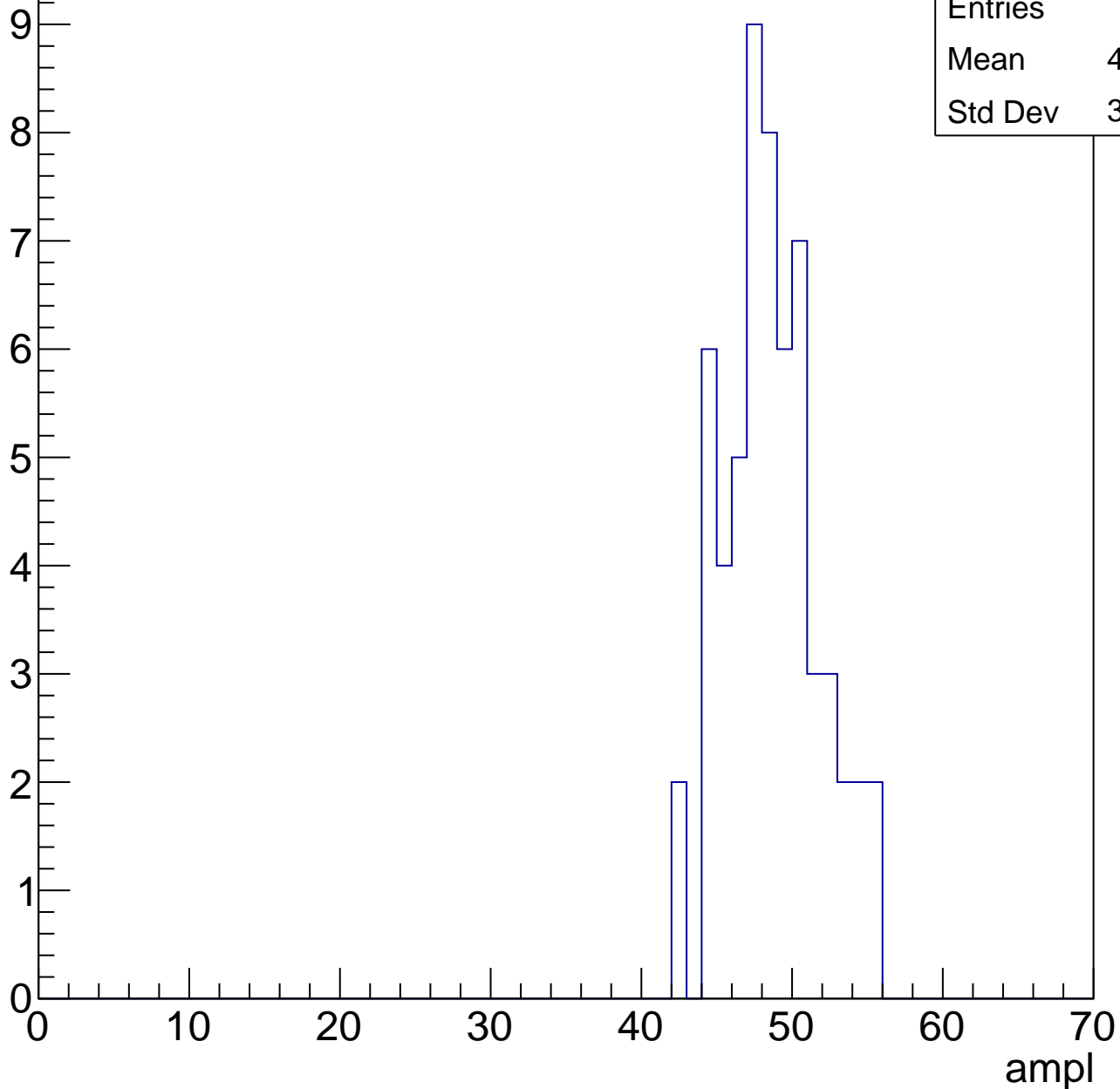


# B1L000S, U25-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

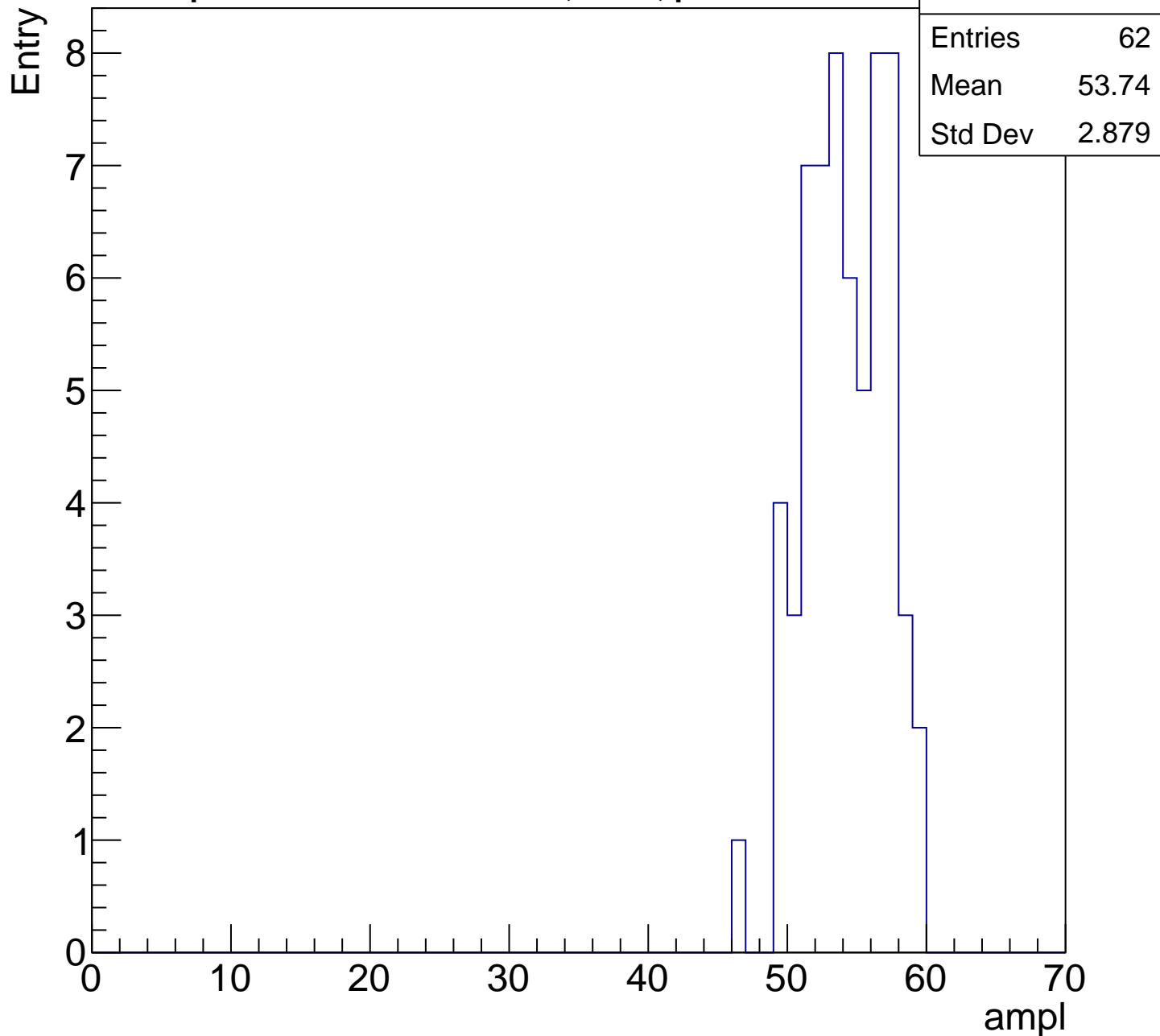
Entry

Entries	59
Mean	48.17
Std Dev	3.082



# B1L000S, U25-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

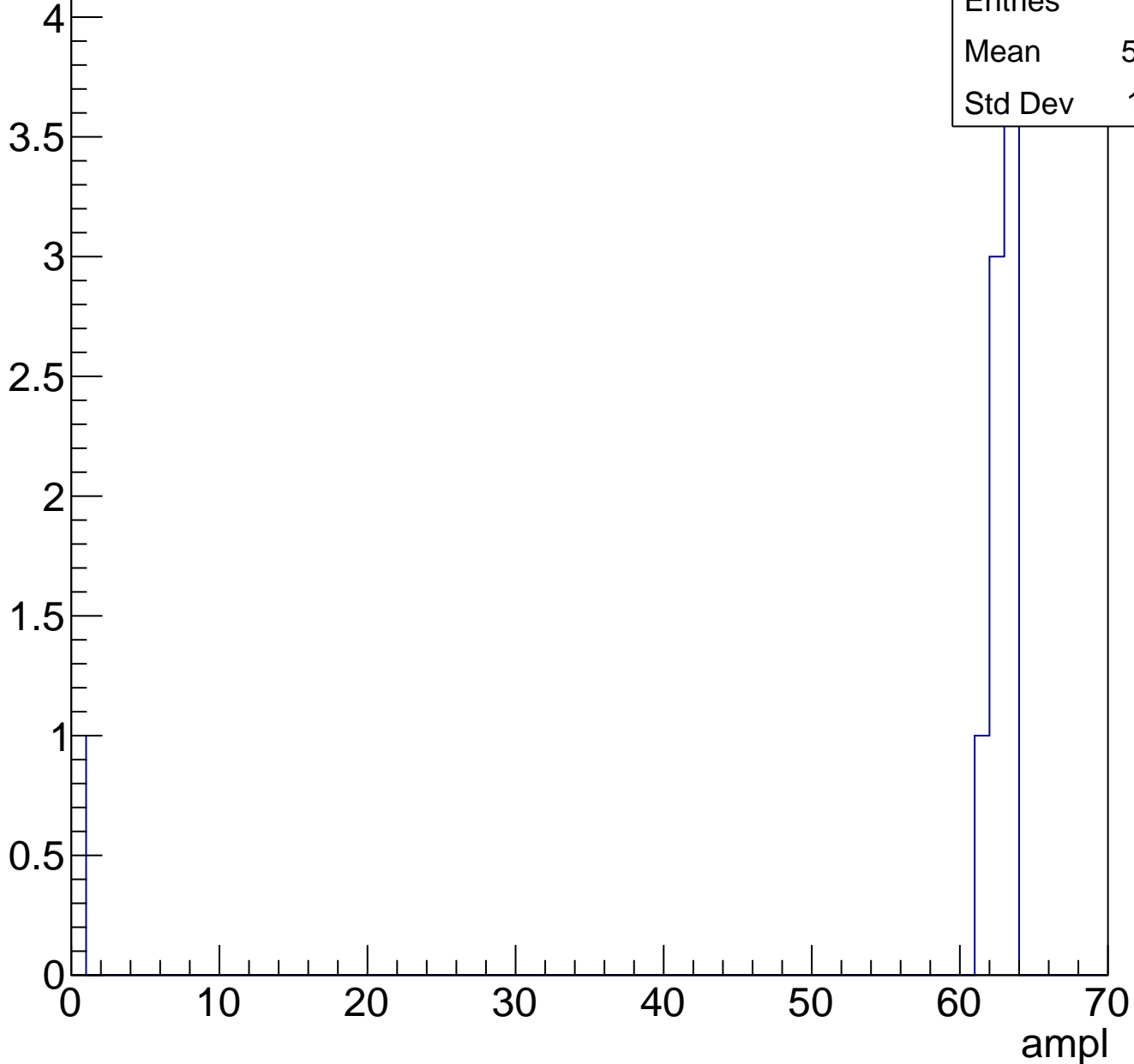
ampl

Entries	56
Mean	59.61
Std Dev	2.373

# B1L000S, U25-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch65, adc0

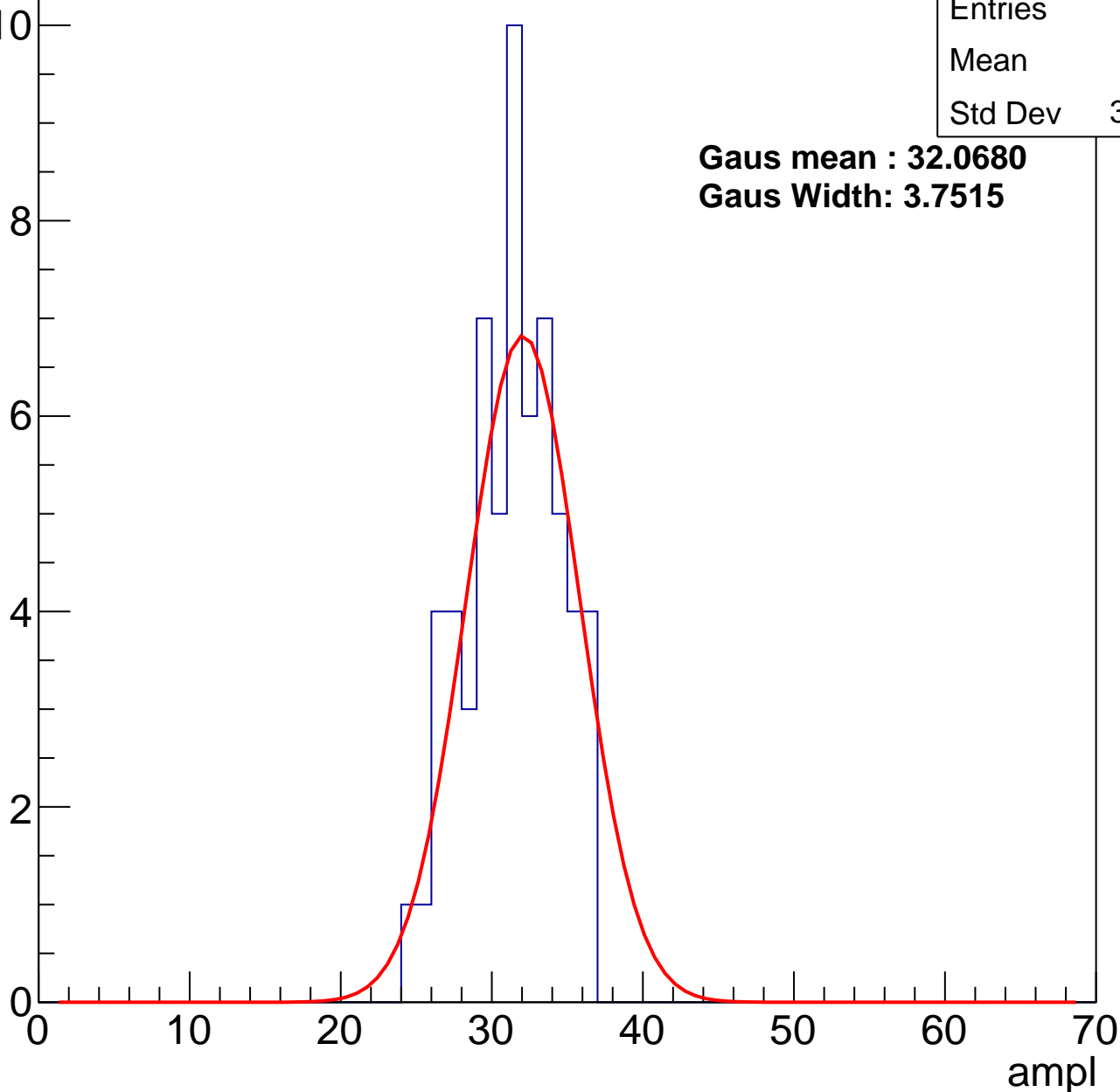
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	30.9
Std Dev	3.007

**Gaus mean : 32.0680**

**Gaus Width: 3.7515**



# B1L000S, U25-ch65, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	37.31
Std Dev	3.63

**Gaus mean : 37.7626**

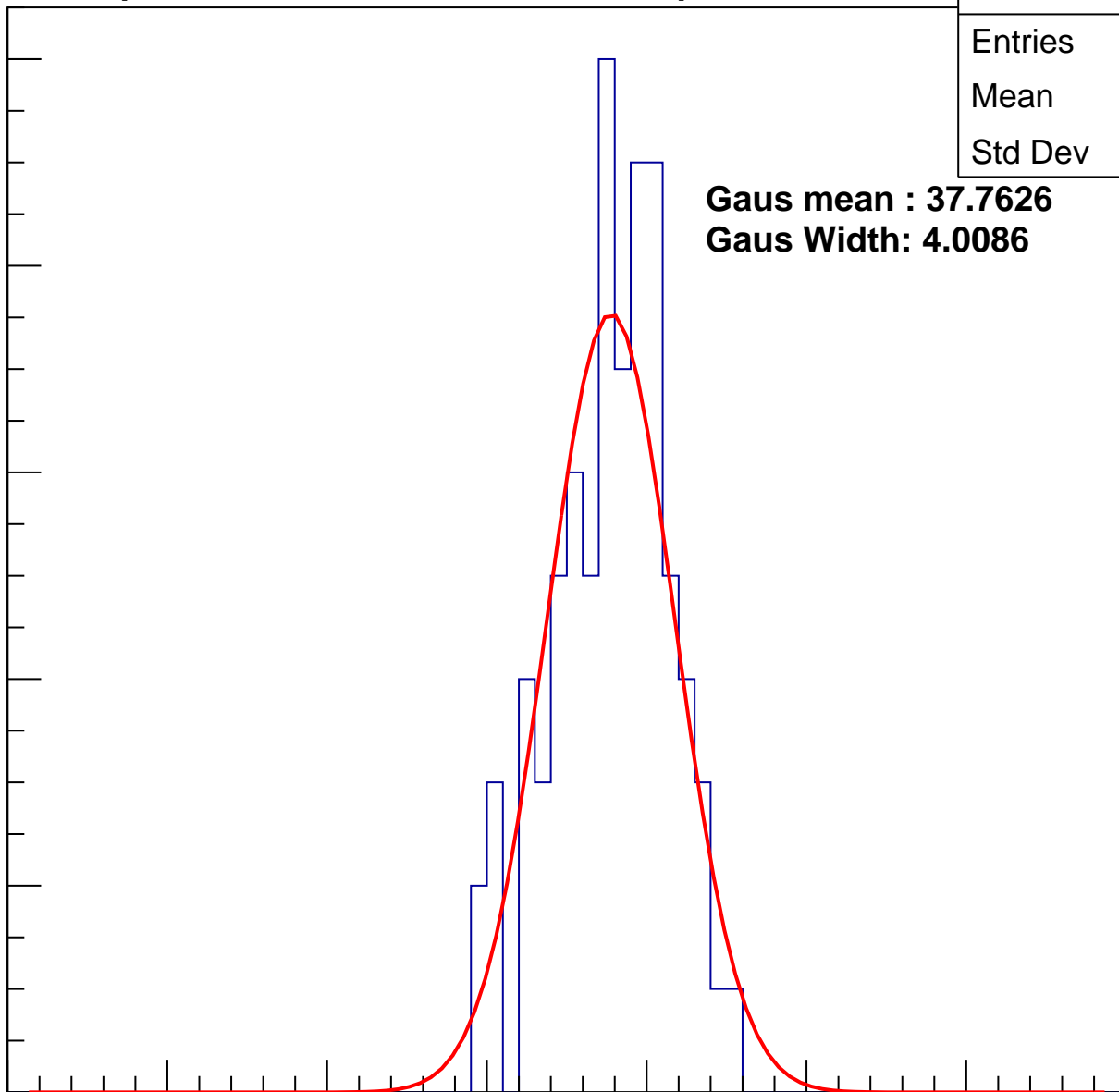
**Gaus Width: 4.0086**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch65, adc2

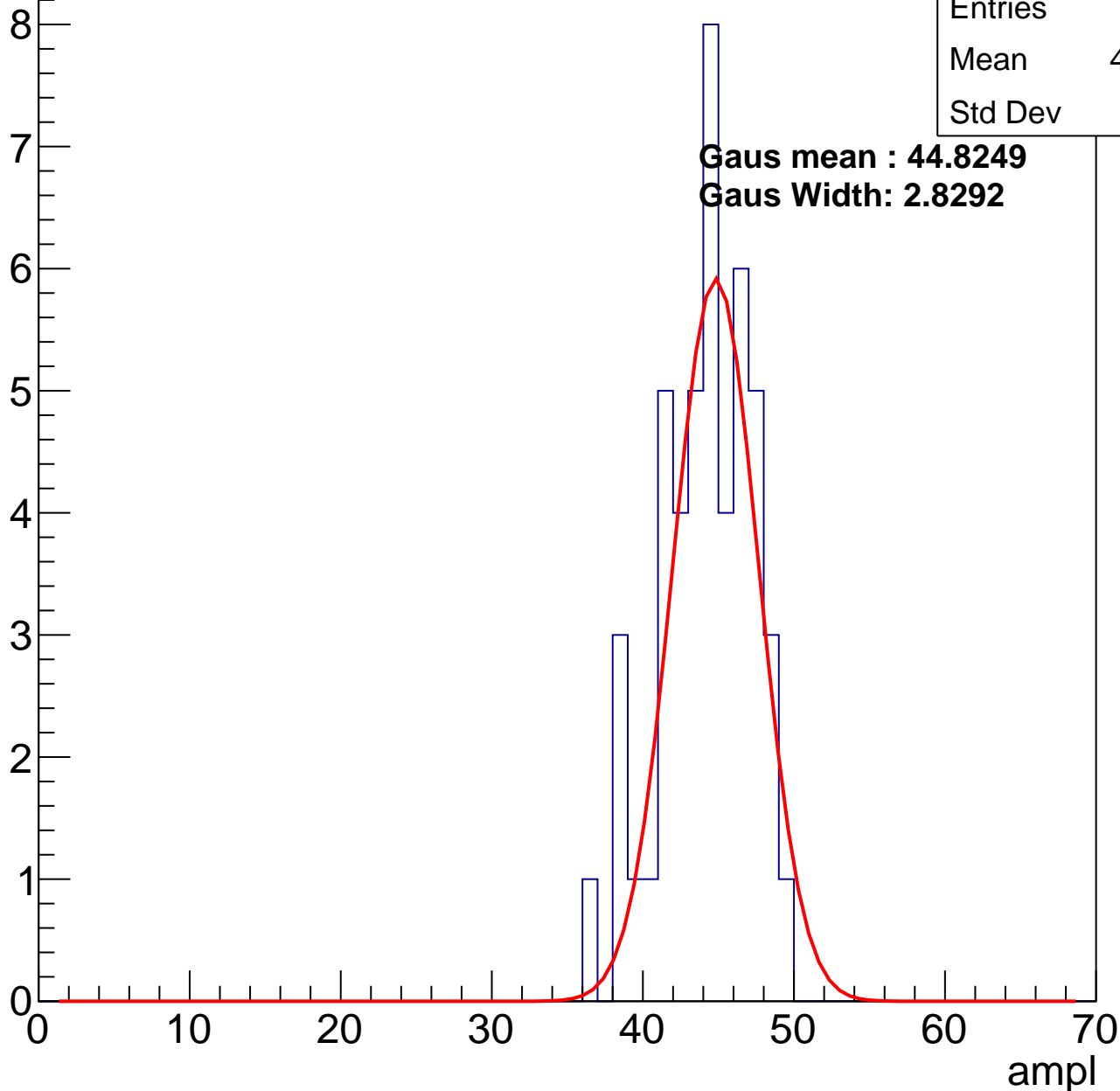
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	43.68
Std Dev	2.99

**Gaus mean : 44.8249**

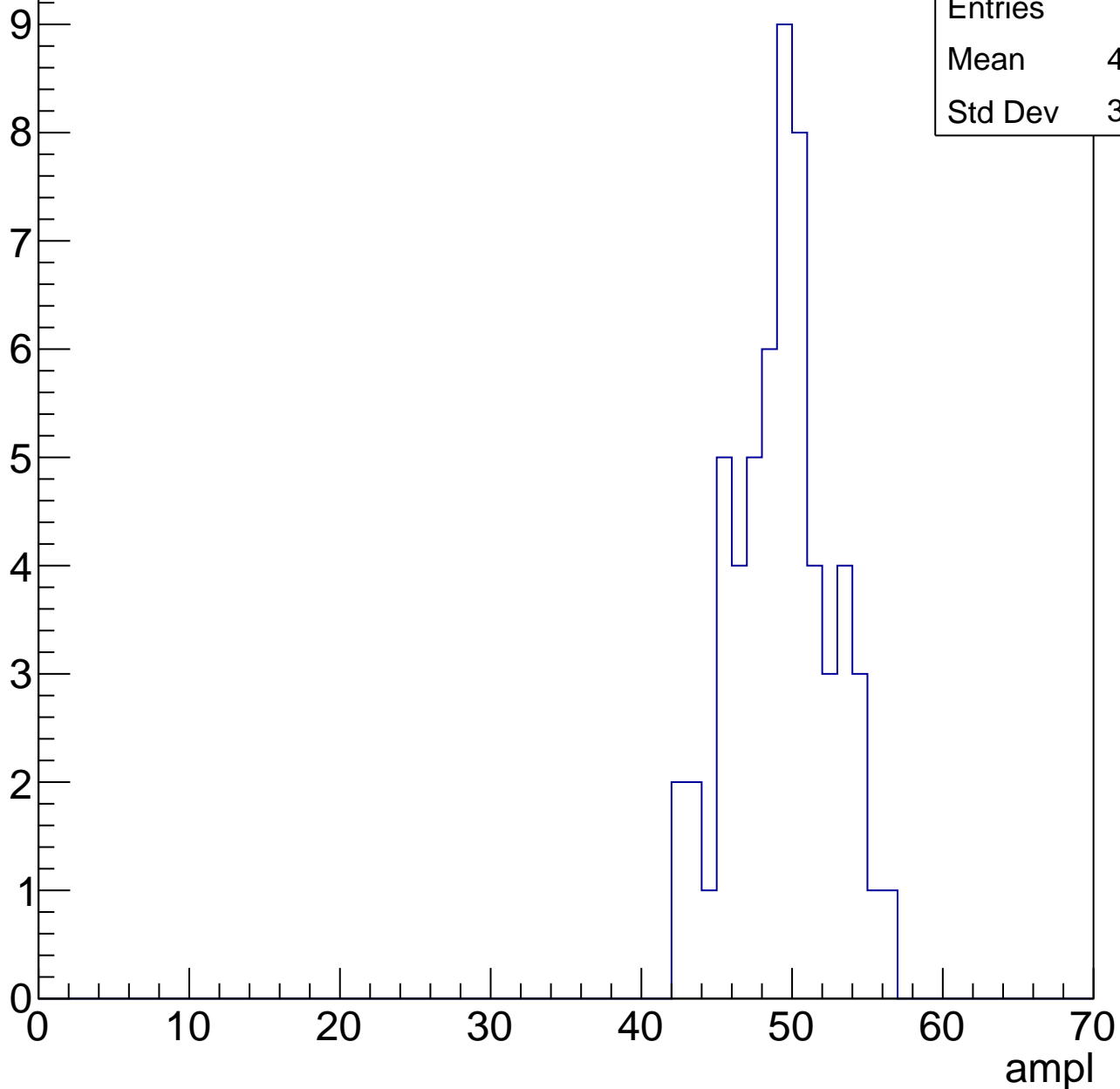
**Gaus Width: 2.8292**



# B1L000S, U25-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



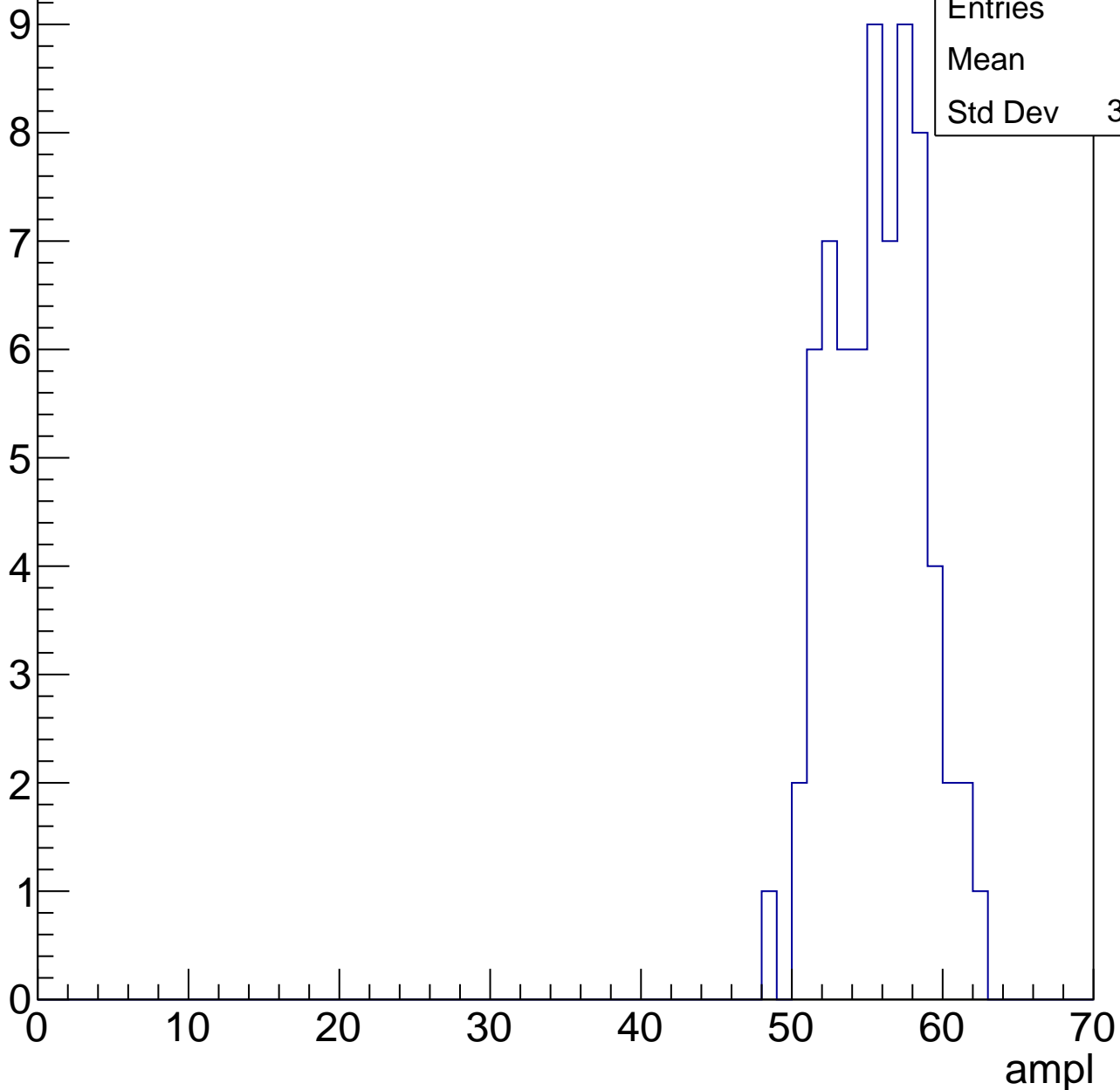
Entries	58
Mean	48.83
Std Dev	3.244

# B1L000S, U25-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	55.2
Std Dev	3.012

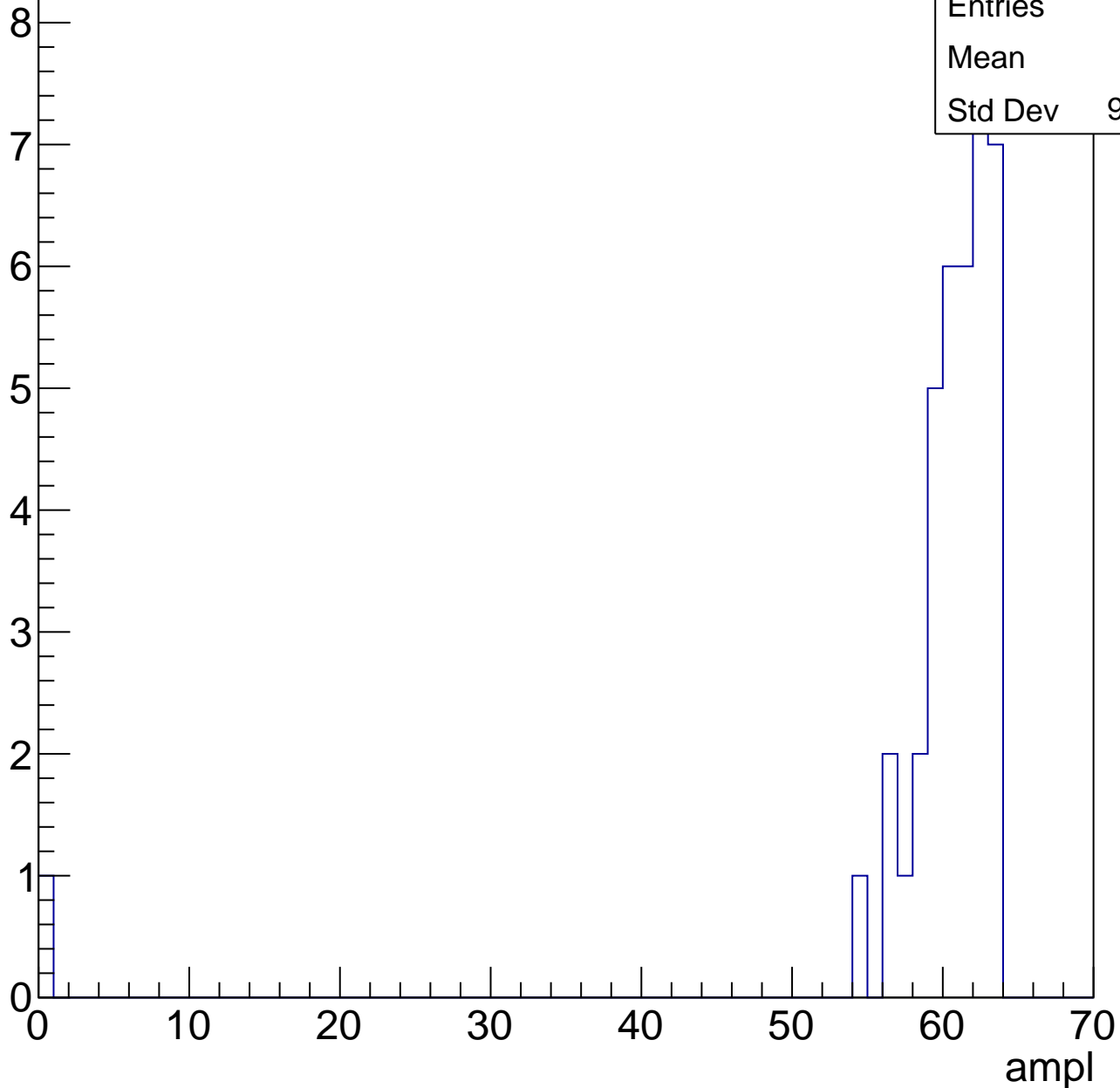


# B1L000S, U25-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	39
Mean	58.9
Std Dev	9.797



# B1L000S, U25-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

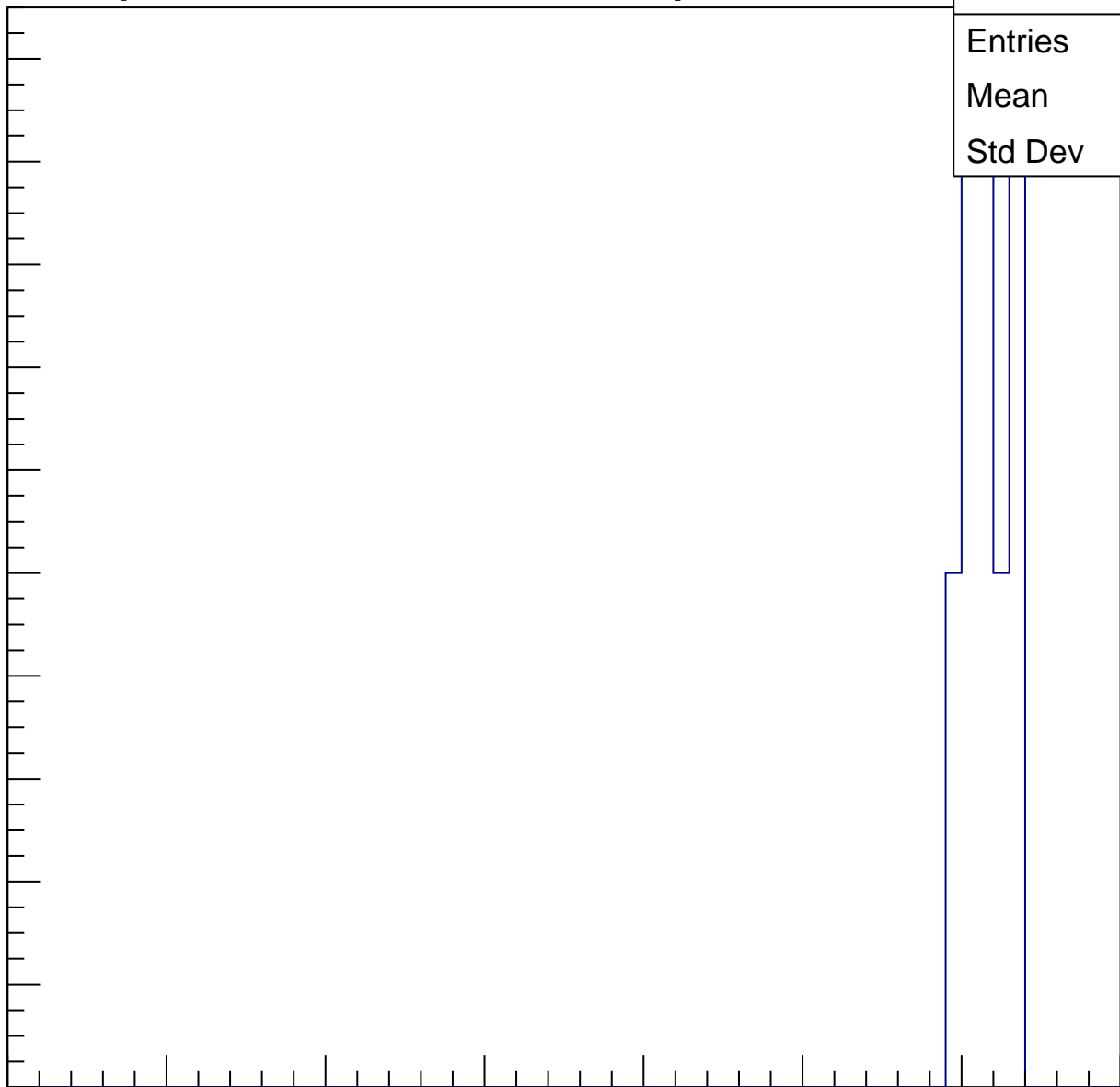
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	61.12
Std Dev	1.364

0 10 20 30 40 50 60 70

ampl

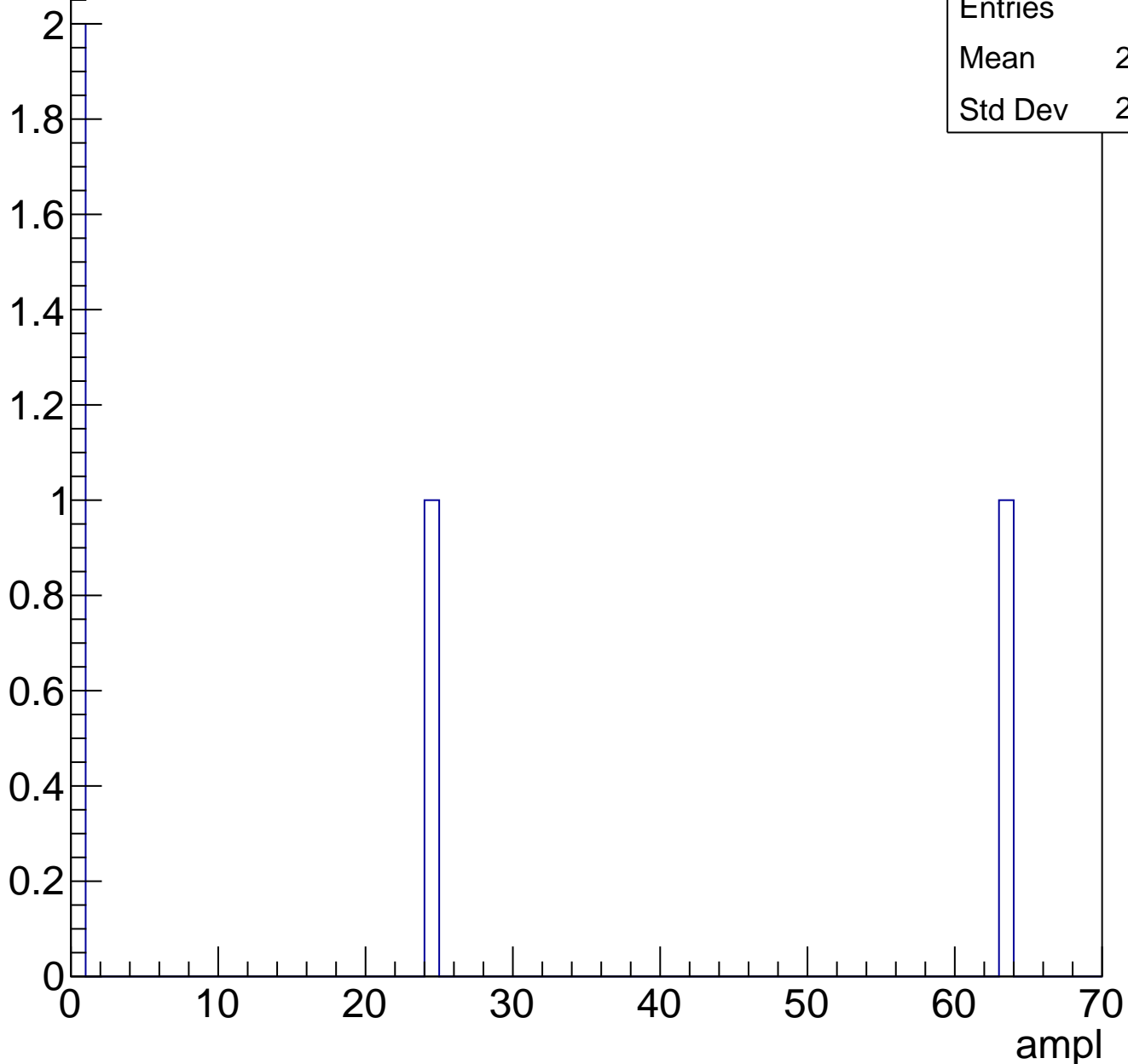




# B1L000S, U25-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	4
Mean	21.75
Std Dev	25.75

# B1L000S, U25-ch66, adc0

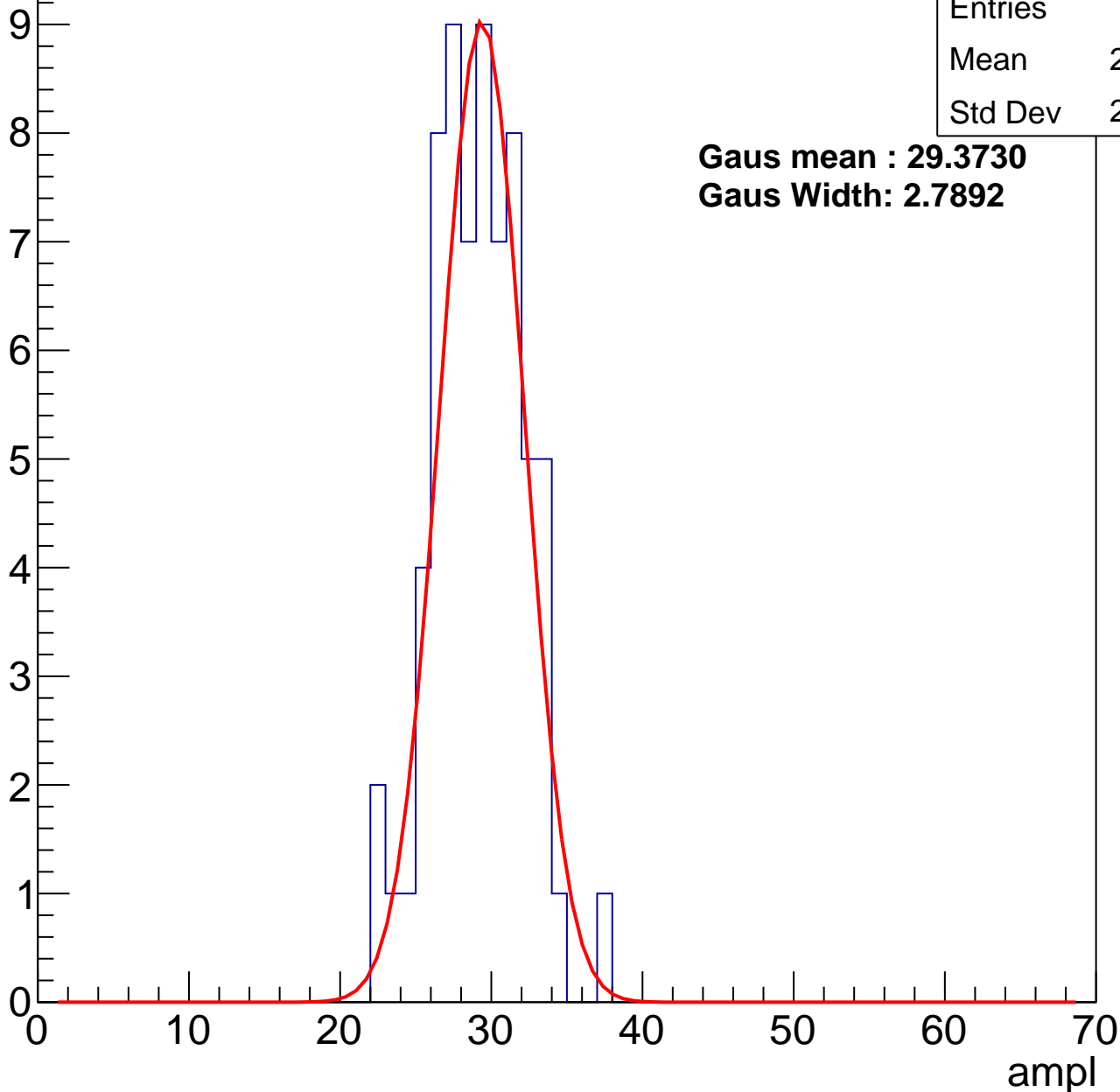
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	28.72
Std Dev	2.935

**Gaus mean : 29.3730**

**Gaus Width: 2.7892**



# B1L000S, U25-ch66, adc1

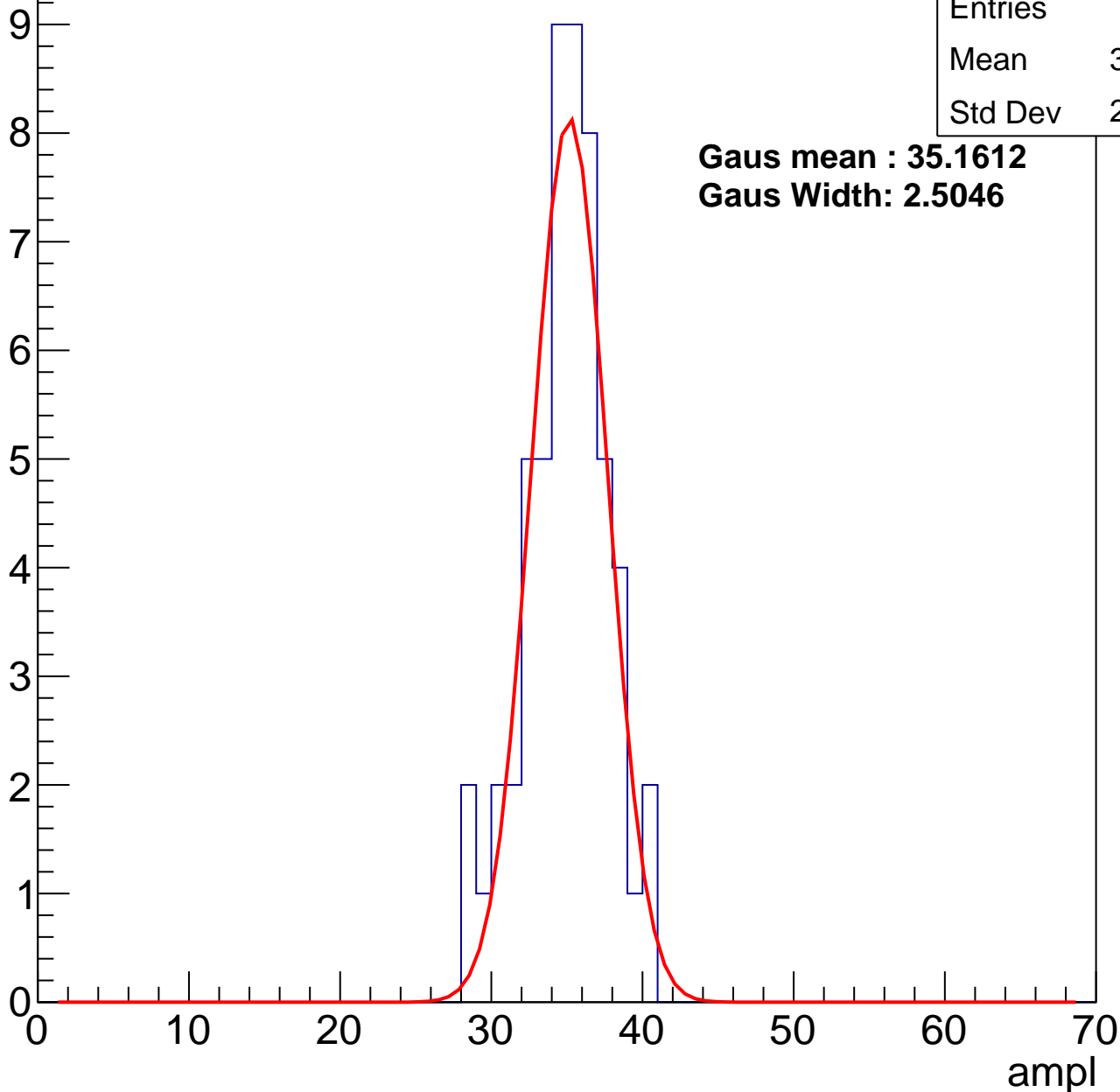
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	34.49
Std Dev	2.716

**Gaus mean : 35.1612**

**Gaus Width: 2.5046**



# B1L000S, U25-ch66, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

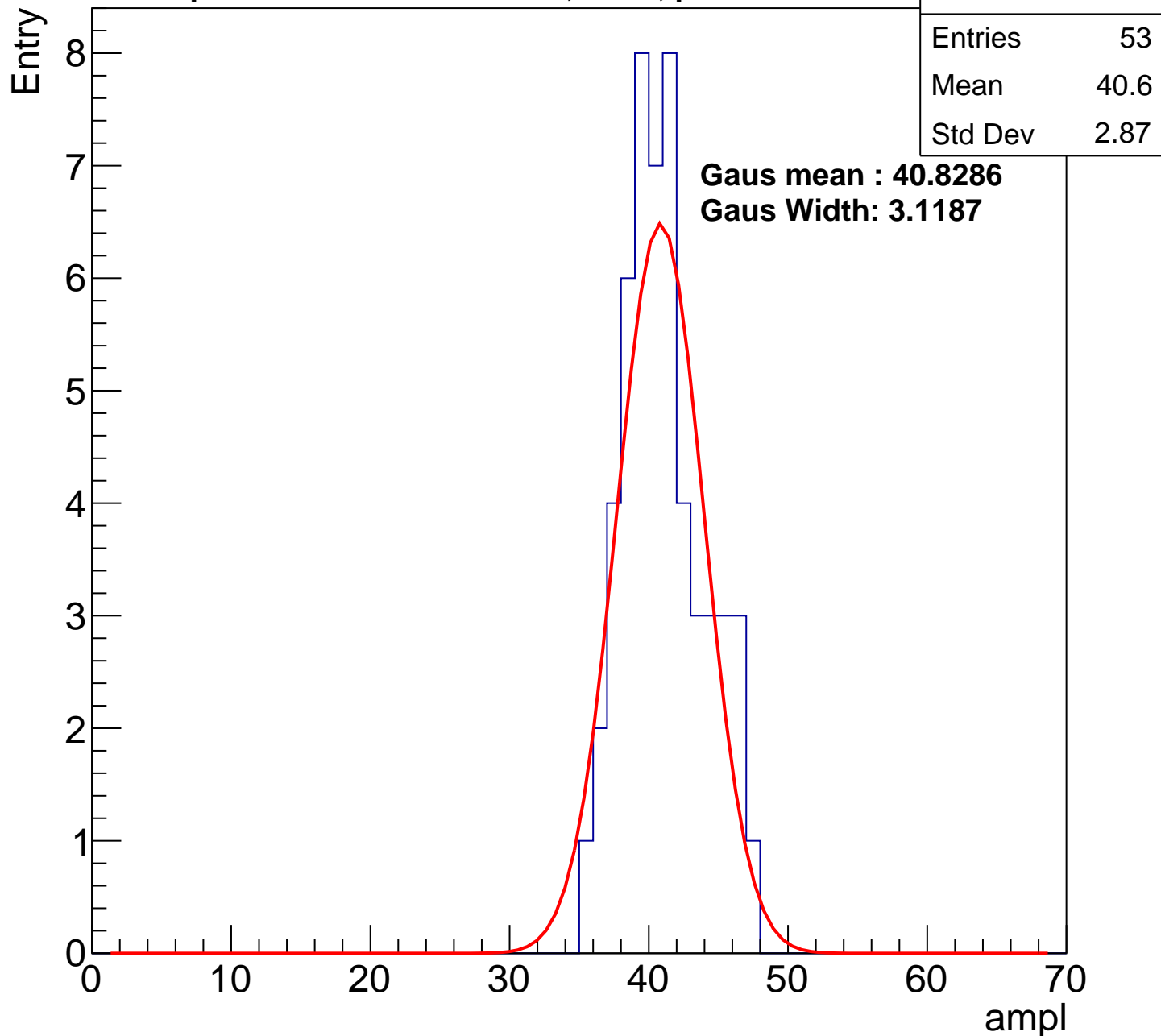
Entries	53
Mean	40.6
Std Dev	2.87

**Gaus mean : 40.8286**

**Gaus Width: 3.1187**

0 10 20 30 40 50 60 70

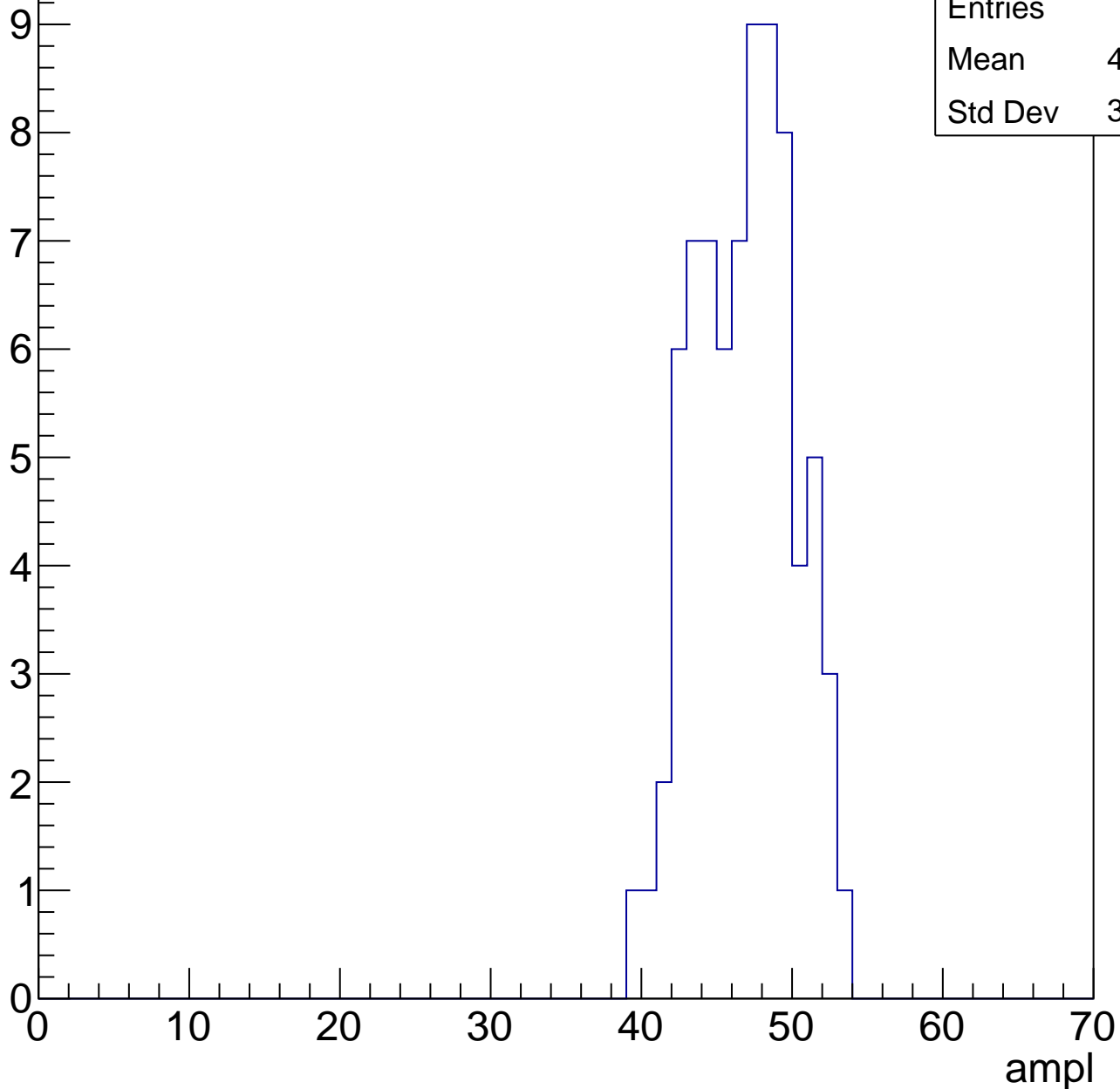
ampl



# B1L000S, U25-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



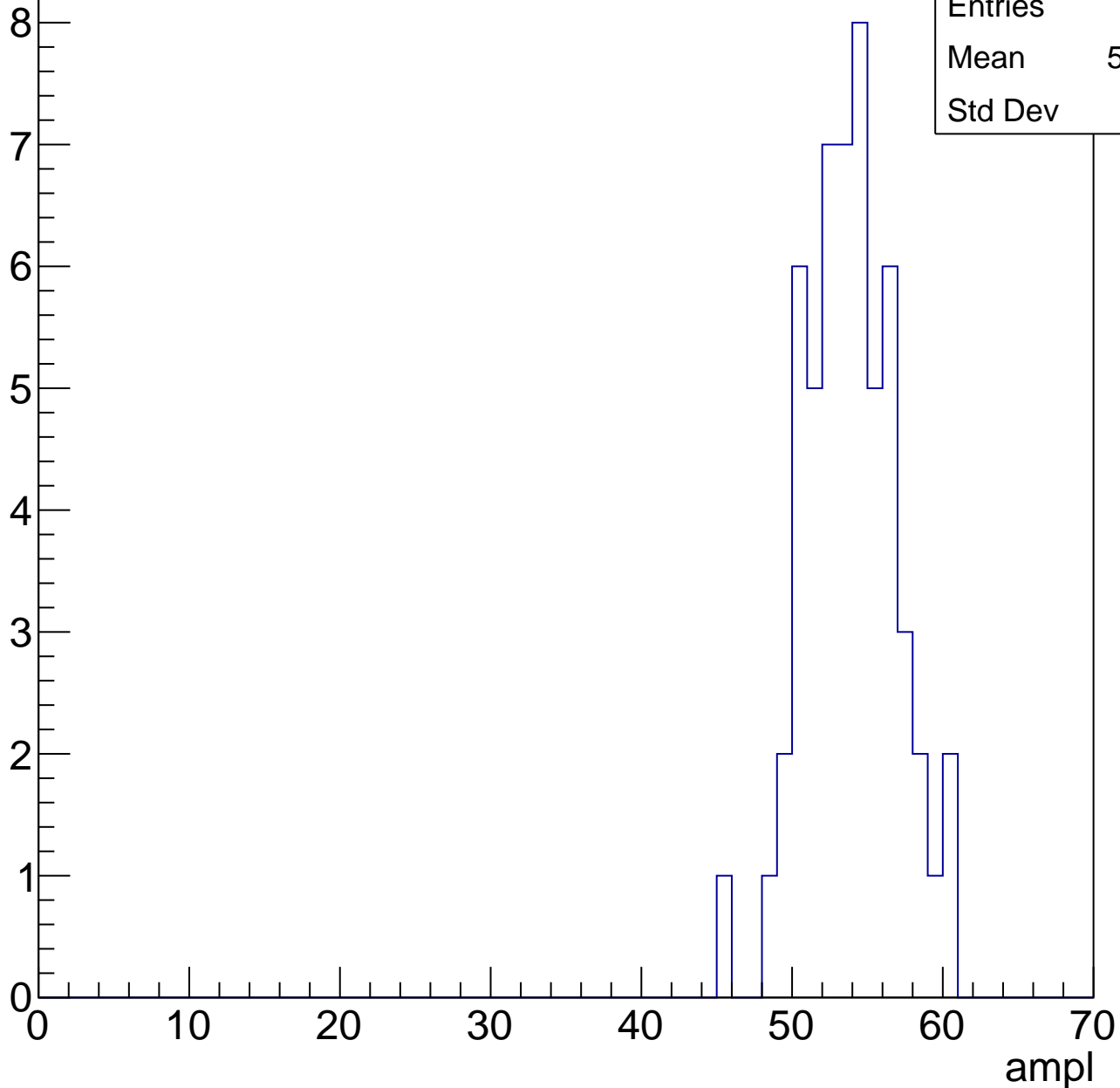
Entries	76
Mean	46.38
Std Dev	3.203

# B1L000S, U25-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

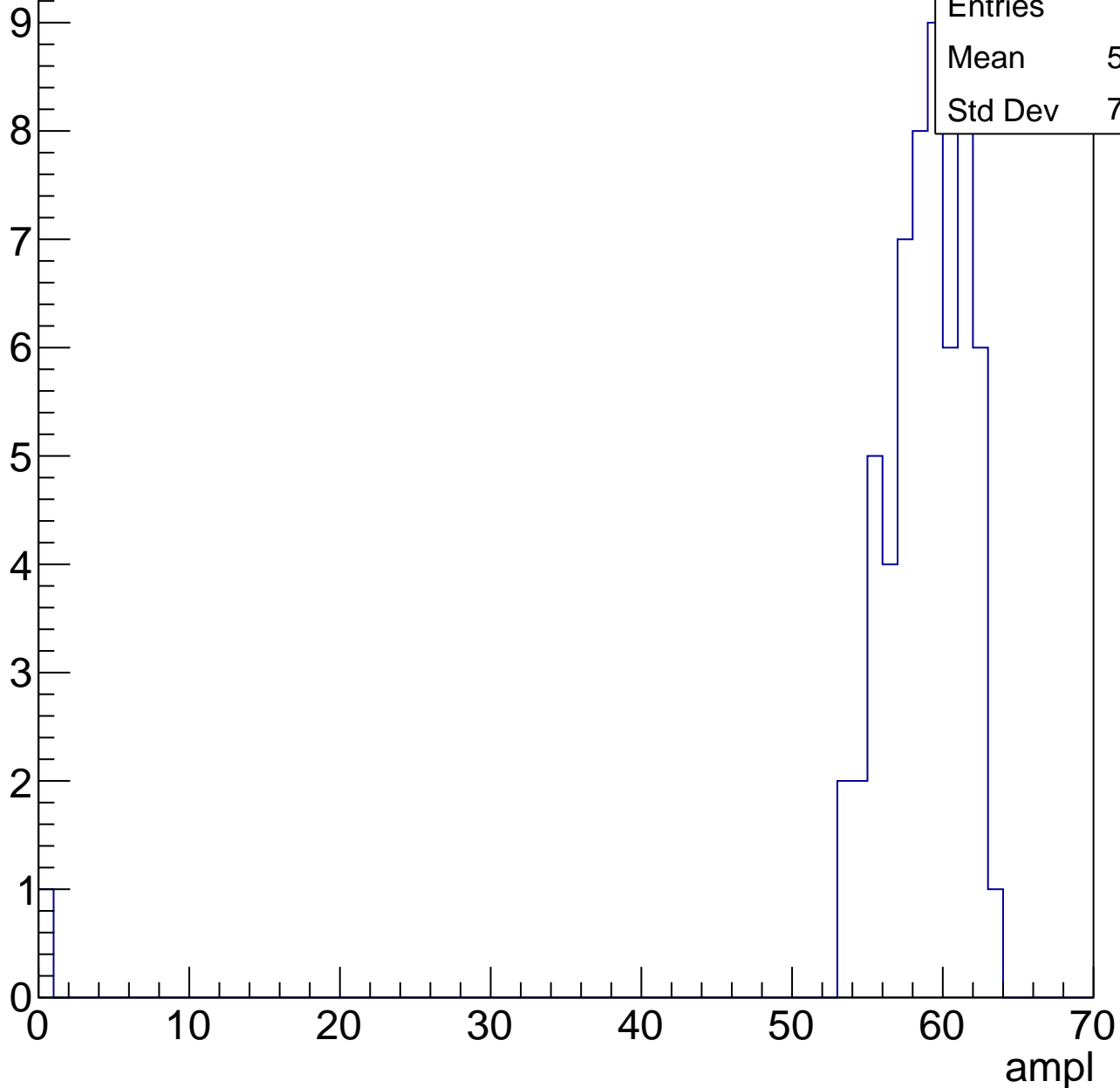
Entries	56
Mean	53.39
Std Dev	3.01



# B1L000S, U25-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

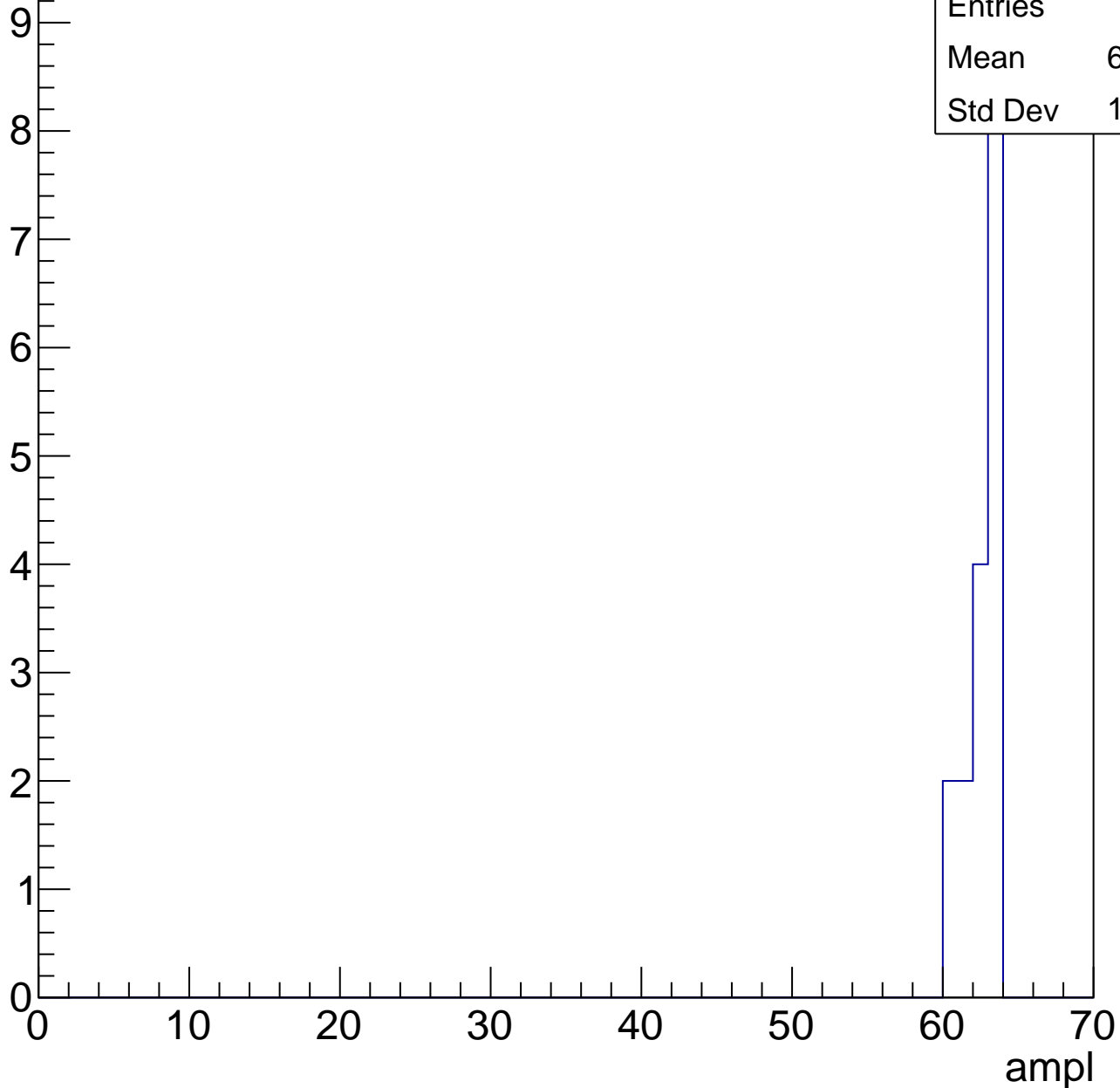
Entry



# B1L000S, U25-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch67, adc0

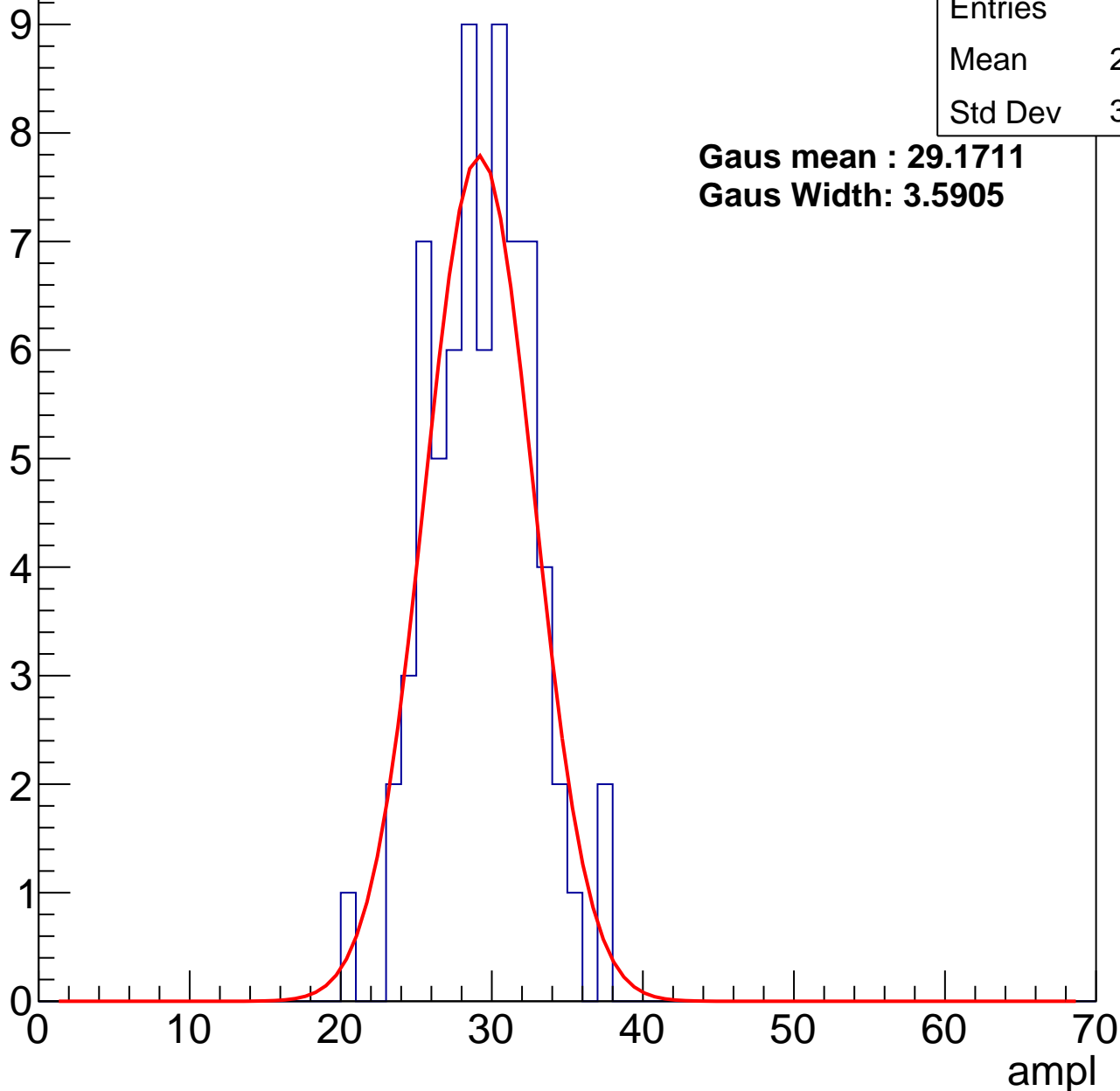
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	28.89
Std Dev	3.346

**Gaus mean : 29.1711**

**Gaus Width: 3.5905**



# B1L000S, U25-ch67, adc1

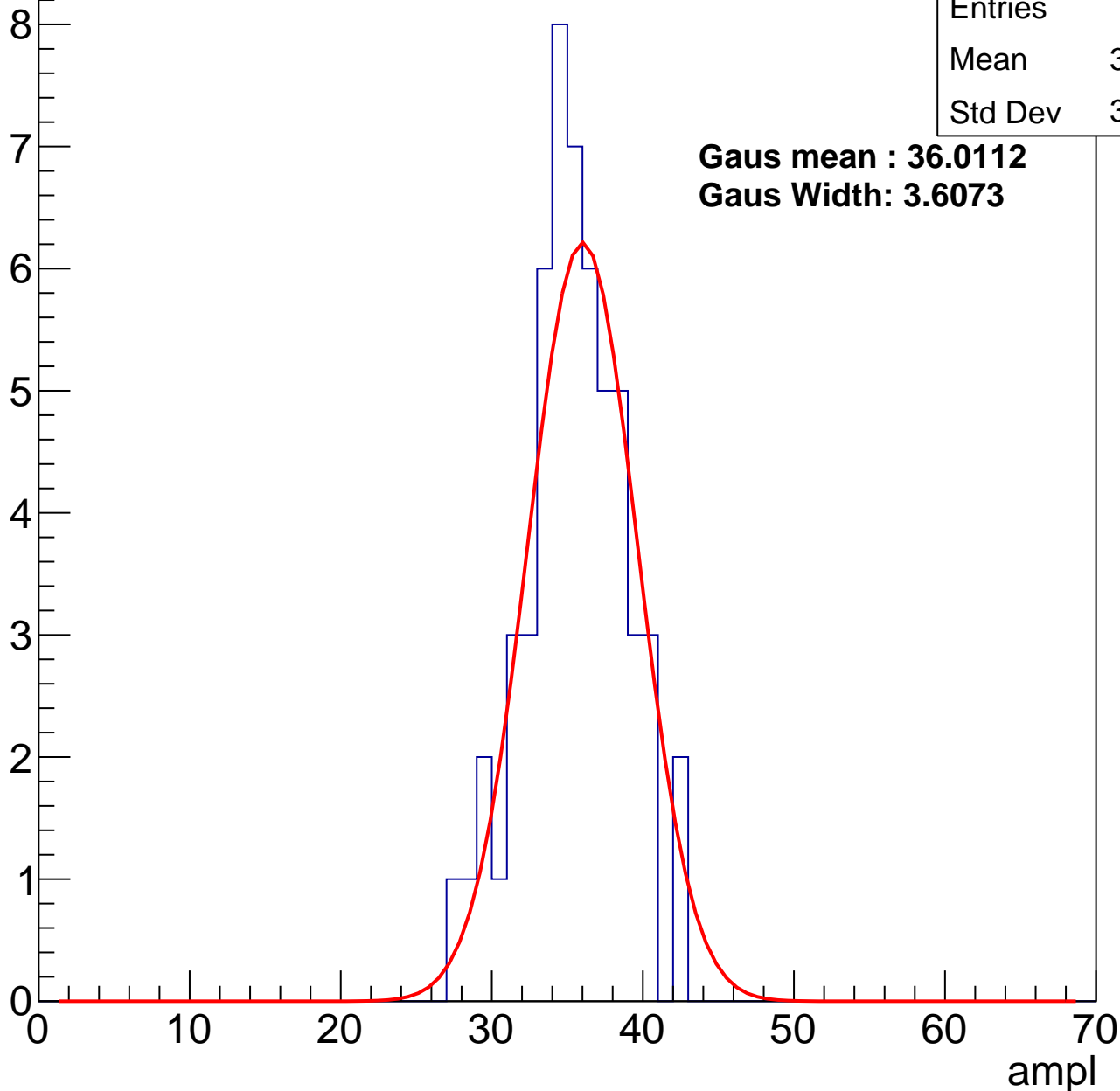
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	34.98
Std Dev	3.298

**Gaus mean : 36.0112**

**Gaus Width: 3.6073**



# B1L000S, U25-ch67, adc2

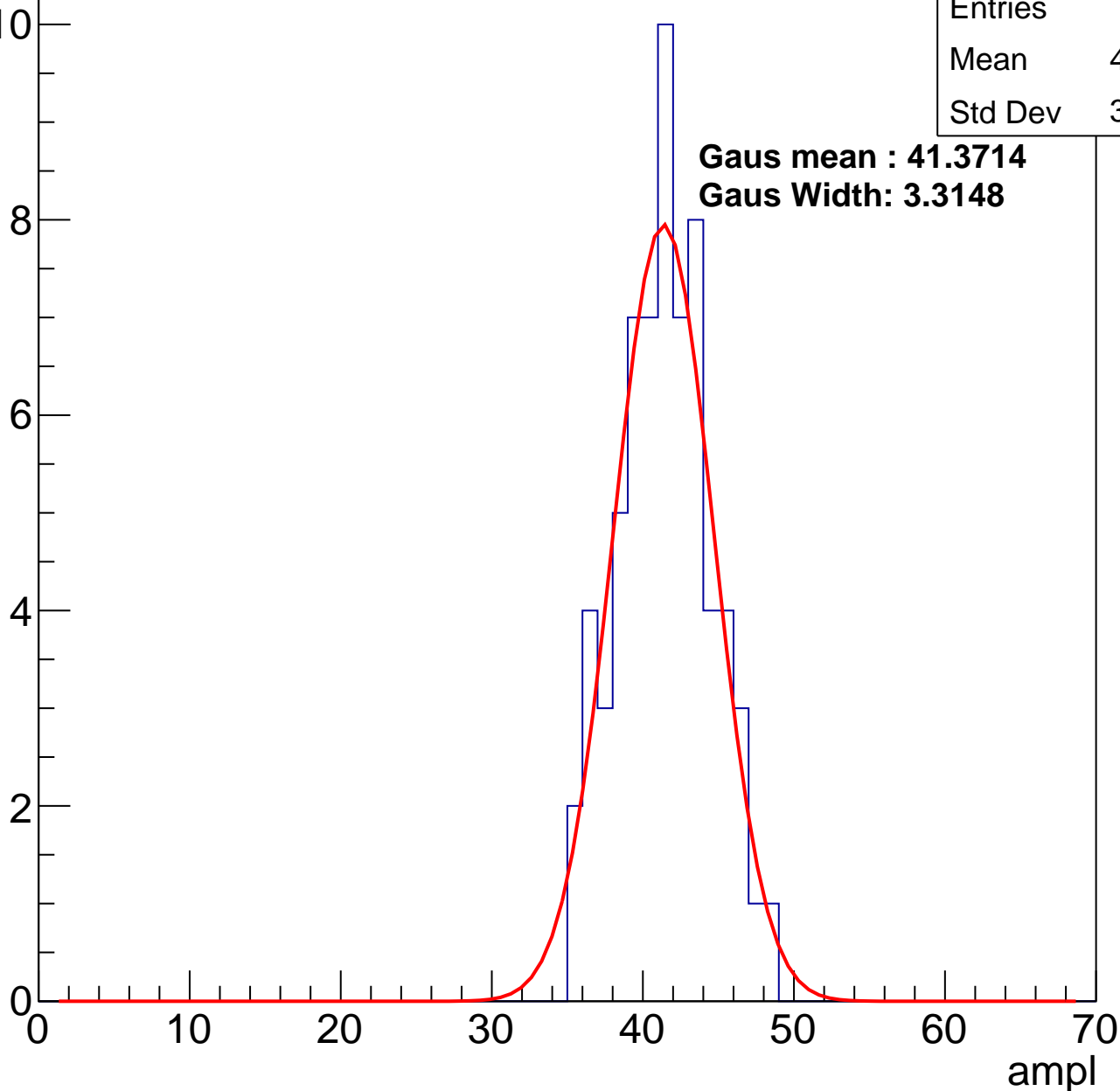
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	40.98
Std Dev	3.013

**Gaus mean : 41.3714**

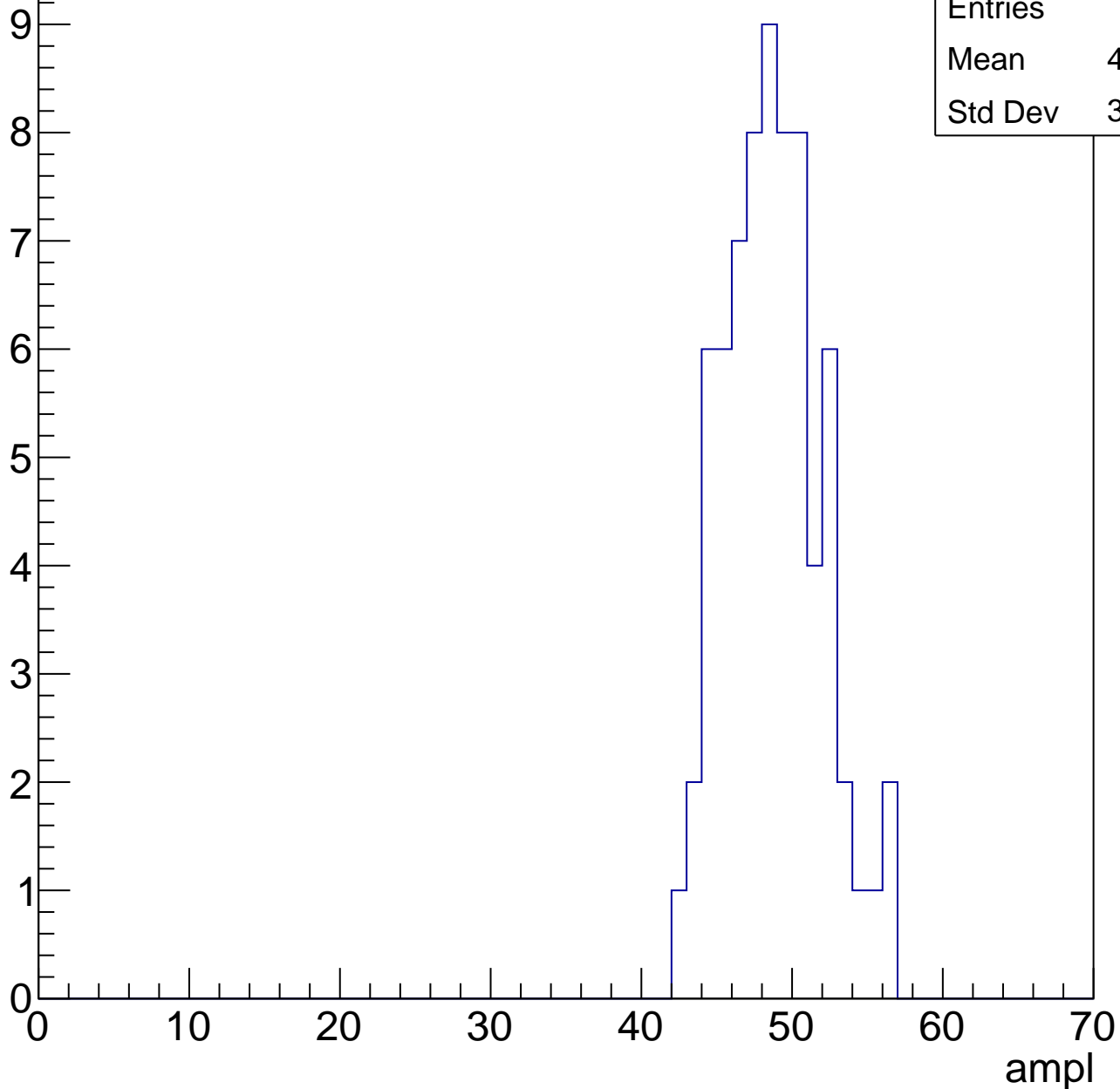
**Gaus Width: 3.3148**



# B1L000S, U25-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



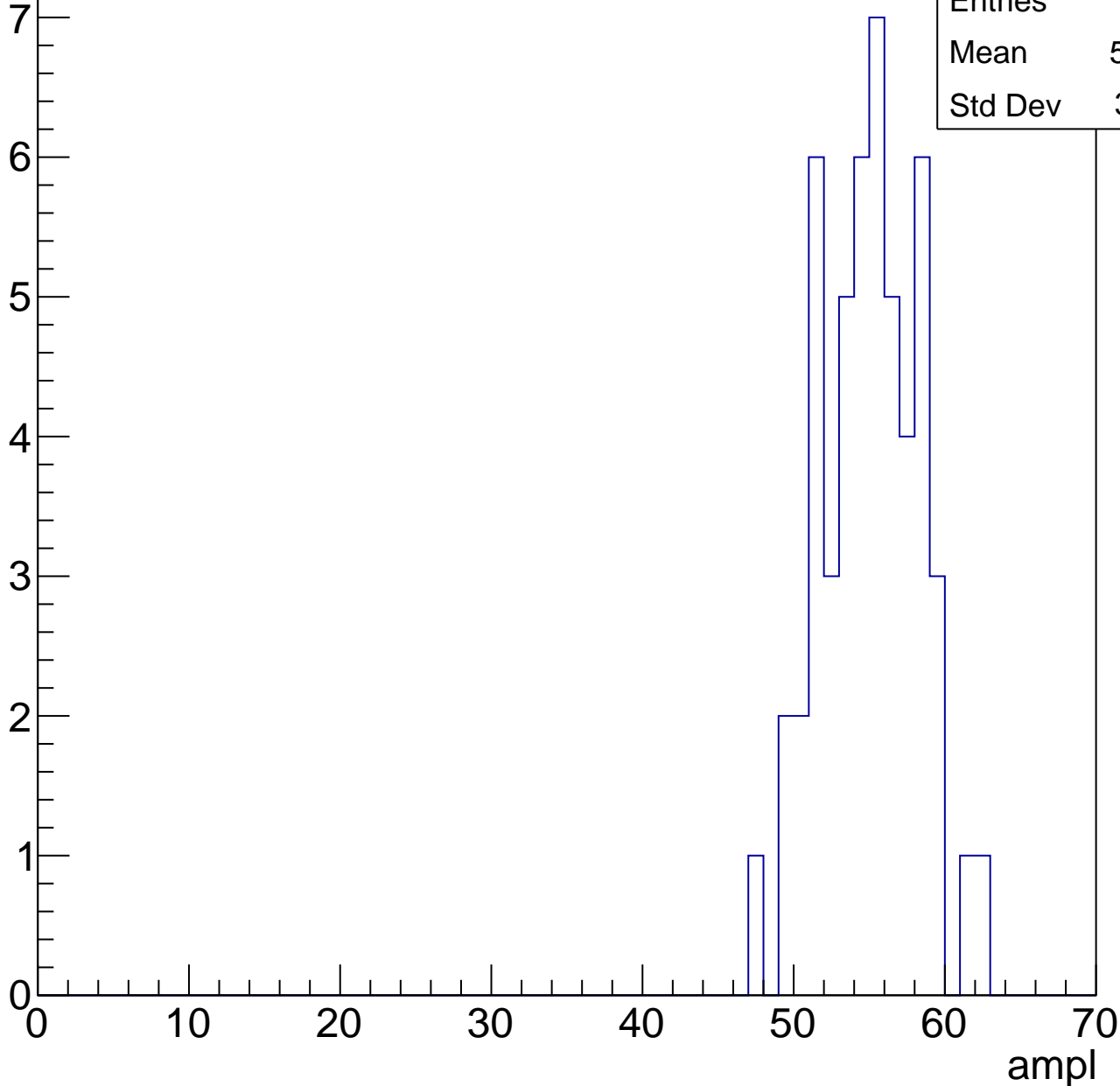
Entries	71
Mean	48.27
Std Dev	3.144

# B1L000S, U25-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	54.56
Std Dev	3.201

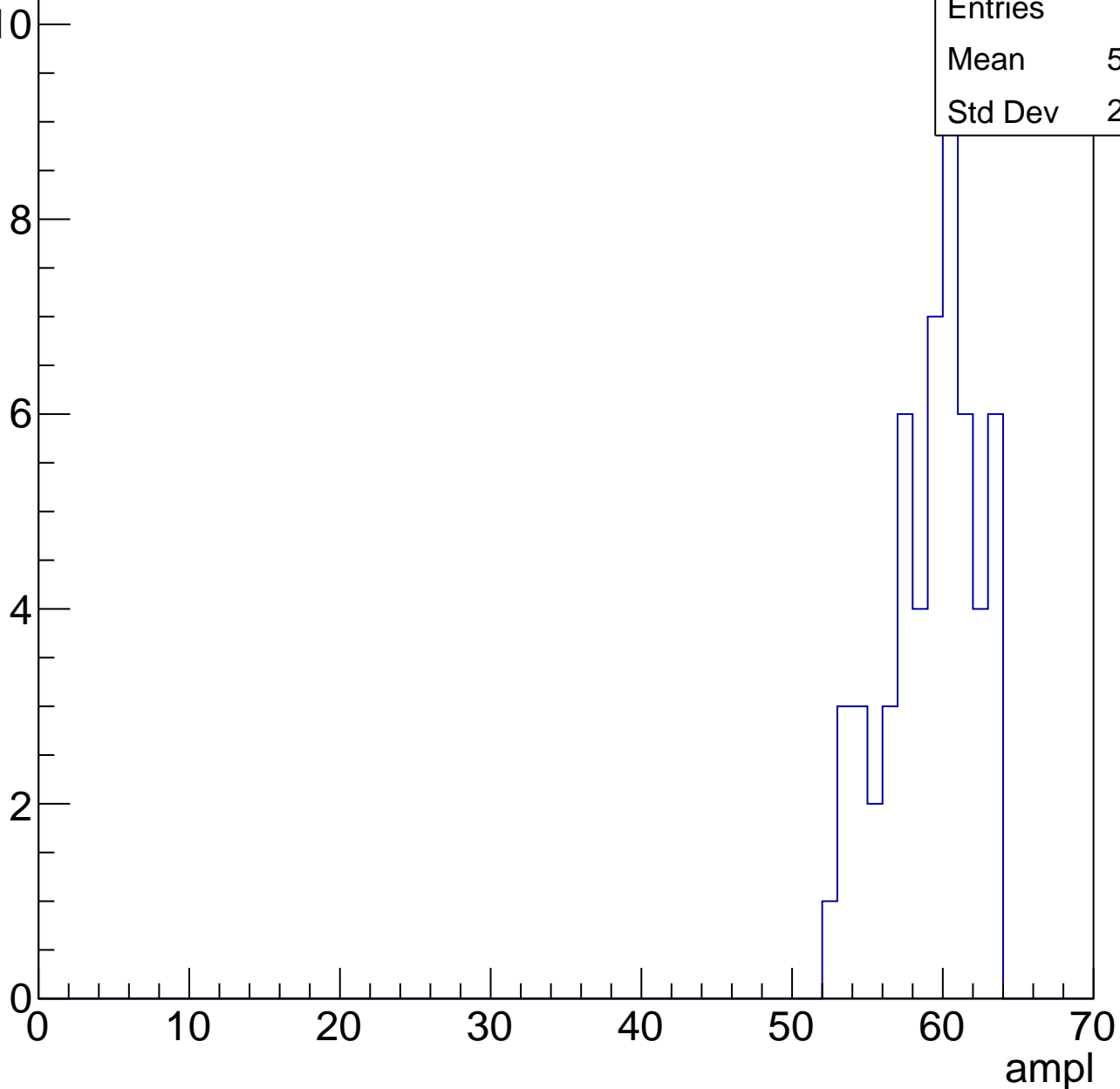


# B1L000S, U25-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	58.73
Std Dev	2.957

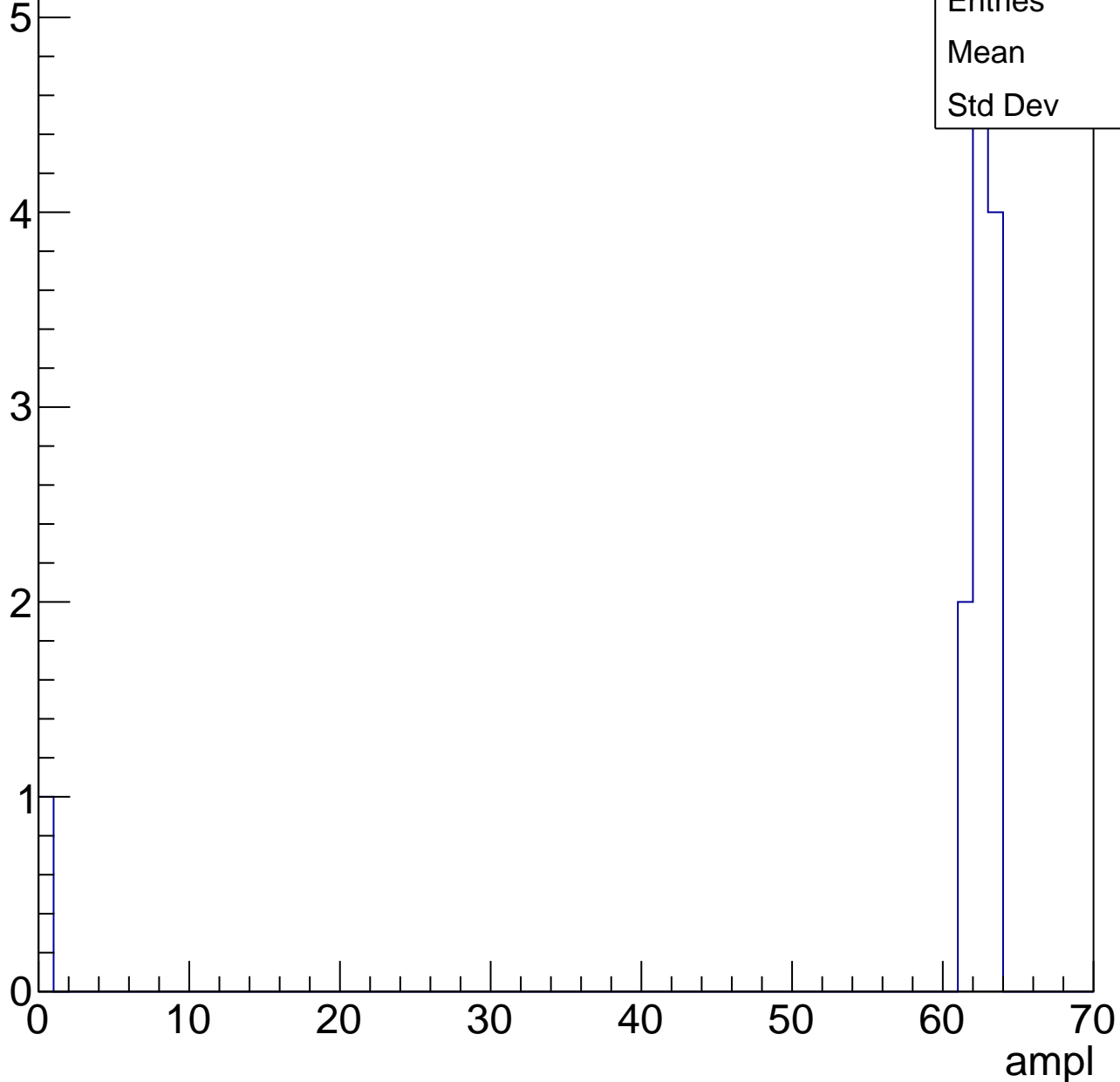


# B1L000S, U25-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	12
Mean	57
Std Dev	17.2

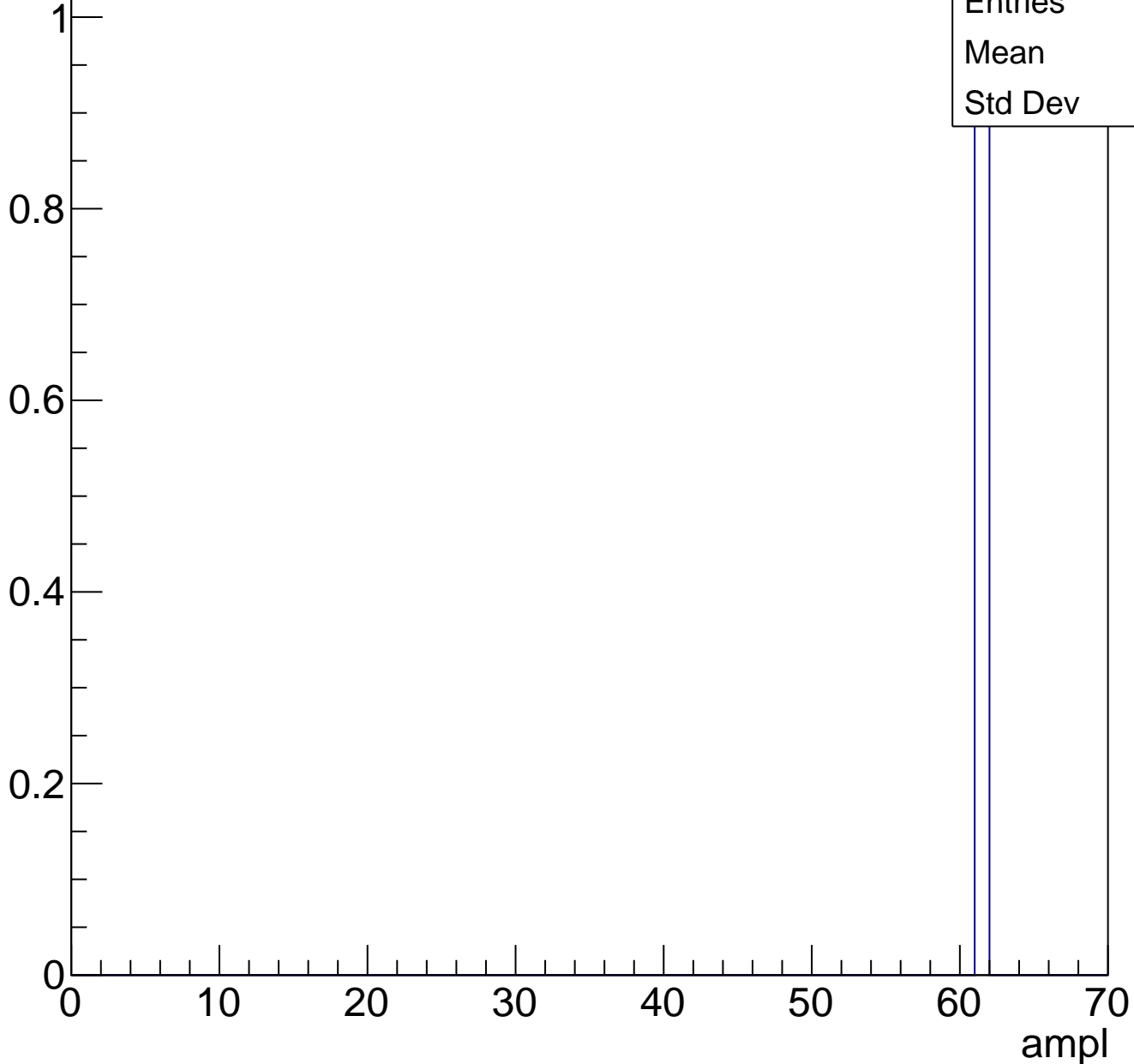




# B1L000S, U25-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch68, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	61
Mean	29.77
Std Dev	3.091

**Gaus mean : 30.6046**

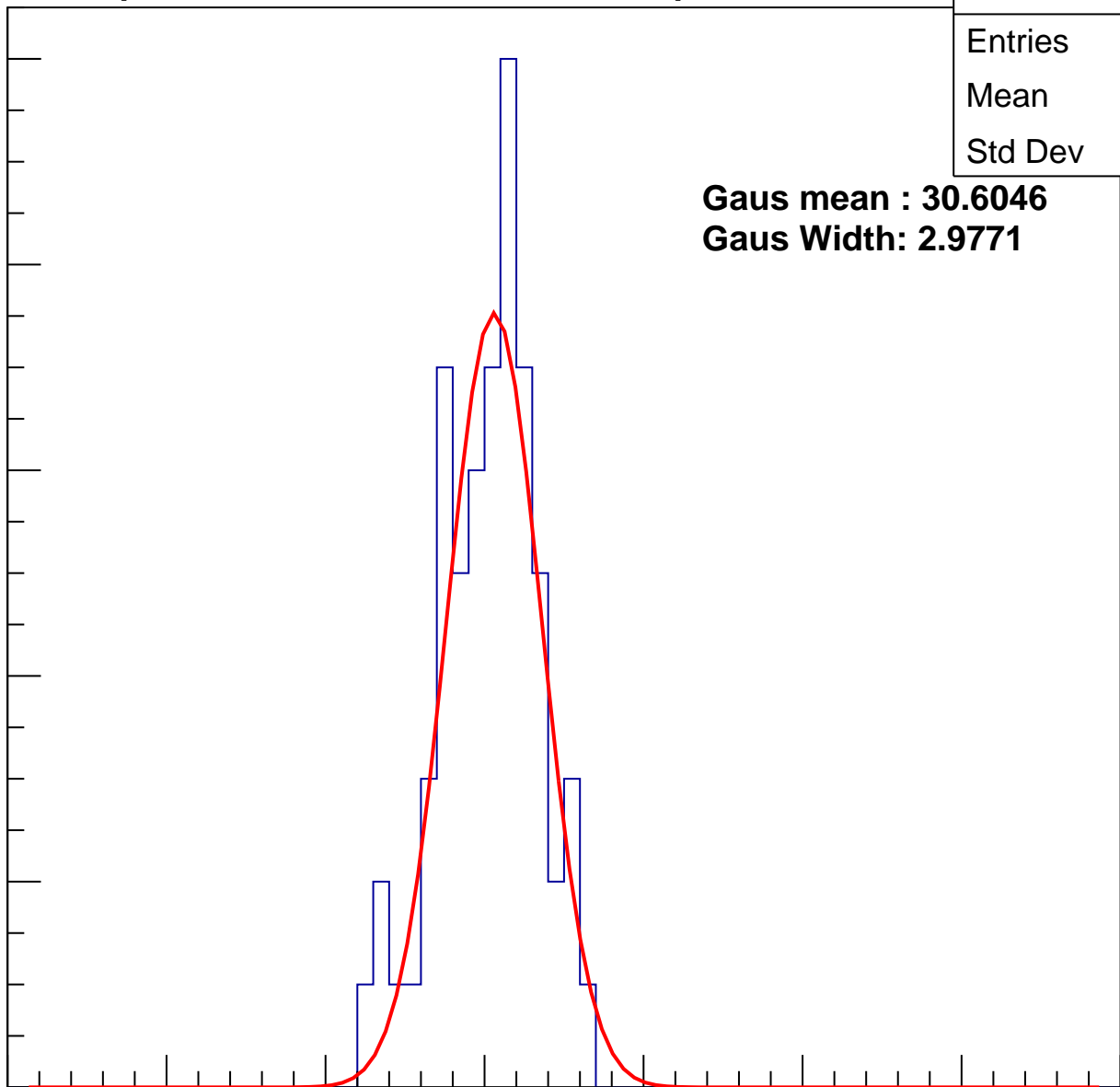
**Gaus Width: 2.9771**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch68, adc1

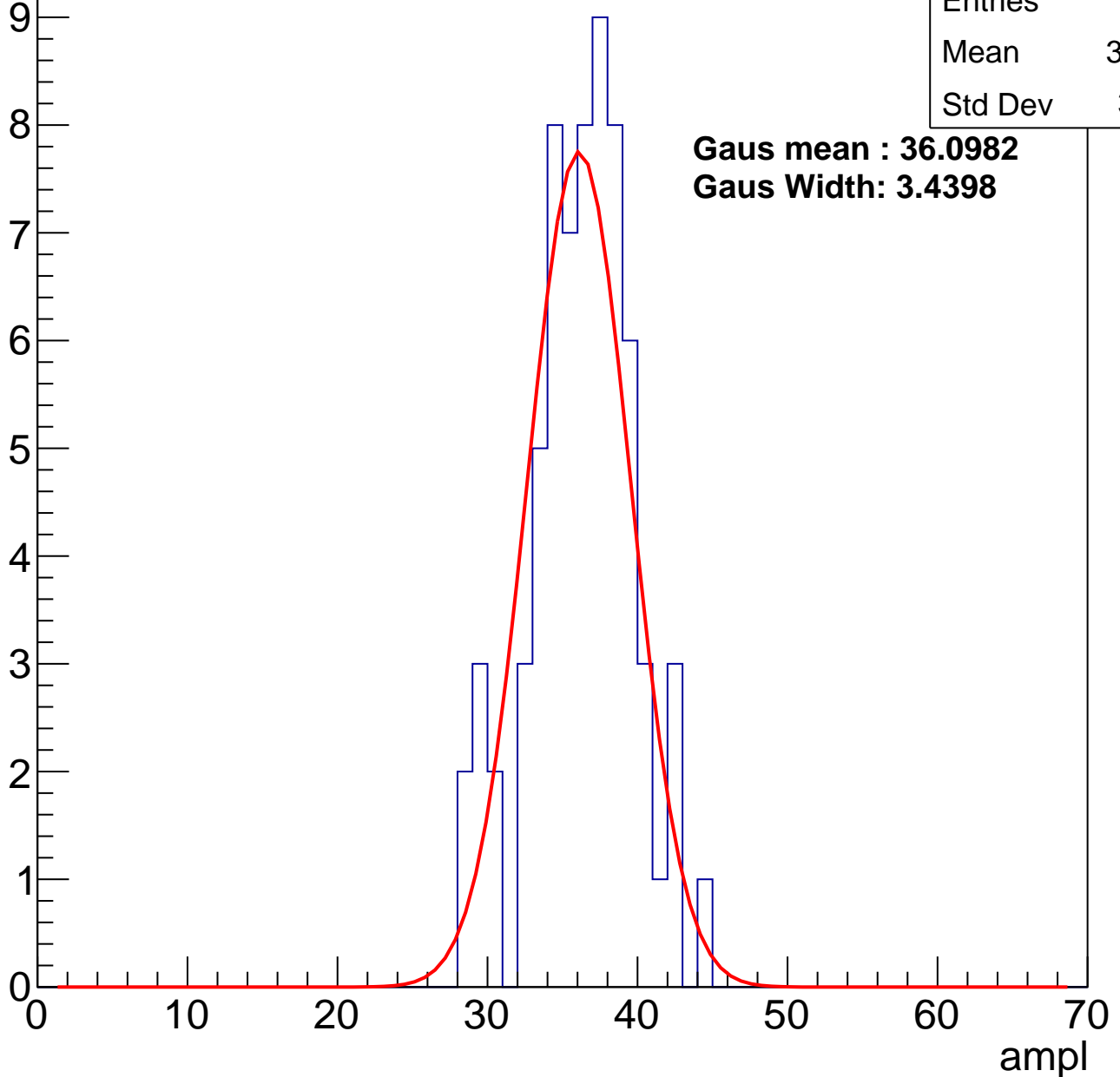
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.81
Std Dev	3.44

**Gaus mean : 36.0982**

**Gaus Width: 3.4398**



# B1L000S, U25-ch68, adc2

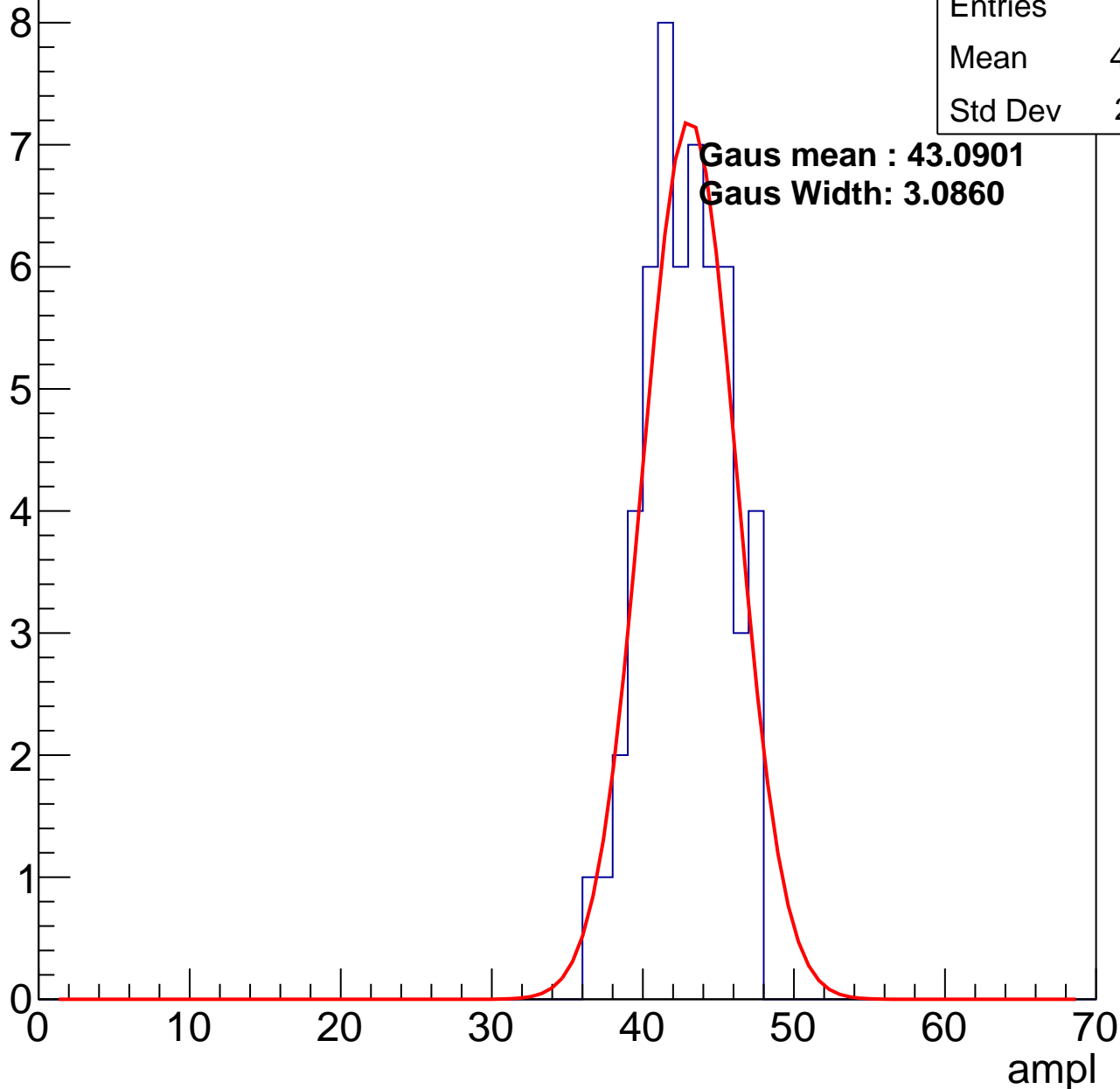
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.33
Std Dev	2.681

**Gaus mean : 43.0901**

**Gaus Width: 3.0860**



# B1L000S, U25-ch68, adc3

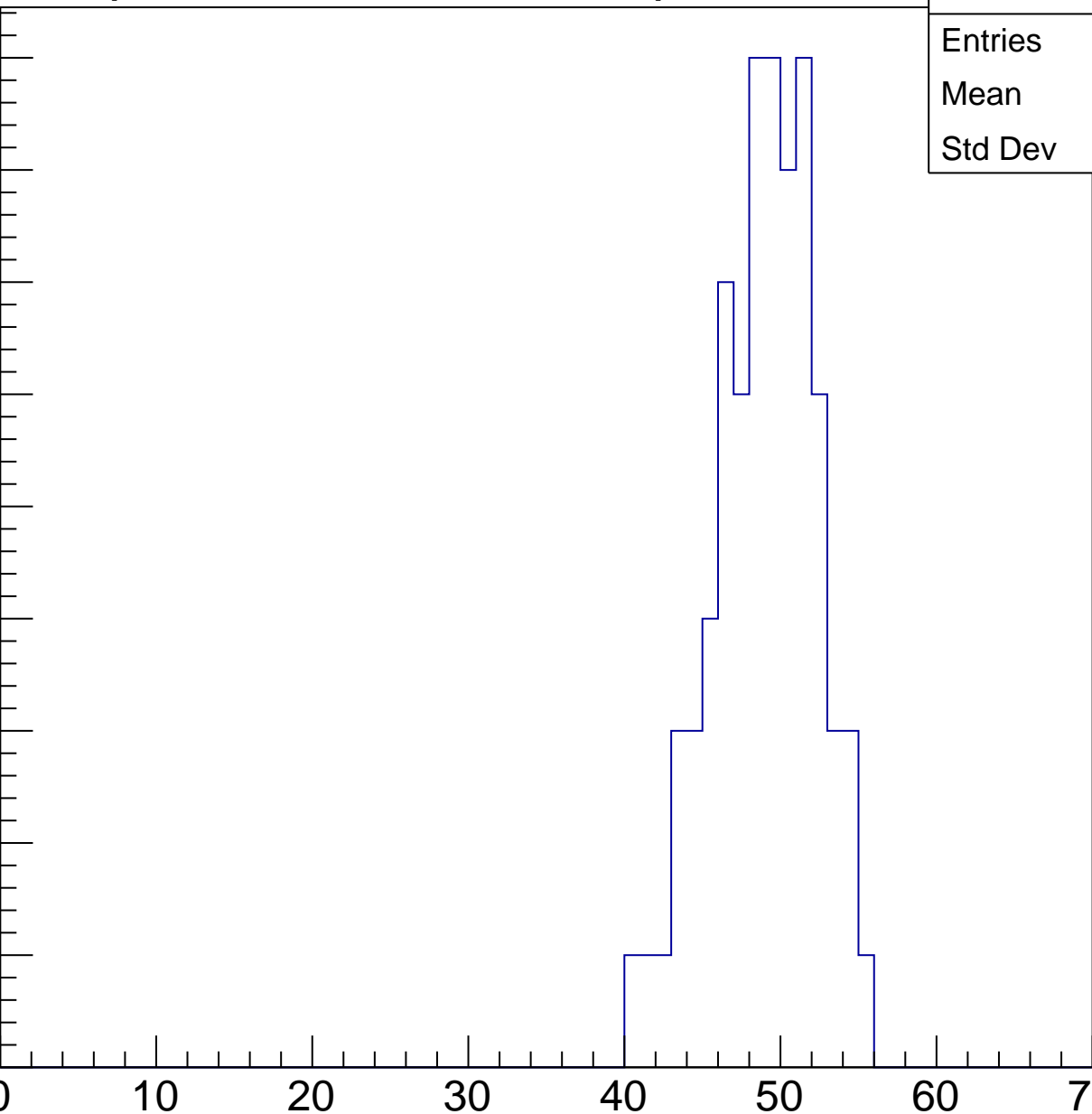
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	74
Mean	48.49
Std Dev	3.235

ampl

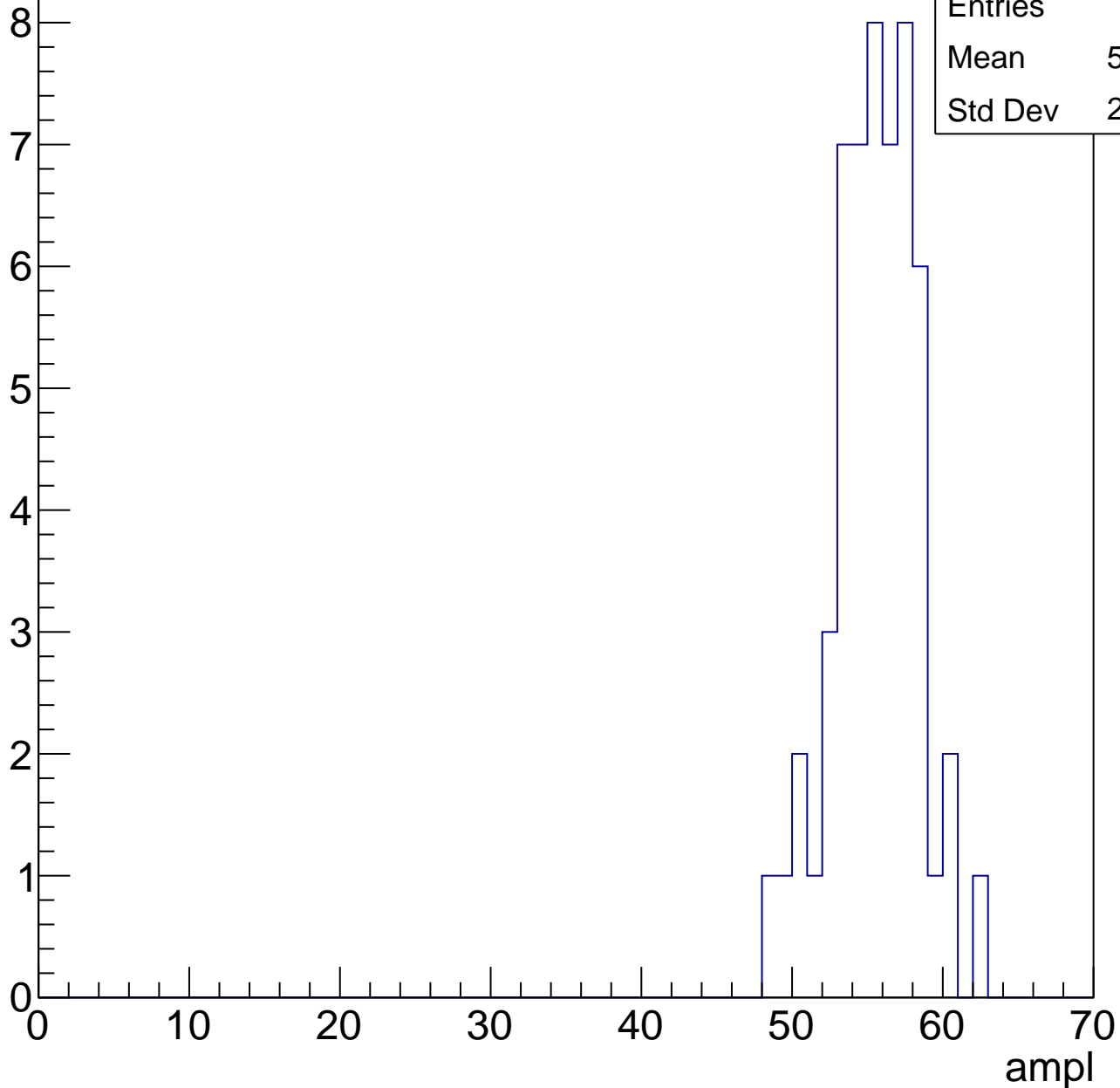


# B1L000S, U25-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

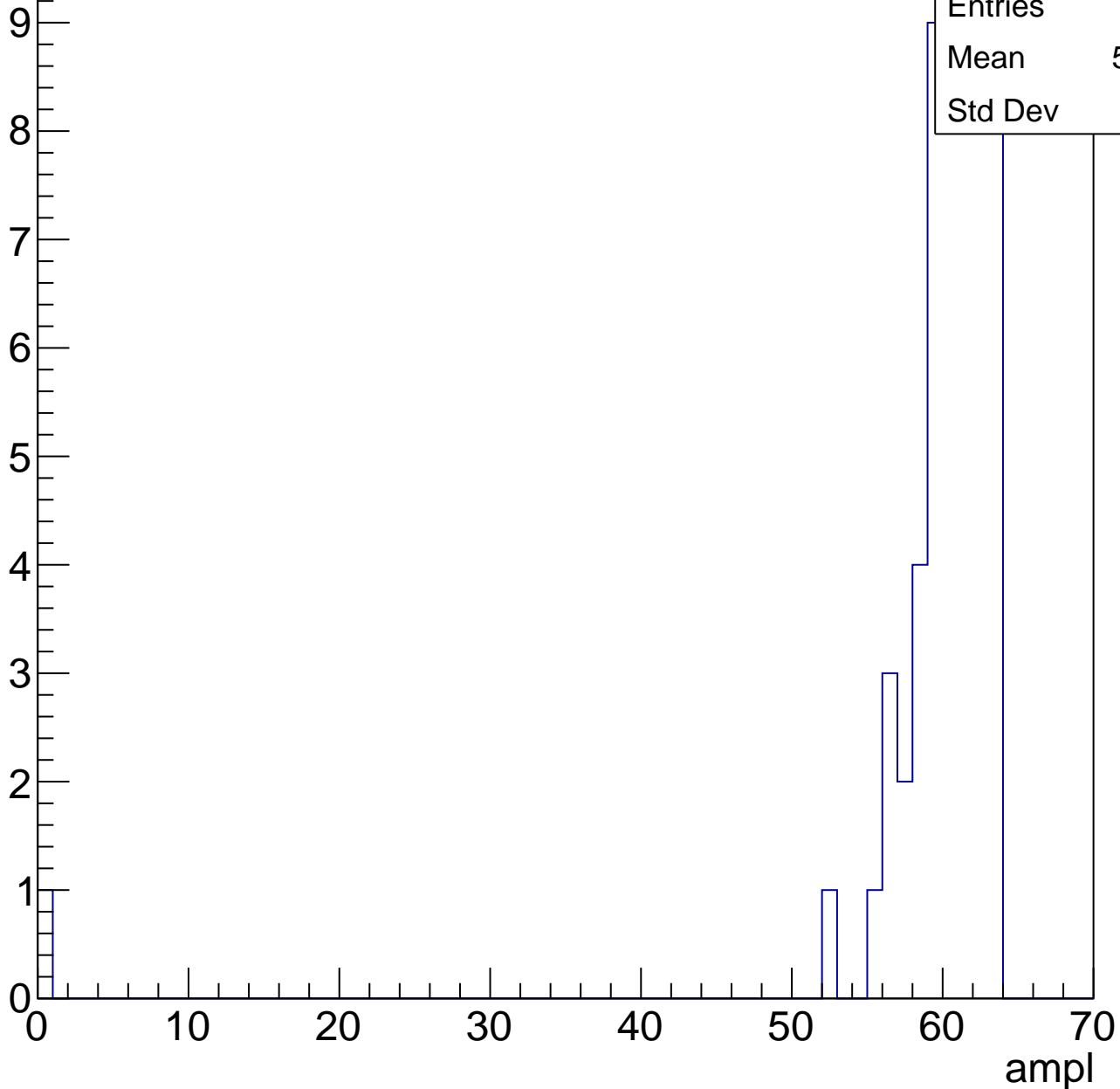
Entries	55
Mean	55.09
Std Dev	2.765



# B1L000S, U25-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch69, adc0

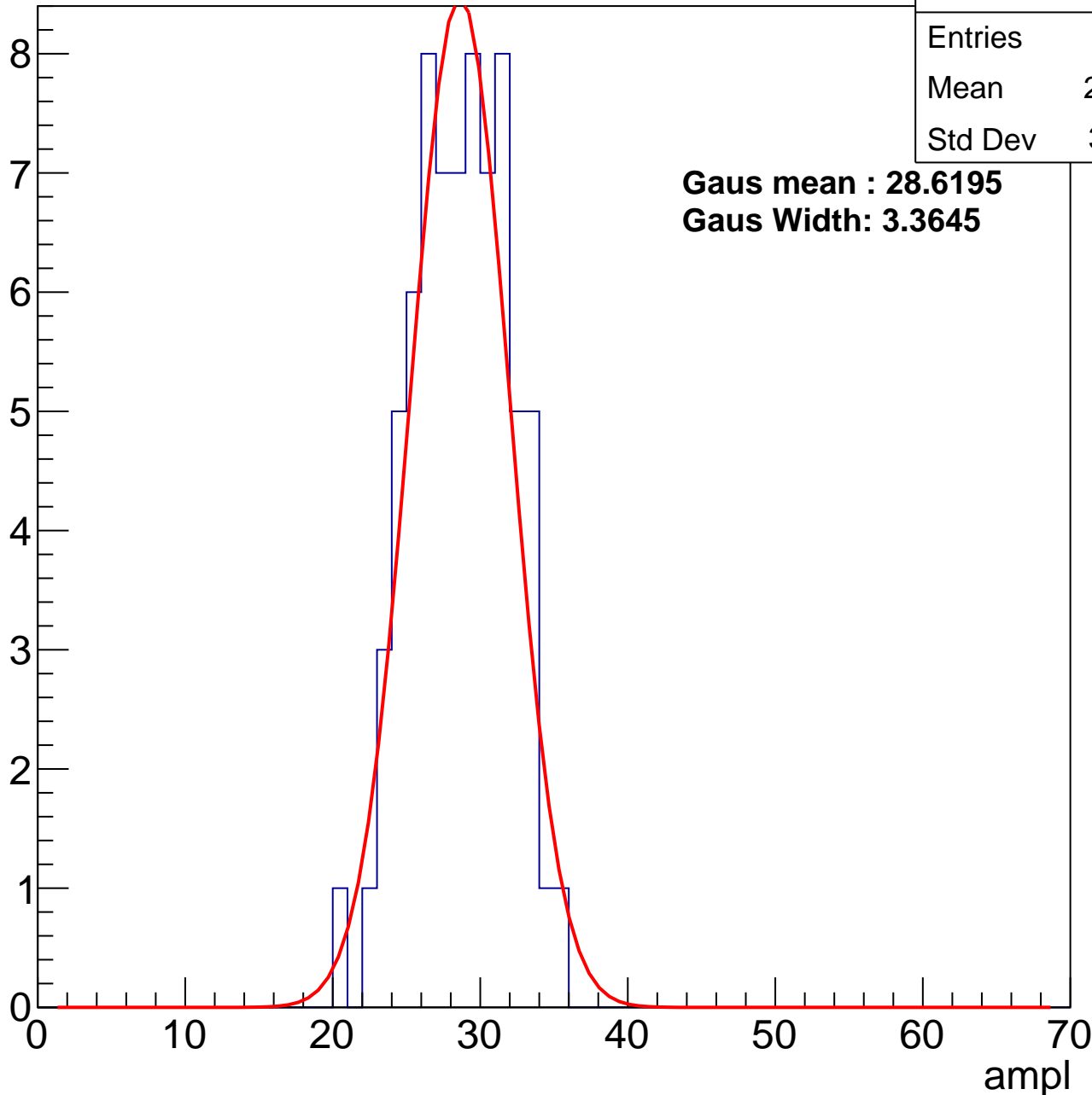
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.19
Std Dev	3.191

**Gaus mean : 28.6195**

**Gaus Width: 3.3645**



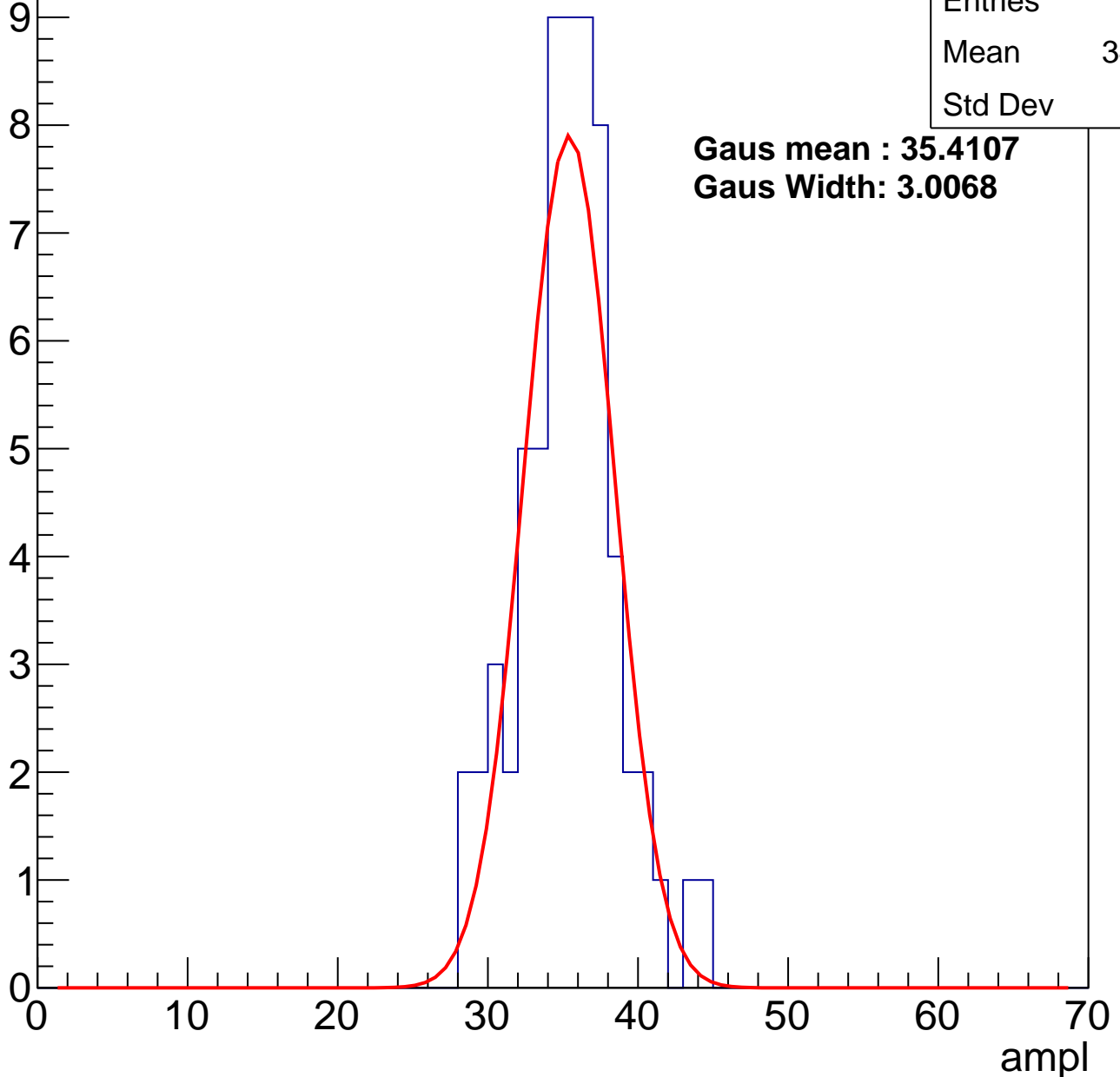
# B1L000S, U25-ch69, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.92
Std Dev	3.26

**Gaus mean : 35.4107**  
**Gaus Width: 3.0068**



# B1L000S, U25-ch69, adc2

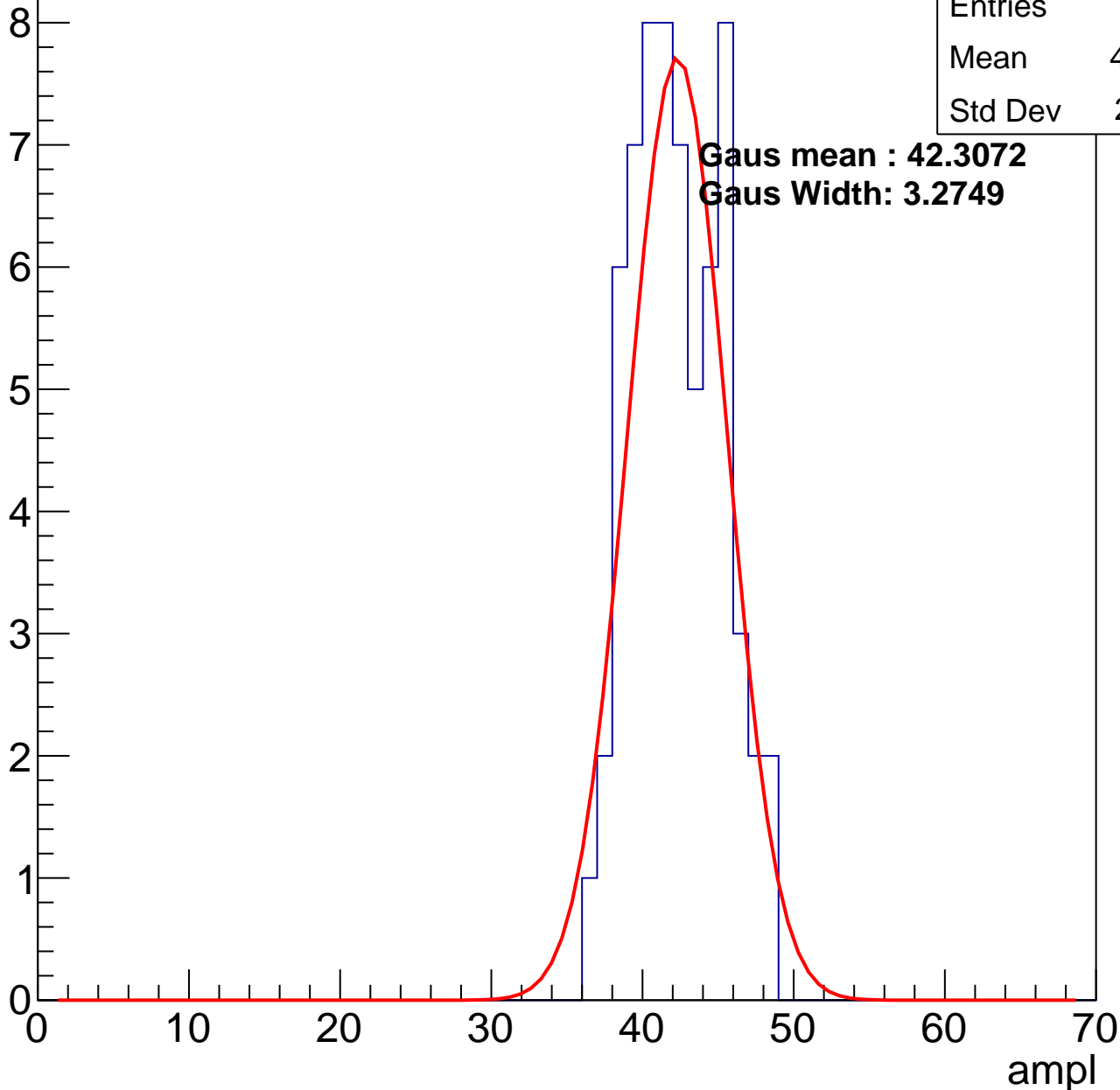
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	41.85
Std Dev	2.921

**Gaus mean : 42.3072**

**Gaus Width: 3.2749**

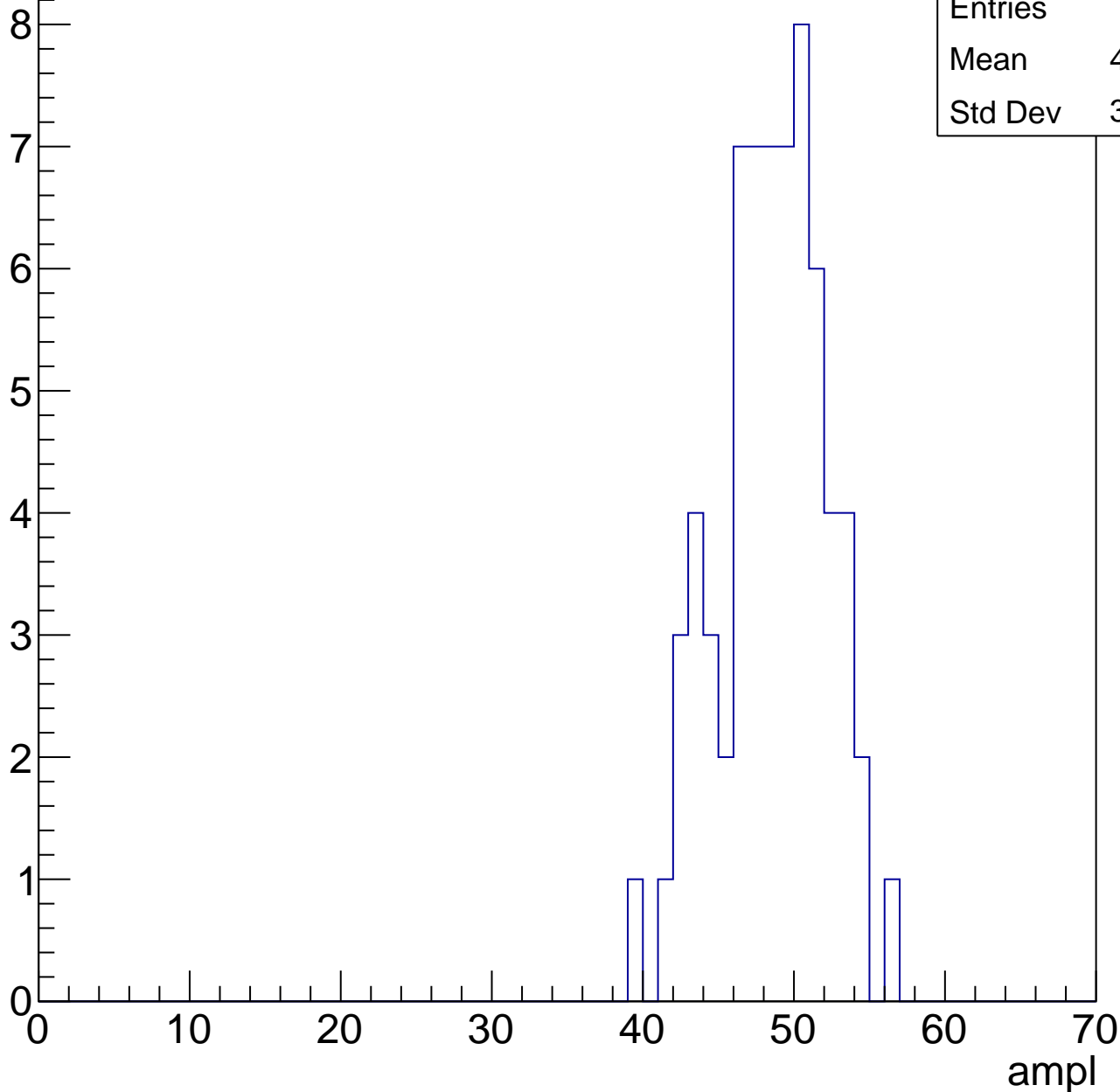


# B1L000S, U25-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

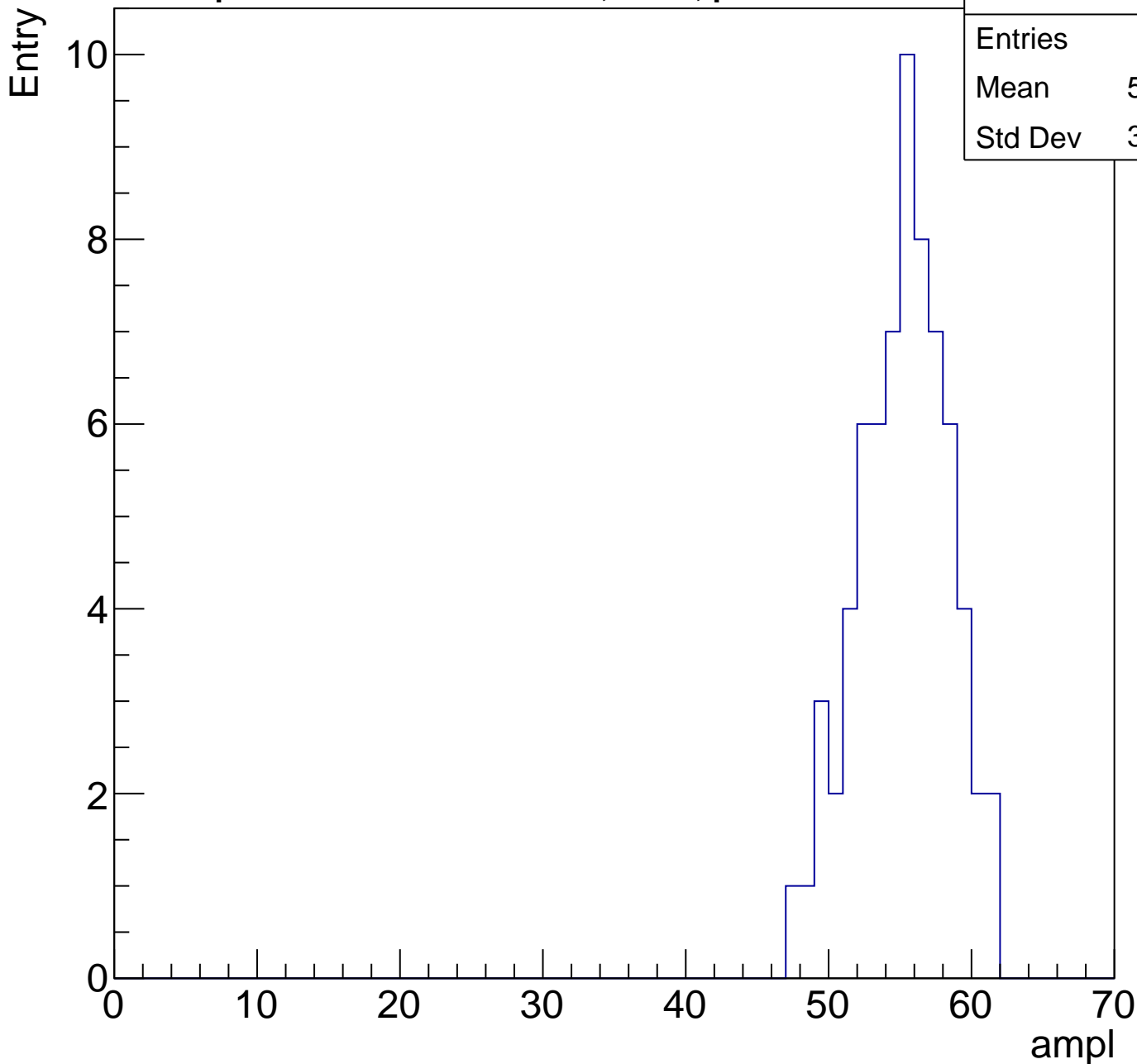
Entries	67
Mean	48.06
Std Dev	3.523



# B1L000S, U25-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	54.74
Std Dev	3.165

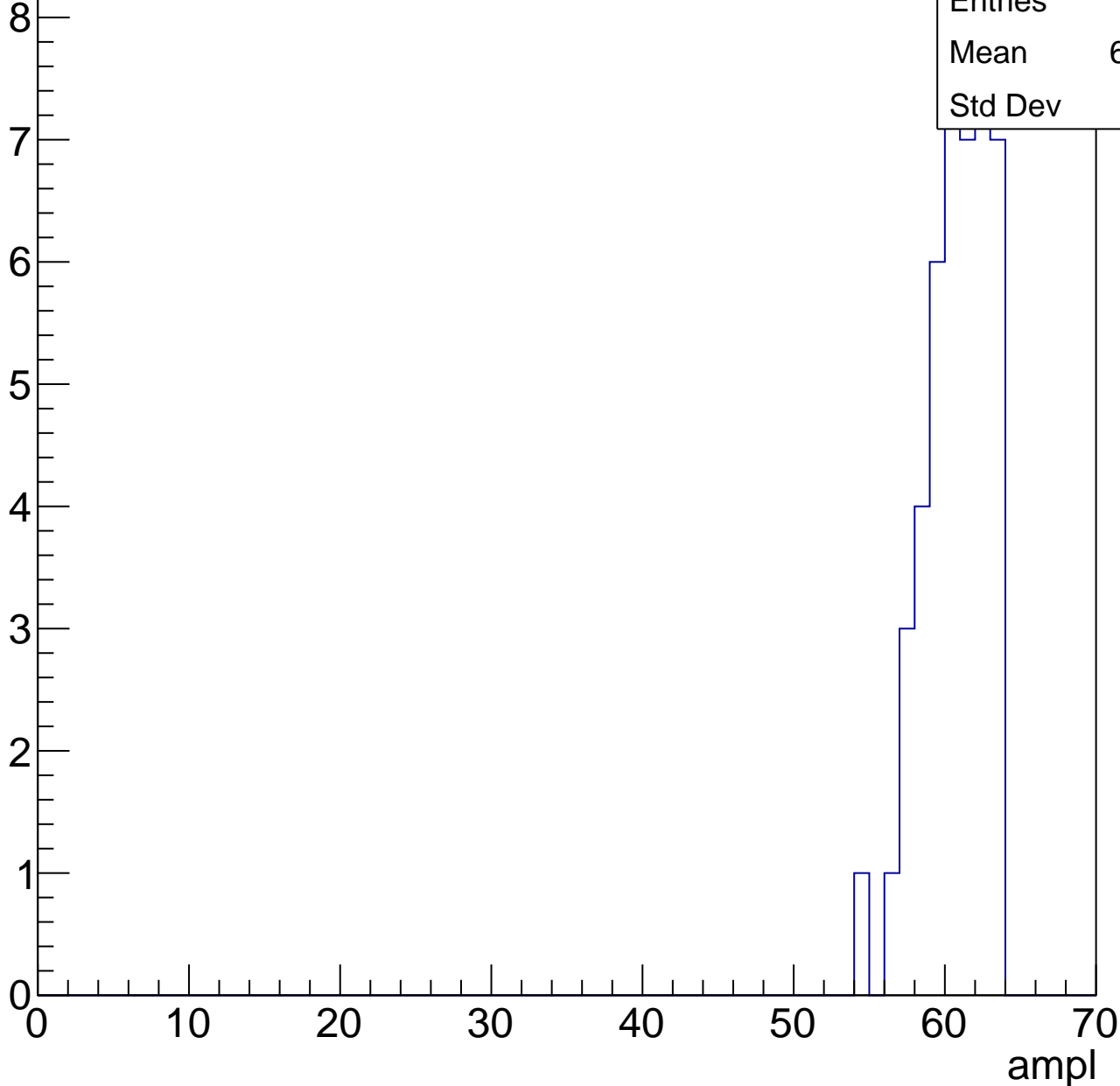


# B1L000S, U25-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	60.24
Std Dev	2.11



# B1L000S, U25-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch70, adc0

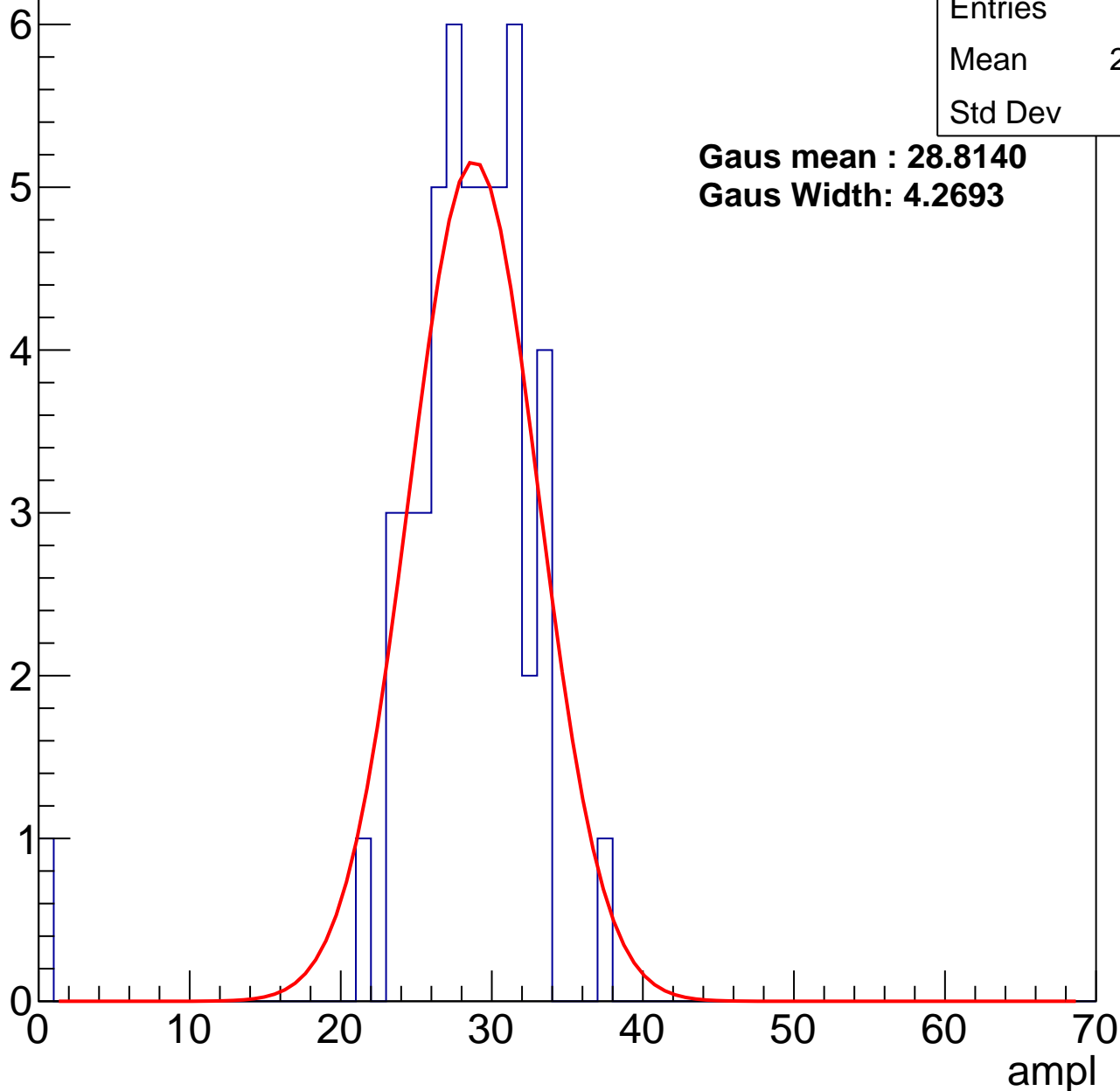
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	27.66
Std Dev	5.09

**Gaus mean : 28.8140**

**Gaus Width: 4.2693**



# B1L000S, U25-ch70, adc1

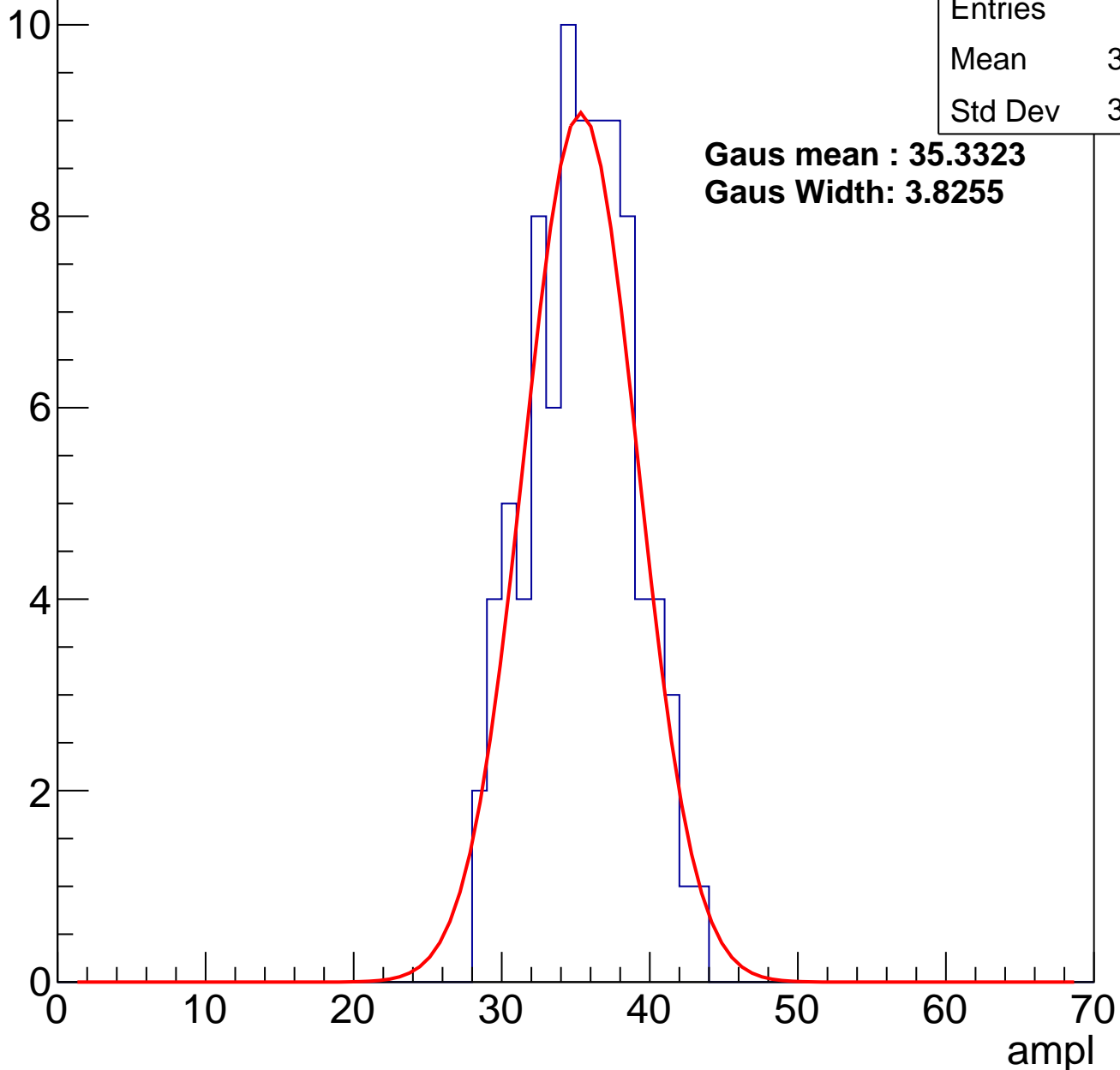
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	87
Mean	34.94
Std Dev	3.455

**Gaus mean : 35.3323**

**Gaus Width: 3.8255**

Entry



# B1L000S, U25-ch70, adc2

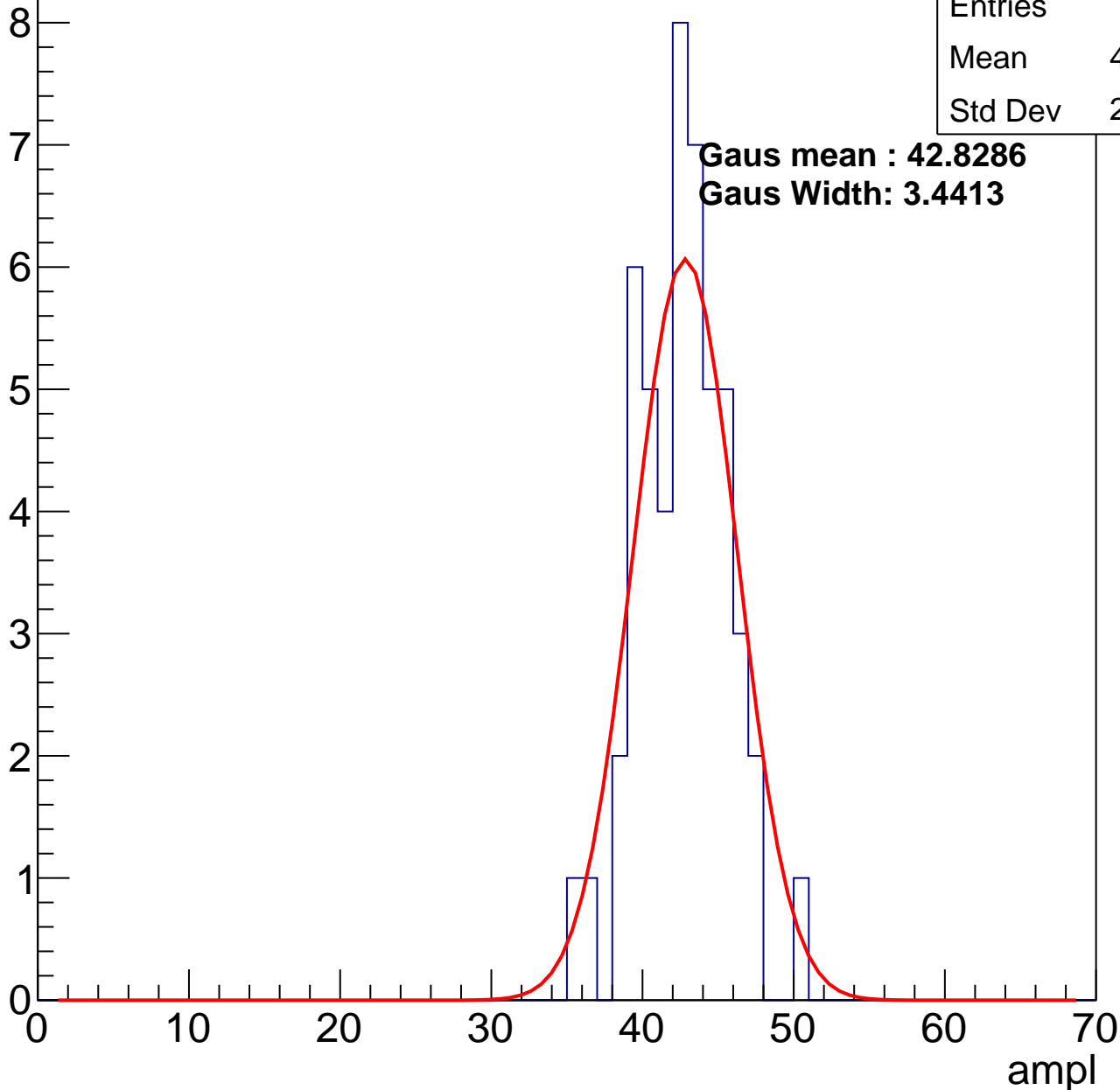
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	42.18
Std Dev	2.924

**Gaus mean : 42.8286**

**Gaus Width: 3.4413**

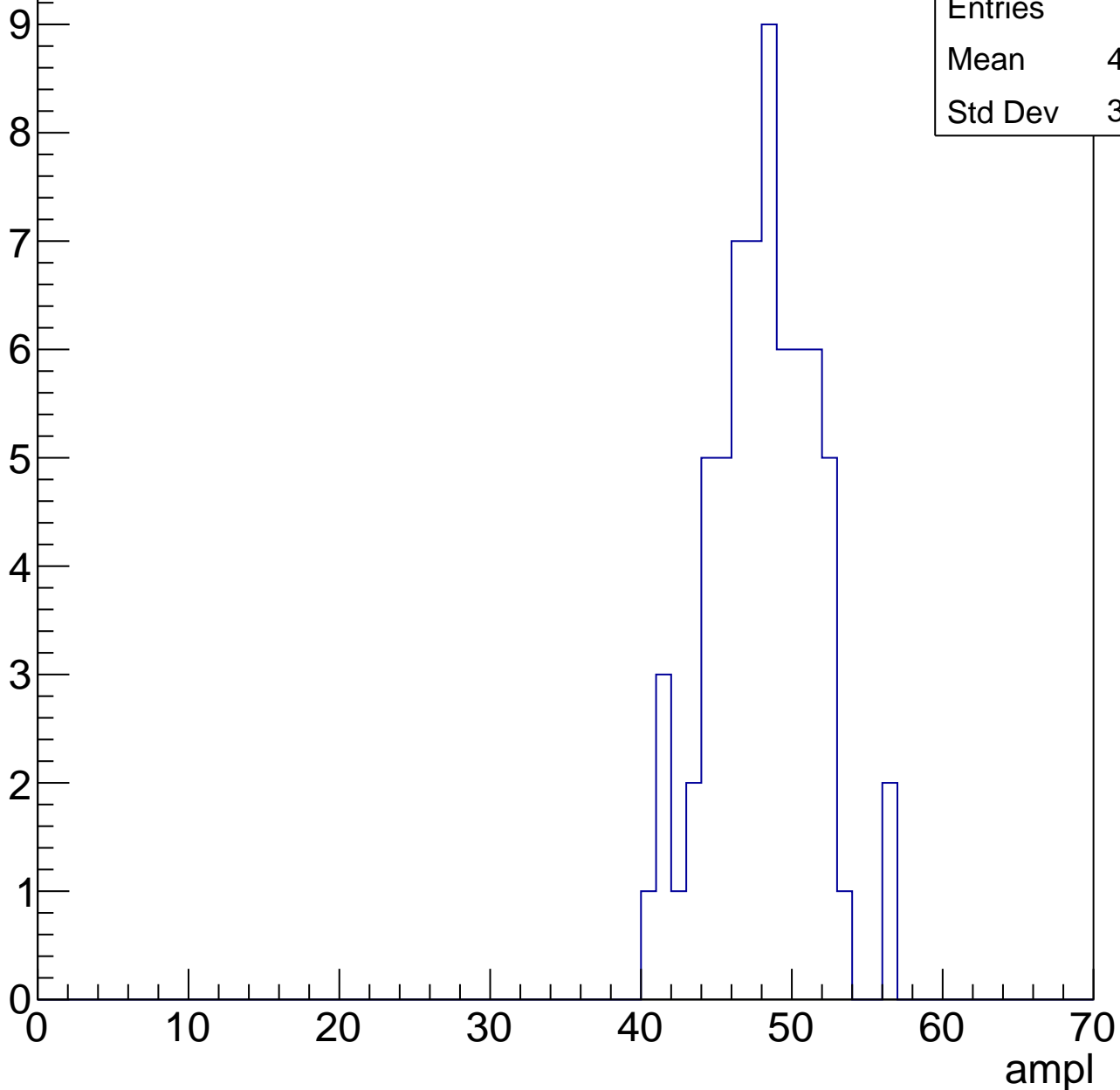


# B1L000S, U25-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	47.64
Std Dev	3.405

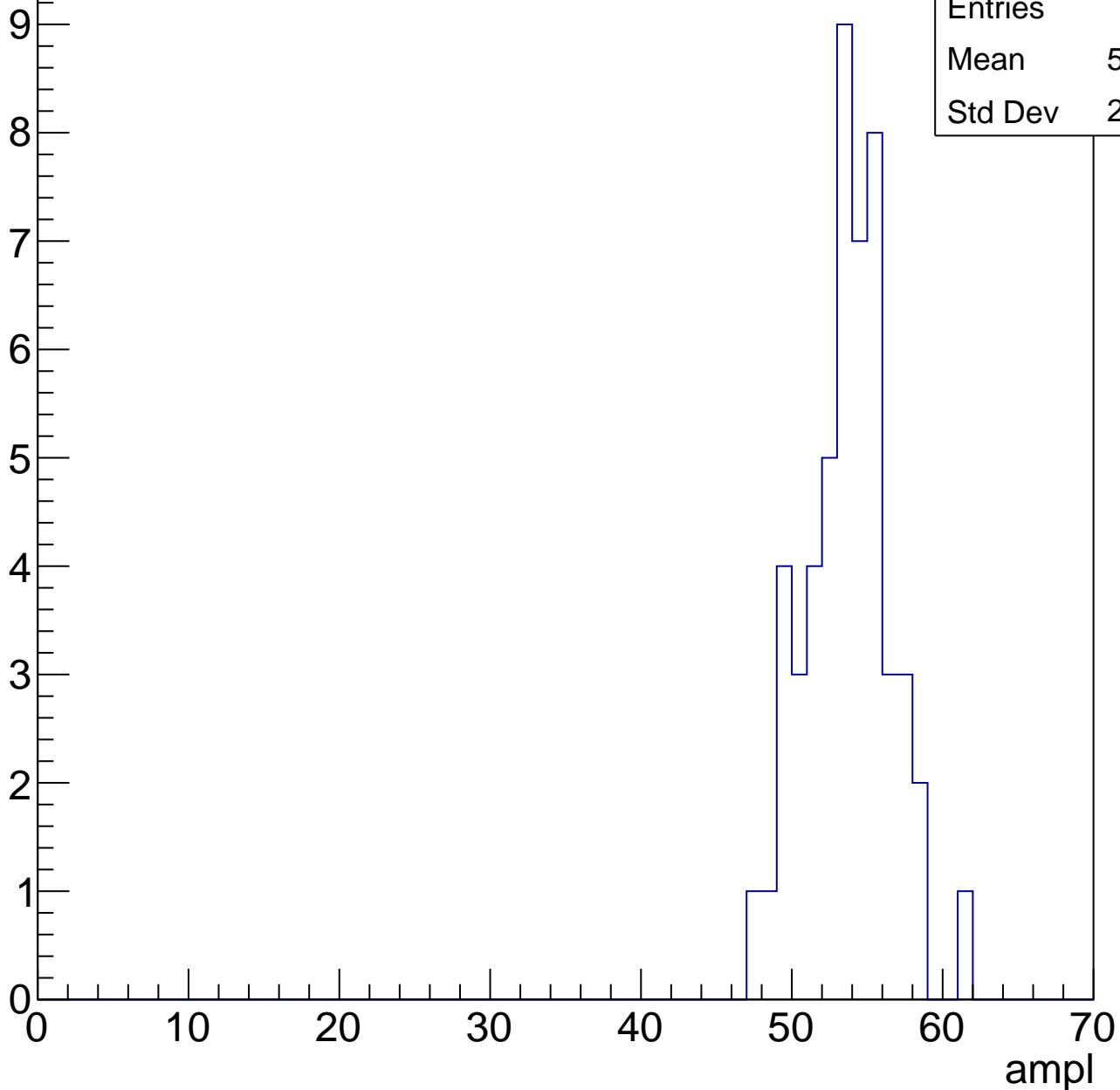


# B1L000S, U25-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

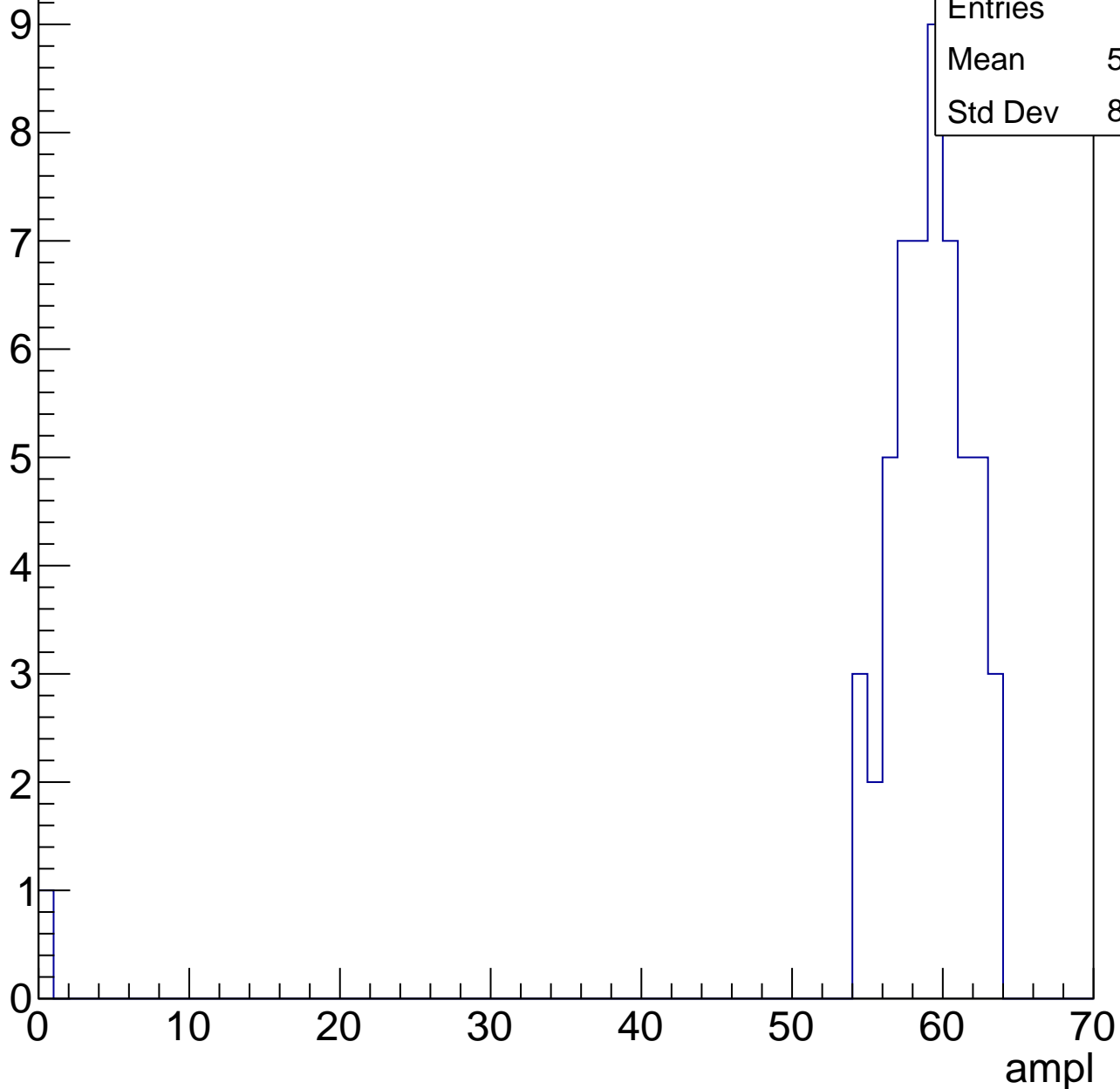
Entries	51
Mean	53.25
Std Dev	2.792



# B1L000S, U25-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

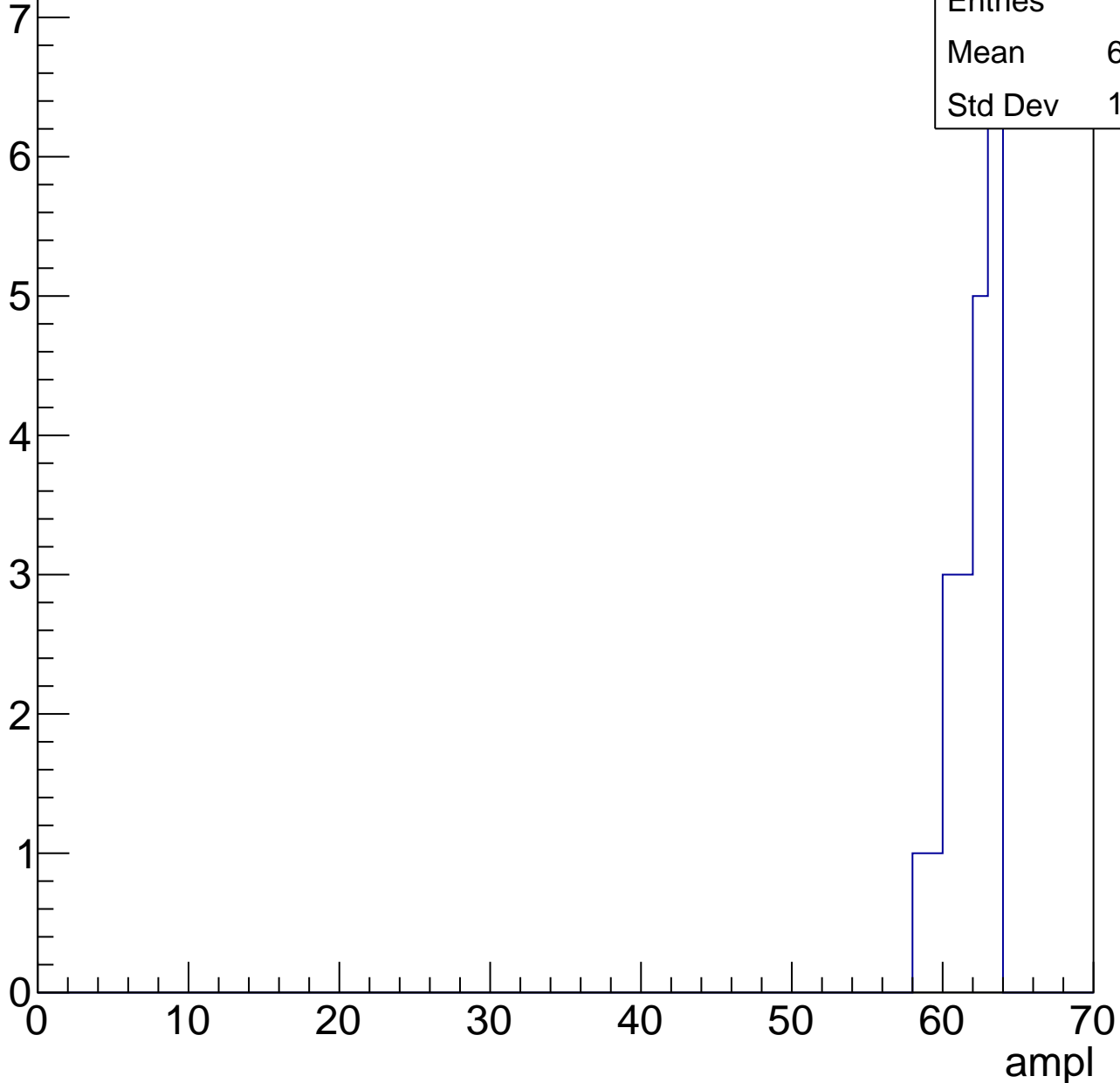


# B1L000S, U25-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.55
Std Dev	1.465





# B1L000S, U25-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch71, adc0

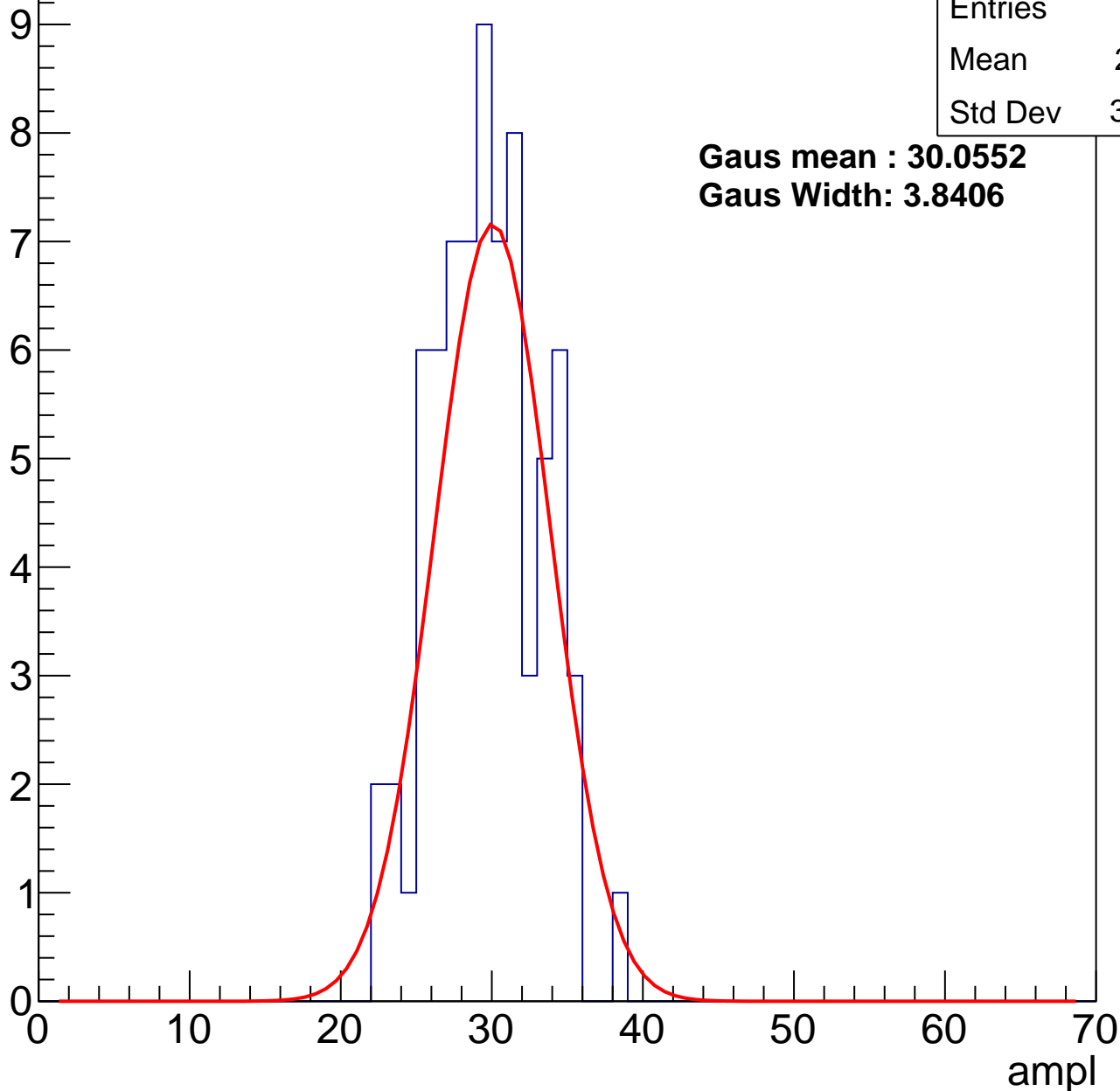
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	29.21
Std Dev	3.436

**Gaus mean : 30.0552**

**Gaus Width: 3.8406**



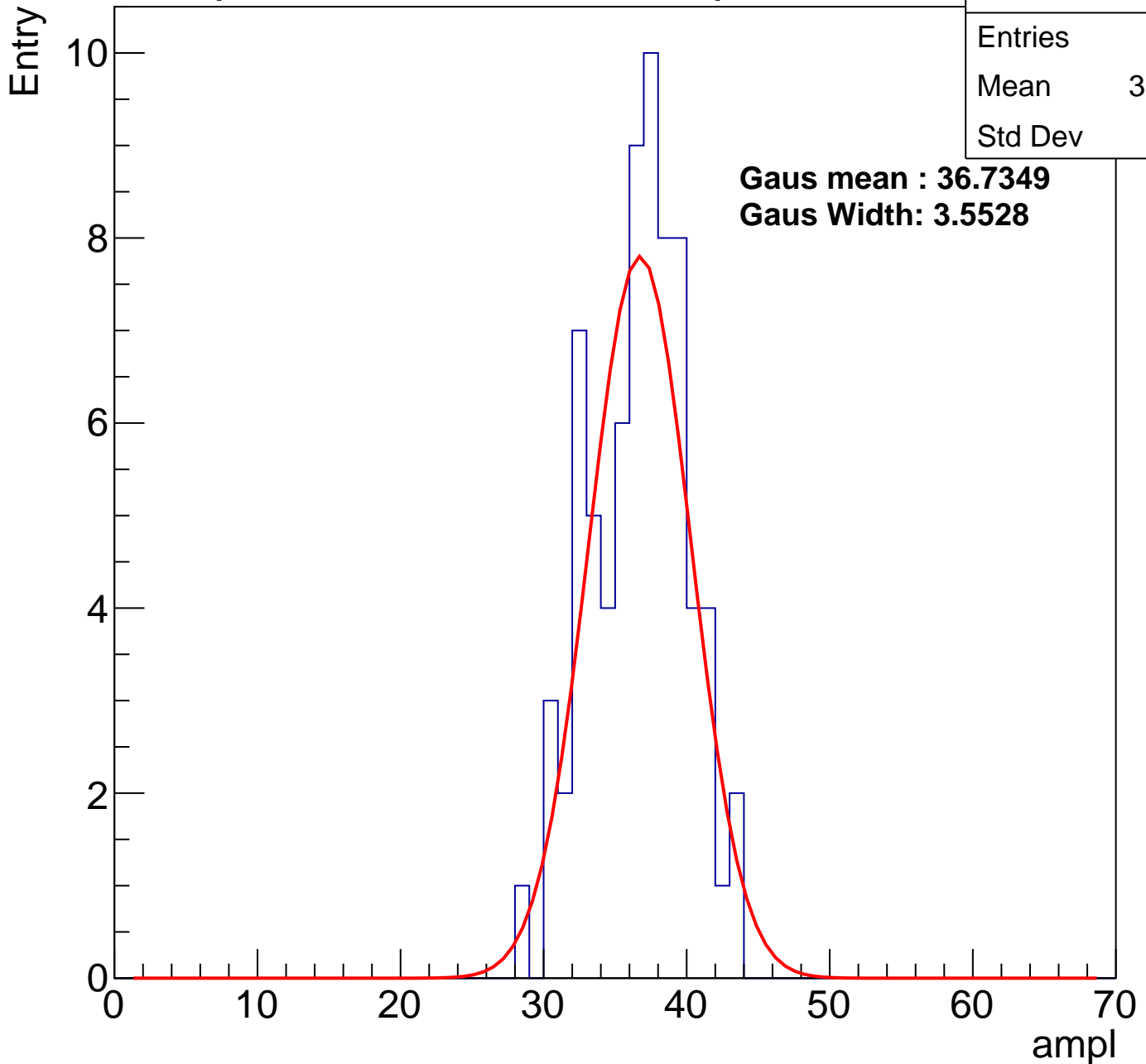
# B1L000S, U25-ch71, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	36.18
Std Dev	3.31

**Gaus mean : 36.7349**

**Gaus Width: 3.5528**



# B1L000S, U25-ch71, adc2

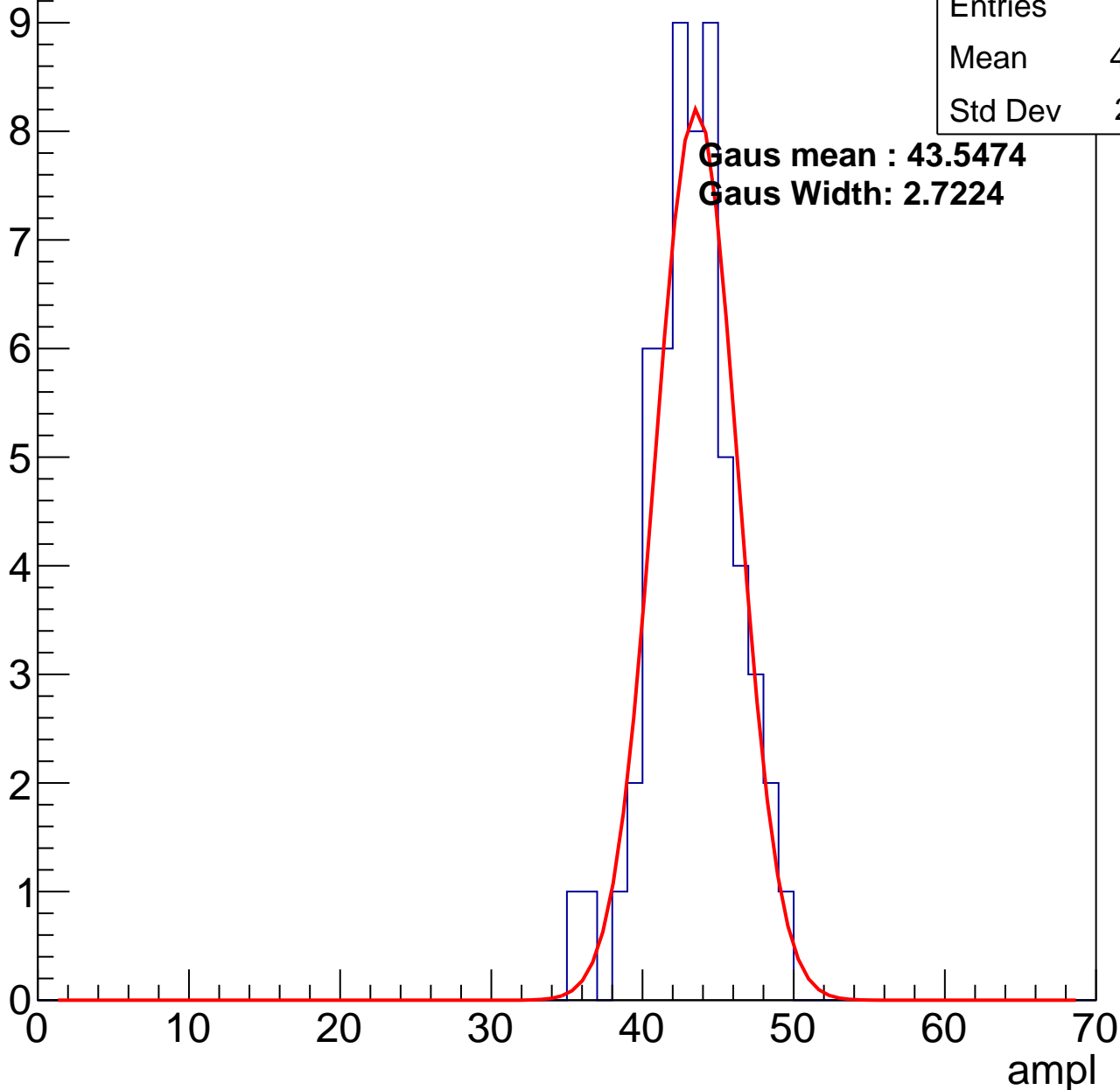
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.86
Std Dev	2.801

**Gaus mean : 43.5474**

**Gaus Width: 2.7224**

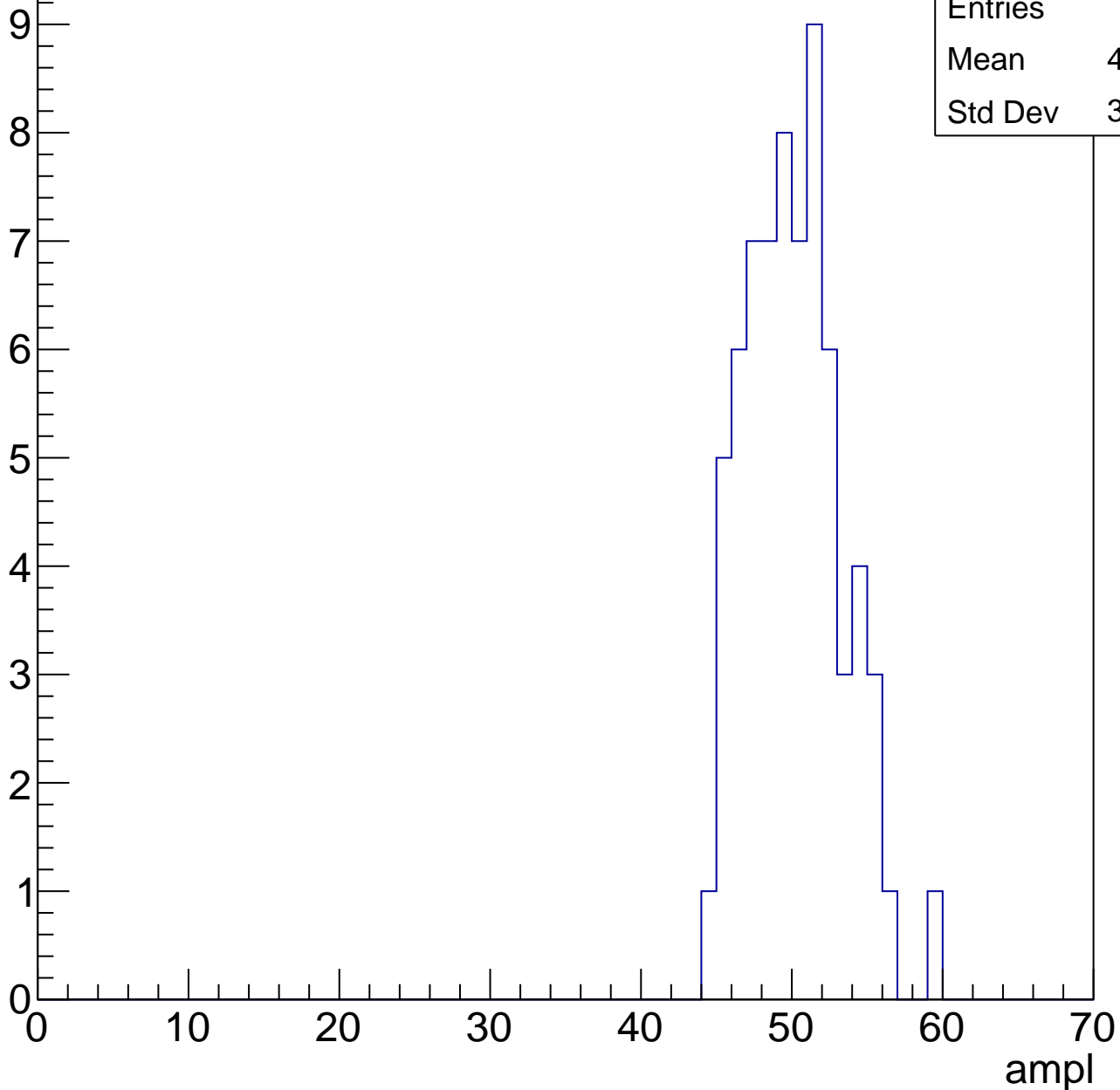


# B1L000S, U25-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

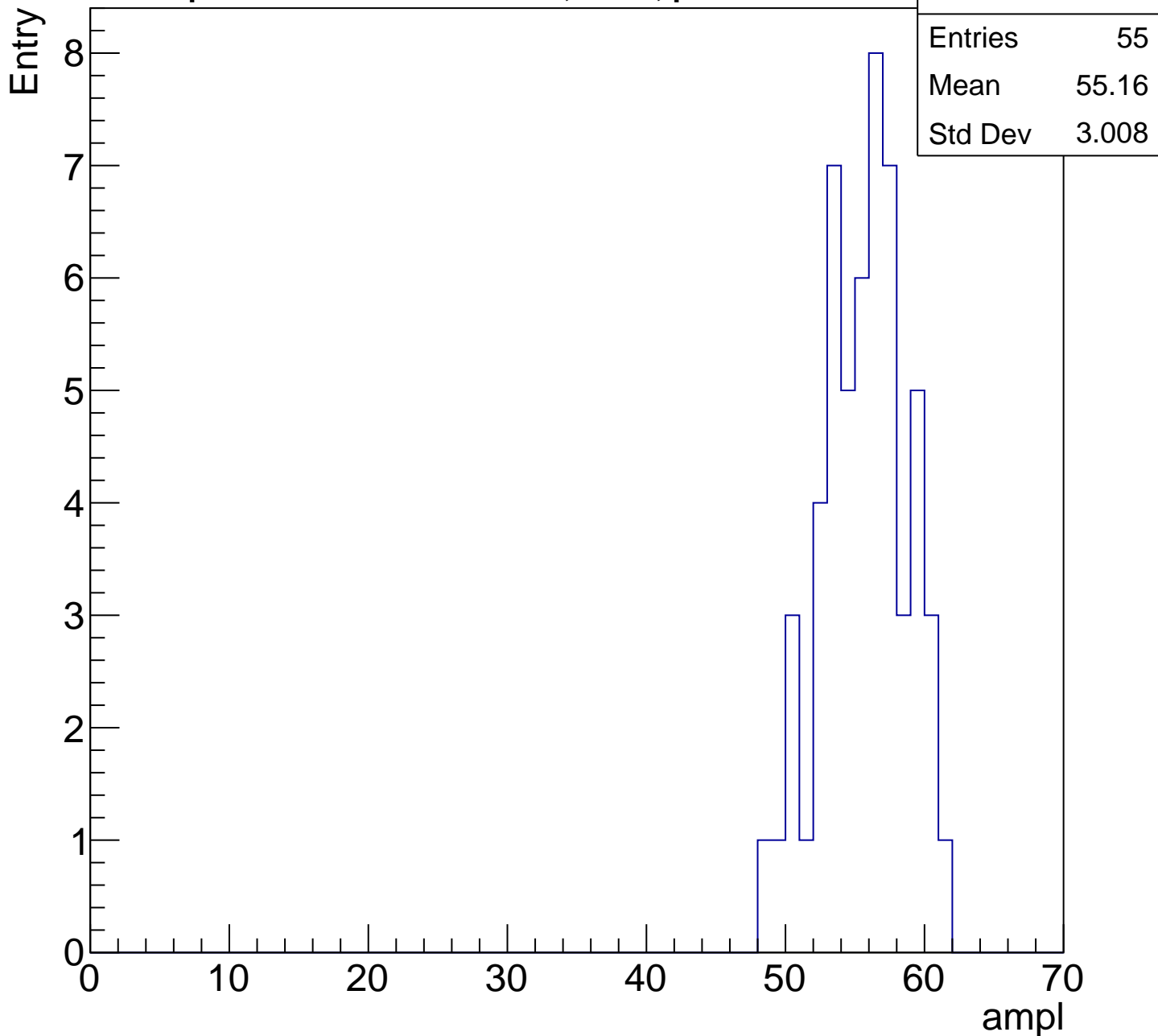
Entry

Entries	68
Mean	49.68
Std Dev	3.127



# B1L000S, U25-ch71, adc4

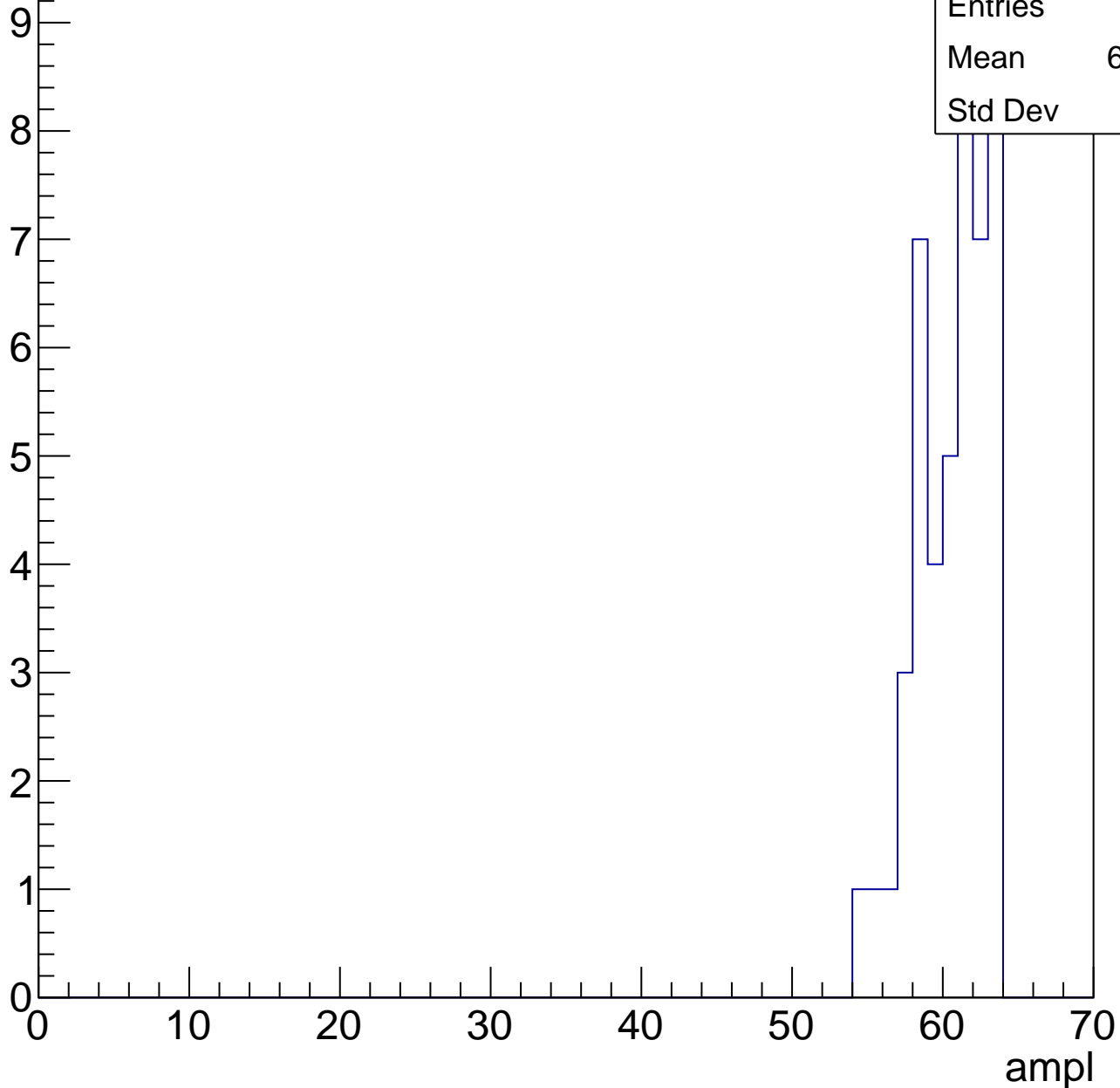
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

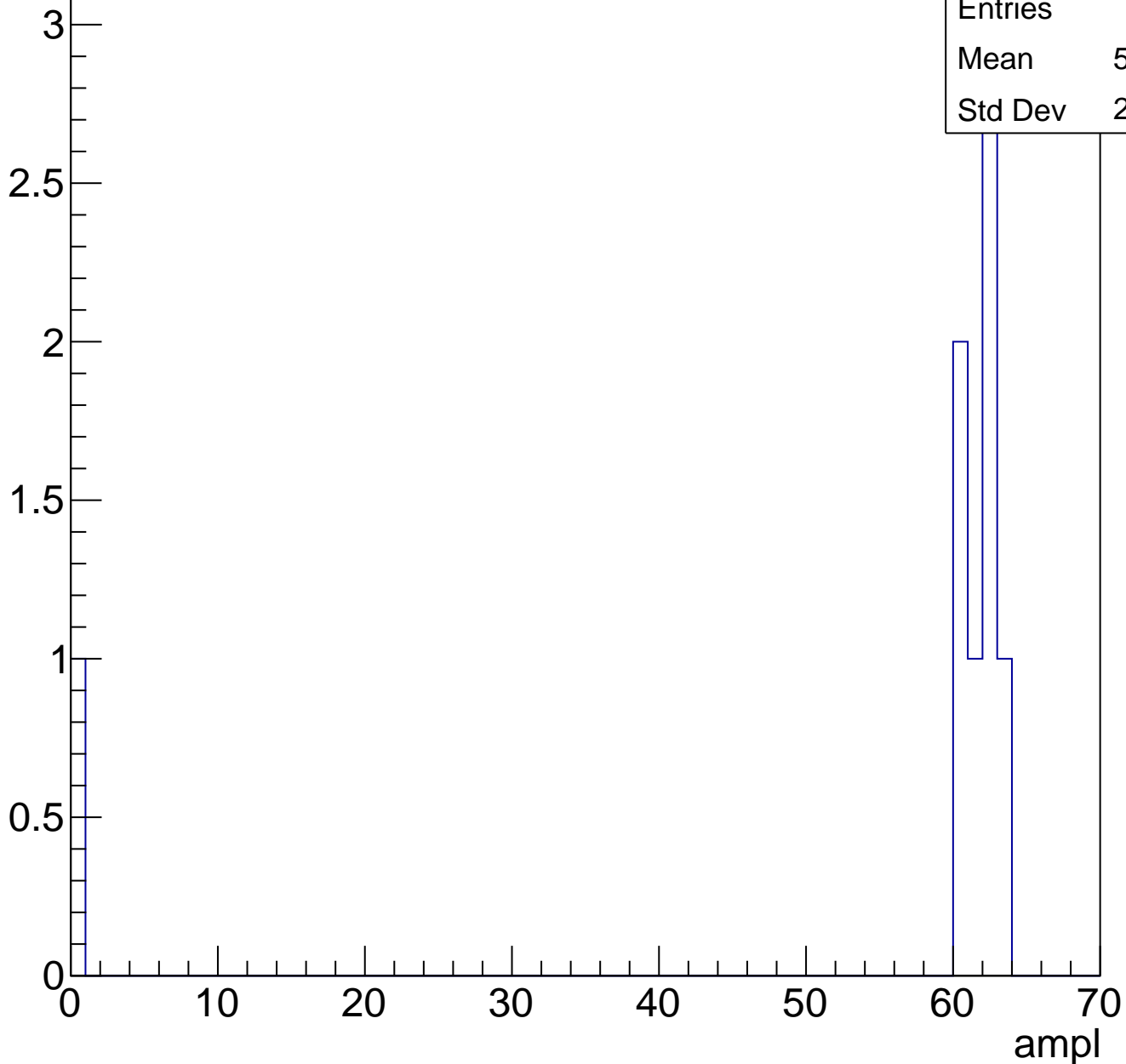
Entry



# B1L000S, U25-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch72, adc0

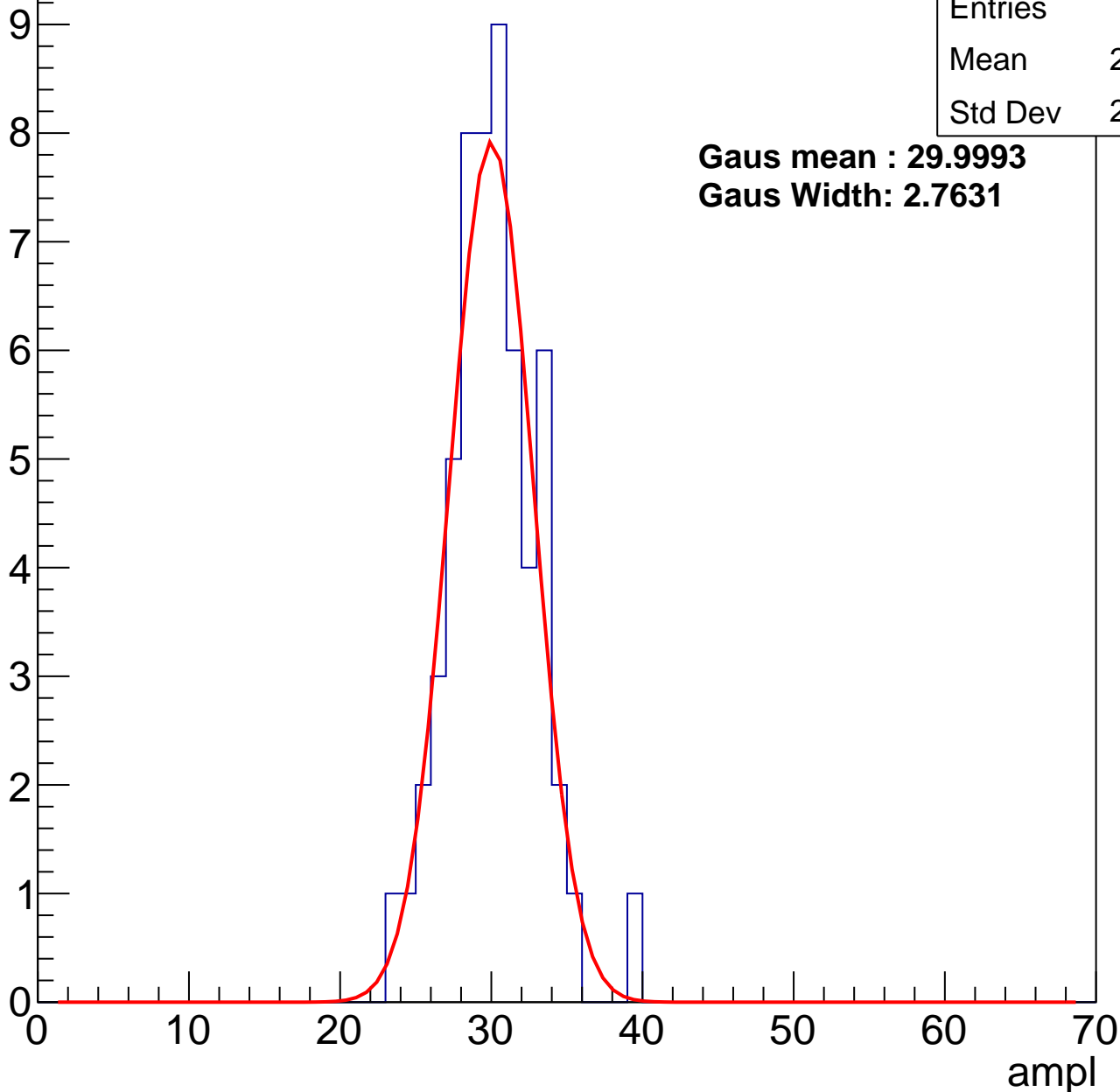
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.65
Std Dev	2.887

**Gaus mean : 29.9993**

**Gaus Width: 2.7631**



# B1L000S, U25-ch72, adc1

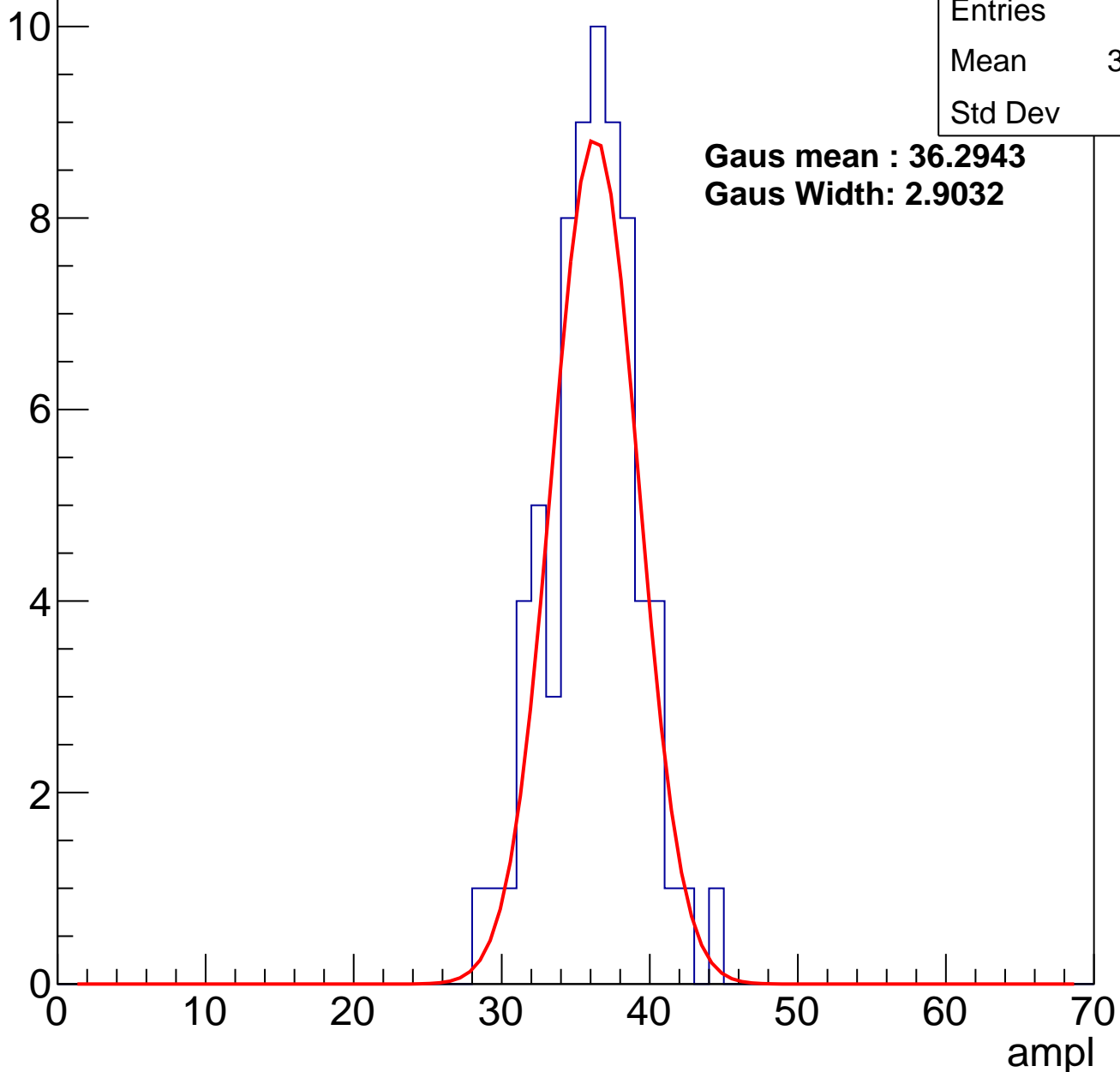
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	35.67
Std Dev	3.06

**Gaus mean : 36.2943**

**Gaus Width: 2.9032**

Entry



# B1L000S, U25-ch72, adc2

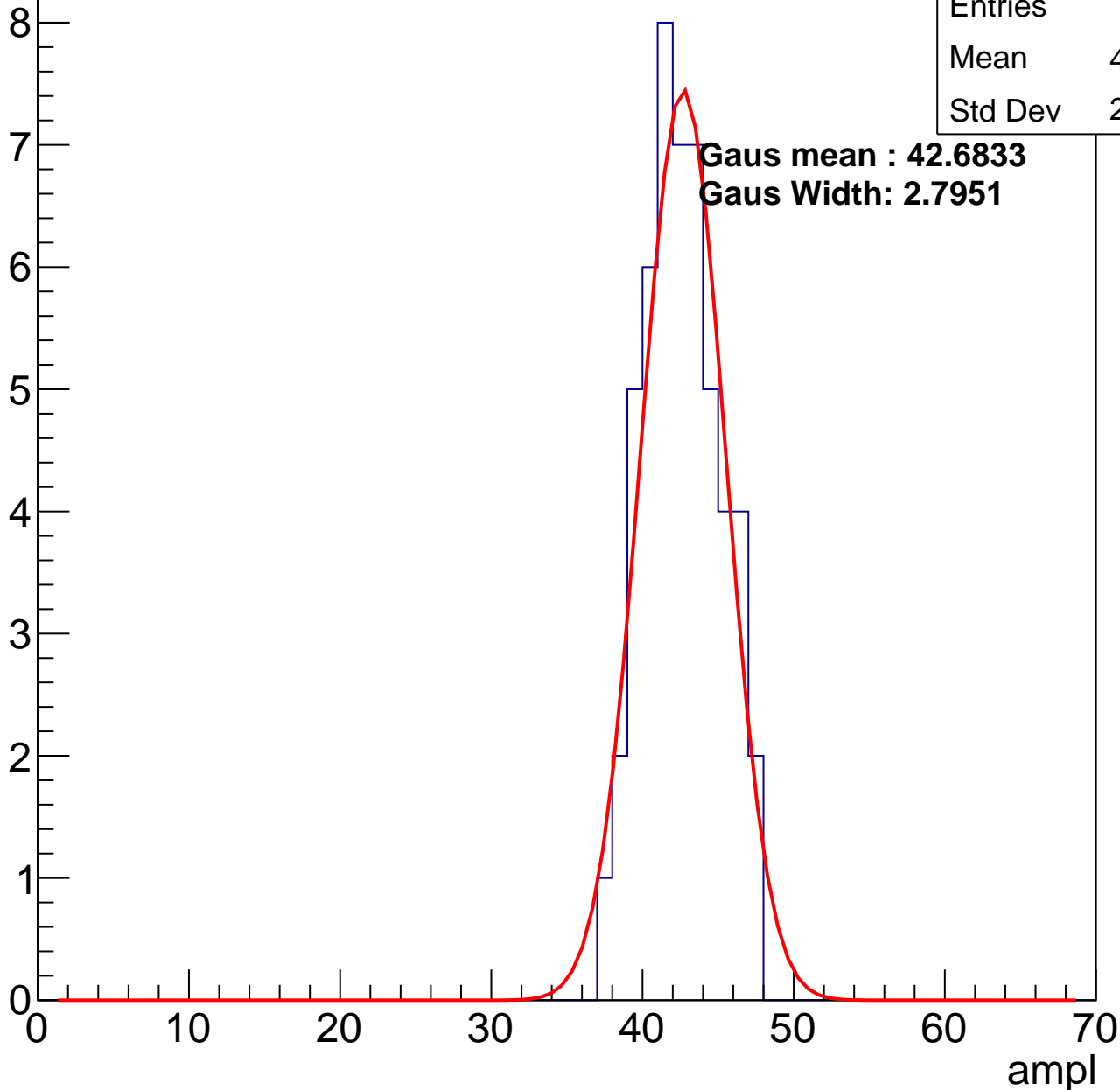
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	42.14
Std Dev	2.466

**Gaus mean : 42.6833**

**Gaus Width: 2.7951**

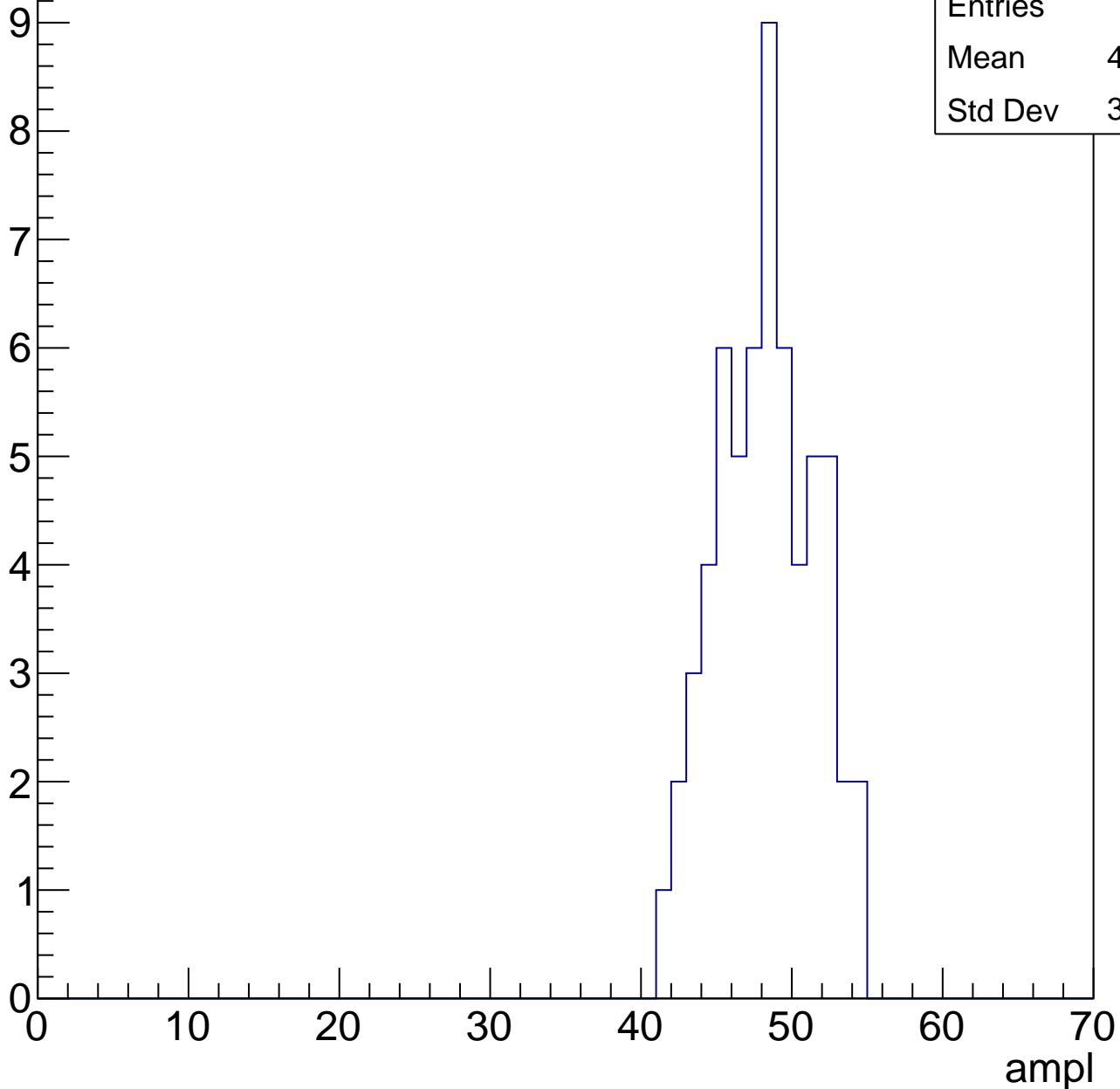


# B1L000S, U25-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

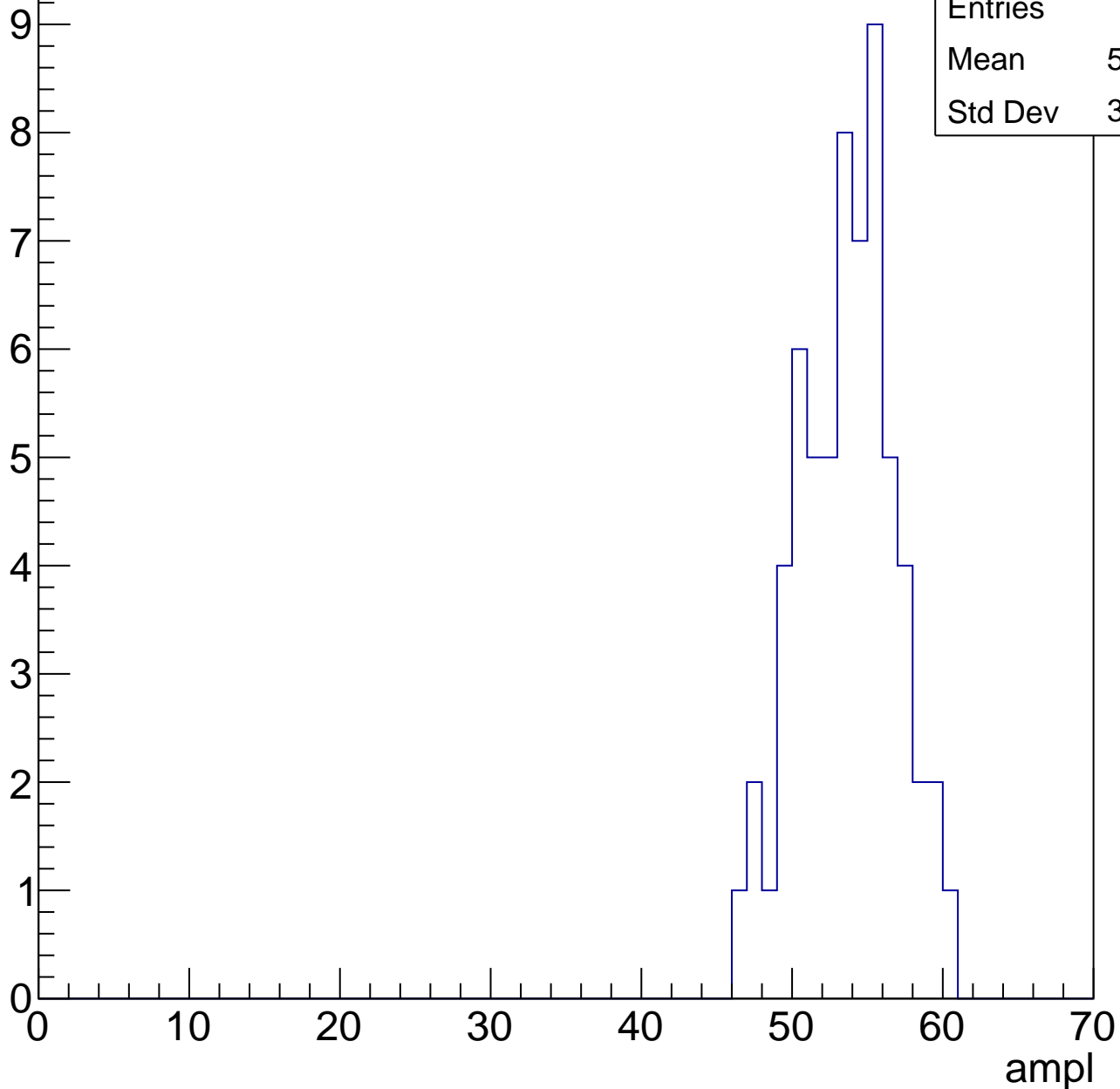
Entries	60
Mean	47.78
Std Dev	3.179



# B1L000S, U25-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch72, adc5

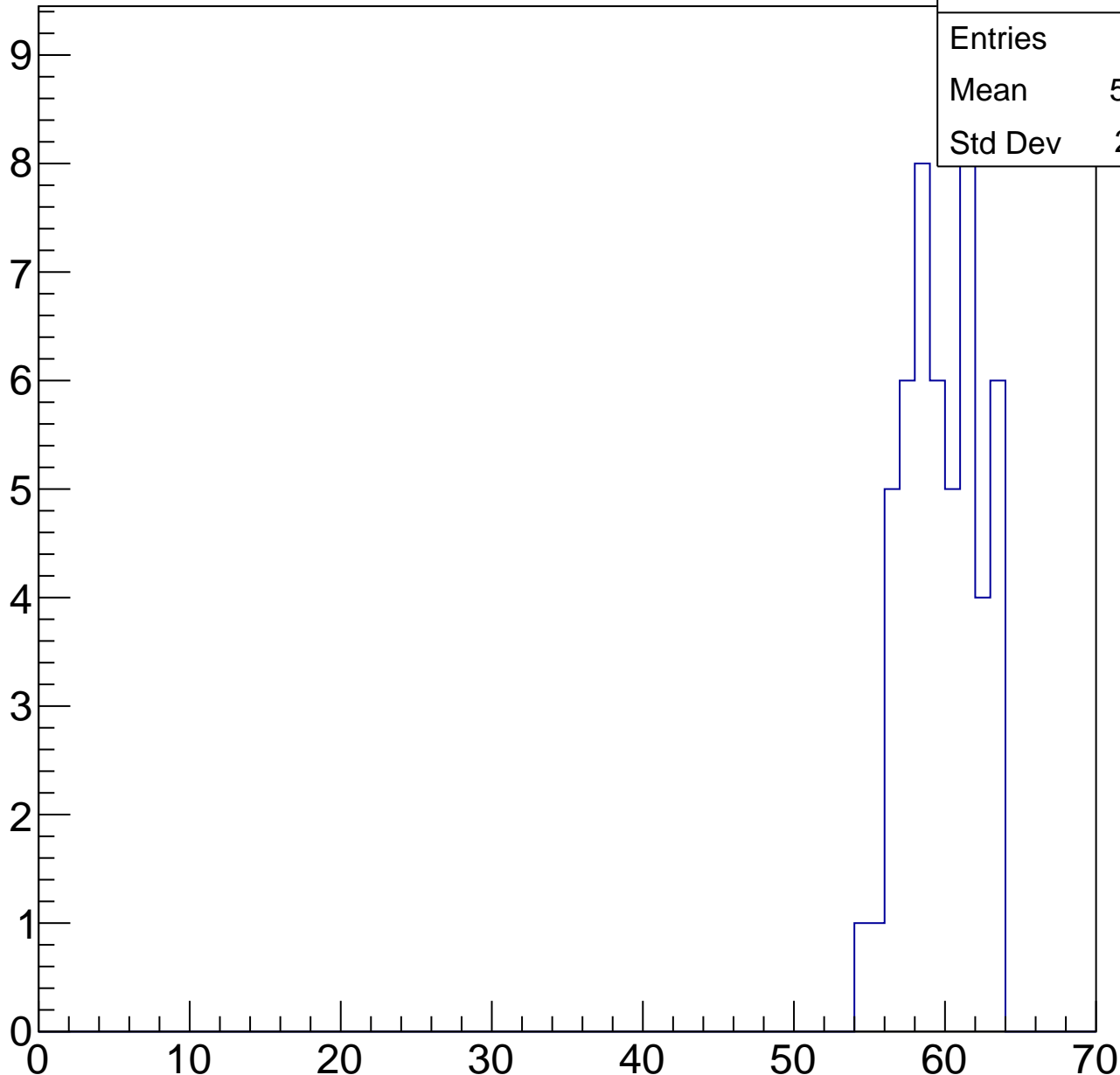
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.29
Std Dev	2.371

ampl

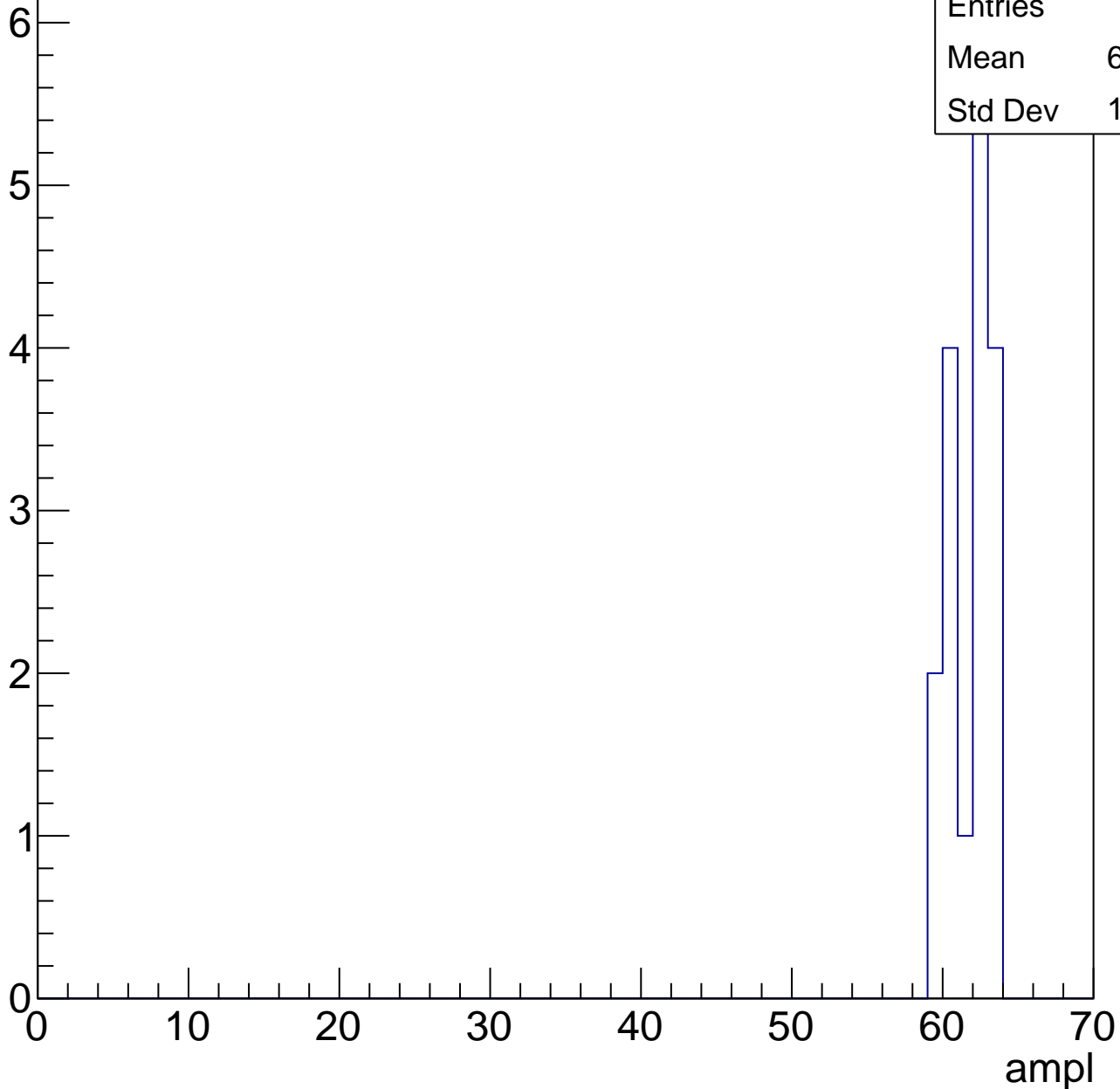


# B1L000S, U25-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.35
Std Dev	1.369





# B1L000S, U25-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch73, adc0

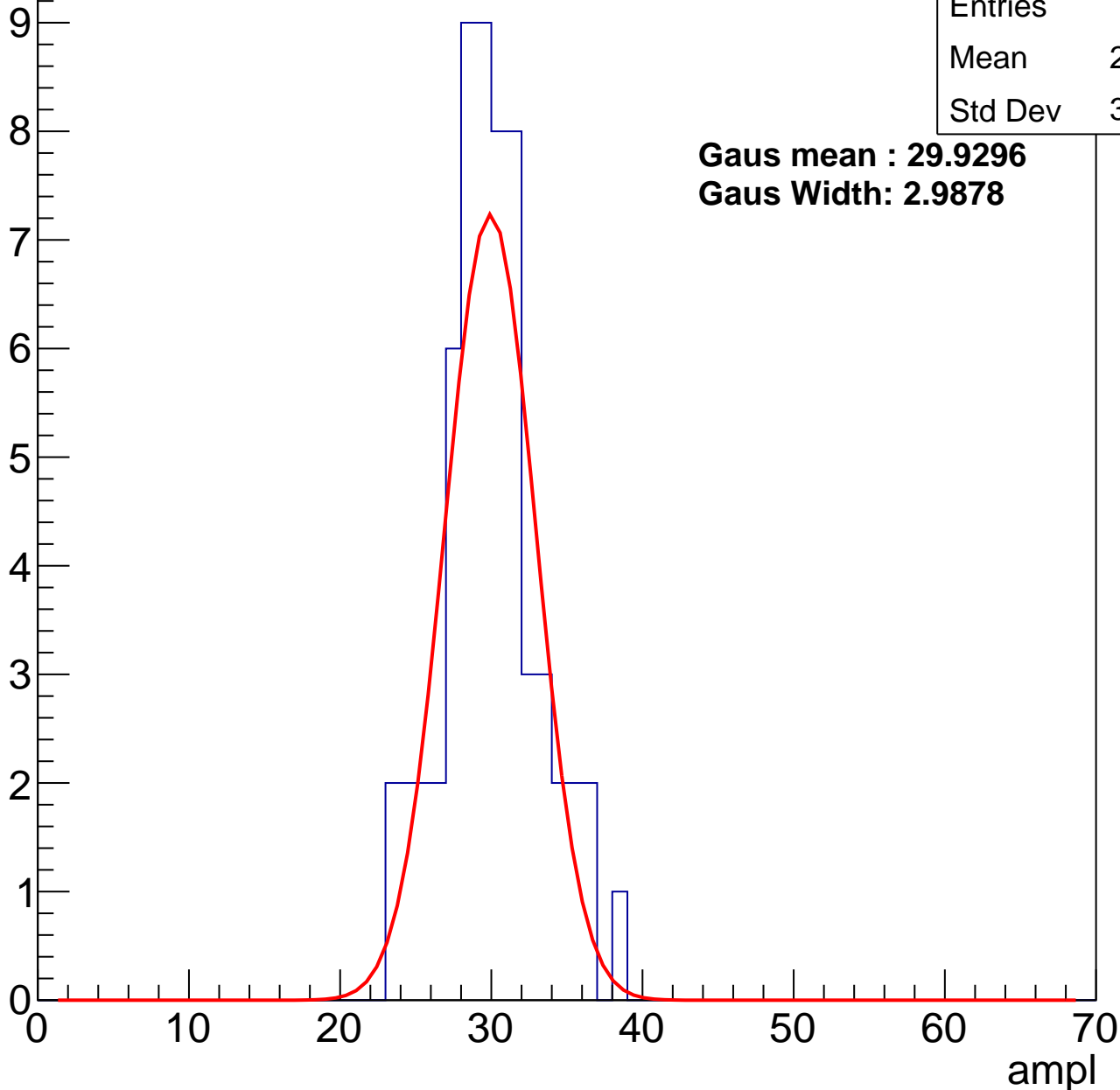
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	29.54
Std Dev	3.144

**Gaus mean : 29.9296**

**Gaus Width: 2.9878**



# B1L000S, U25-ch73, adc1

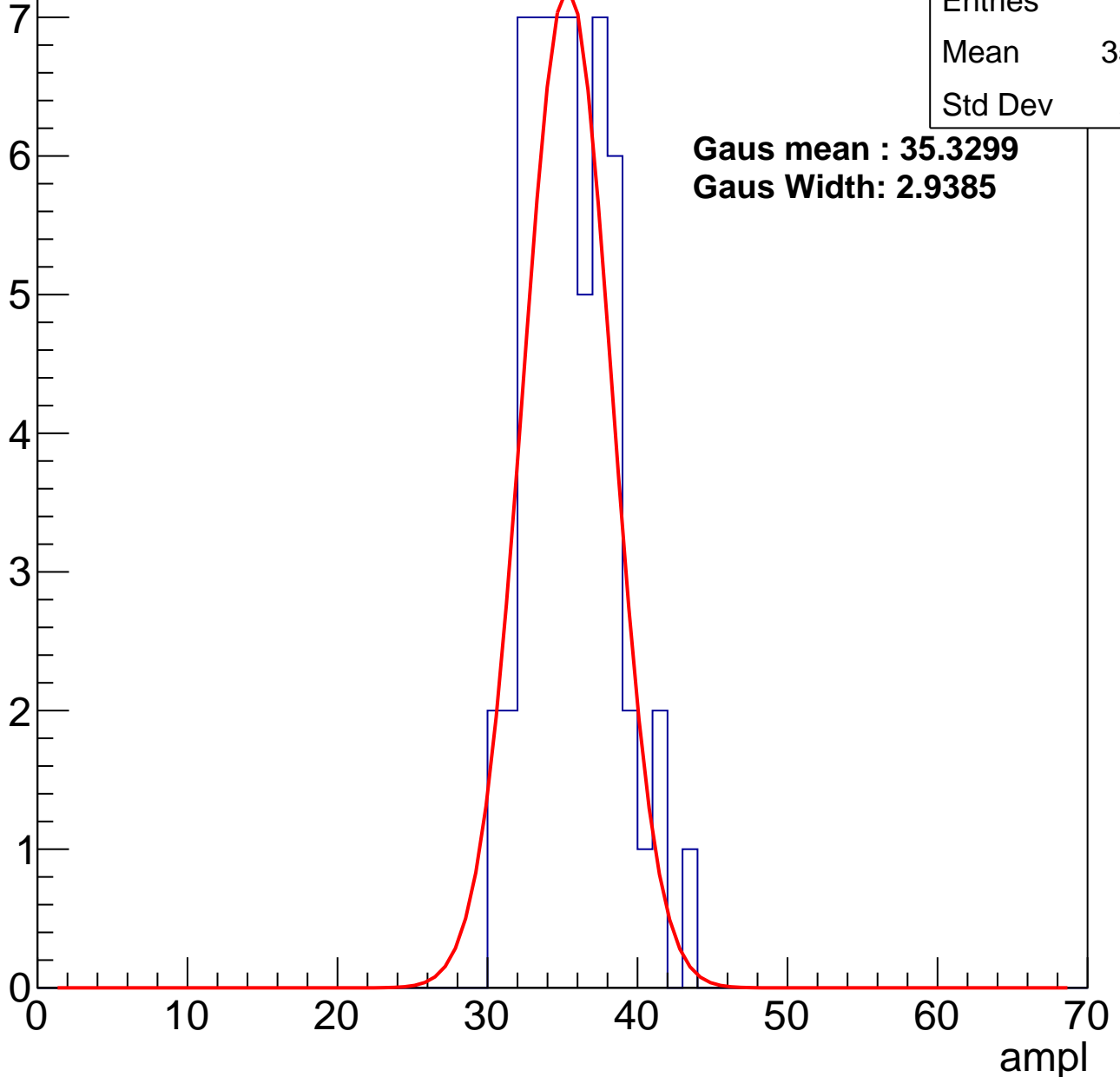
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	35.18
Std Dev	2.86

**Gaus mean : 35.3299**

**Gaus Width: 2.9385**



# B1L000S, U25-ch73, adc2

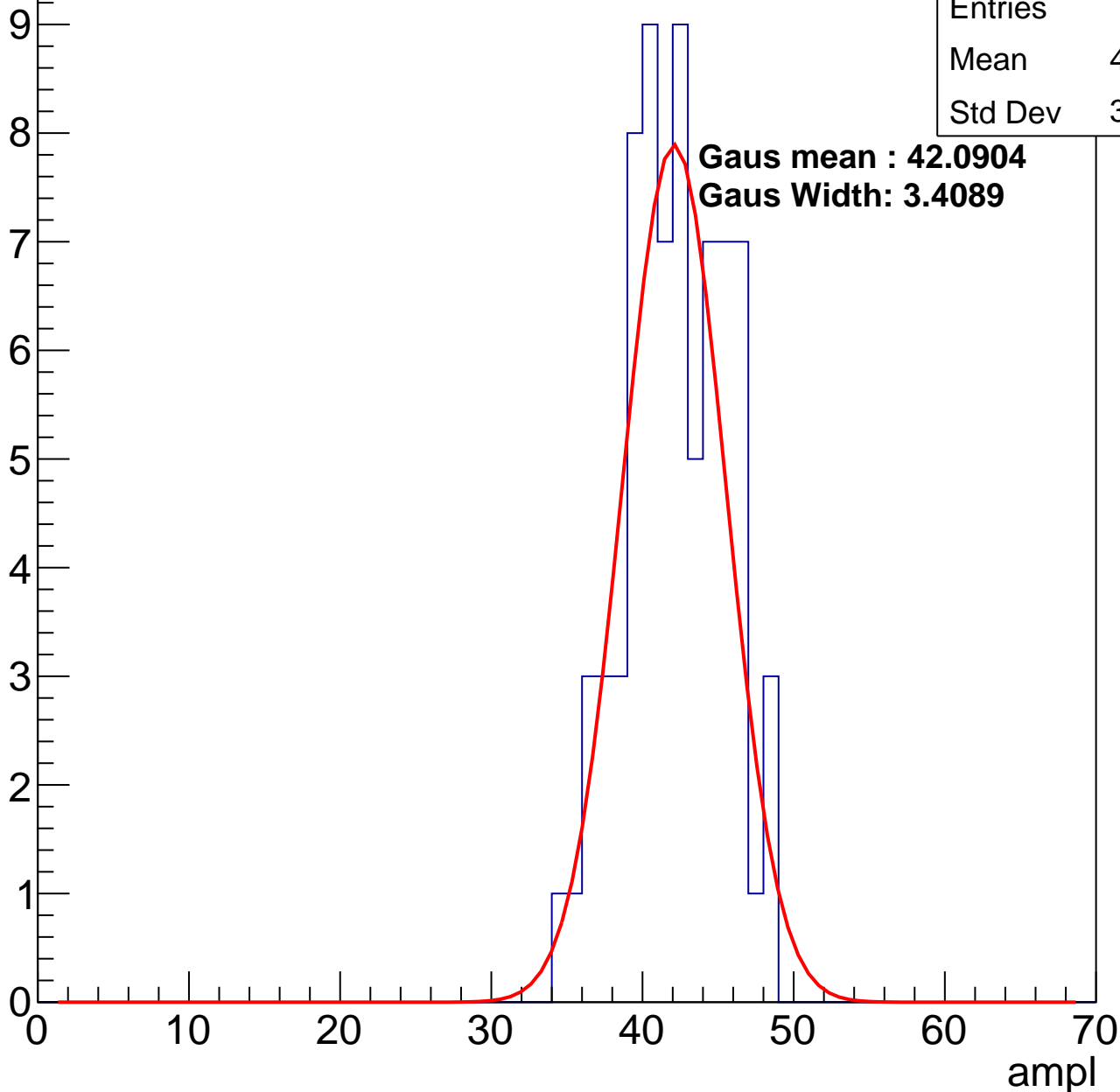
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	74
Mean	41.76
Std Dev	3.279

**Gaus mean : 42.0904**

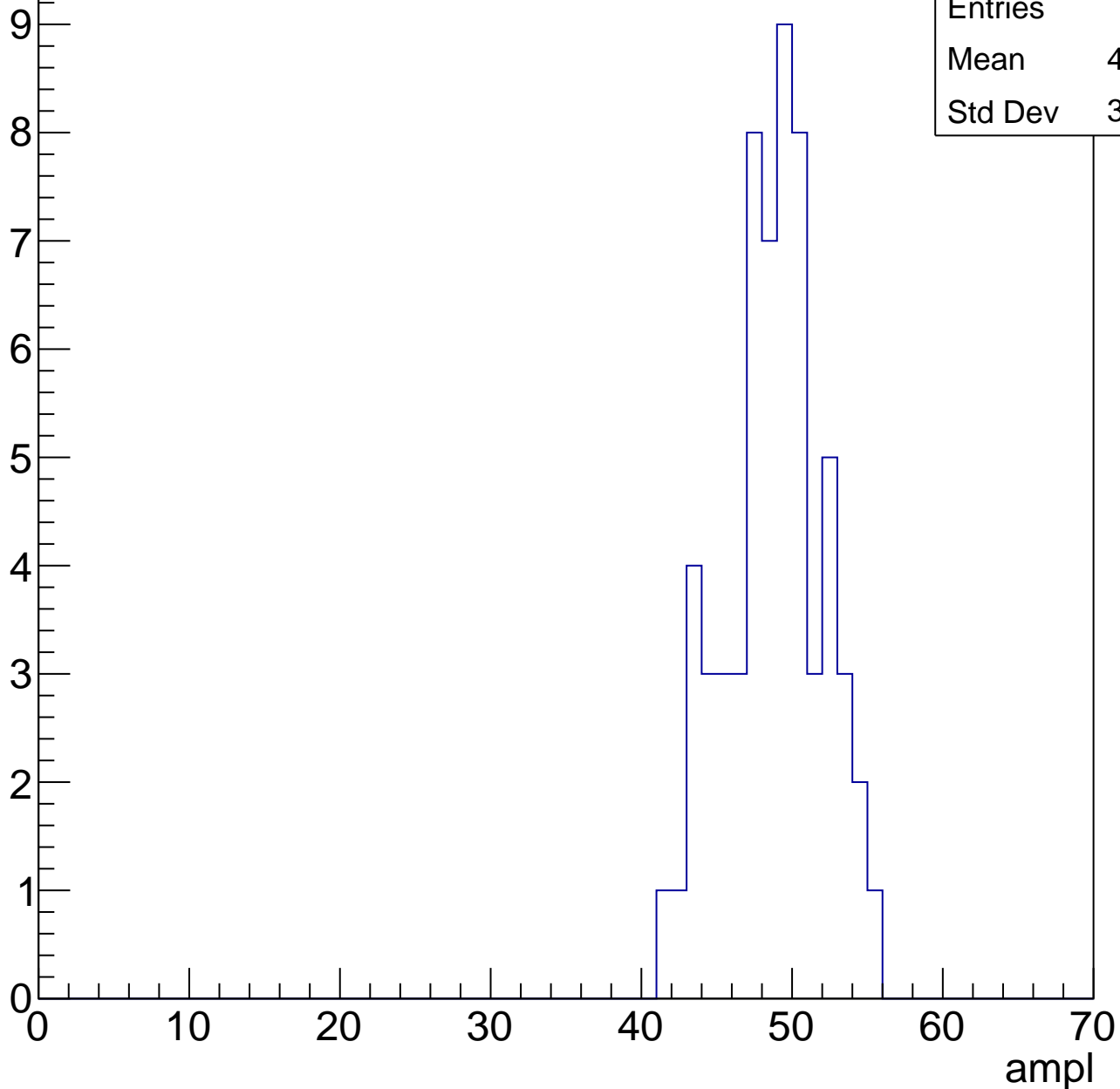
**Gaus Width: 3.4089**



# B1L000S, U25-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

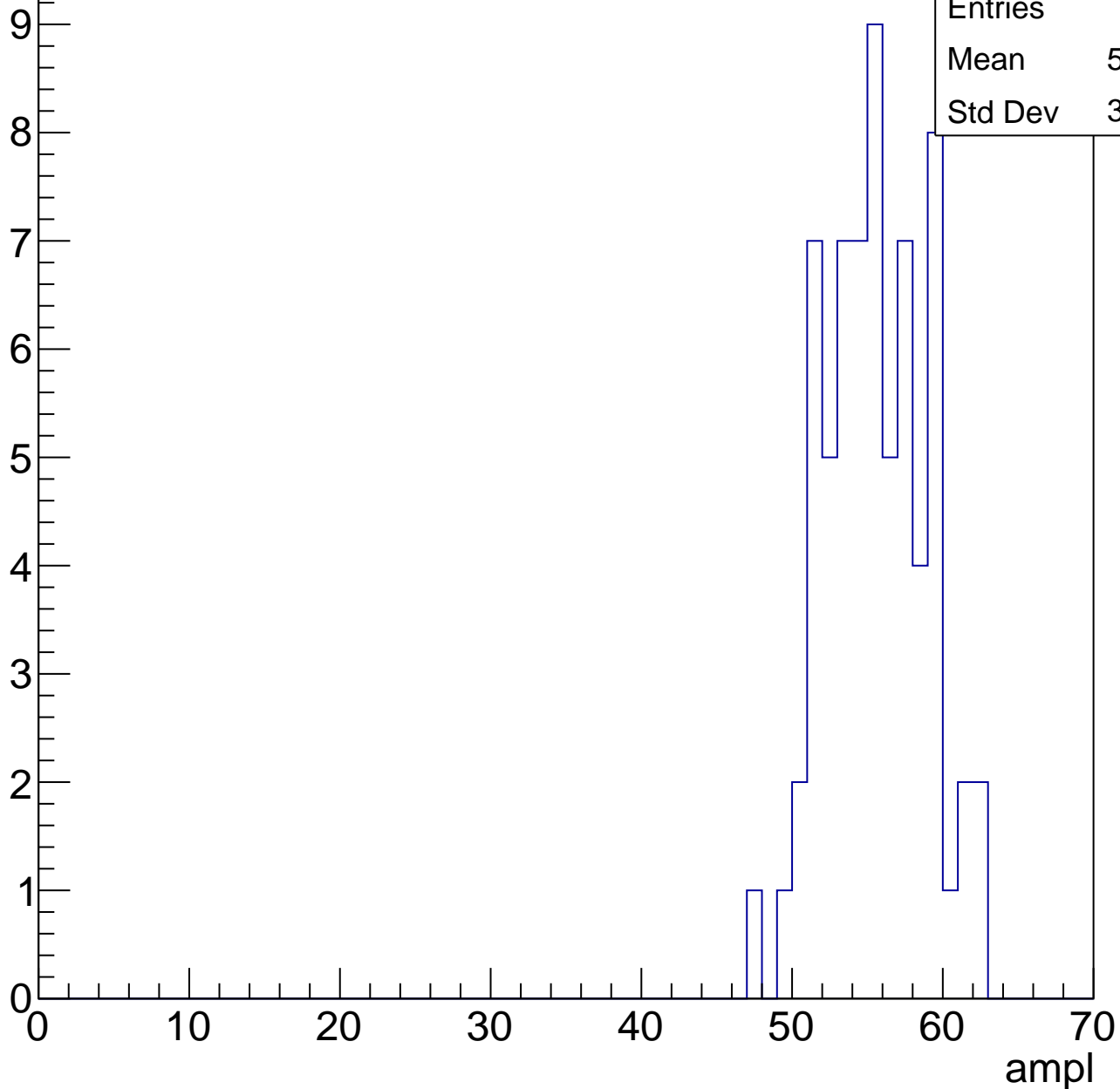


Entries	61
Mean	48.33
Std Dev	3.182

# B1L000S, U25-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

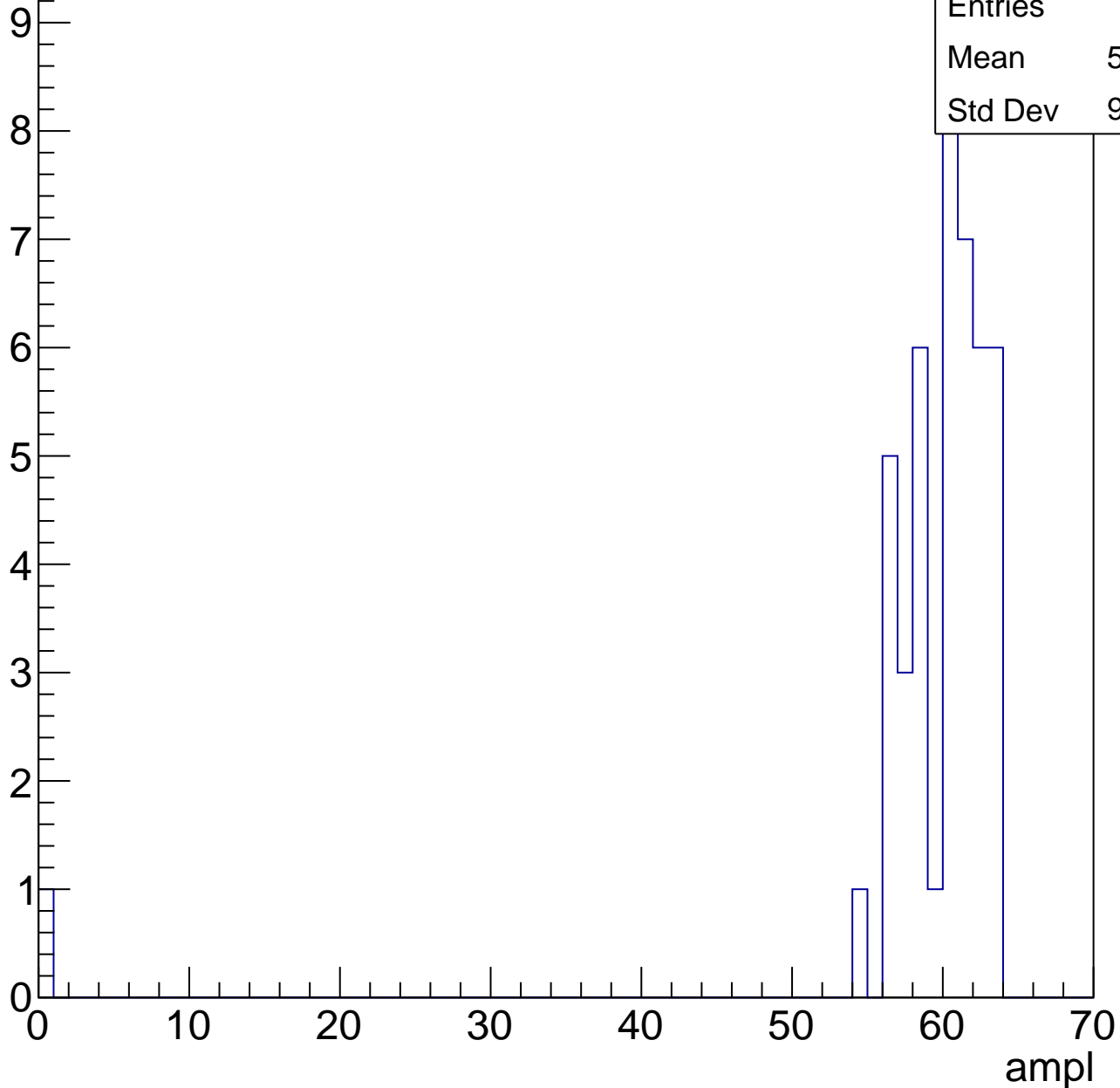
Entry



# B1L000S, U25-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

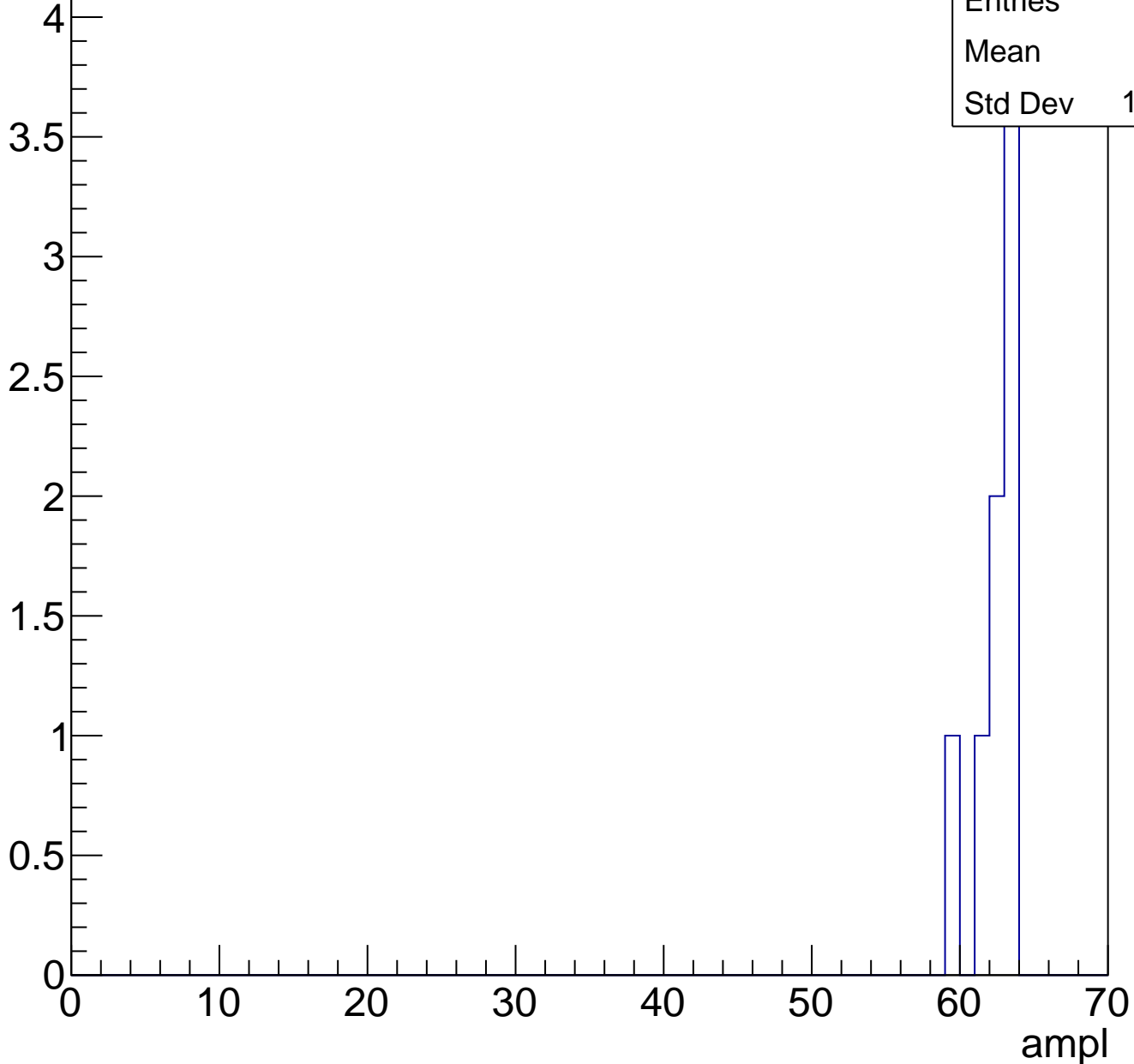
Entry



# B1L000S, U25-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	9
Std Dev	9

# B1L000S, U25-ch74, adc0

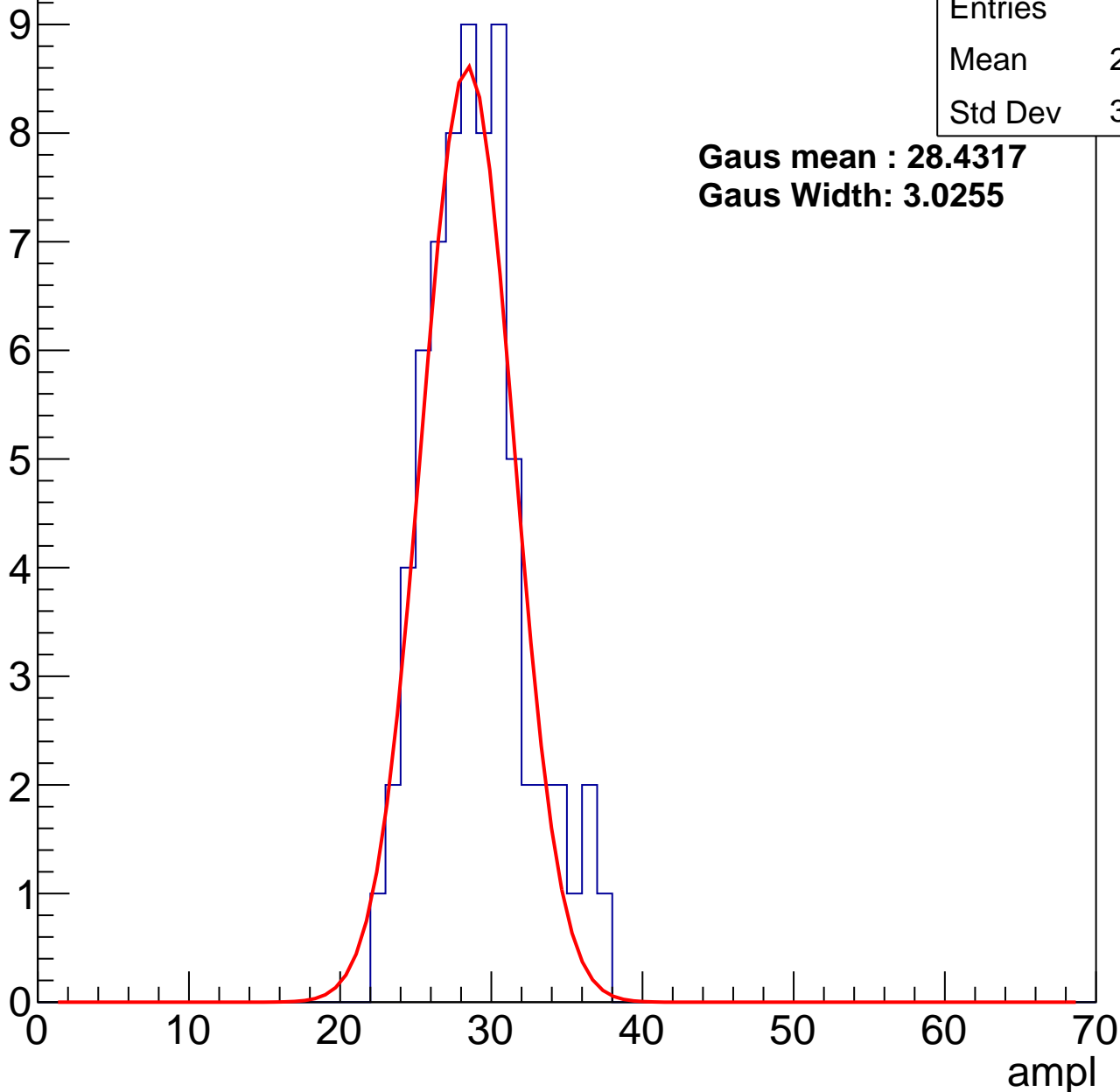
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.45
Std Dev	3.246

**Gaus mean : 28.4317**

**Gaus Width: 3.0255**



# B1L000S, U25-ch74, adc1

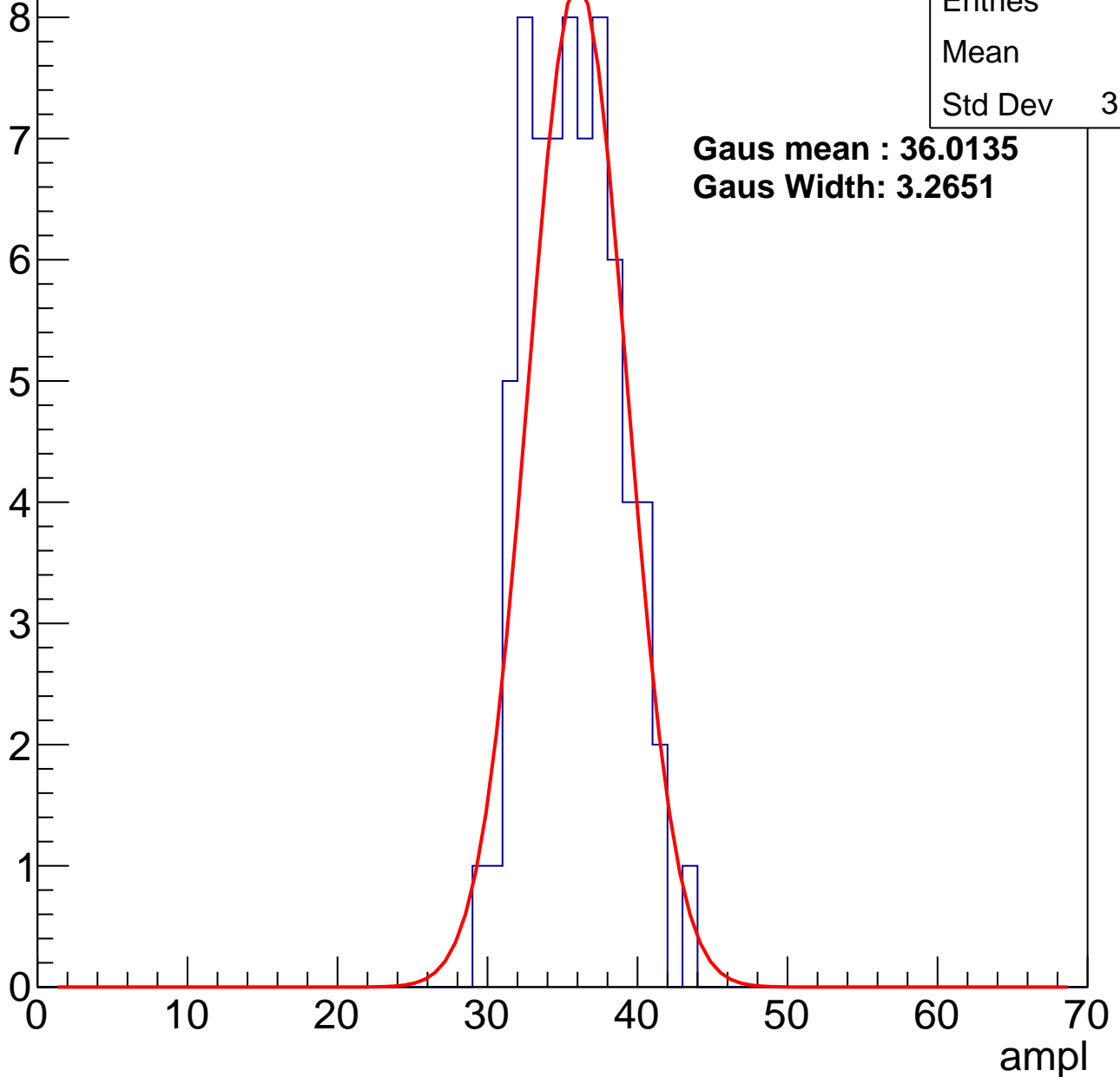
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	35.3
Std Dev	3.033

**Gaus mean : 36.0135**

**Gaus Width: 3.2651**



# B1L000S, U25-ch74, adc2

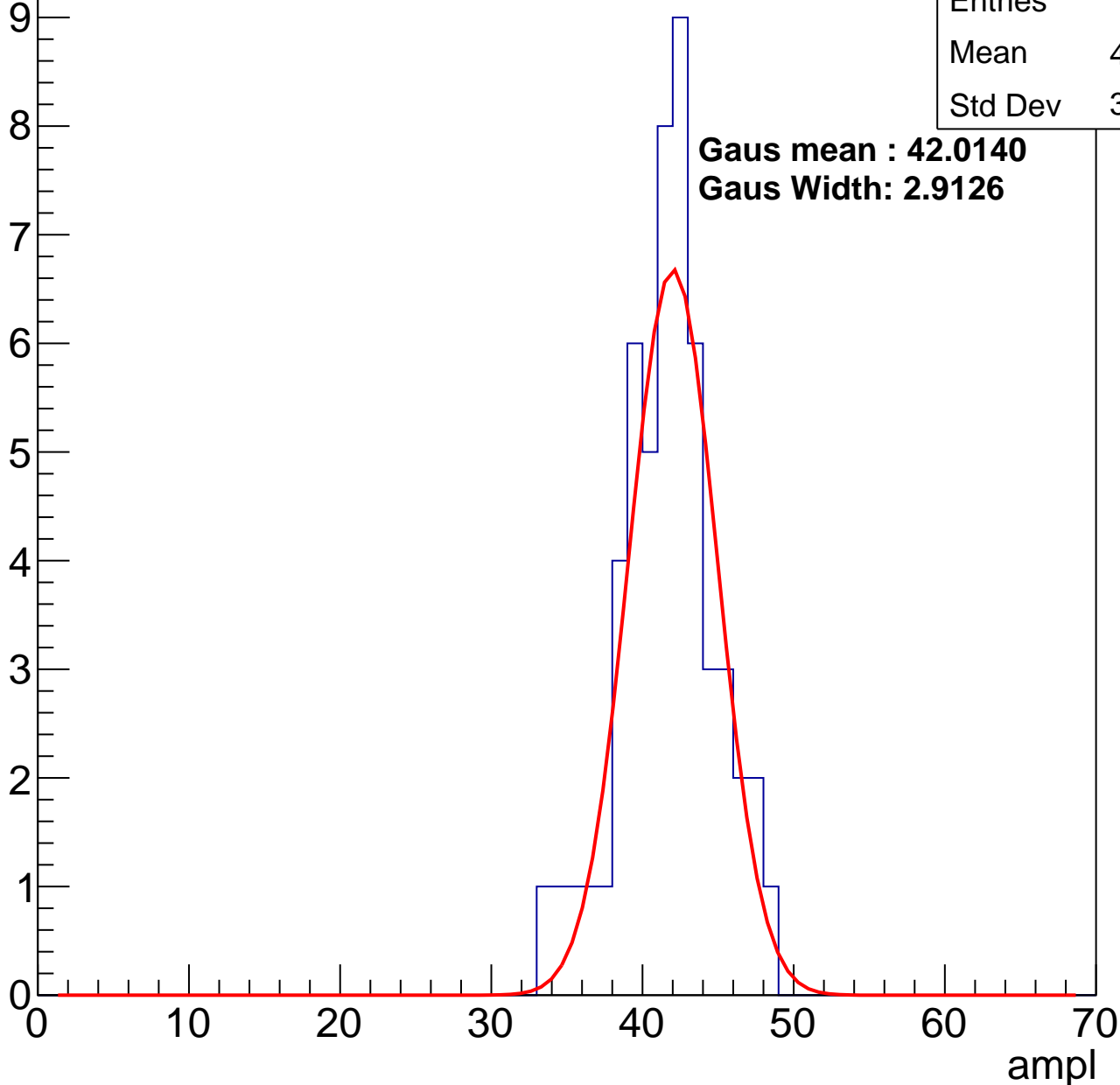
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	41.22
Std Dev	3.137

**Gaus mean : 42.0140**

**Gaus Width: 2.9126**

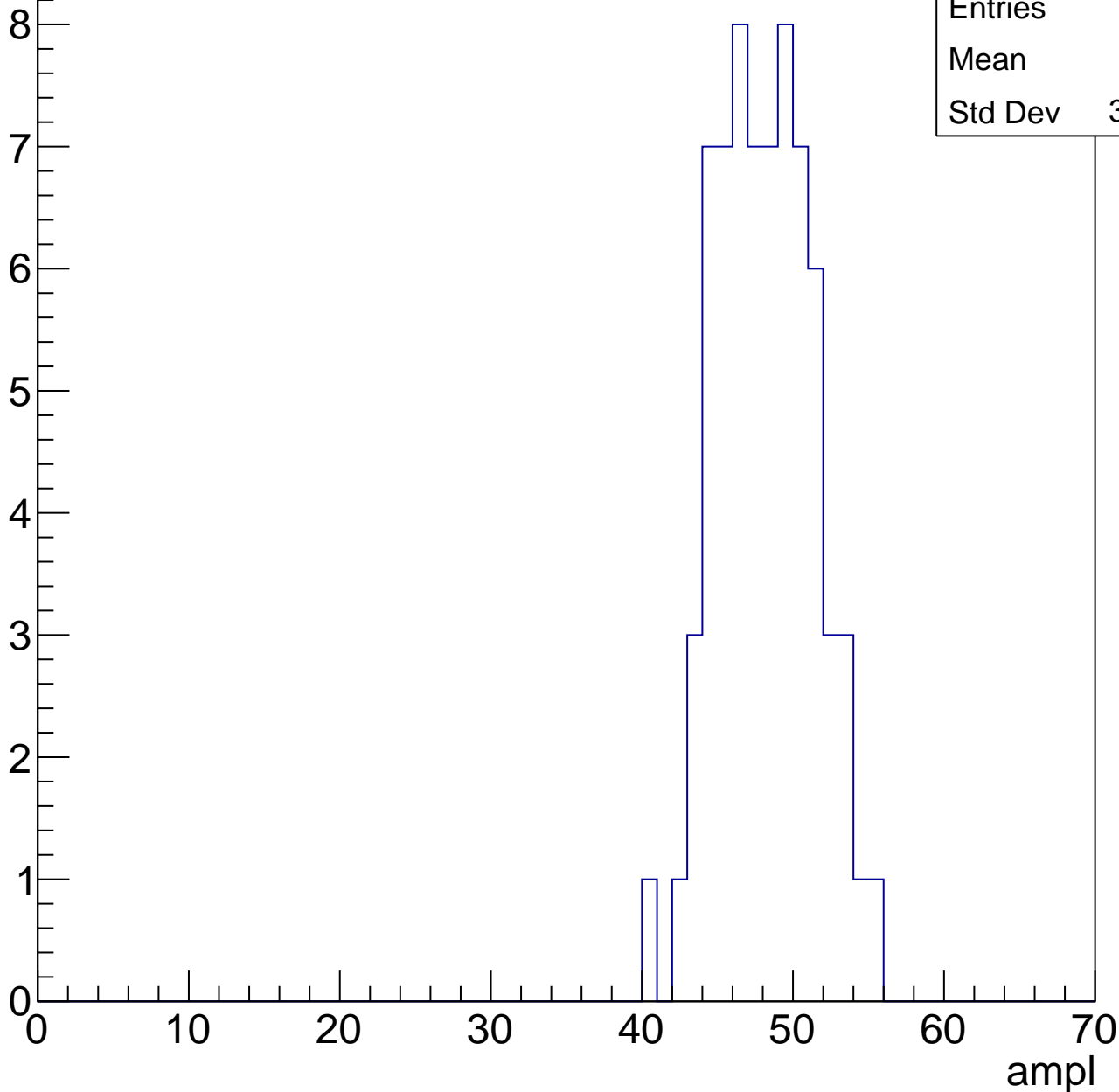


# B1L000S, U25-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.7
Std Dev	3.118

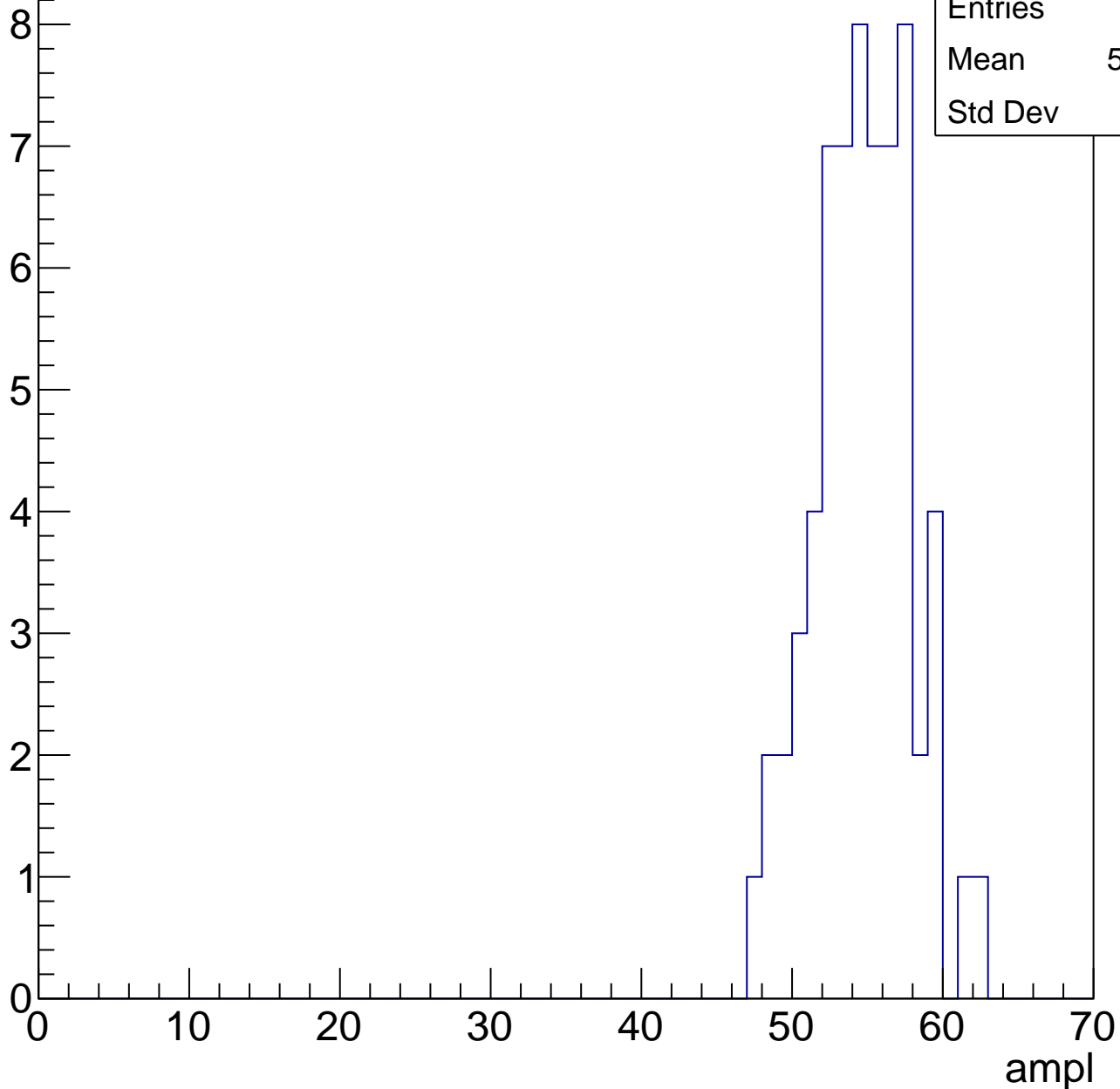


# B1L000S, U25-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	54.22
Std Dev	3.16

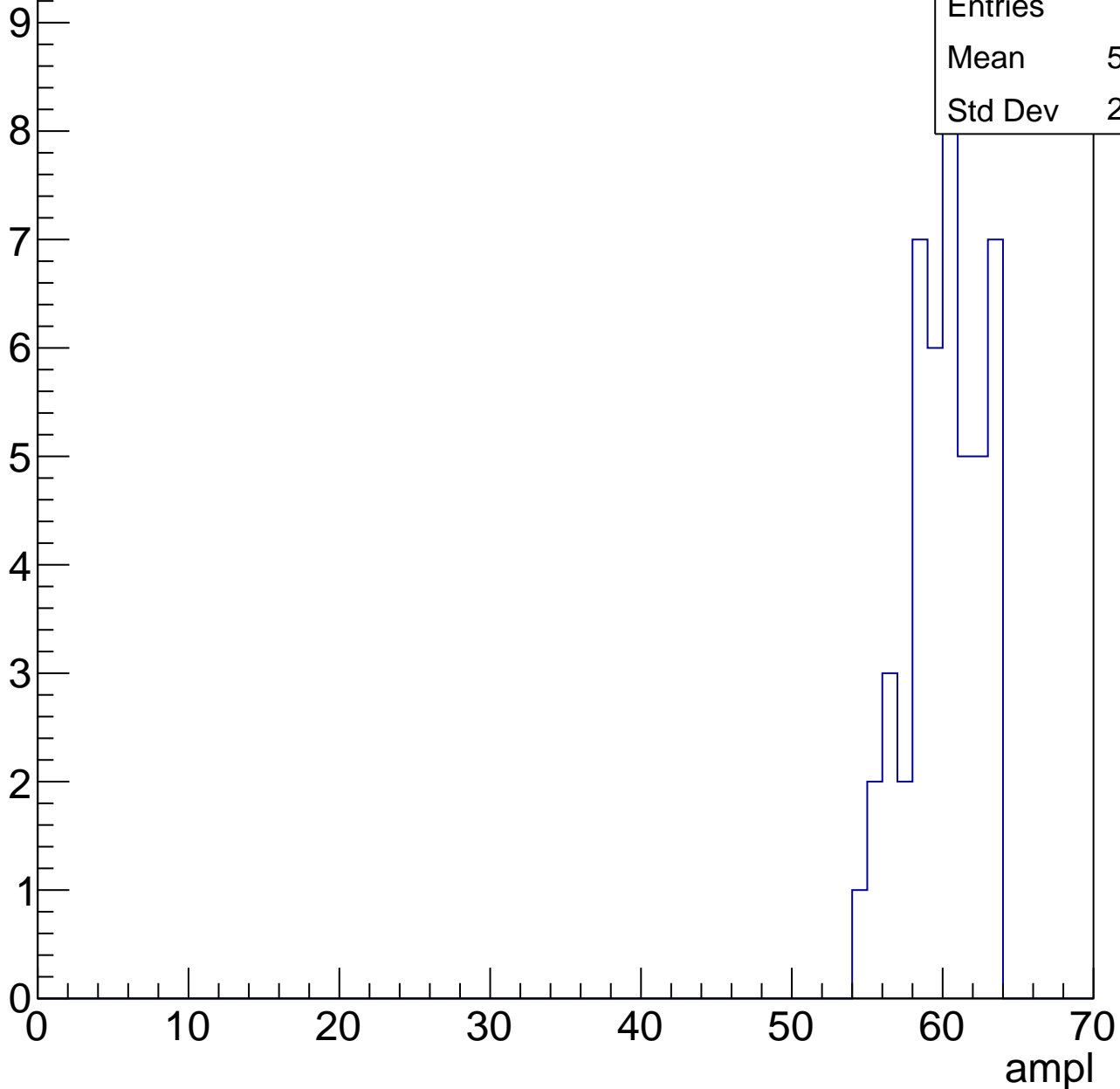


# B1L000S, U25-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

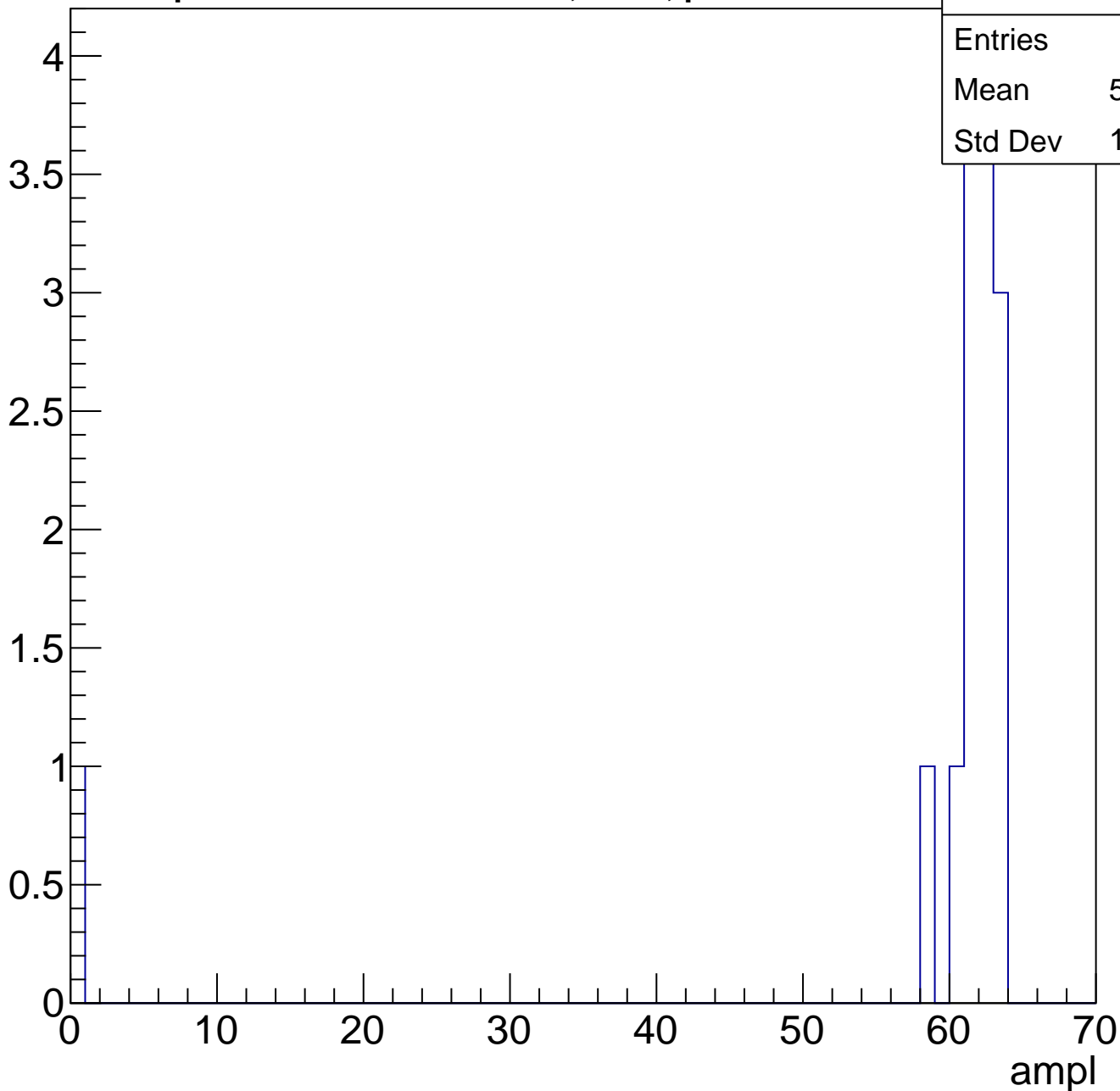
Entries	47
Mean	59.62
Std Dev	2.384



# B1L000S, U25-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	14
Mean	57.07
Std Dev	15.88



# B1L000S, U25-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch75, adc0

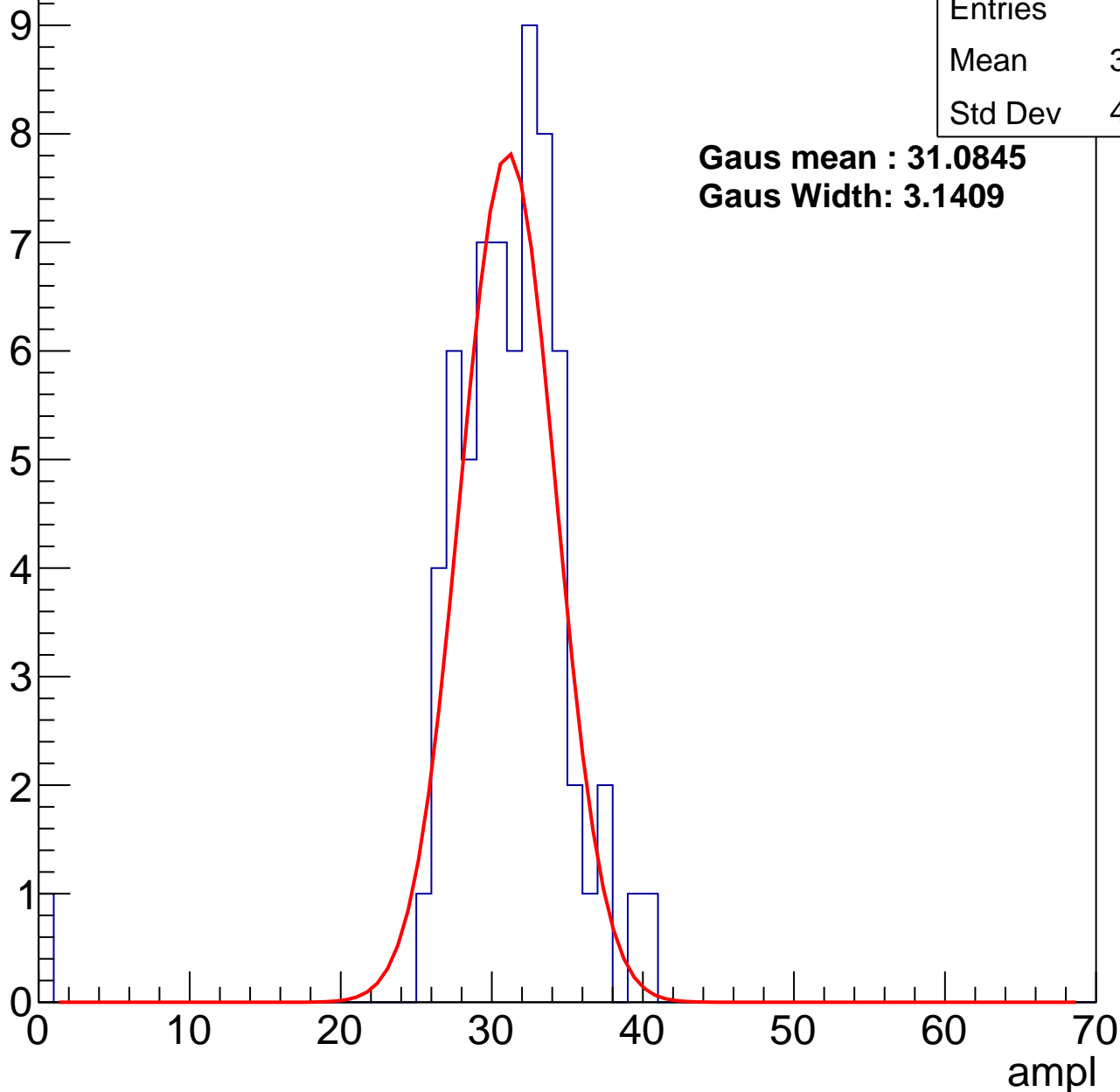
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	30.52
Std Dev	4.927

**Gaus mean : 31.0845**

**Gaus Width: 3.1409**



# B1L000S, U25-ch75, adc1

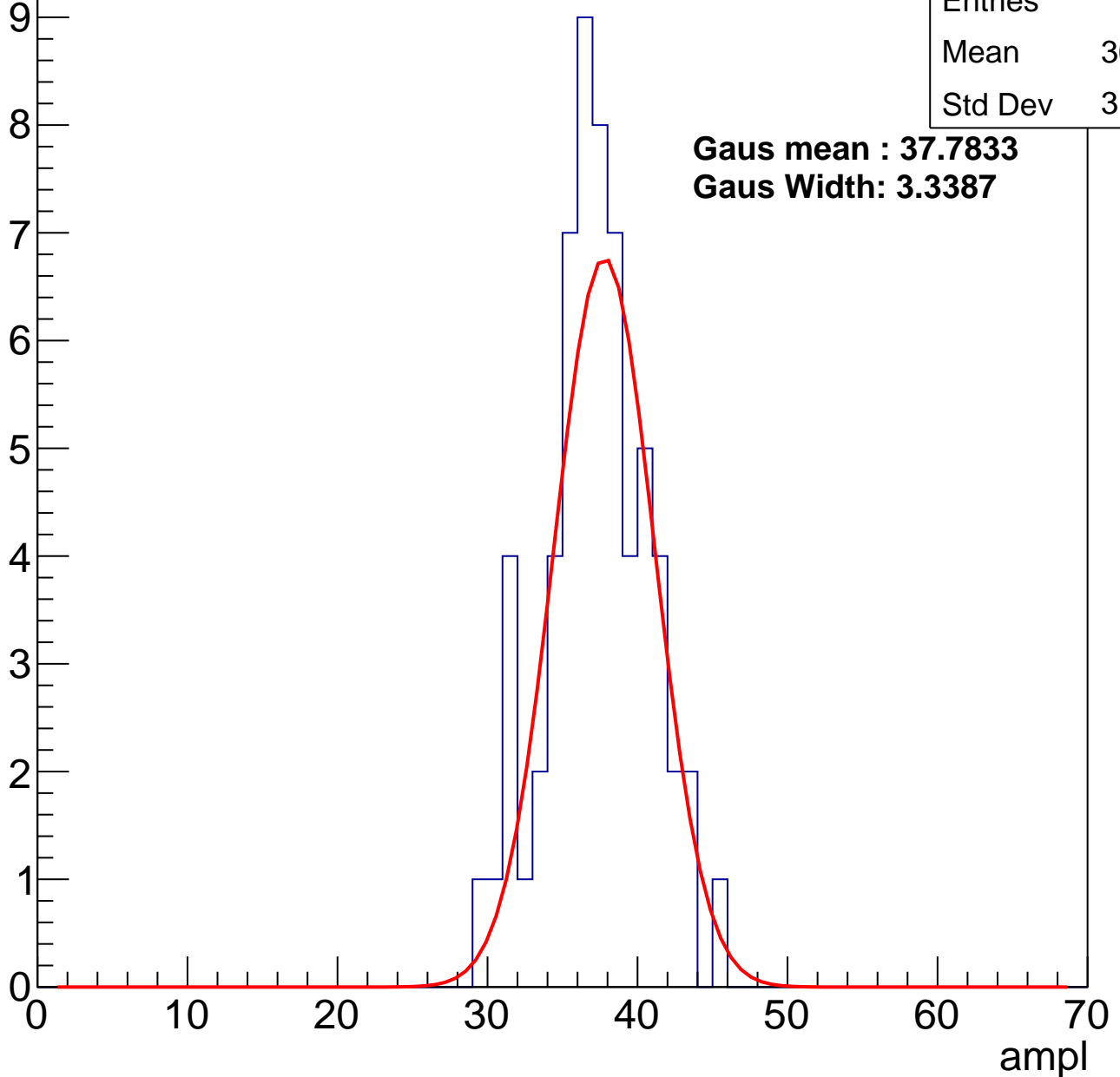
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	36.82
Std Dev	3.367

**Gaus mean : 37.7833**

**Gaus Width: 3.3387**



# B1L000S, U25-ch75, adc2

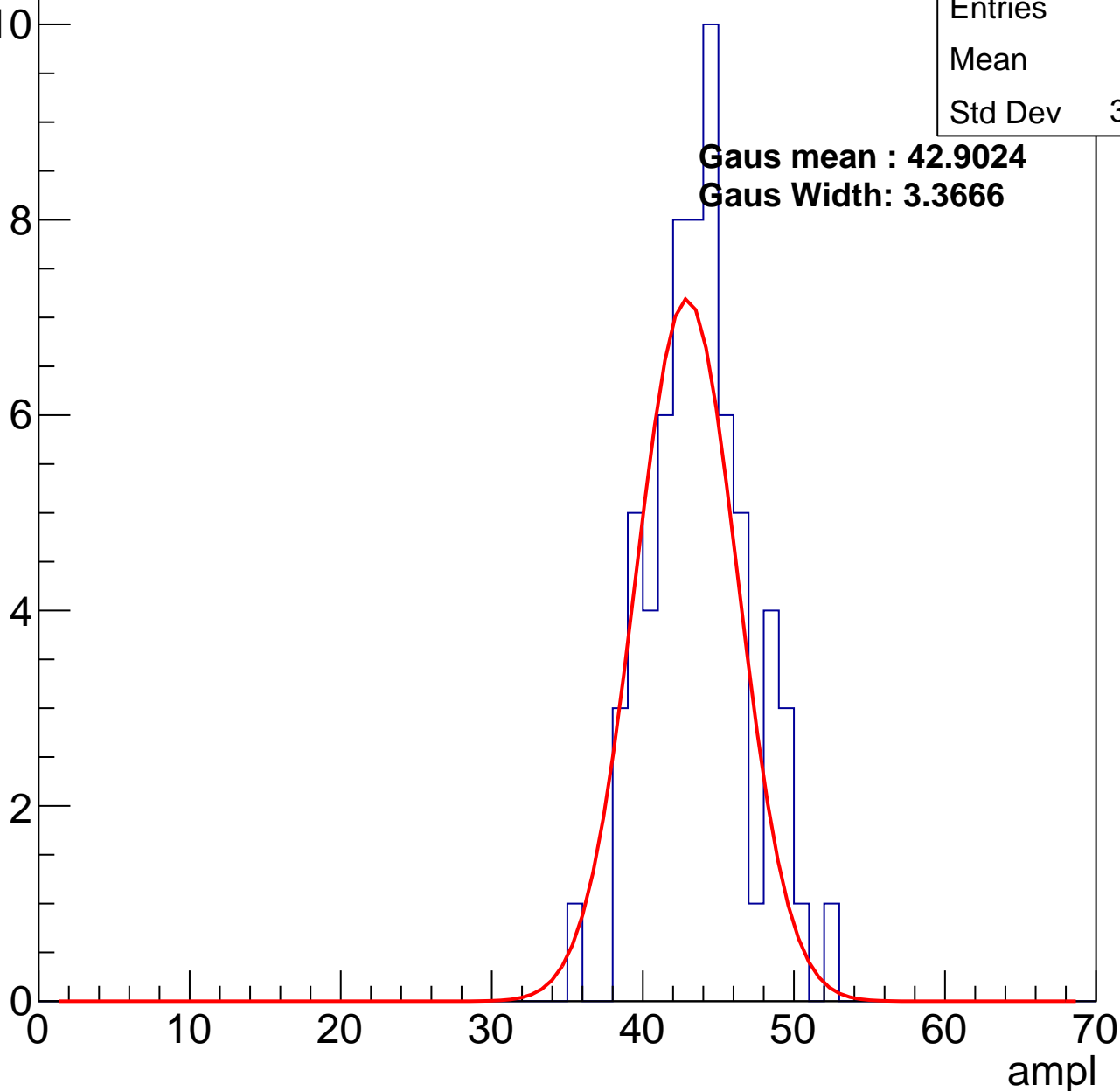
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	43.3
Std Dev	3.289

**Gaus mean : 42.9024**

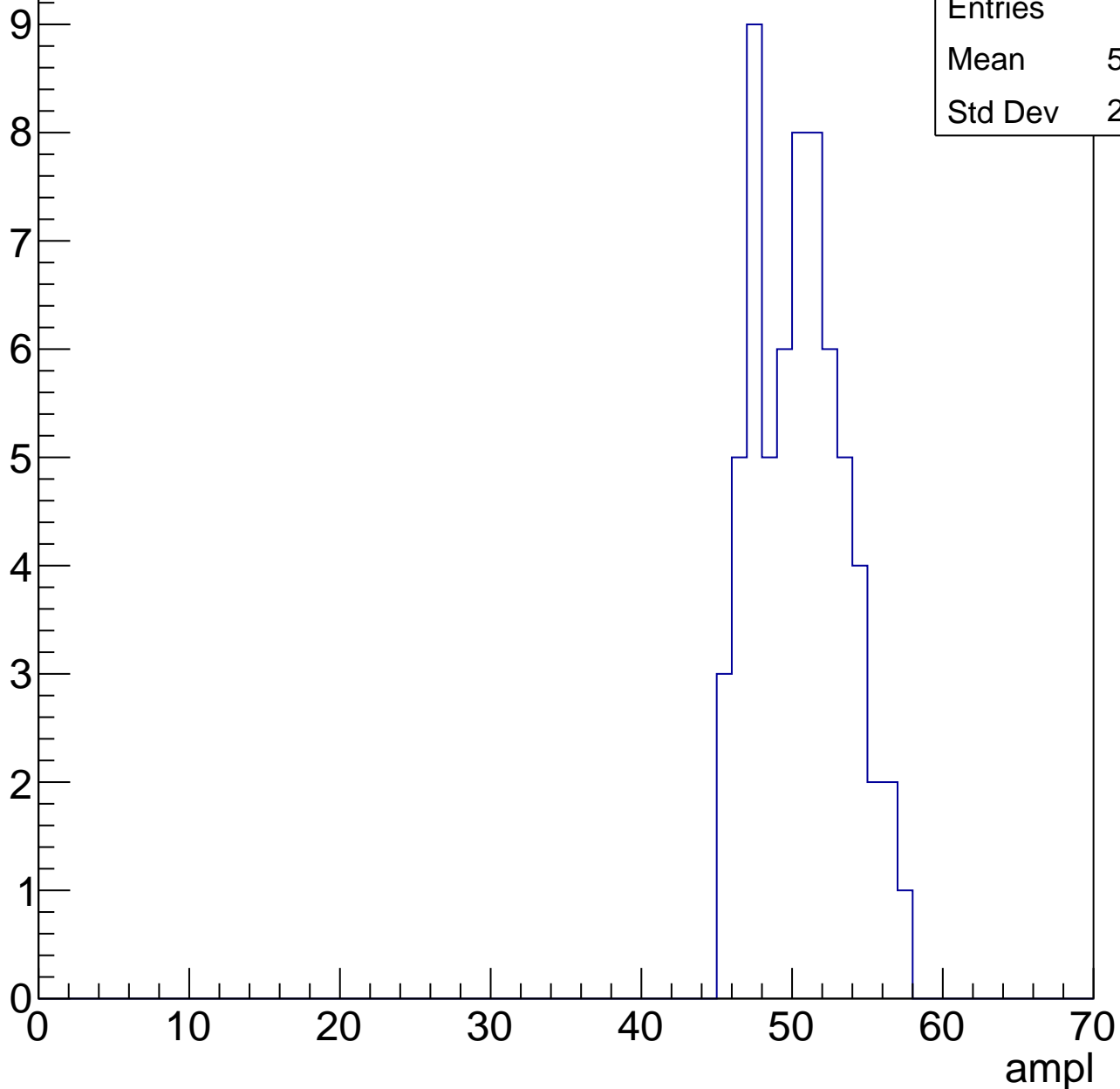
**Gaus Width: 3.3666**



# B1L000S, U25-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

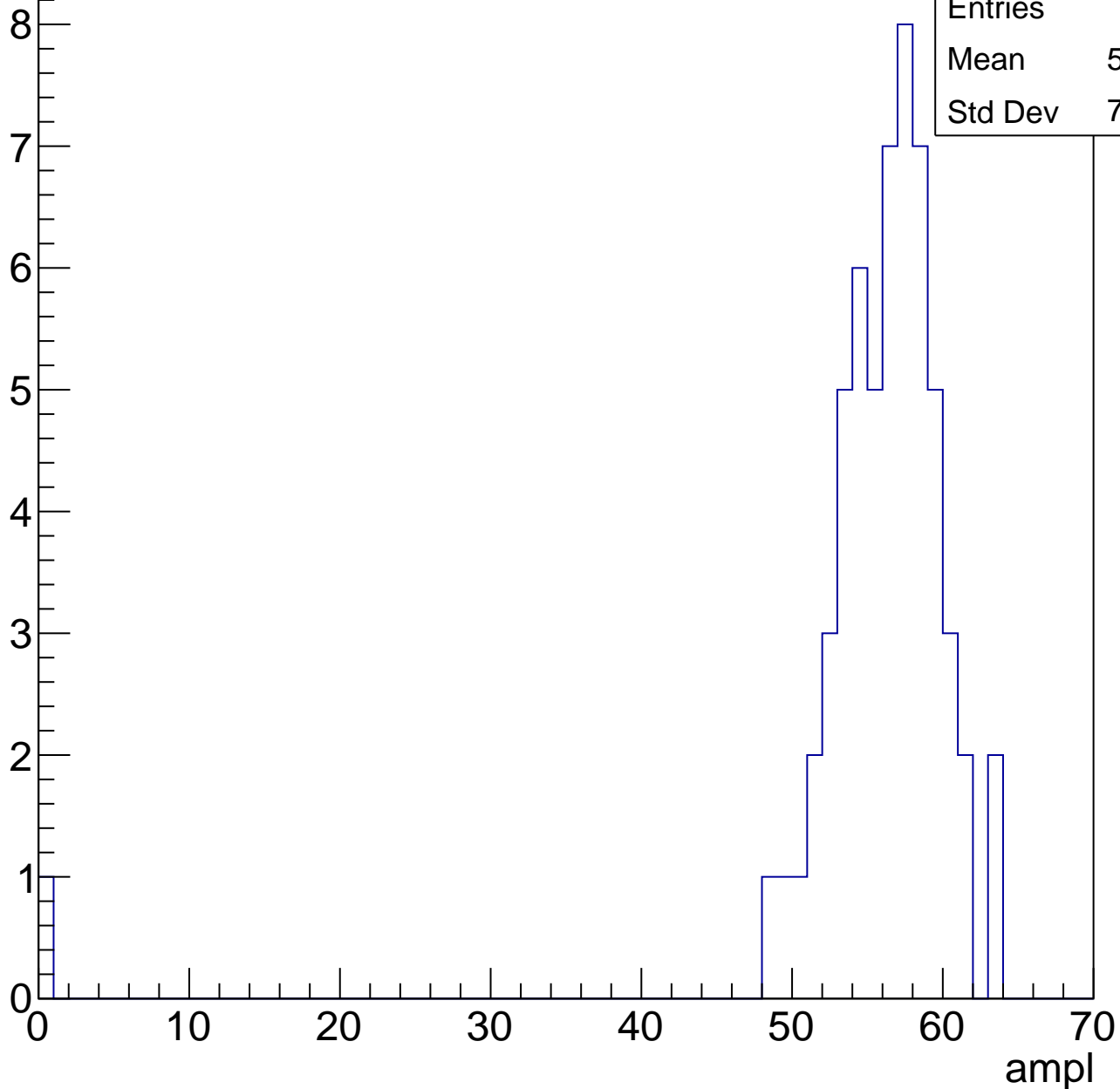


Entries	64
Mean	50.03
Std Dev	2.995

# B1L000S, U25-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

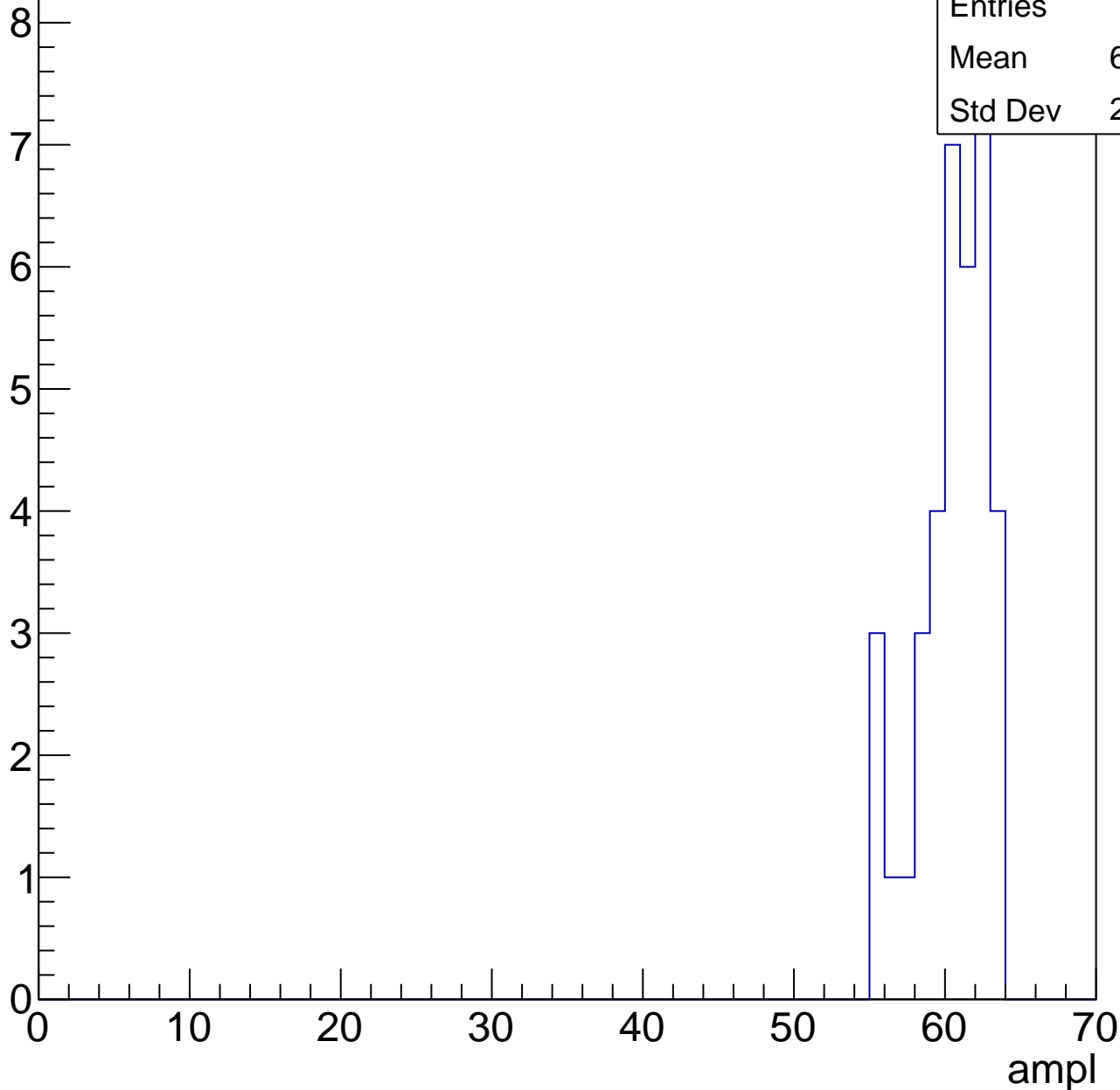


# B1L000S, U25-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

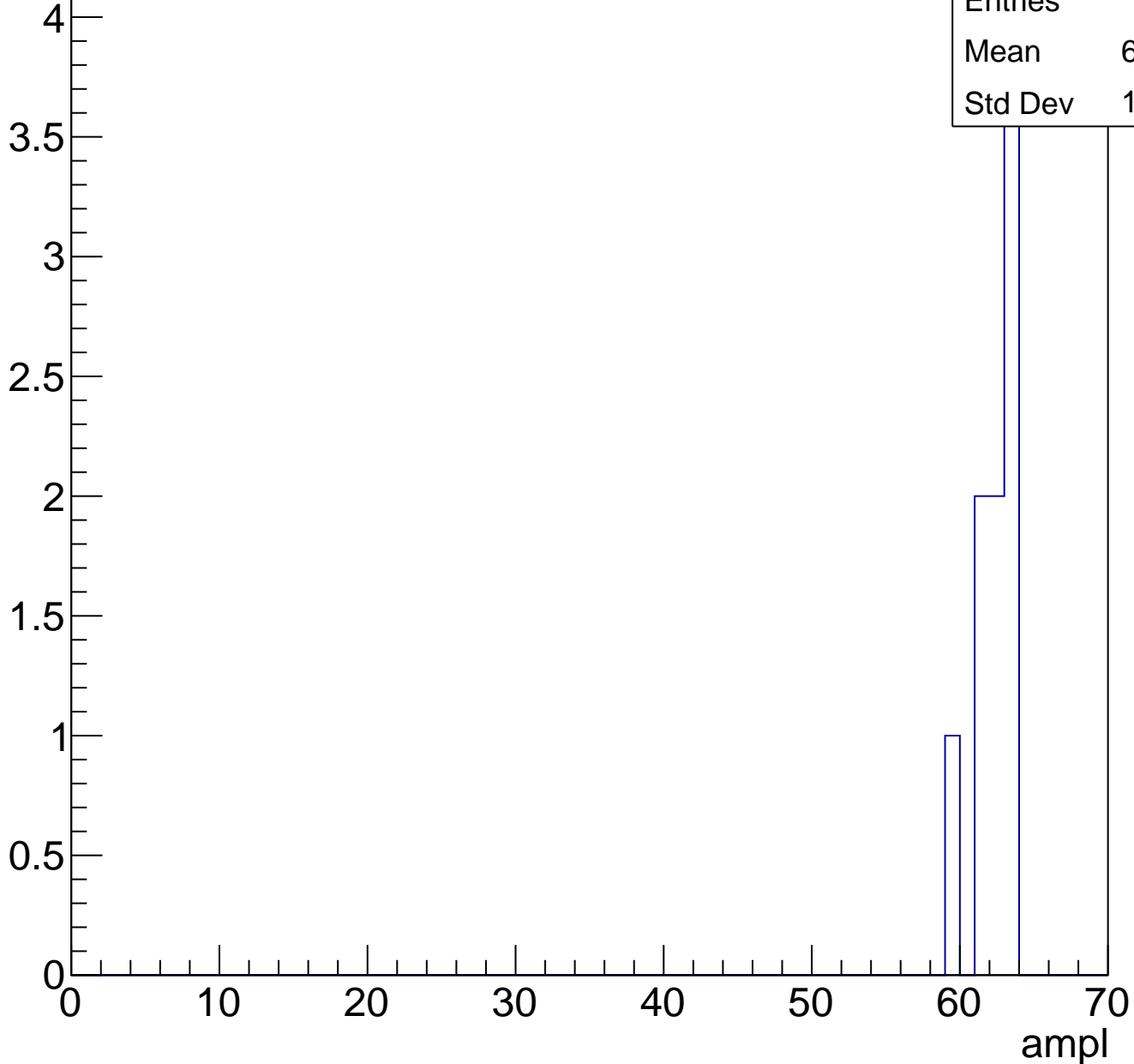
Entries	37
Mean	60.05
Std Dev	2.265



# B1L000S, U25-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch76, adc0

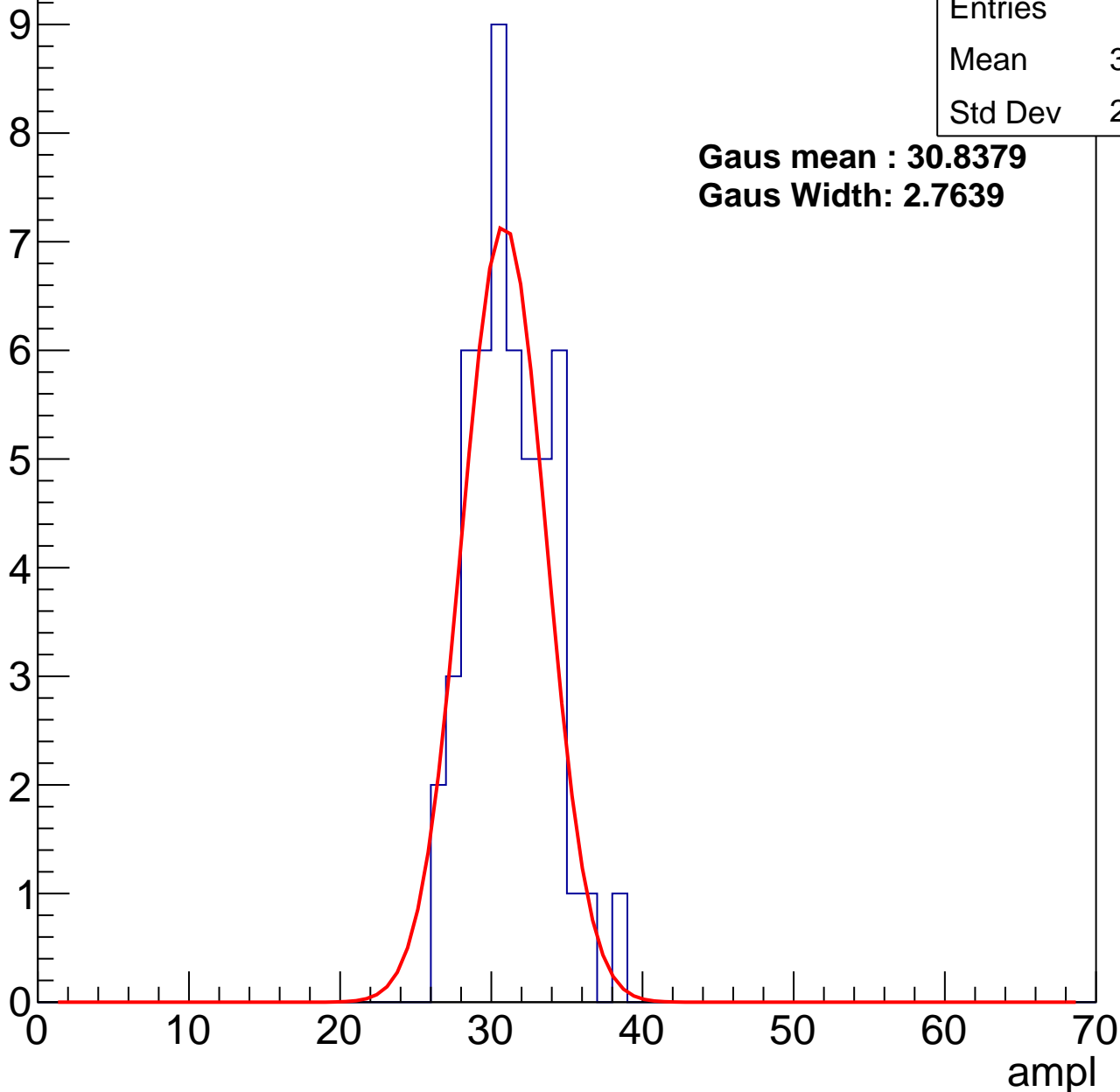
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	30.76
Std Dev	2.624

**Gaus mean : 30.8379**

**Gaus Width: 2.7639**



# B1L000S, U25-ch76, adc1

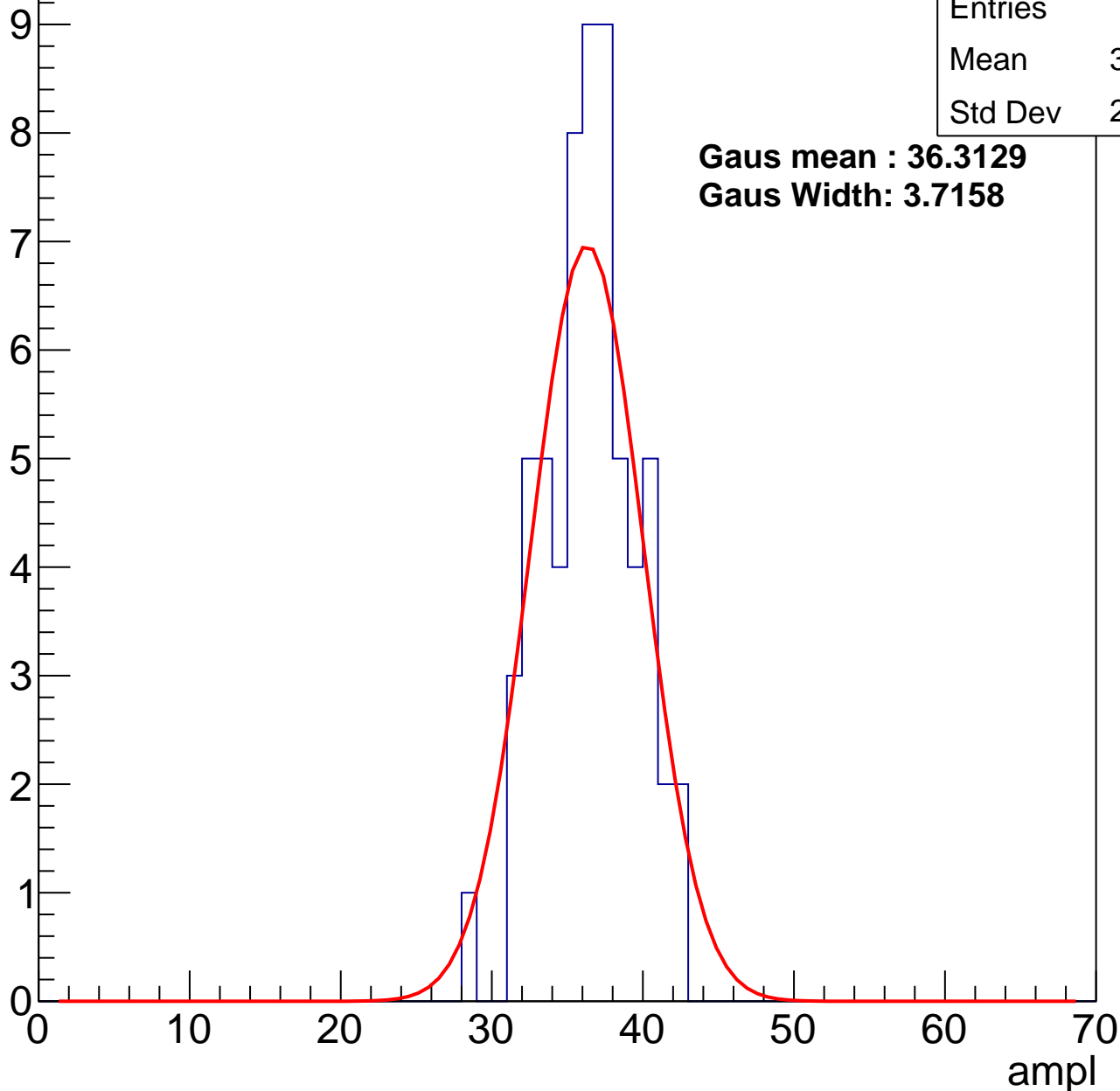
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.98
Std Dev	2.992

**Gaus mean : 36.3129**

**Gaus Width: 3.7158**



# B1L000S, U25-ch76, adc2

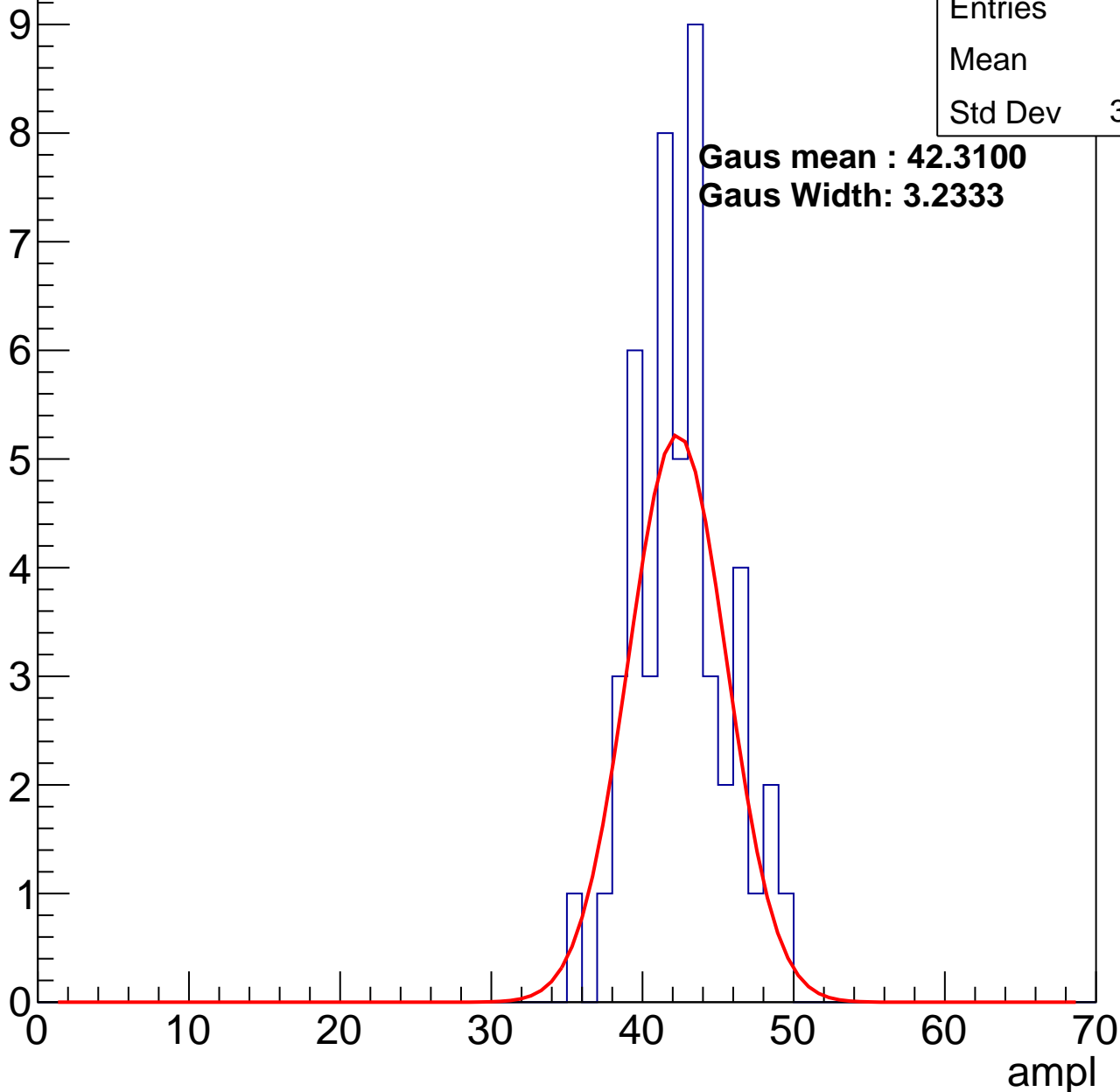
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	42.1
Std Dev	3.012

**Gaus mean : 42.3100**

**Gaus Width: 3.2333**

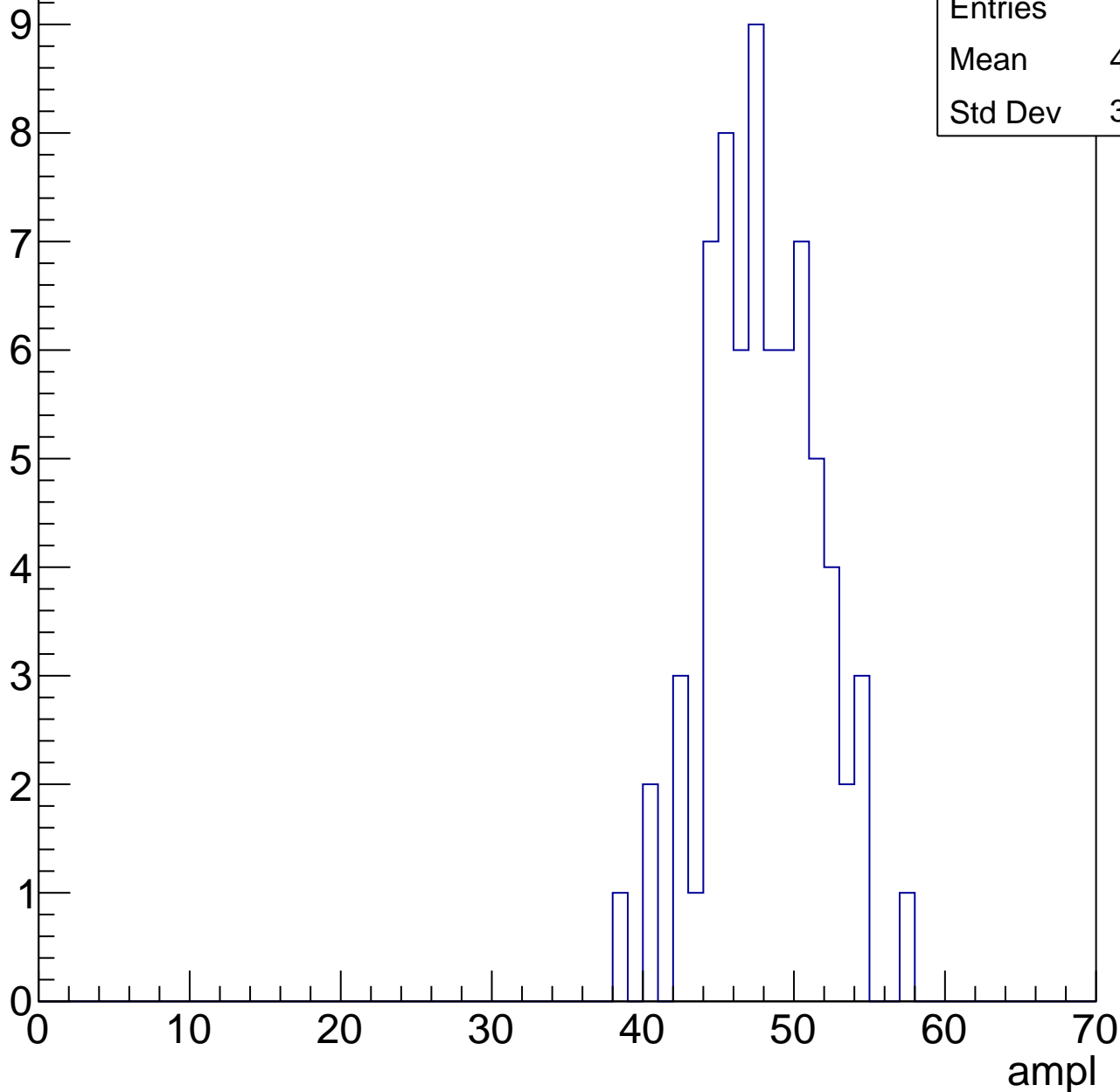


# B1L000S, U25-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	47.52
Std Dev	3.646

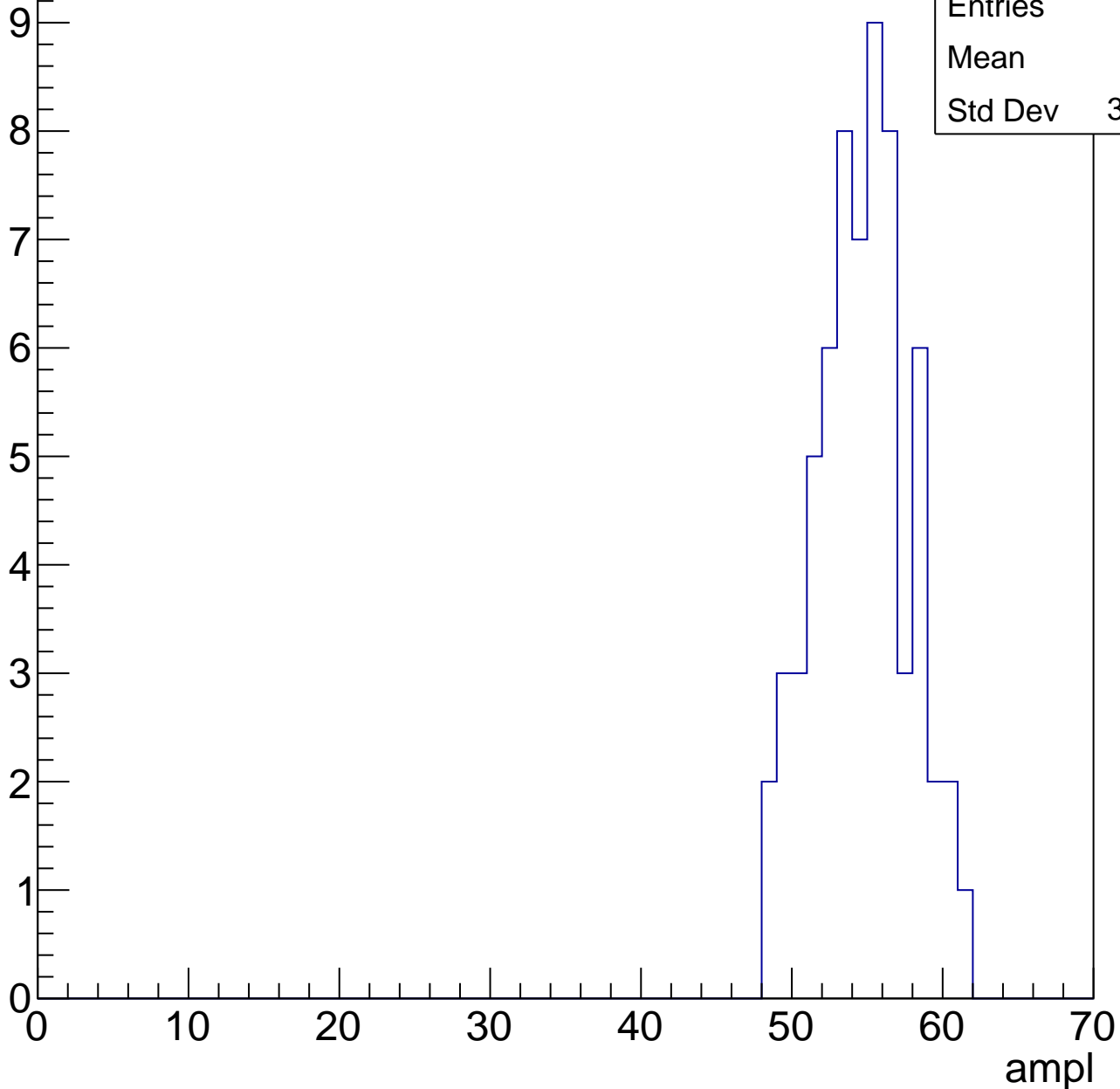


# B1L000S, U25-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

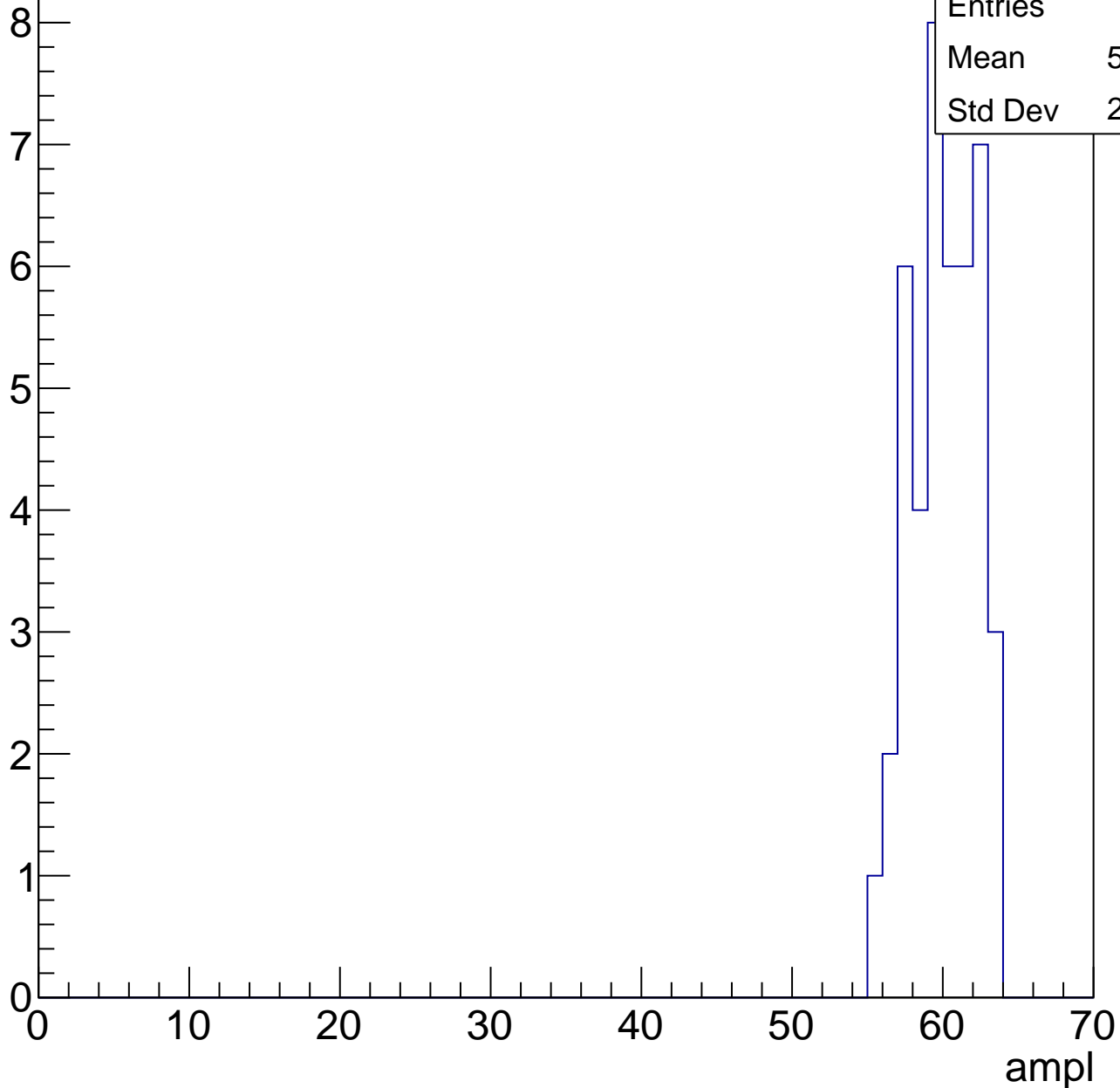
Entries	65
Mean	54.2
Std Dev	3.049



# B1L000S, U25-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

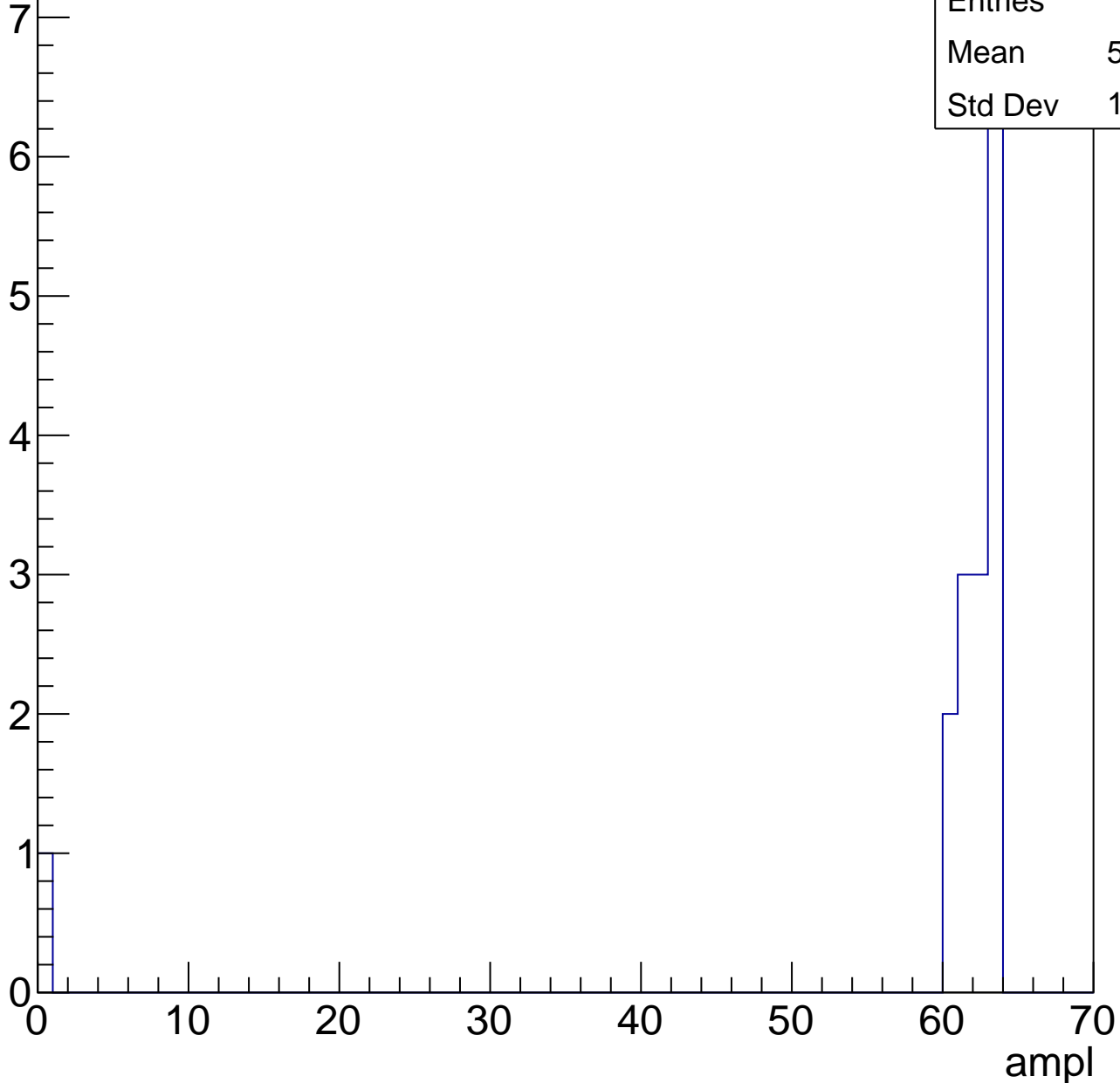


# B1L000S, U25-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	16
Mean	58.12
Std Dev	15.05





# B1L000S, U25-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch77, adc0

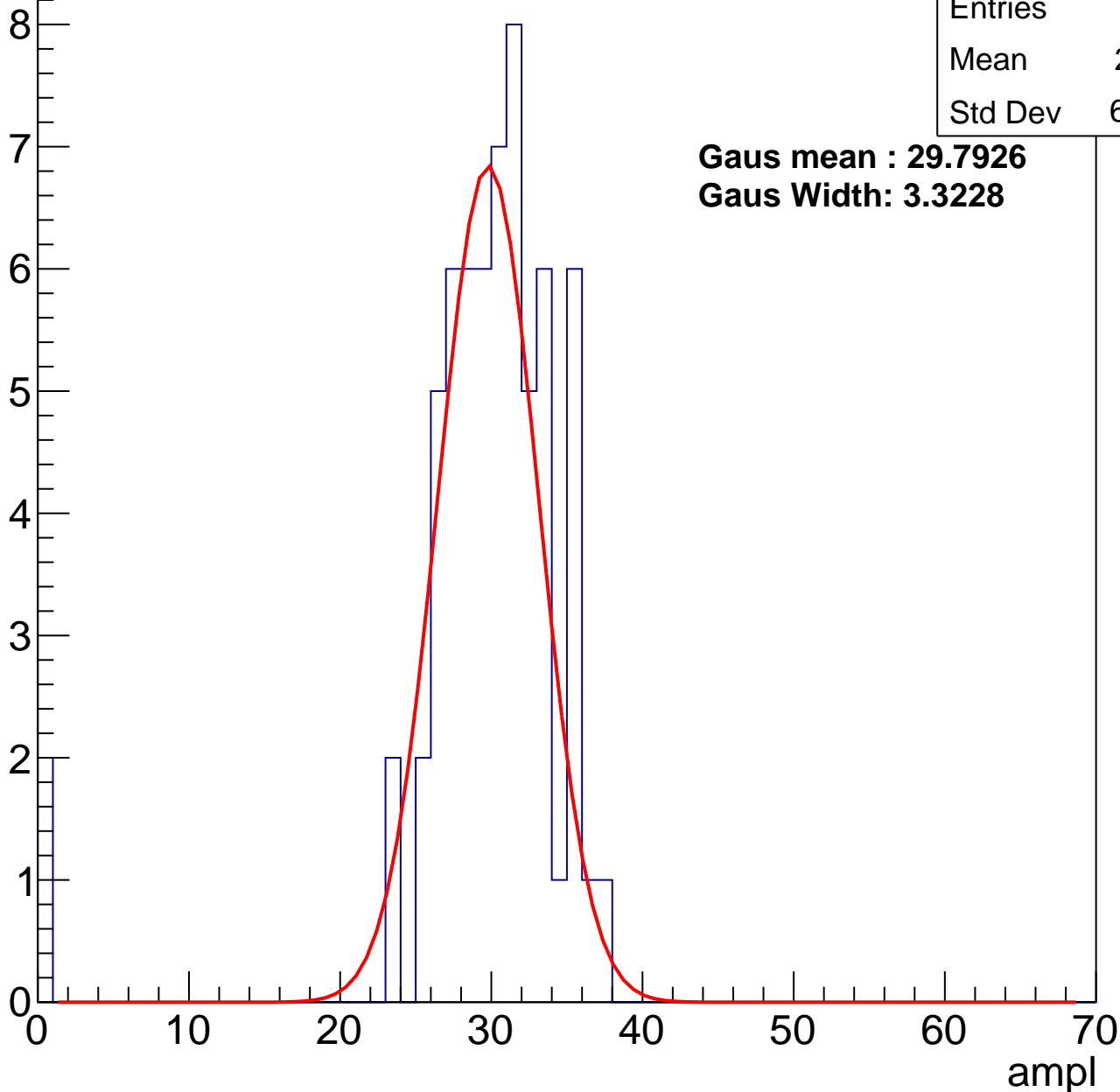
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.11
Std Dev	6.116

**Gaus mean : 29.7926**

**Gaus Width: 3.3228**



# B1L000S, U25-ch77, adc1

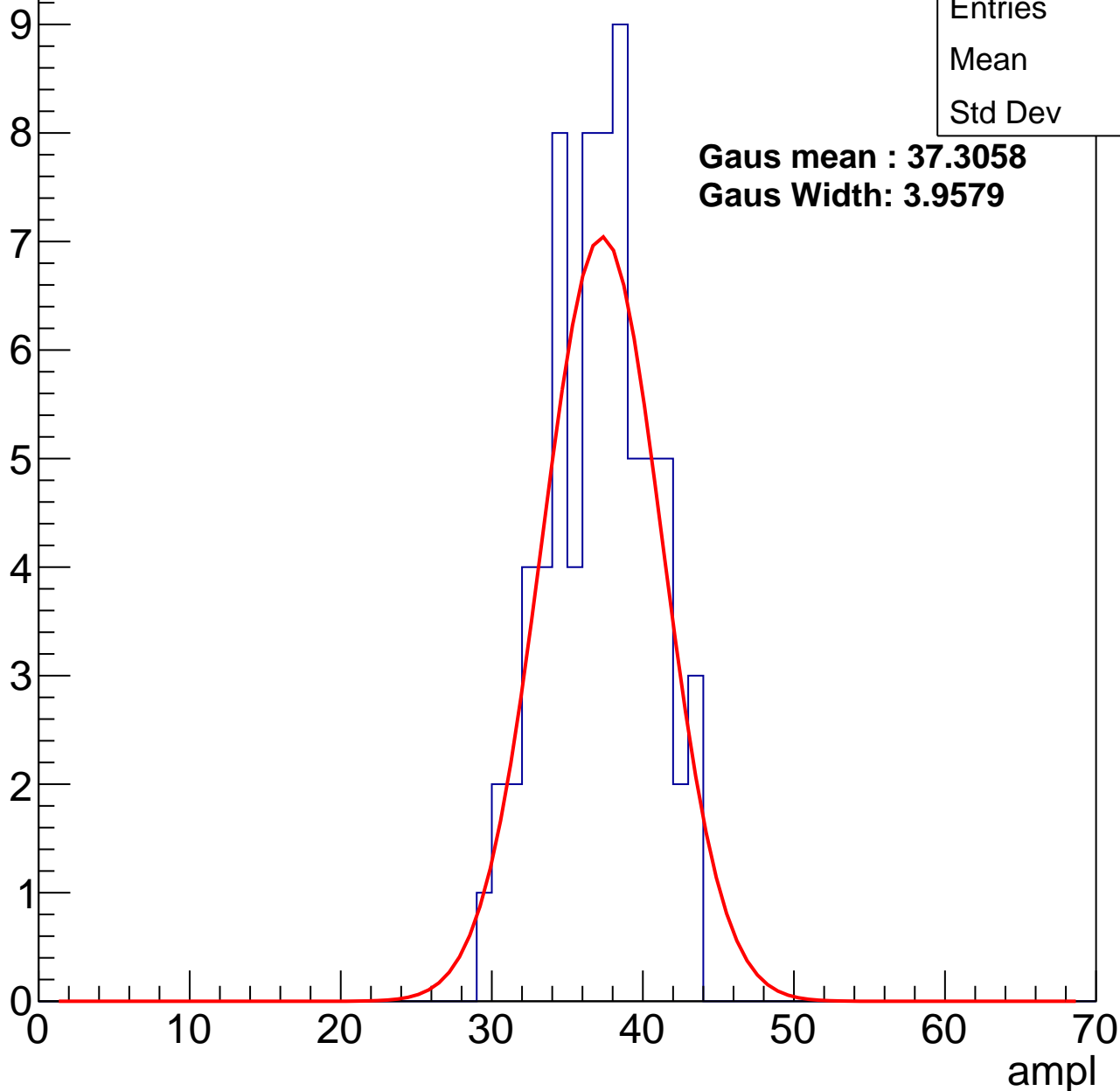
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.6
Std Dev	3.37

**Gaus mean : 37.3058**

**Gaus Width: 3.9579**



# B1L000S, U25-ch77, adc2

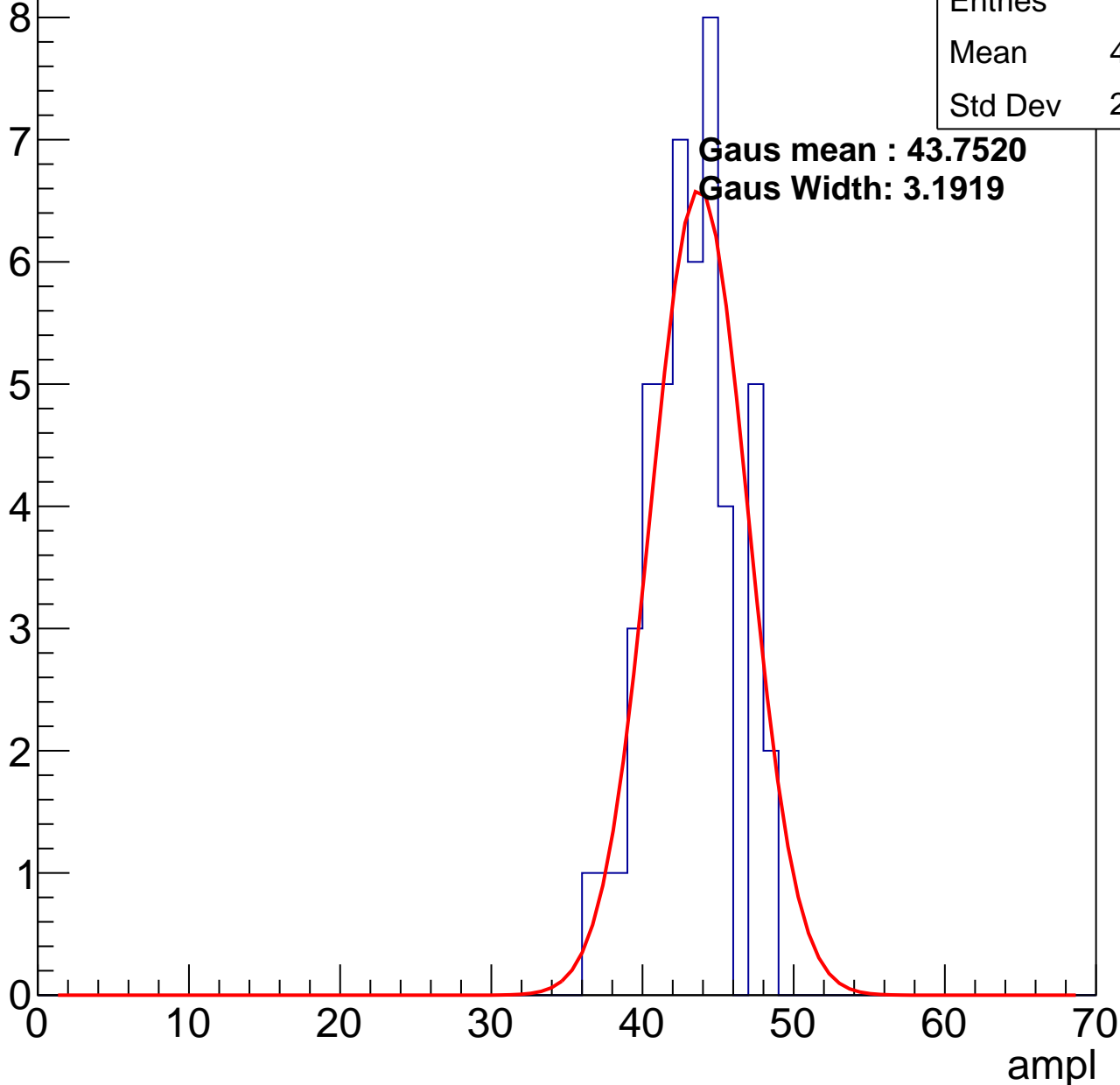
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	42.67
Std Dev	2.809

**Gaus mean : 43.7520**

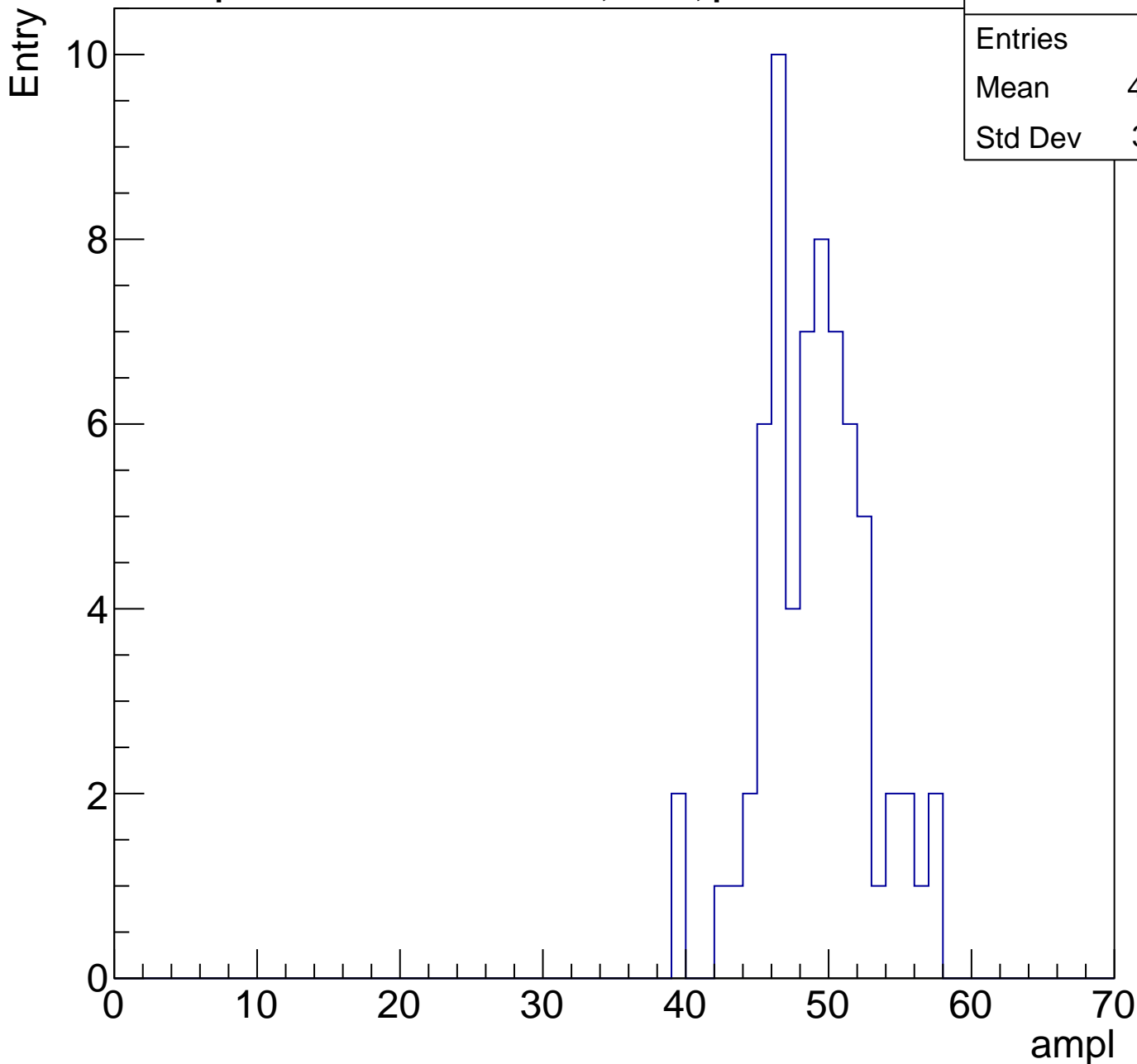
**Gaus Width: 3.1919**



# B1L000S, U25-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	48.57
Std Dev	3.711

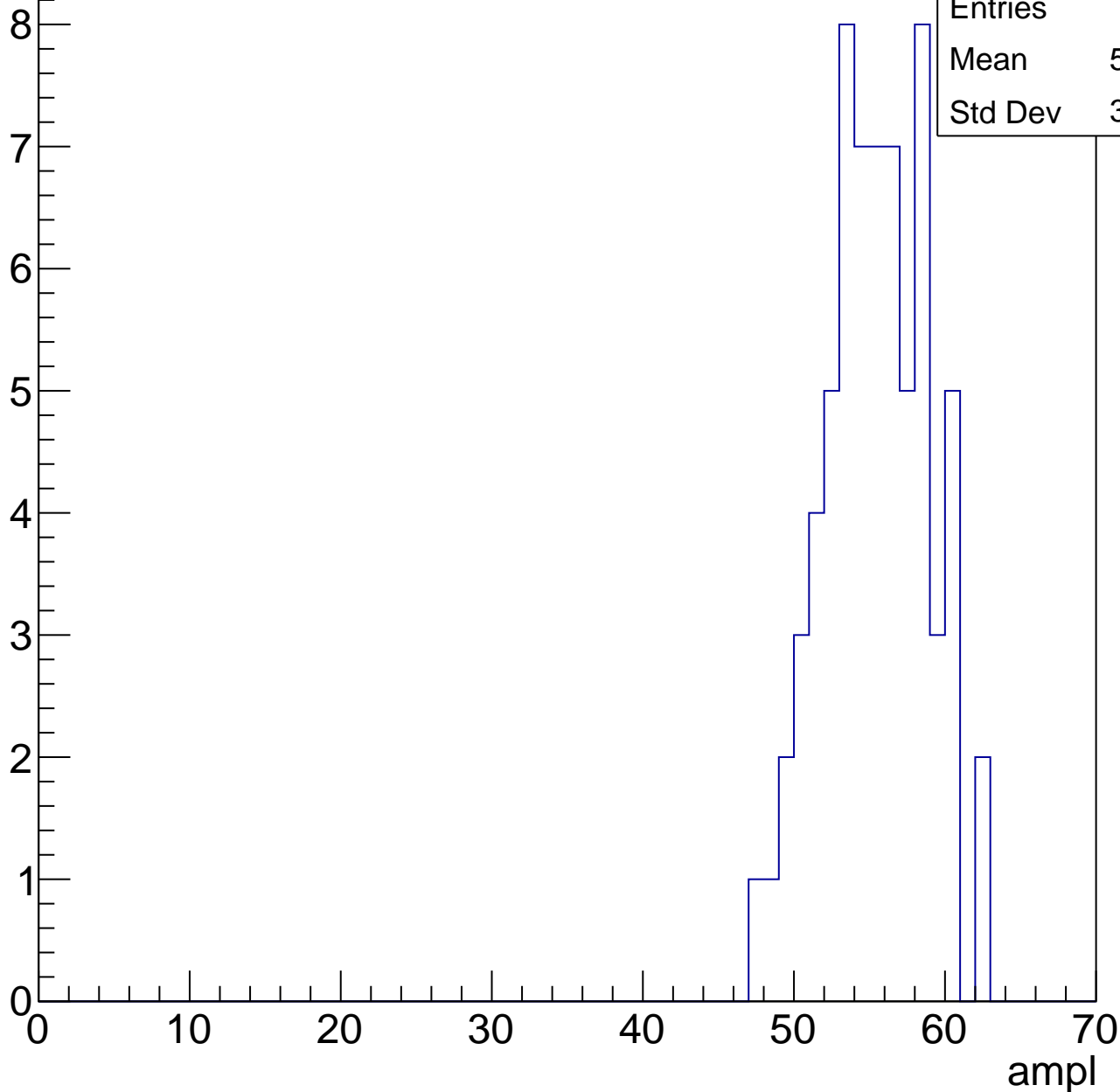


# B1L000S, U25-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	54.94
Std Dev	3.382

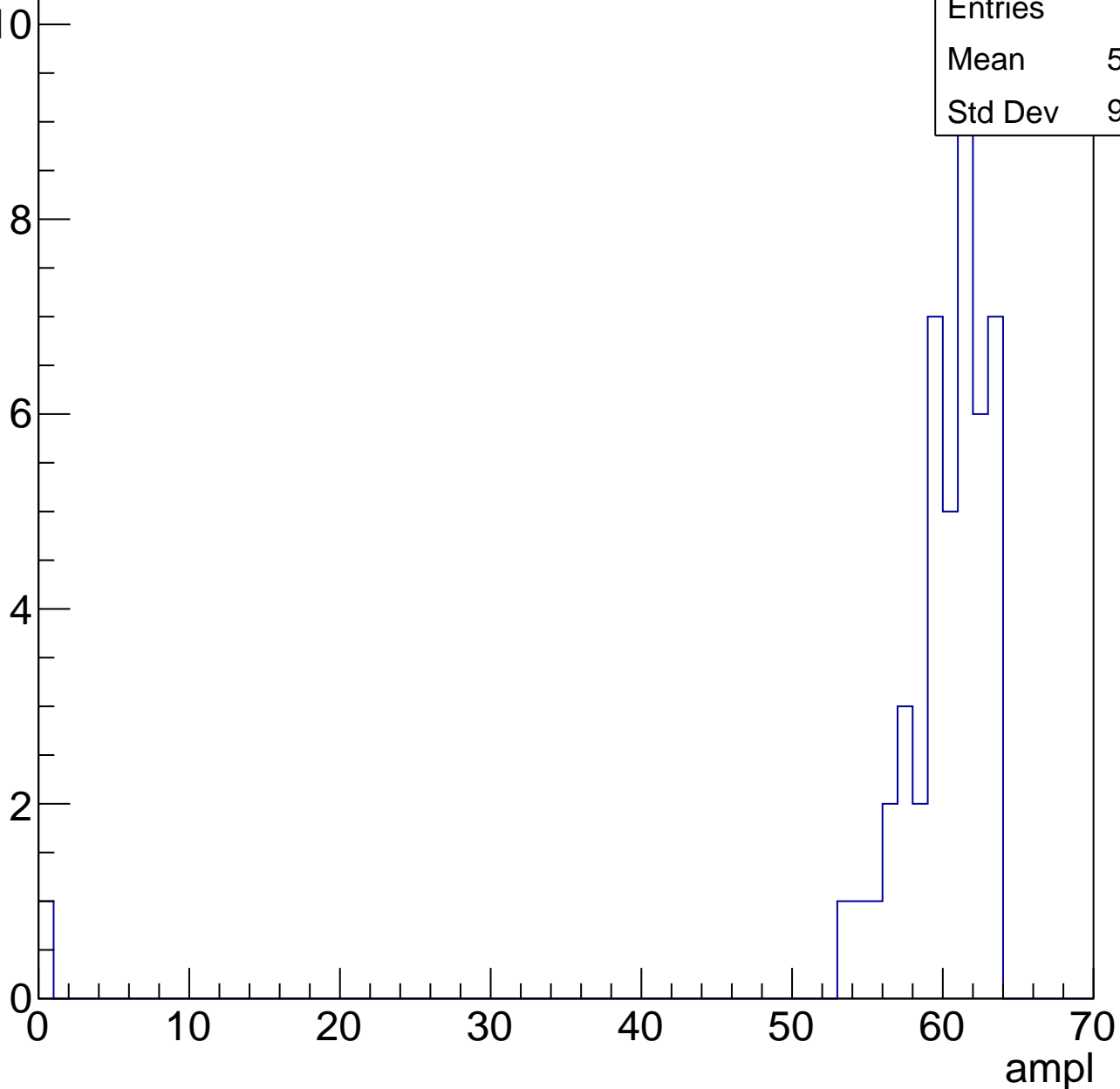


# B1L000S, U25-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	58.63
Std Dev	9.083



# B1L000S, U25-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

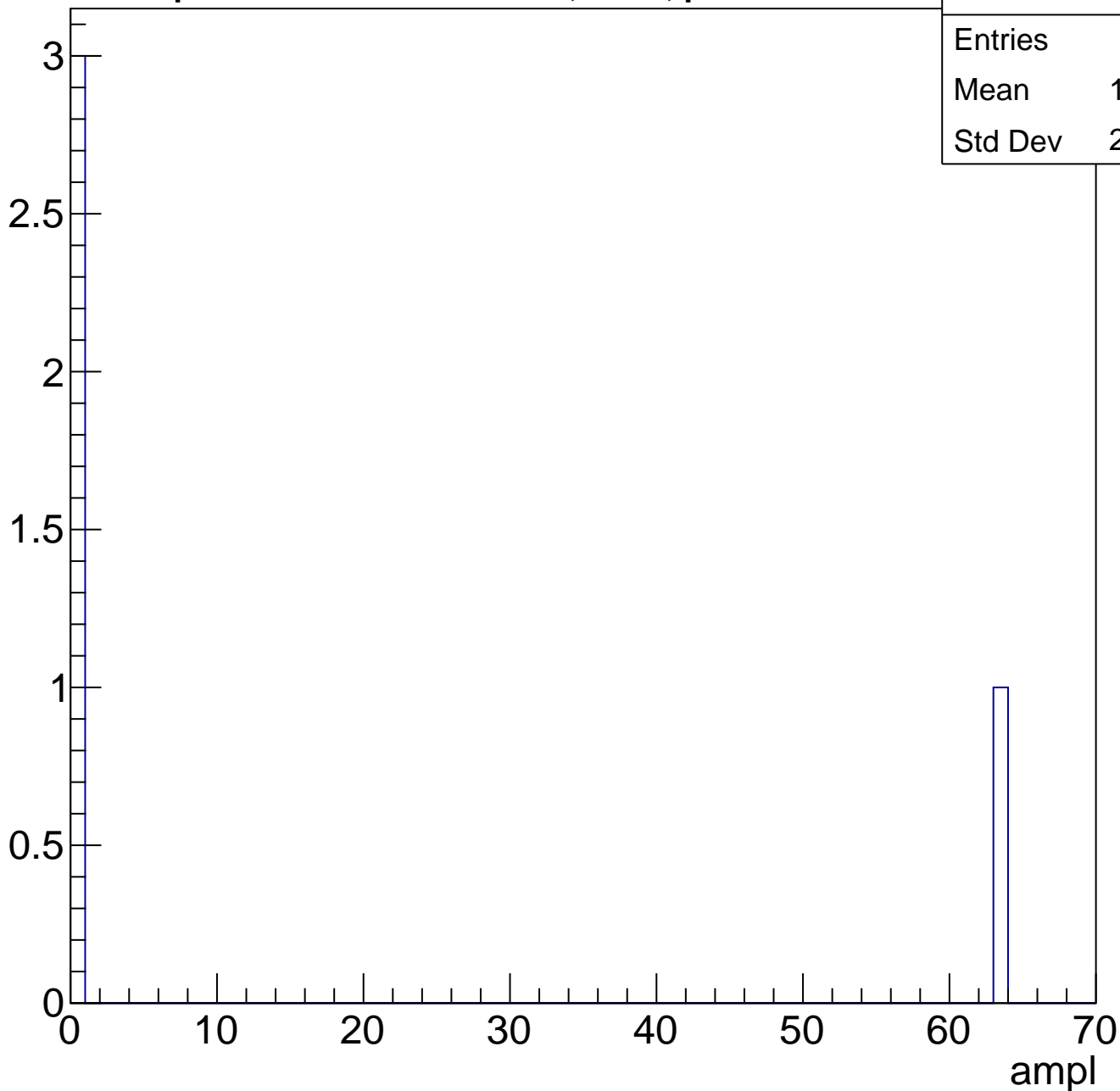




# B1L000S, U25-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch78, adc0

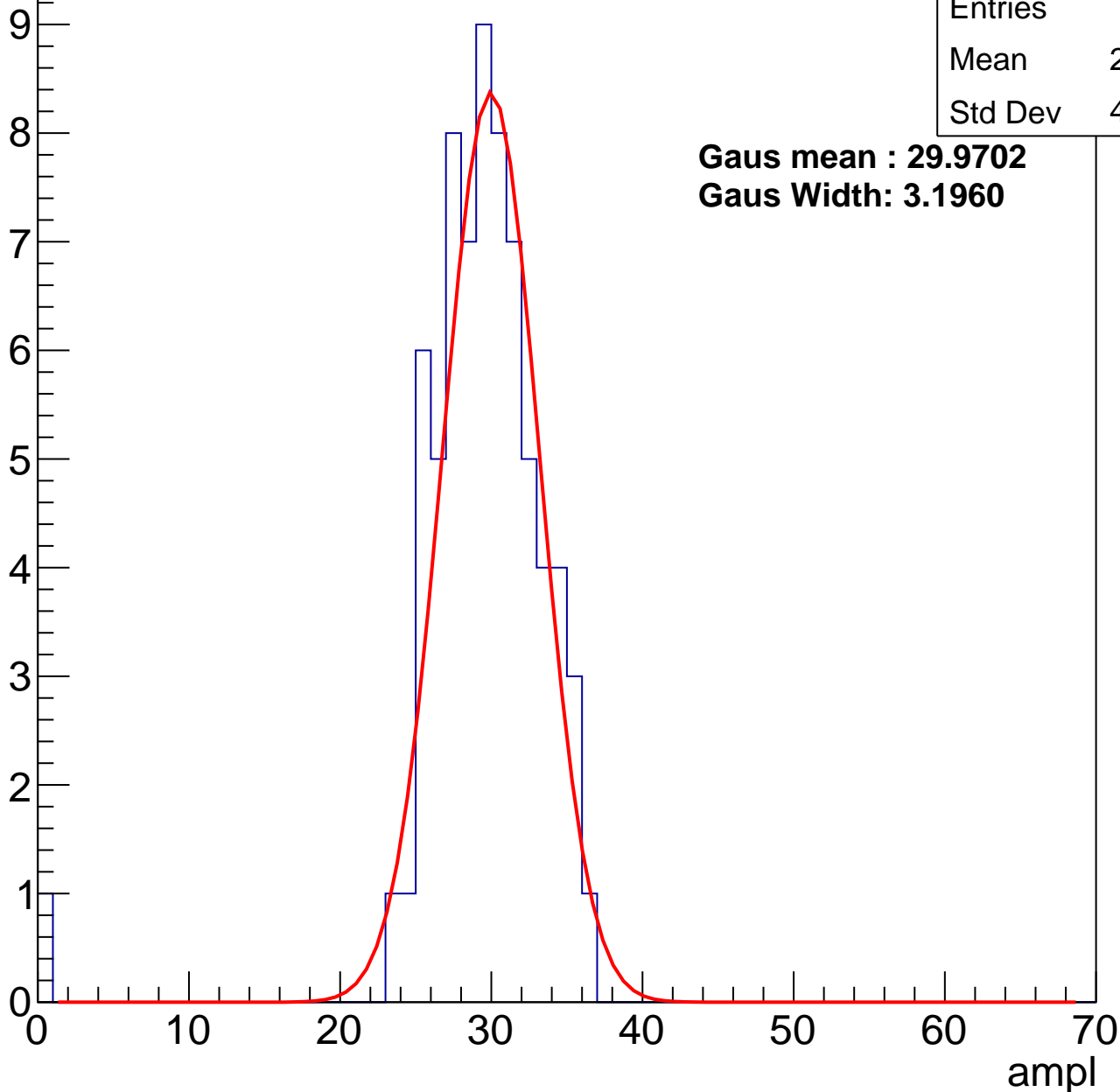
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.94
Std Dev	4.604

**Gaus mean : 29.9702**

**Gaus Width: 3.1960**



# B1L000S, U25-ch78, adc1

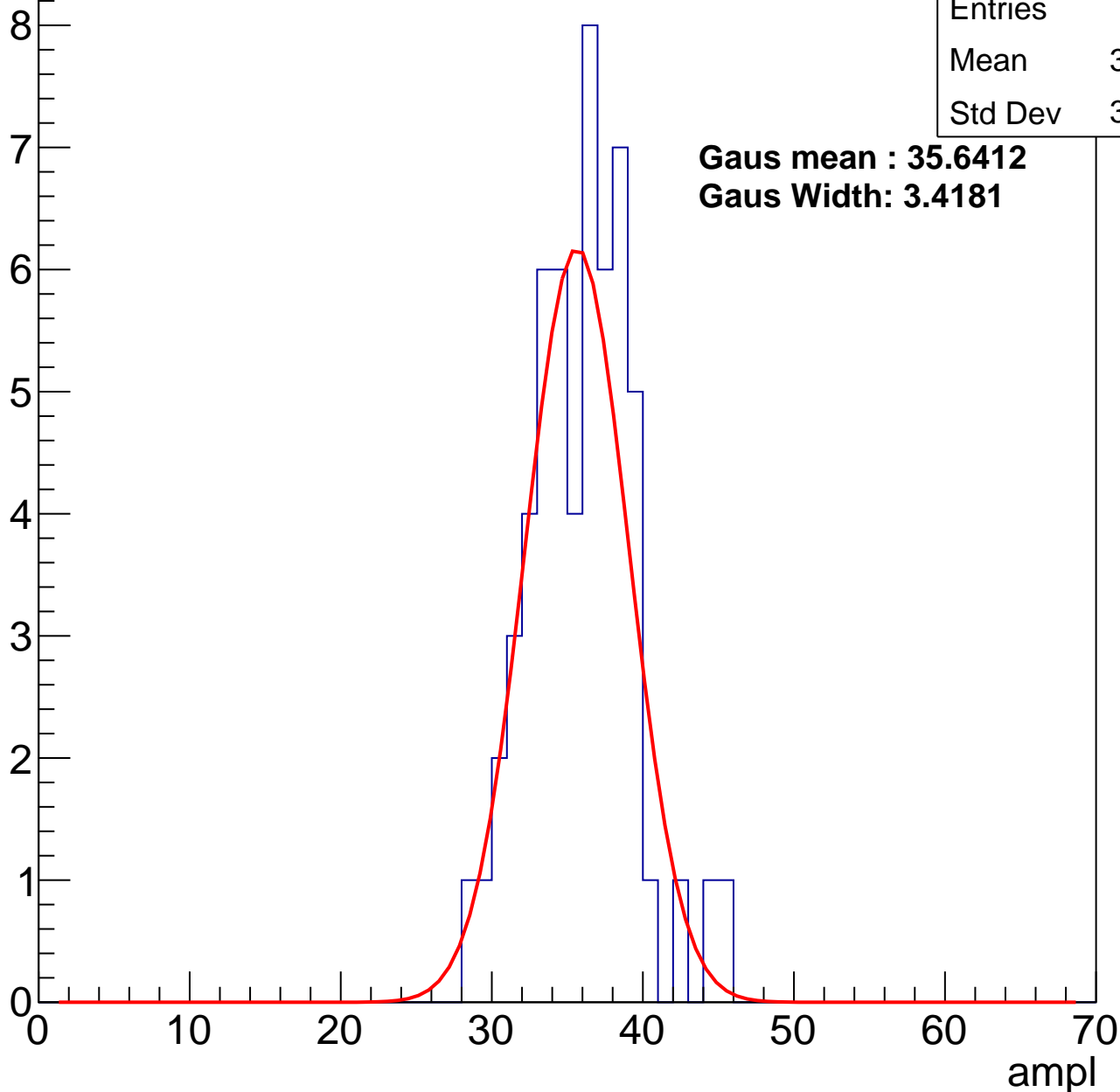
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	35.47
Std Dev	3.419

**Gaus mean : 35.6412**

**Gaus Width: 3.4181**



# B1L000S, U25-ch78, adc2

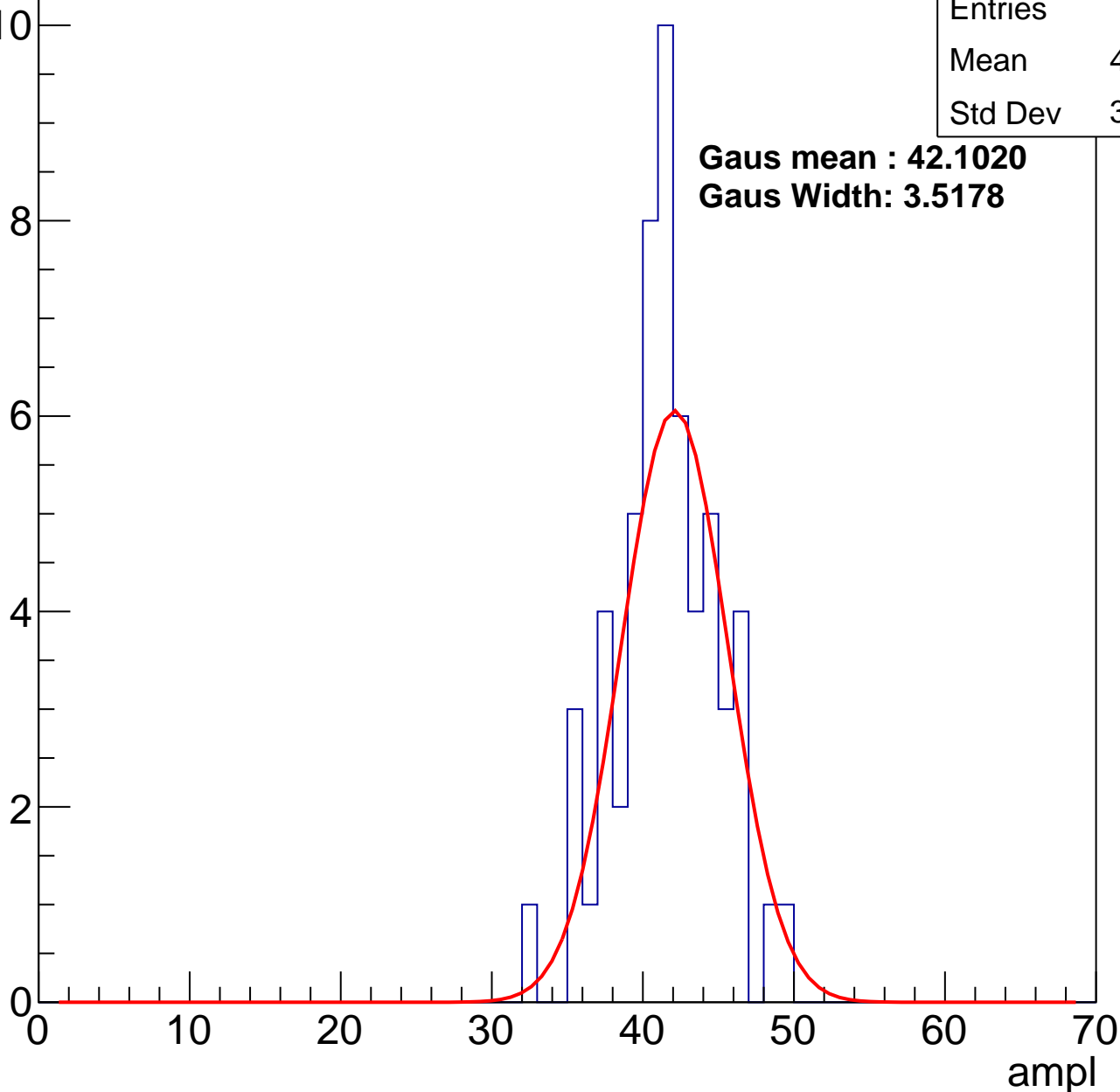
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.07
Std Dev	3.352

**Gaus mean : 42.1020**

**Gaus Width: 3.5178**

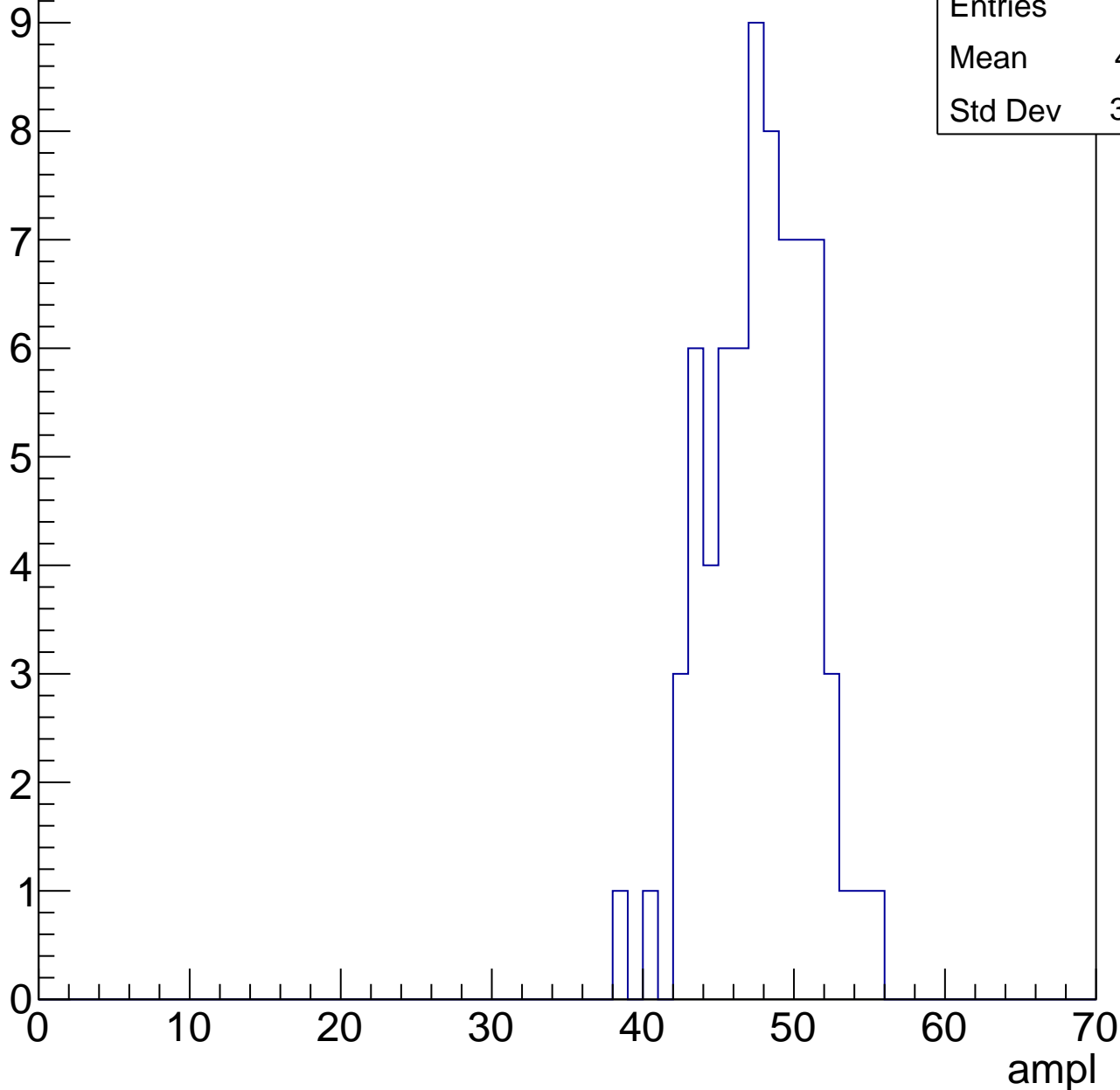


# B1L000S, U25-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	47.31
Std Dev	3.347

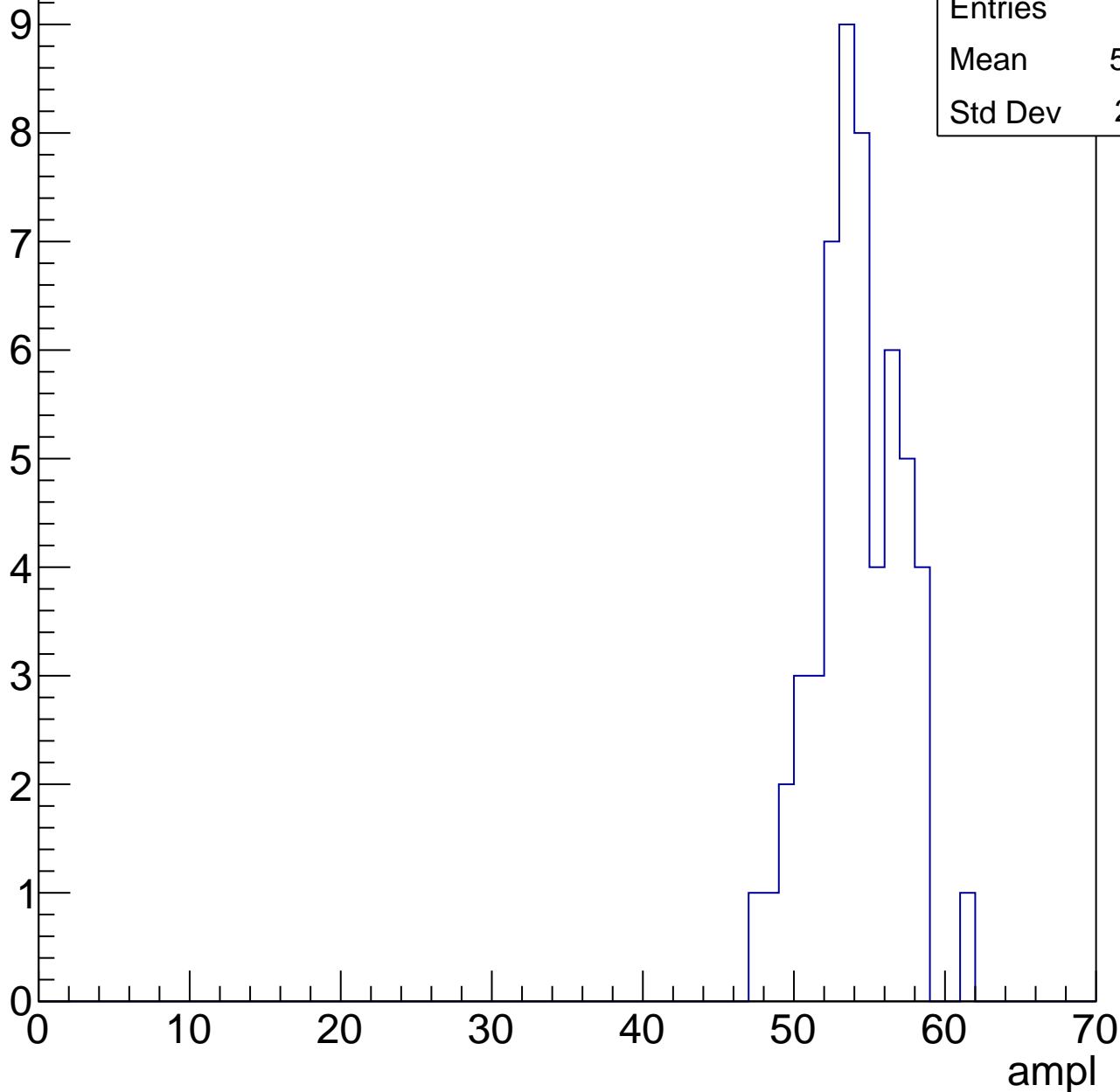


# B1L000S, U25-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

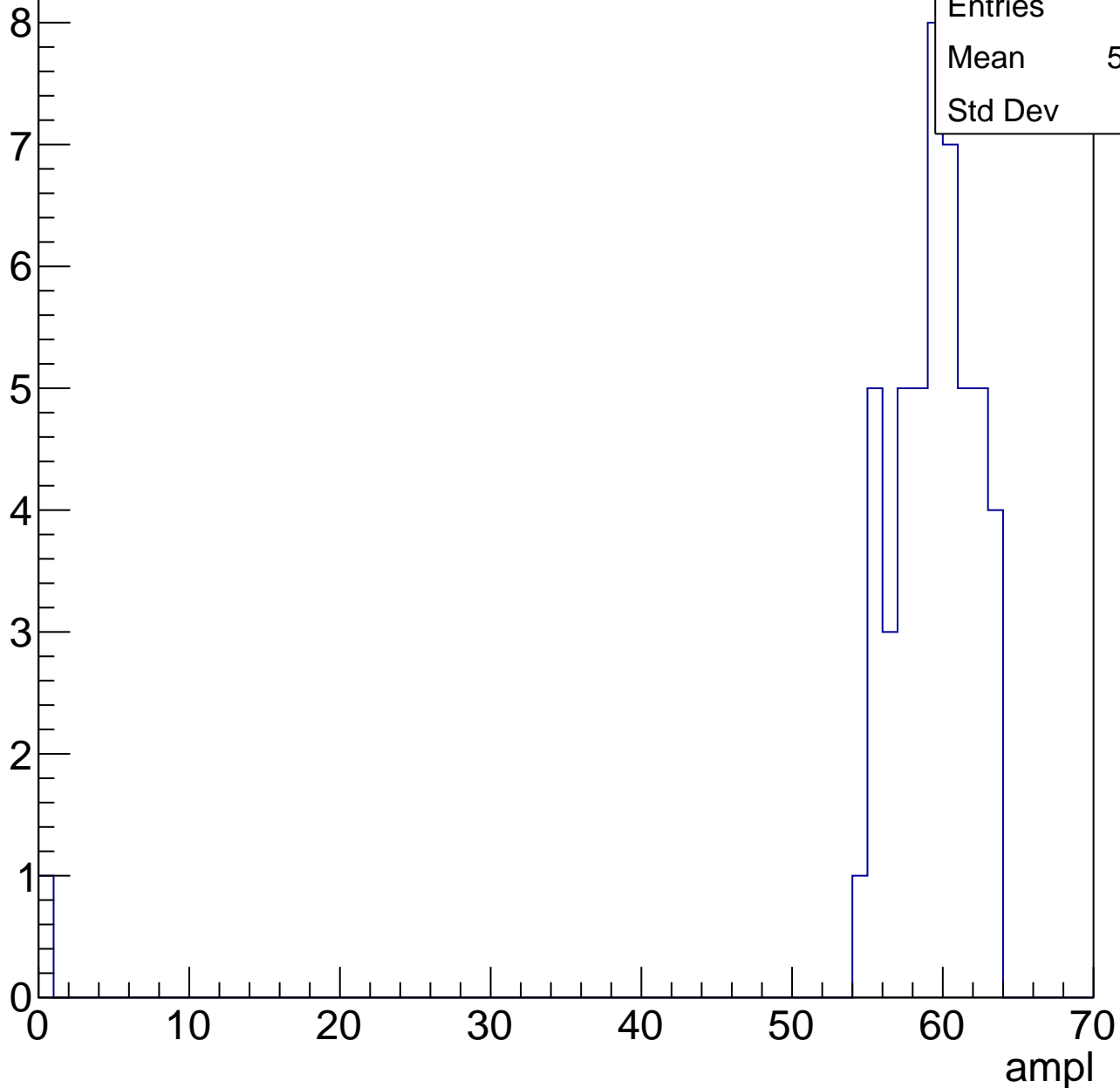
Entries	54
Mean	53.76
Std Dev	2.821



# B1L000S, U25-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

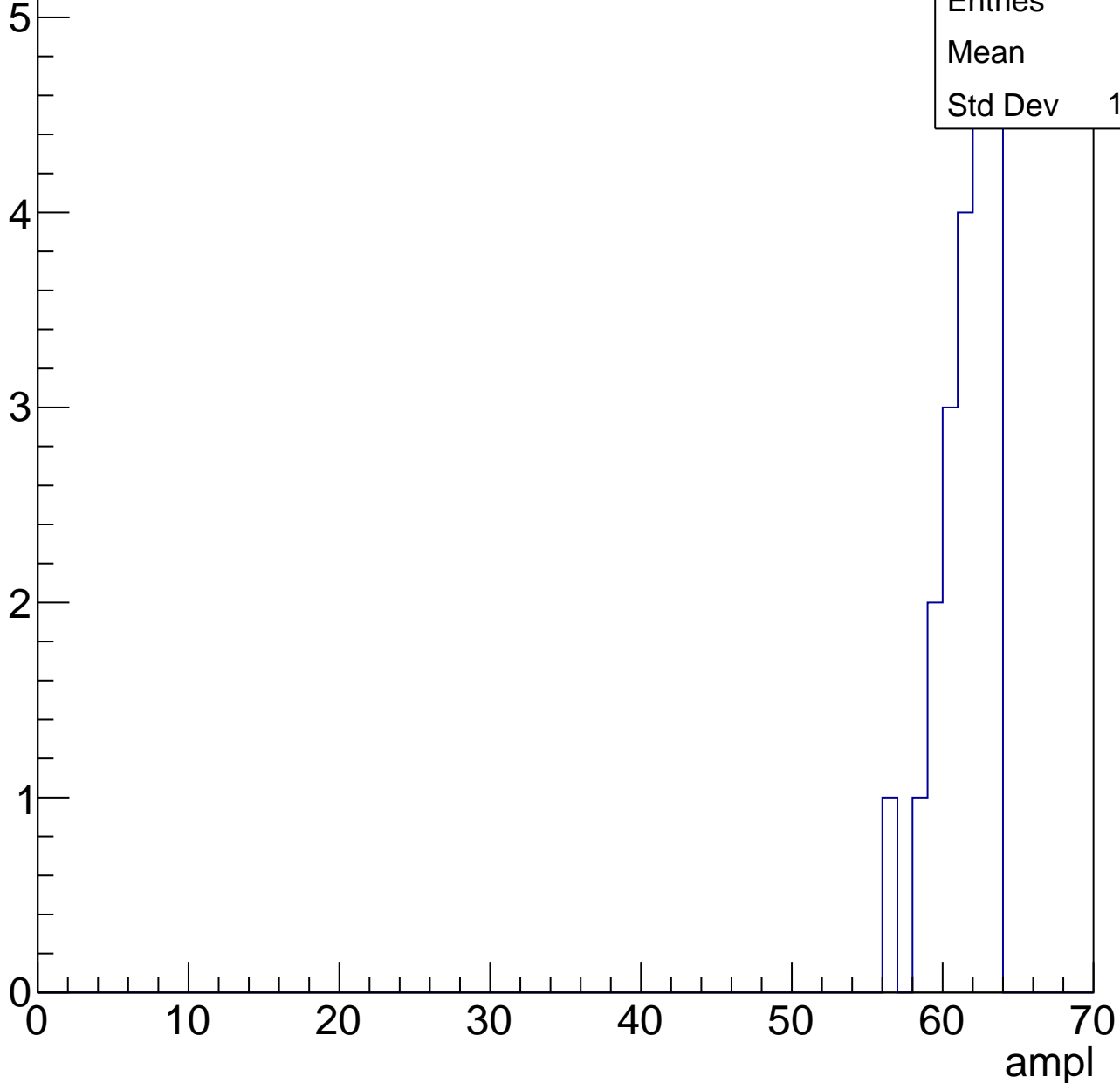


# B1L000S, U25-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	61
Std Dev	1.826





# B1L000S, U25-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L000S, U25-ch79, adc0

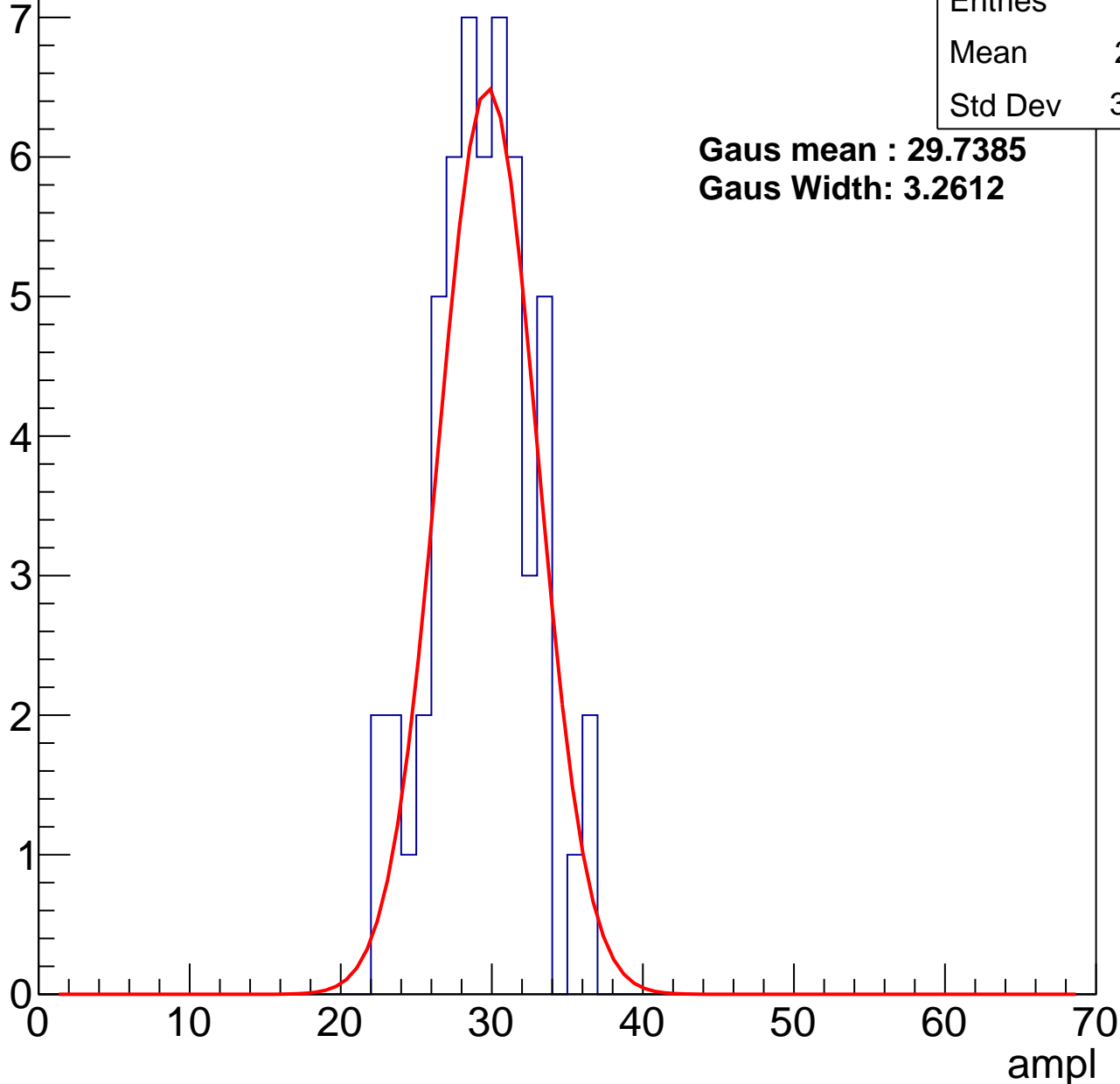
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	28.91
Std Dev	3.232

**Gaus mean : 29.7385**

**Gaus Width: 3.2612**



# B1L000S, U25-ch79, adc1

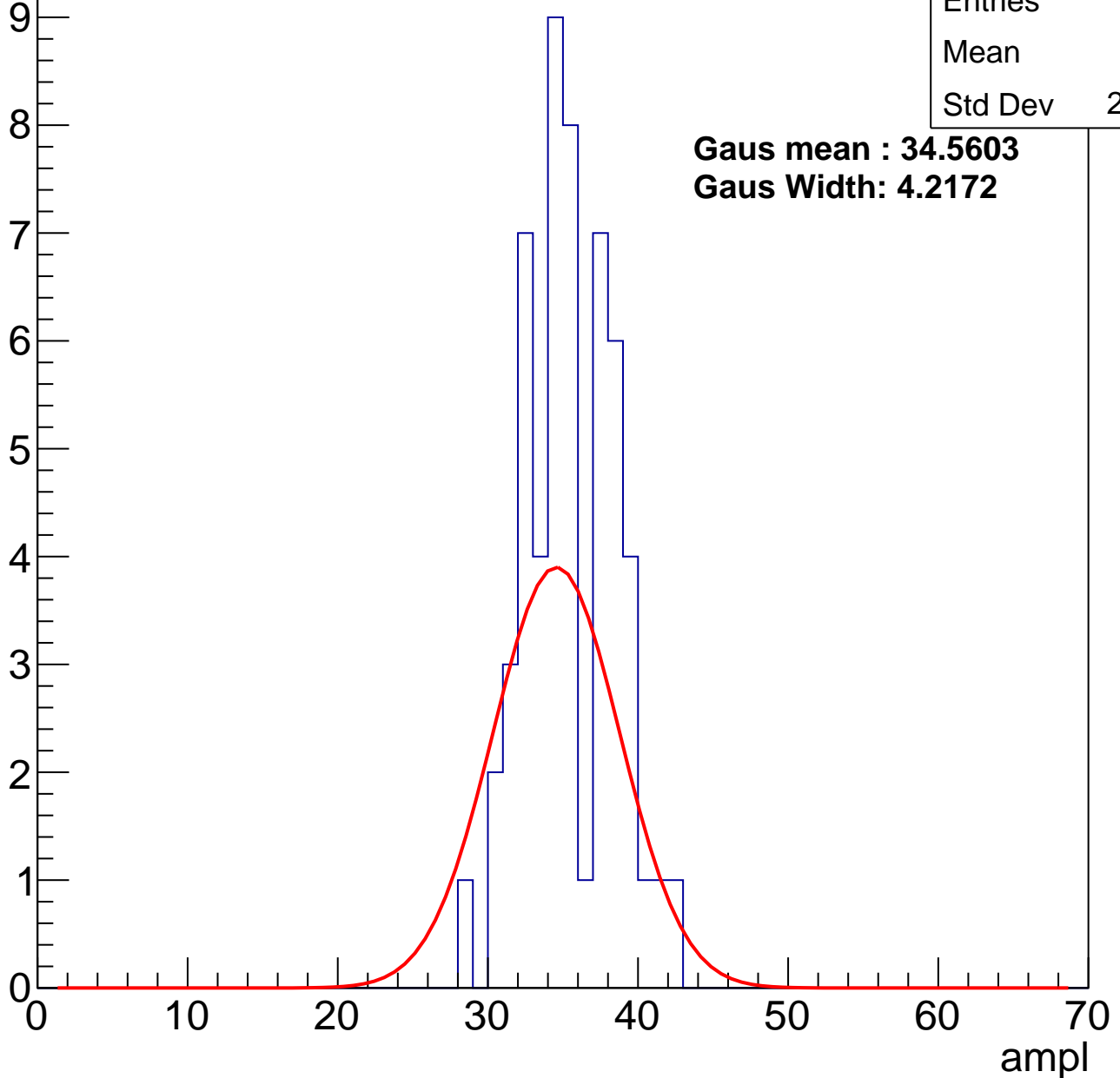
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	35
Std Dev	2.991

**Gaus mean : 34.5603**

**Gaus Width: 4.2172**



# B1L000S, U25-ch79, adc2

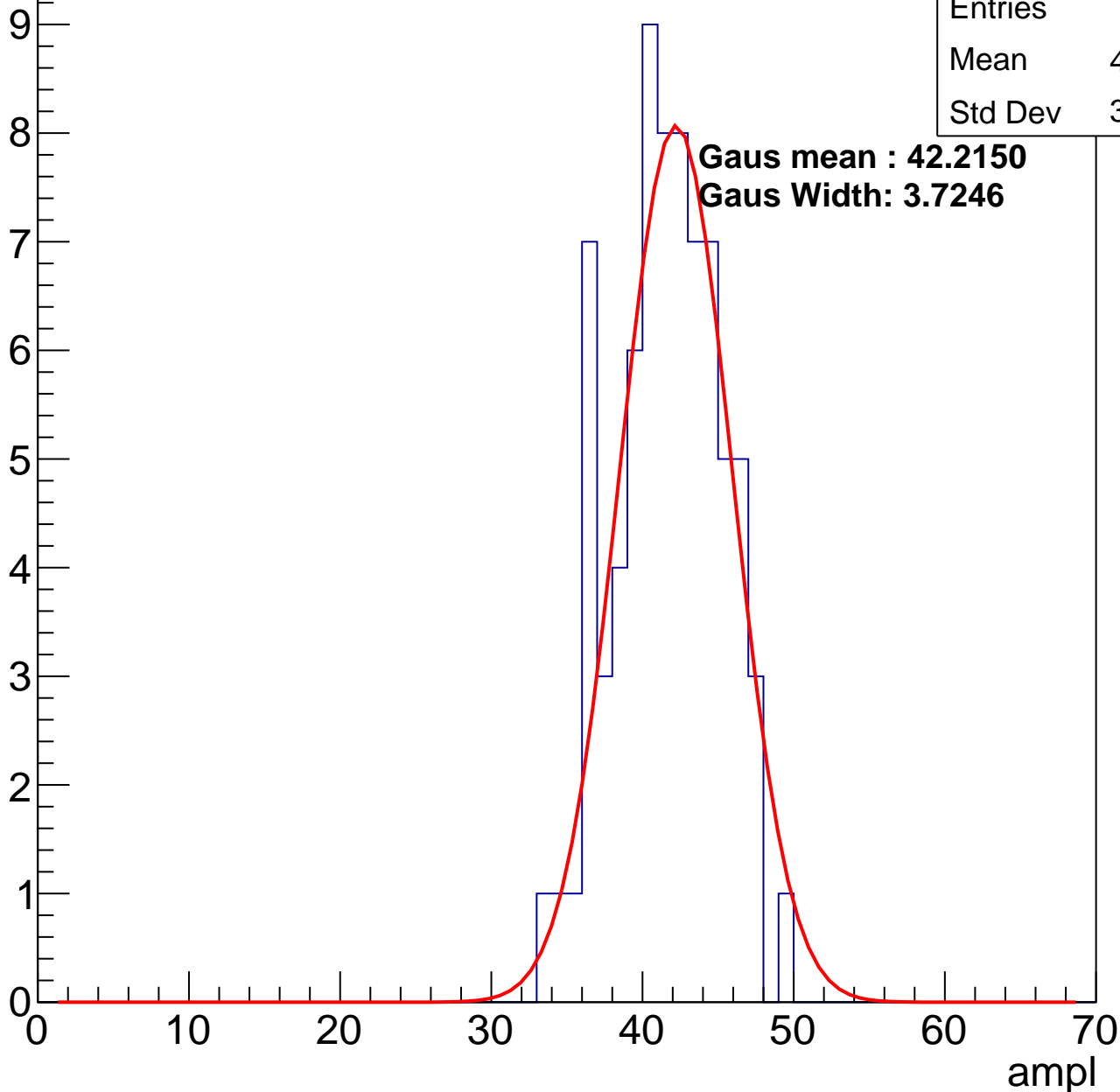
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	41.17
Std Dev	3.469

**Gaus mean : 42.2150**

**Gaus Width: 3.7246**

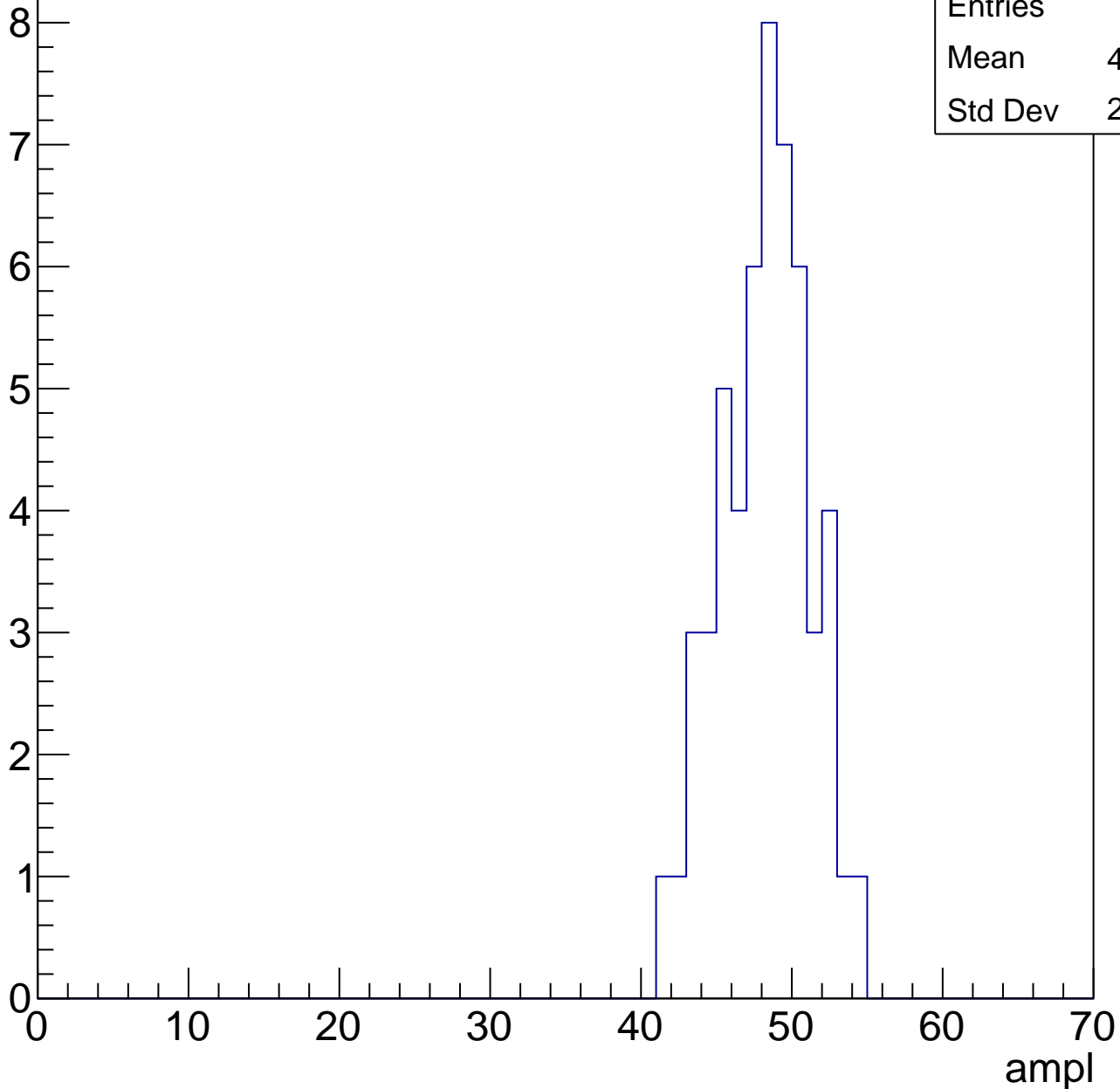


# B1L000S, U25-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

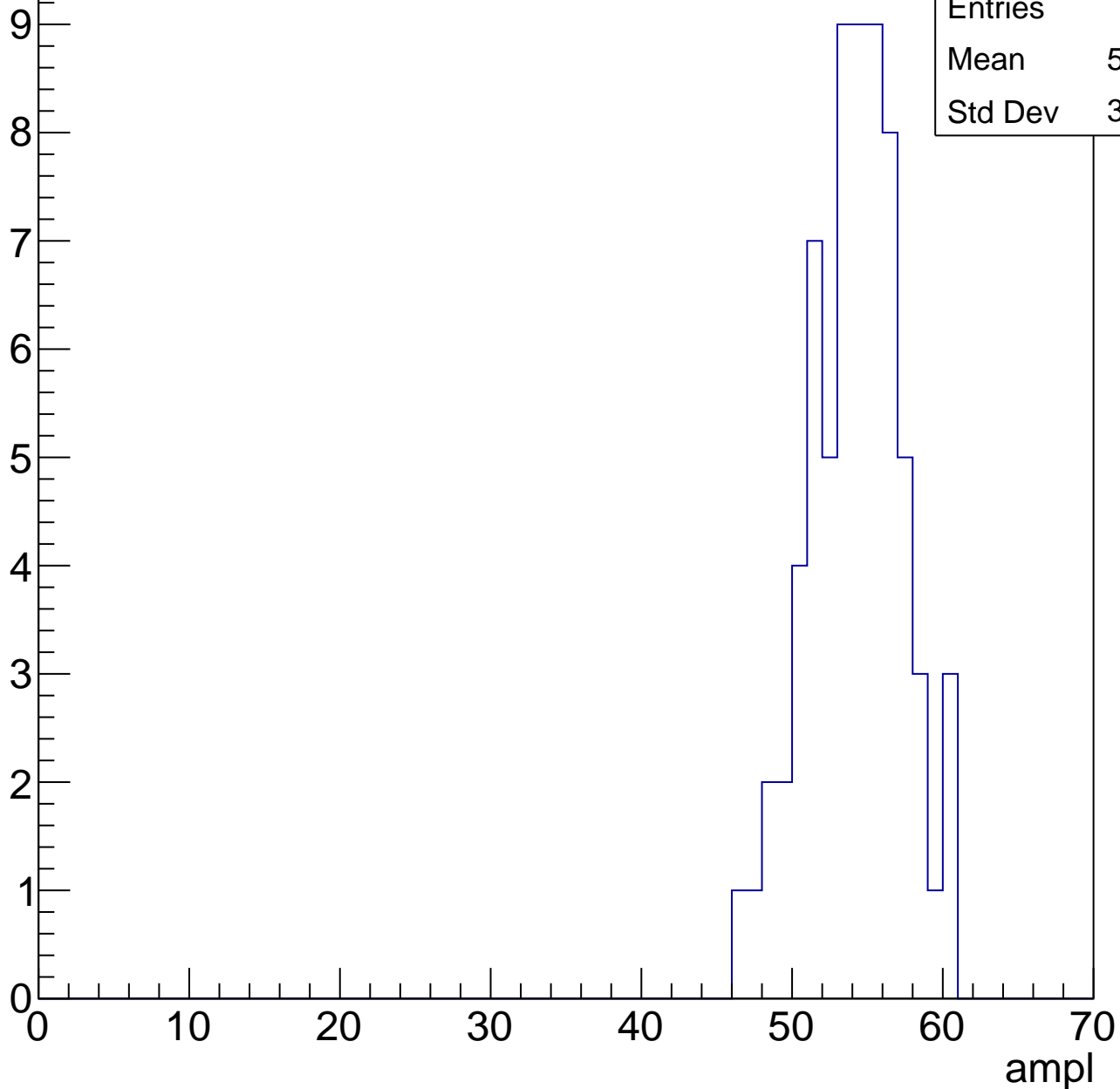
Entries	53
Mean	47.74
Std Dev	2.928



# B1L000S, U25-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



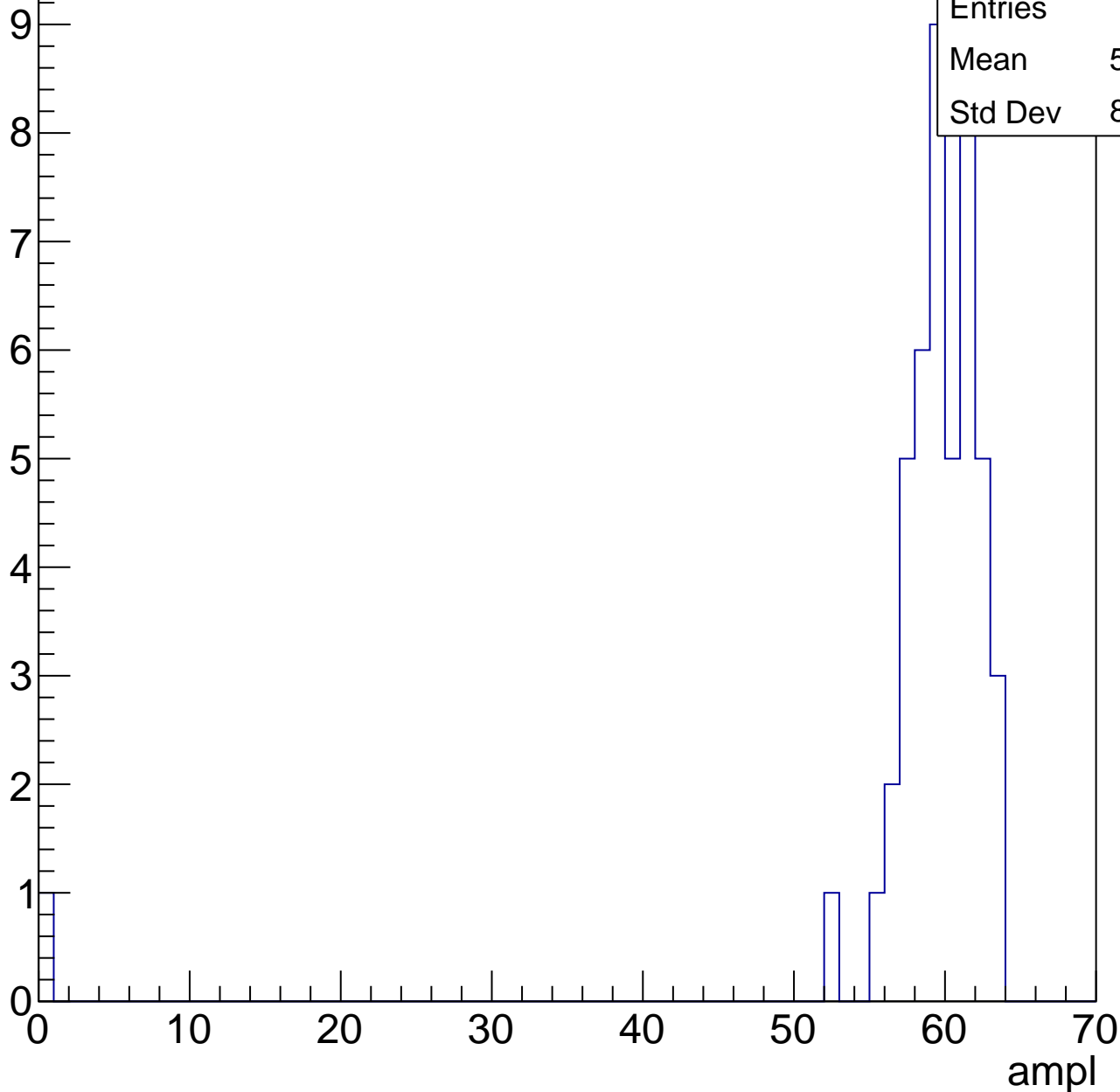
Entries	69
Mean	53.74
Std Dev	3.077

# B1L000S, U25-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	58.04
Std Dev	8.942

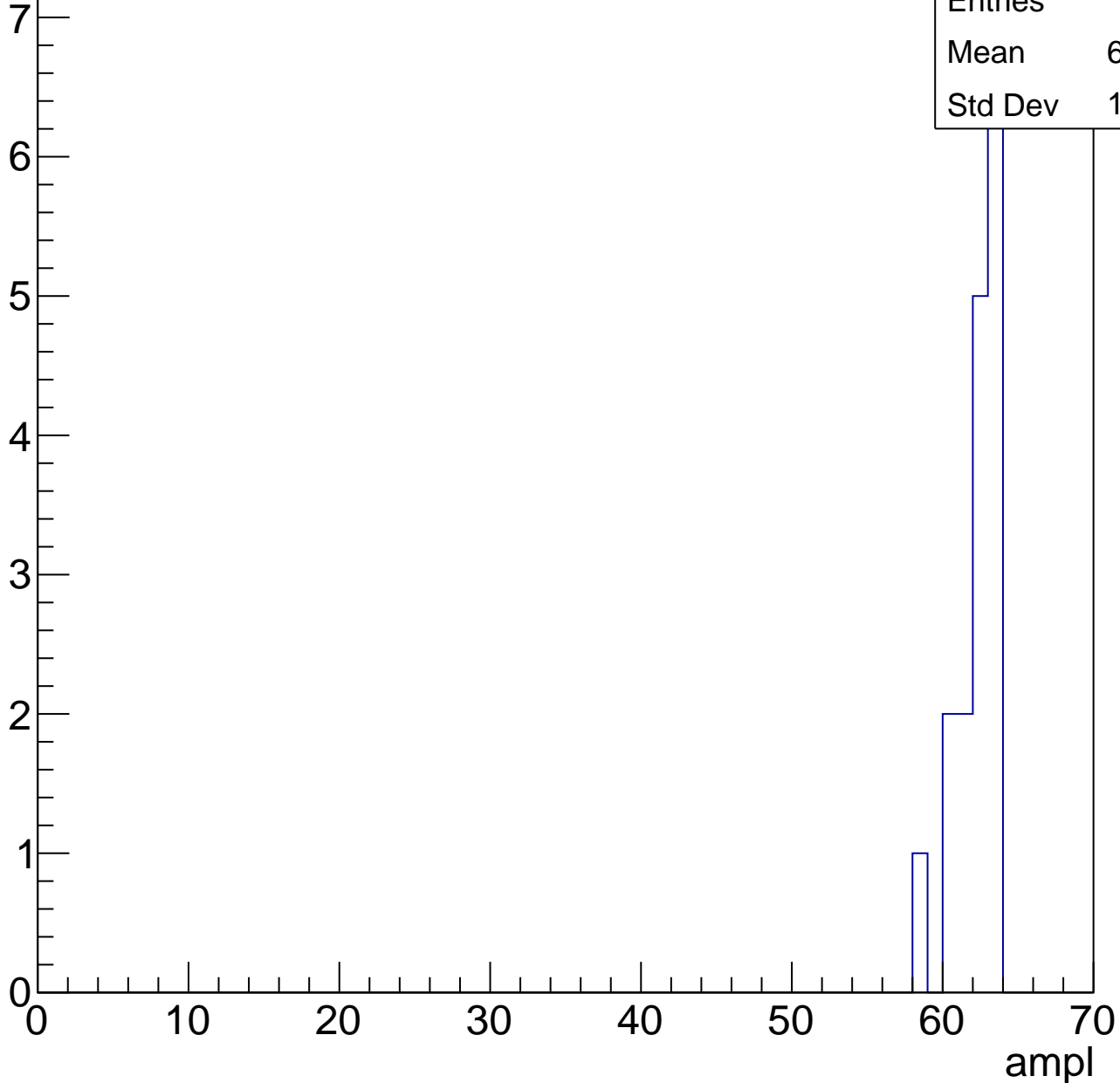


# B1L000S, U25-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	61.82
Std Dev	1.382





# B1L000S, U25-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch80, adc0

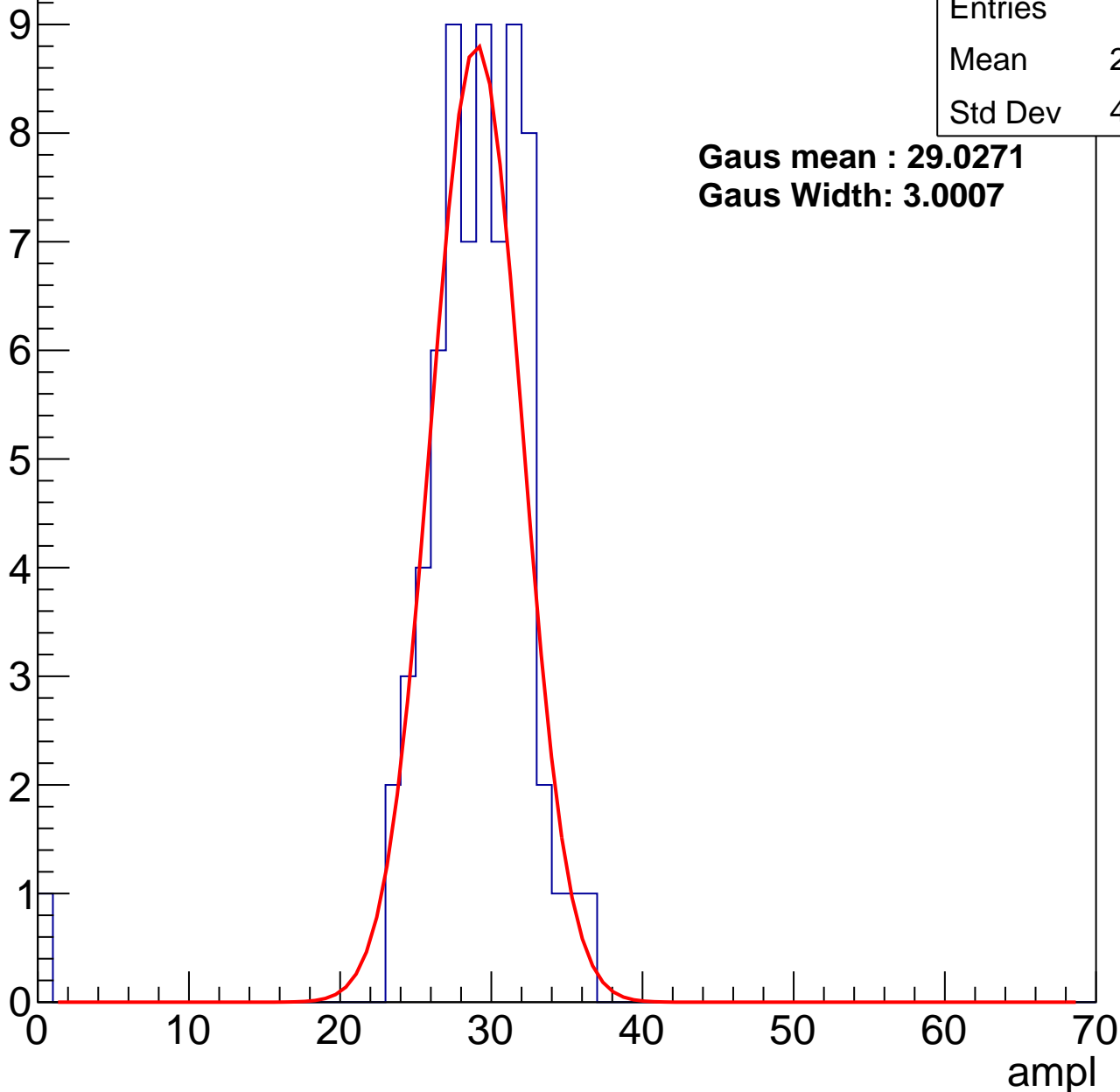
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	28.43
Std Dev	4.445

**Gaus mean : 29.0271**

**Gaus Width: 3.0007**



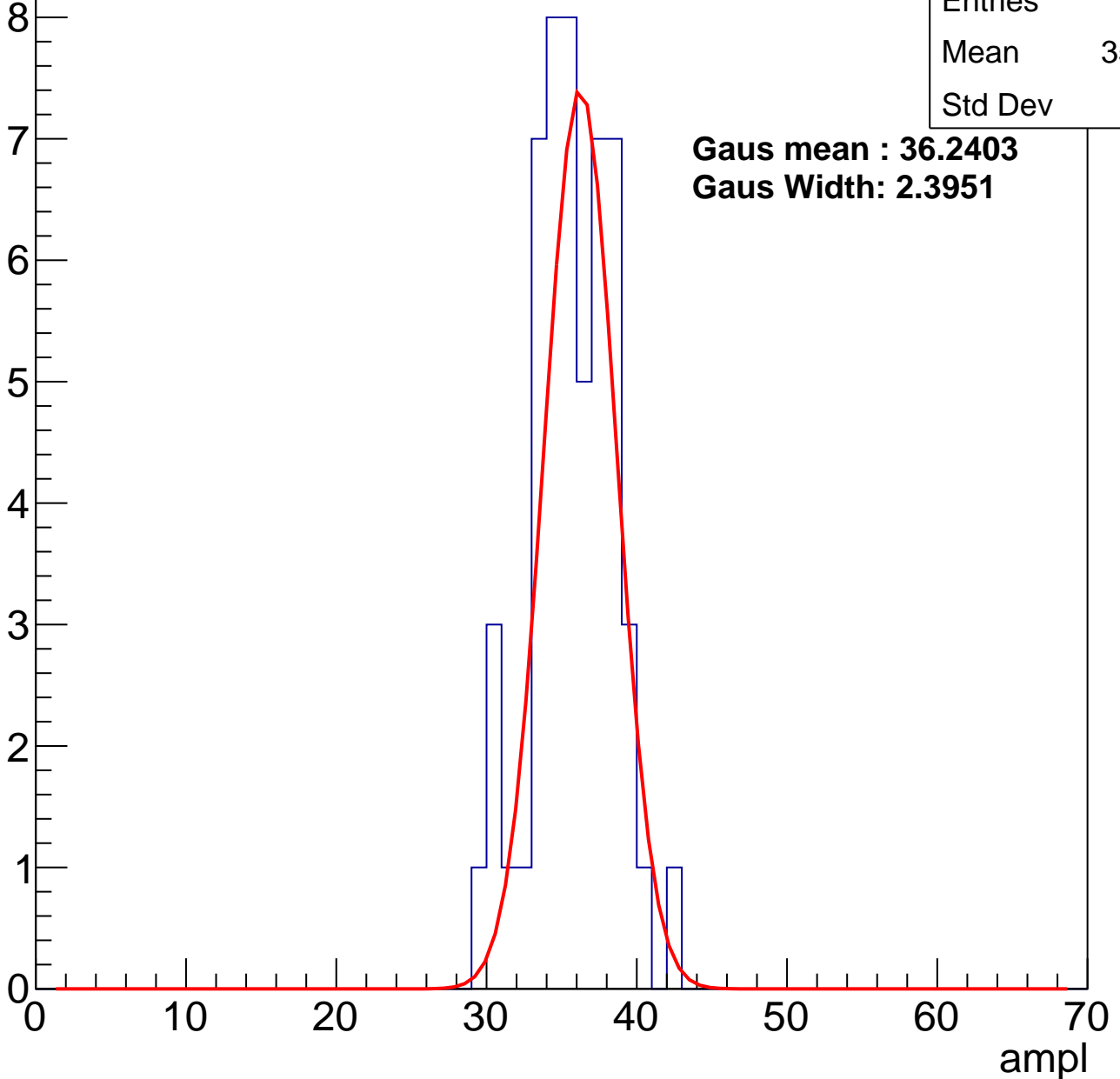
# B1L000S, U25-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	35.26
Std Dev	2.7

**Gaus mean : 36.2403**  
**Gaus Width: 2.3951**



# B1L000S, U25-ch80, adc2

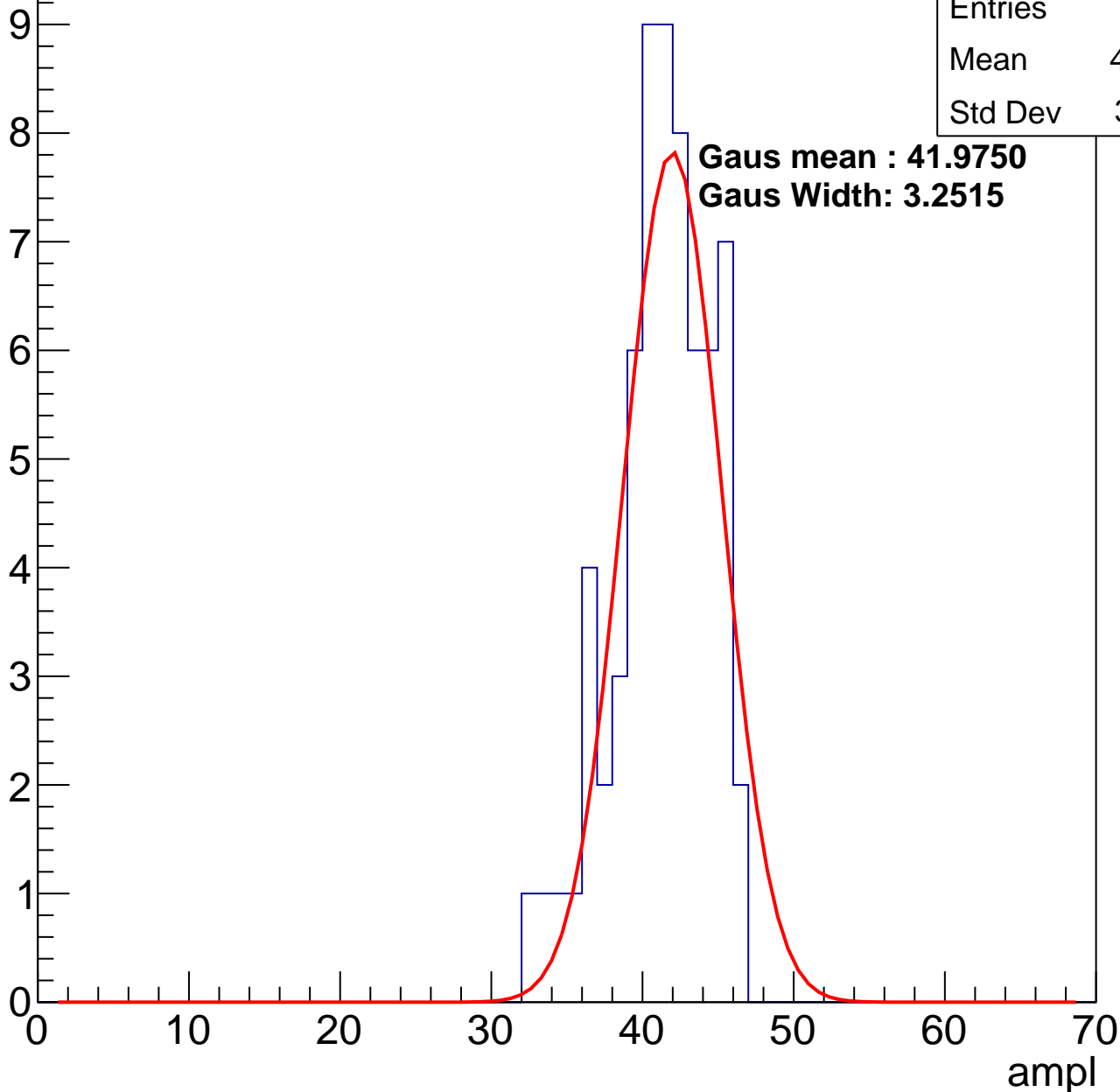
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	40.82
Std Dev	3.181

**Gaus mean : 41.9750**

**Gaus Width: 3.2515**

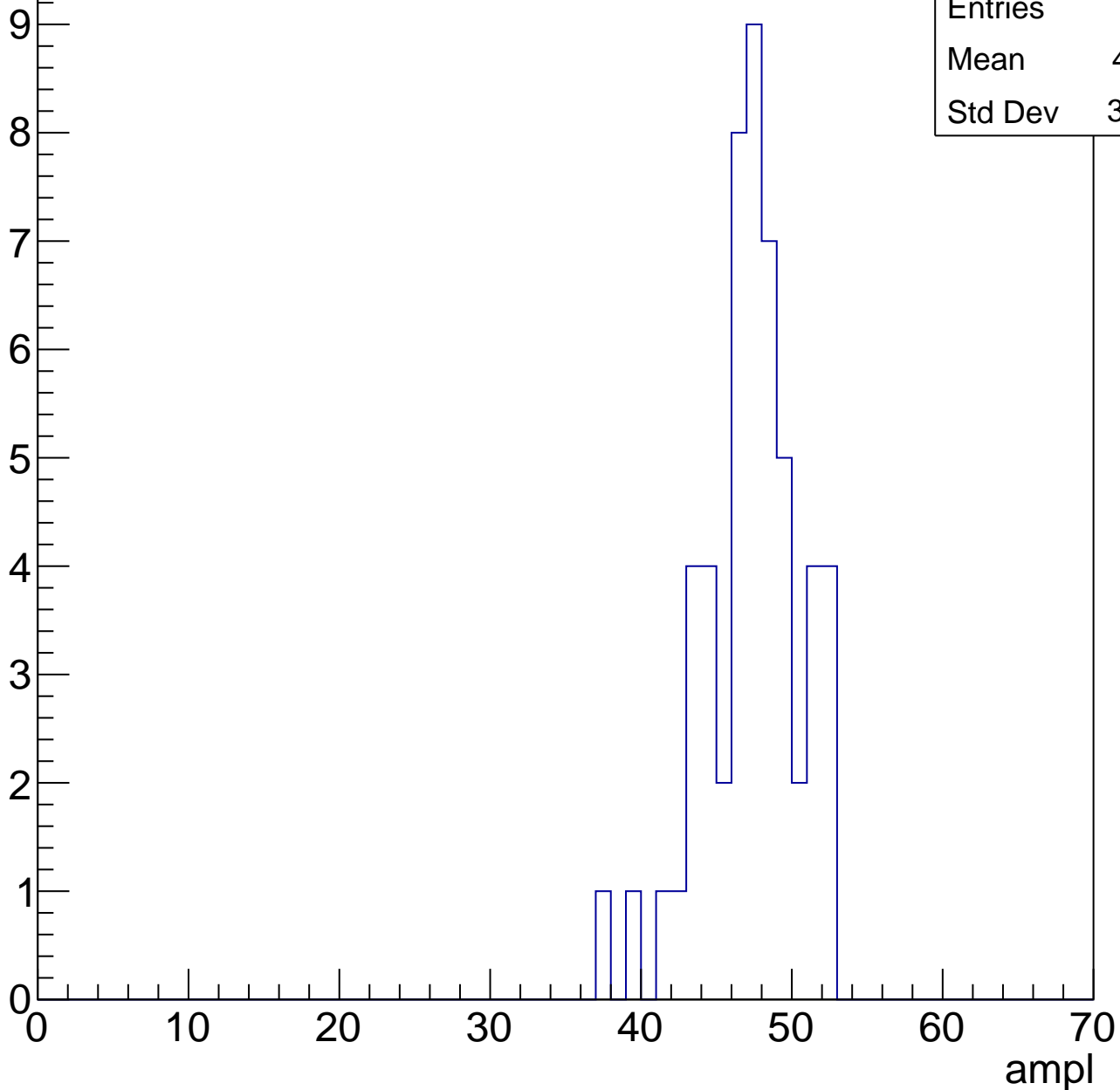


# B1L000S, U25-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

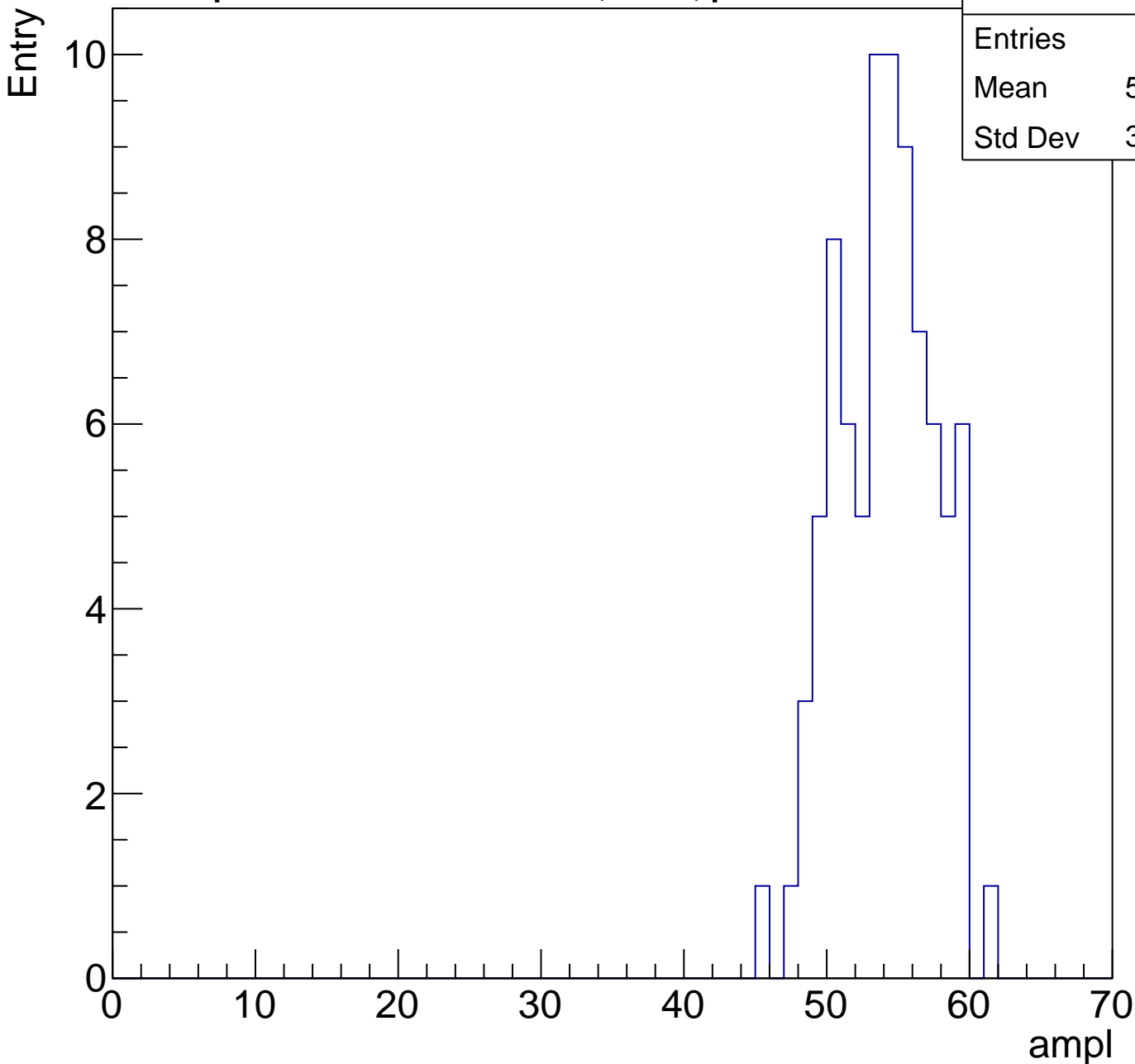
Entries	53
Mean	46.81
Std Dev	3.216



# B1L000S, U25-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

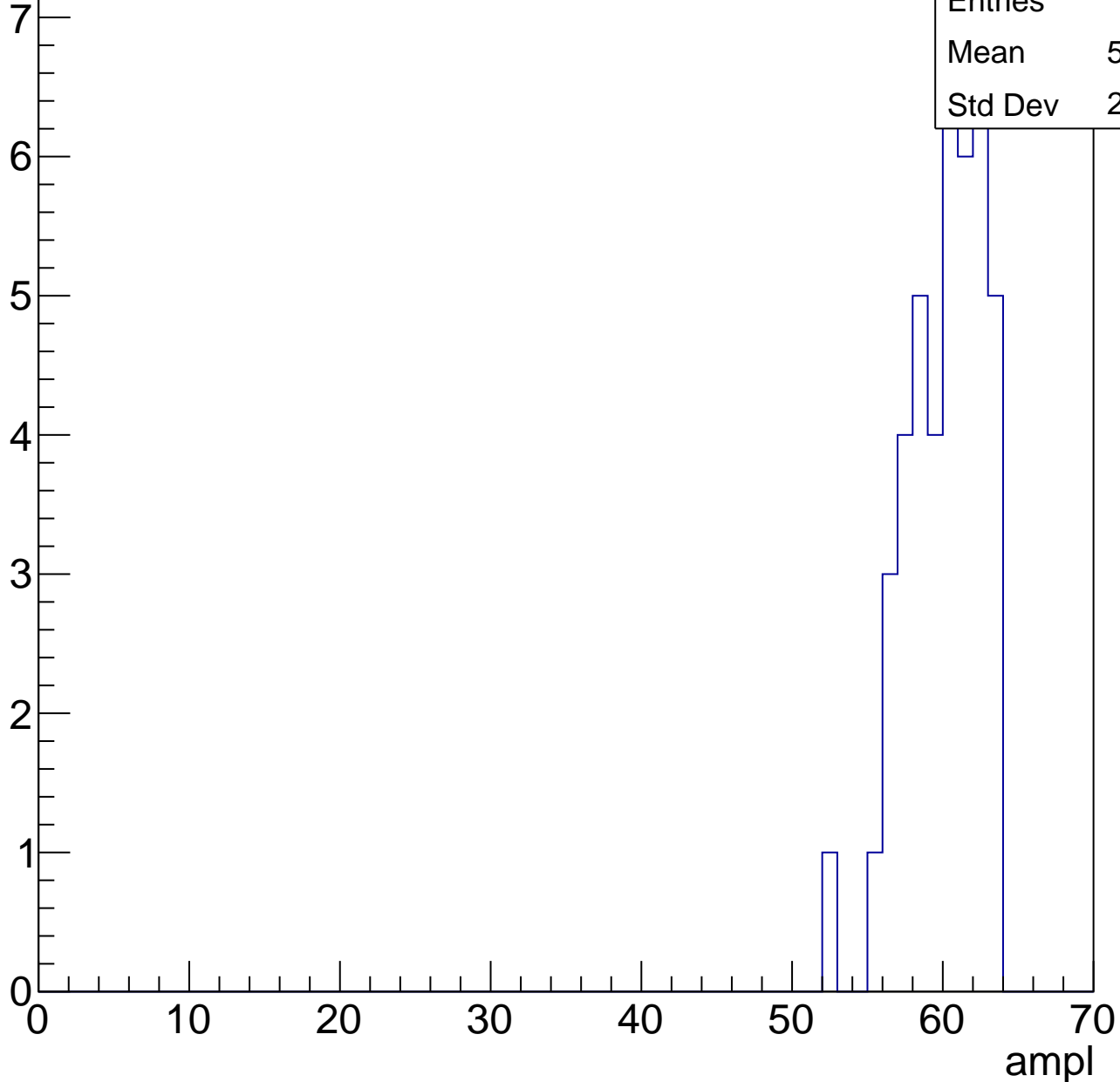
Entries	83
Mean	53.63
Std Dev	3.354



# B1L000S, U25-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

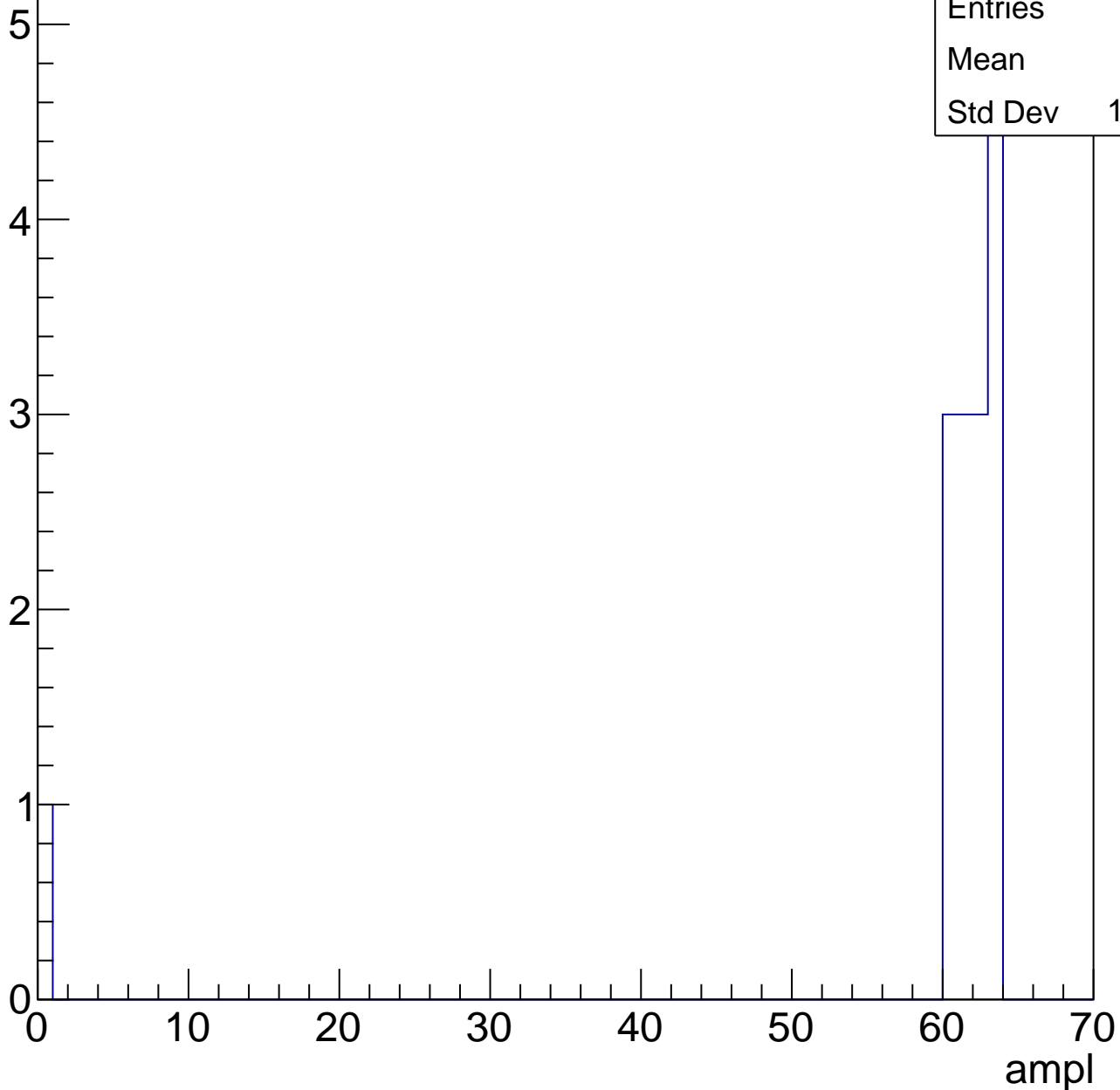


# B1L000S, U25-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.6
Std Dev	15.44





# B1L000S, U25-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L000S, U25-ch81, adc0

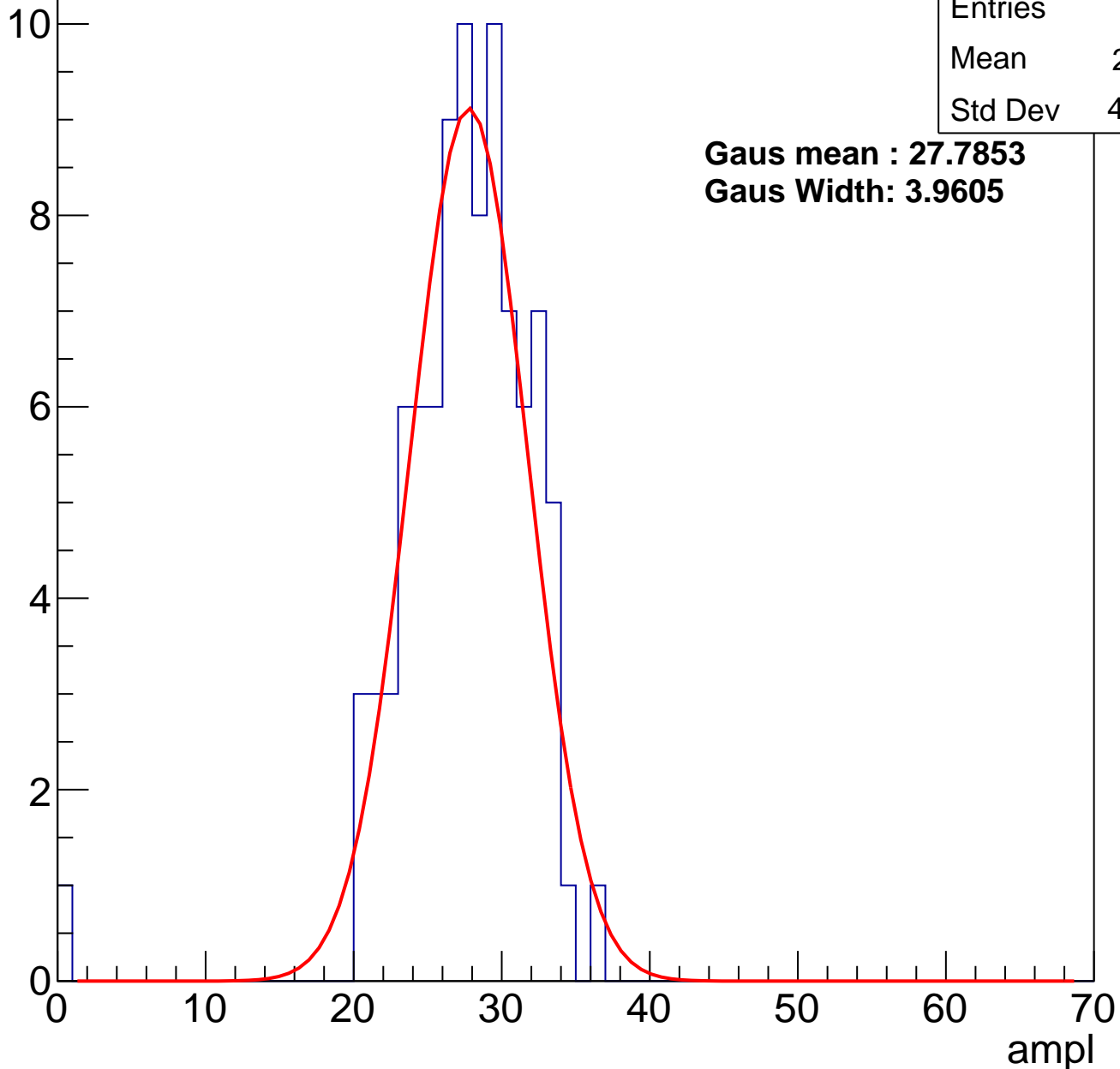
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	92
Mean	27.11
Std Dev	4.584

**Gaus mean : 27.7853**

**Gaus Width: 3.9605**

Entry

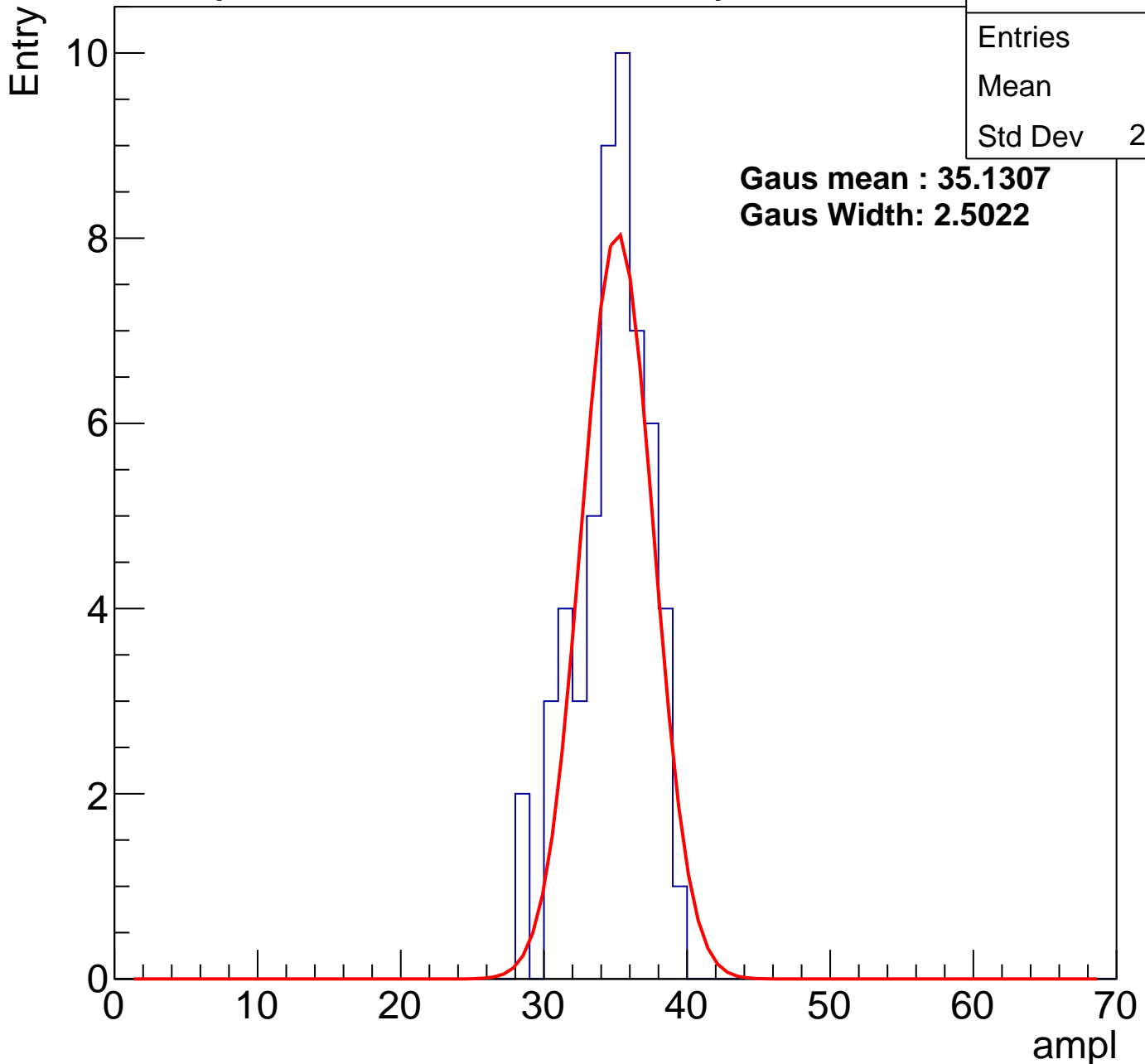


# B1L000S, U25-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	54
Mean	34.3
Std Dev	2.543

**Gaus mean : 35.1307**  
**Gaus Width: 2.5022**



# B1L000S, U25-ch81, adc2

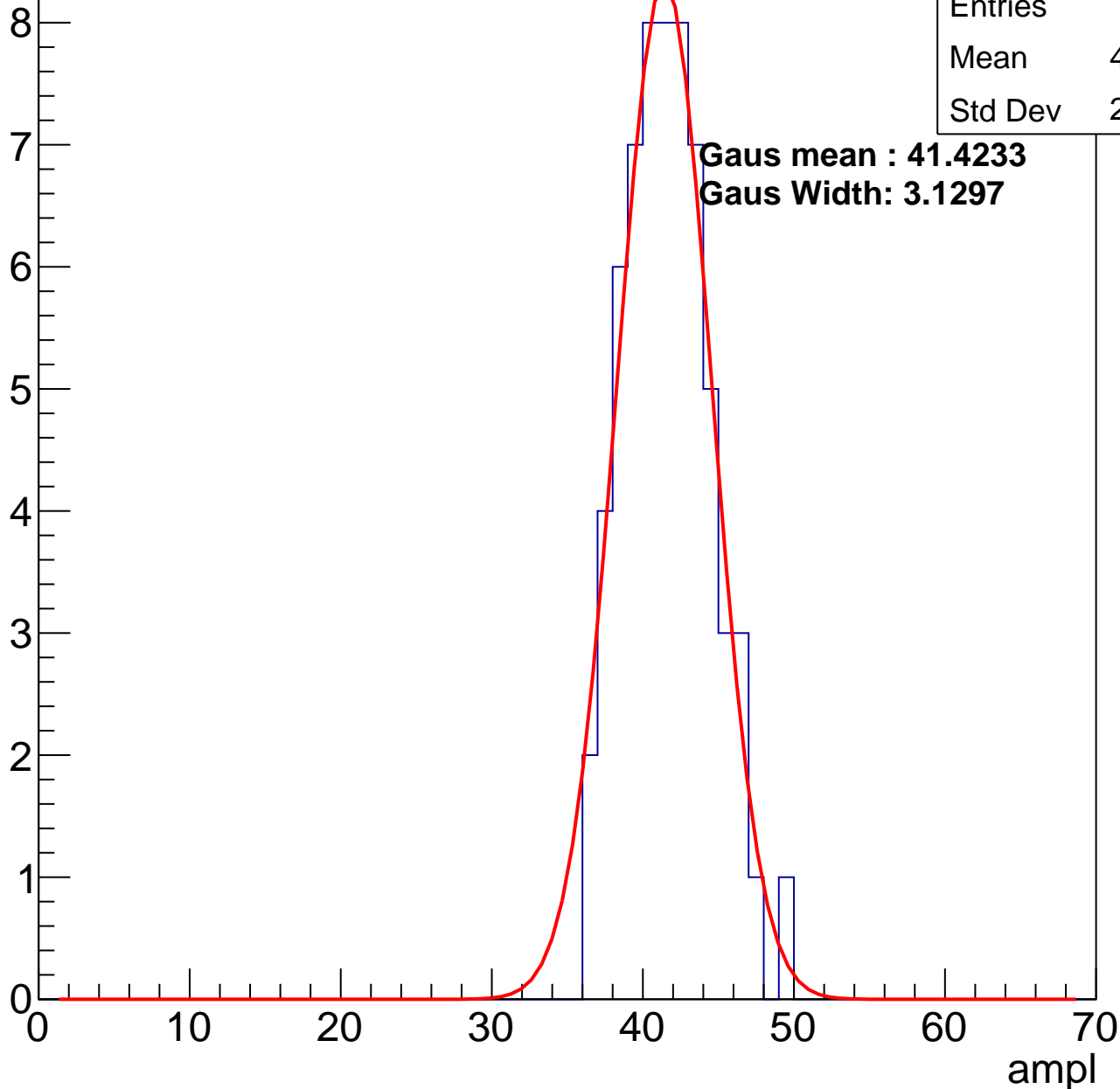
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	41.19
Std Dev	2.833

**Gaus mean : 41.4233**

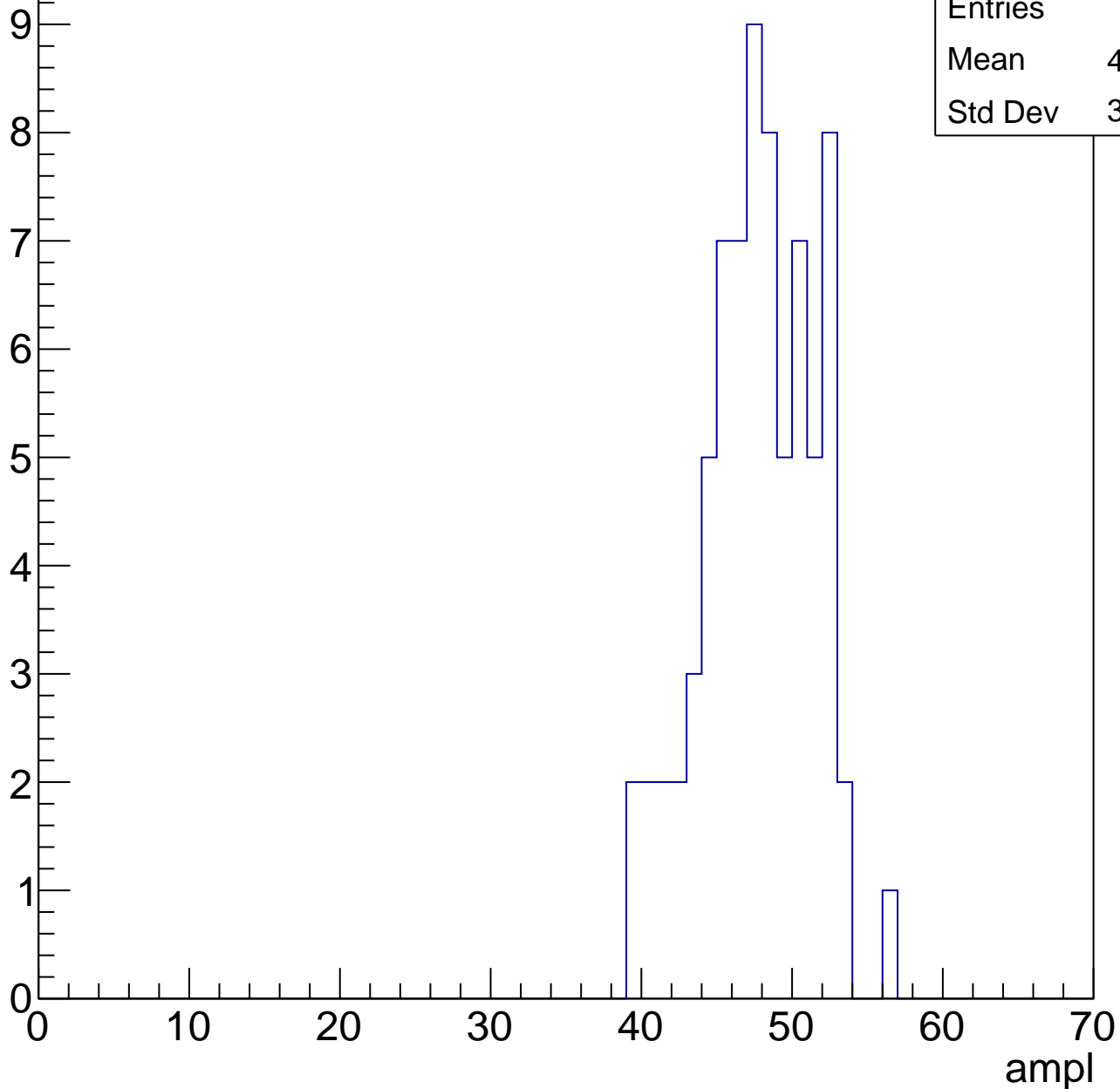
**Gaus Width: 3.1297**



# B1L000S, U25-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



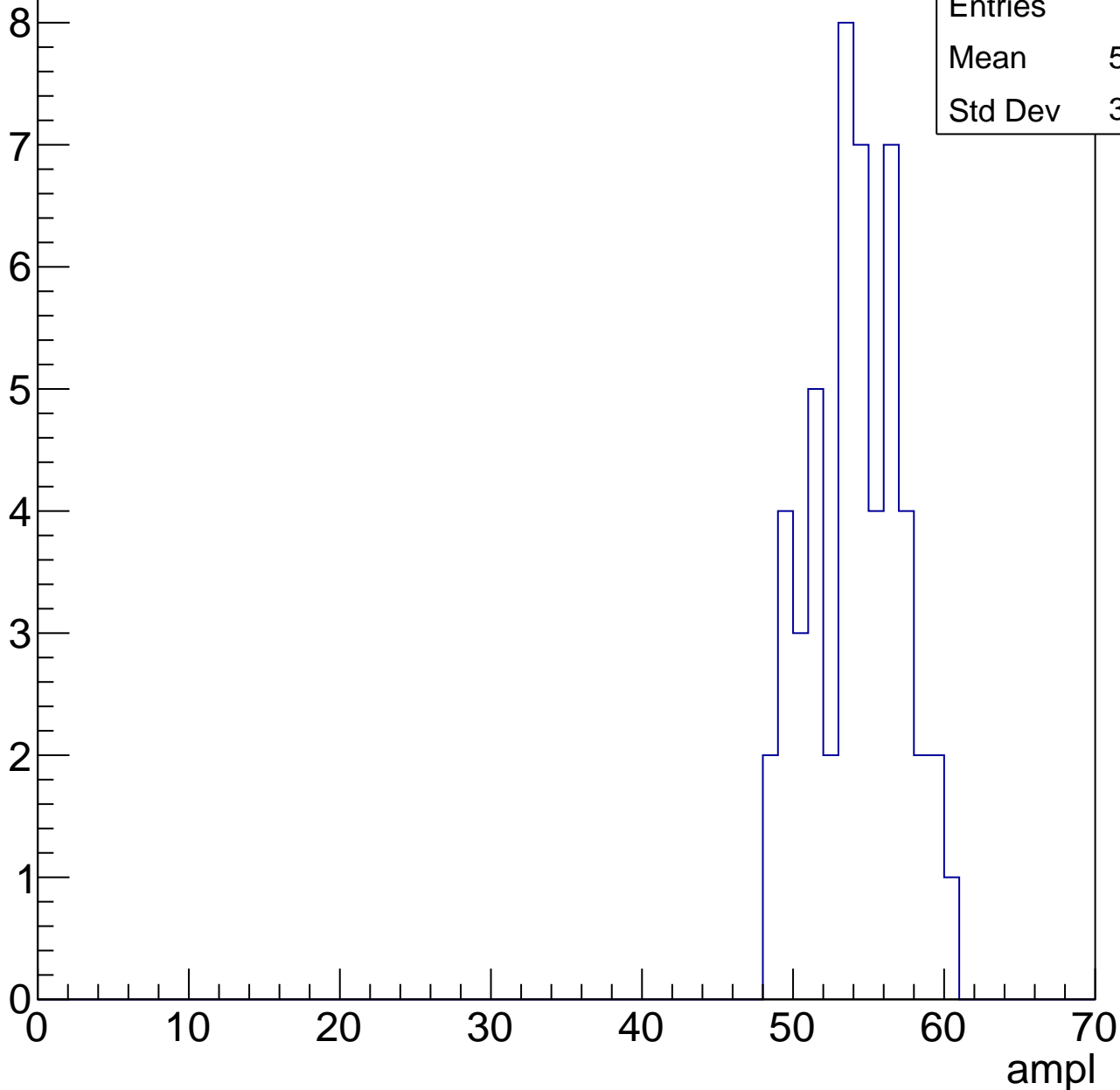
Entries	75
Mean	47.27
Std Dev	3.642

# B1L000S, U25-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

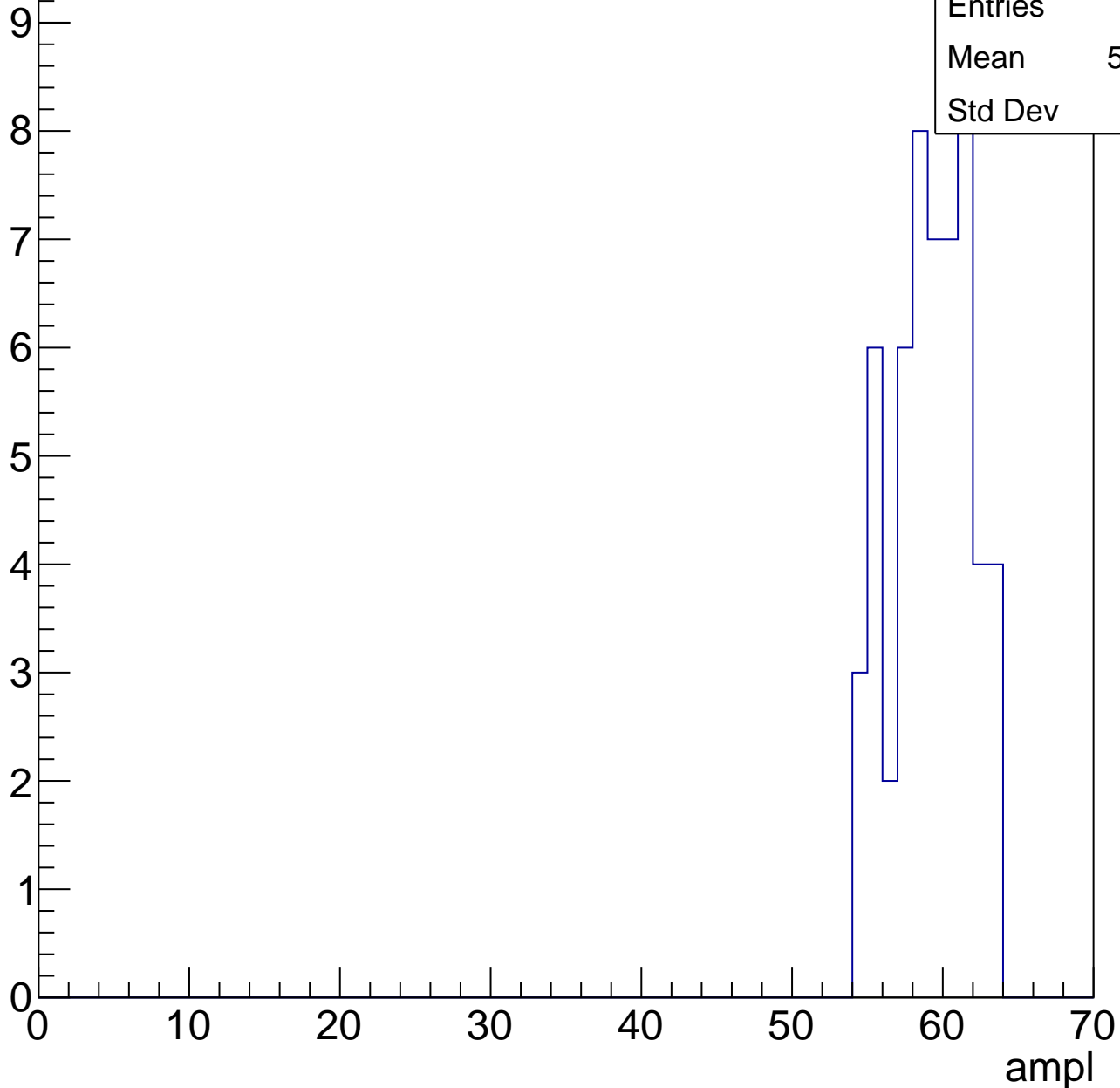
Entries	51
Mean	53.67
Std Dev	3.008



# B1L000S, U25-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



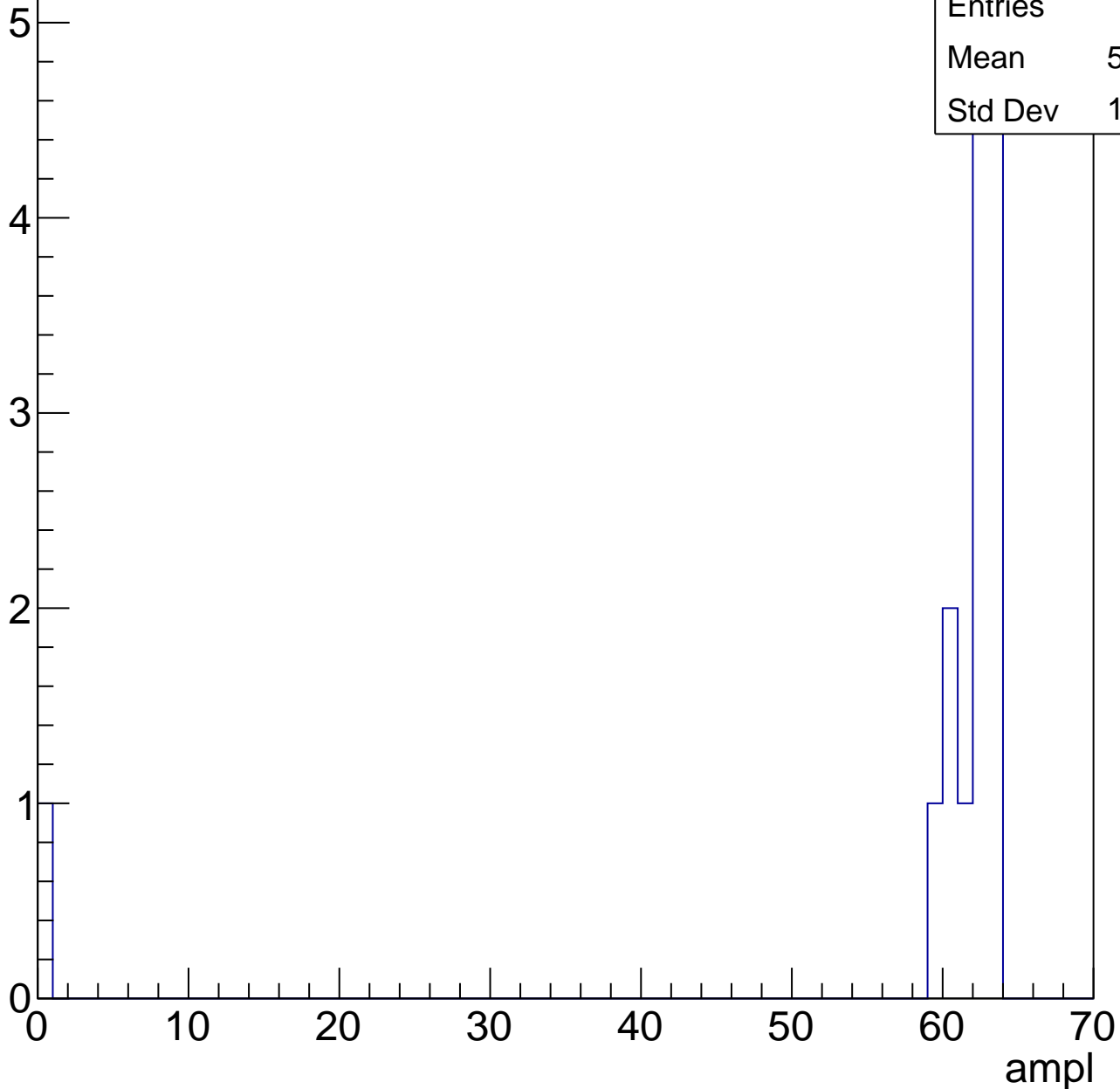
Entries	56
Mean	58.79
Std Dev	2.54

# B1L000S, U25-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.67
Std Dev	15.46

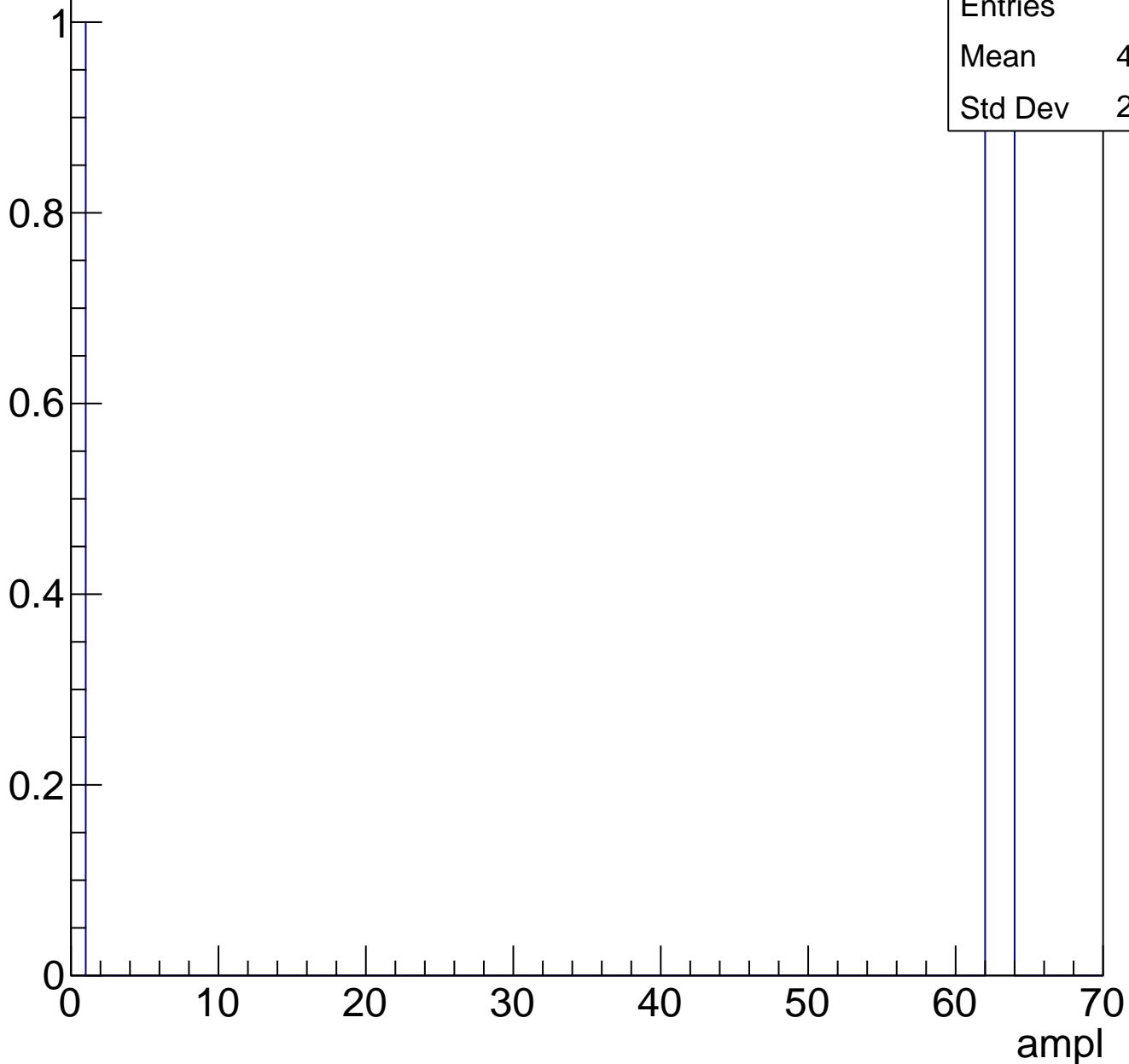




# B1L000S, U25-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch82, adc0

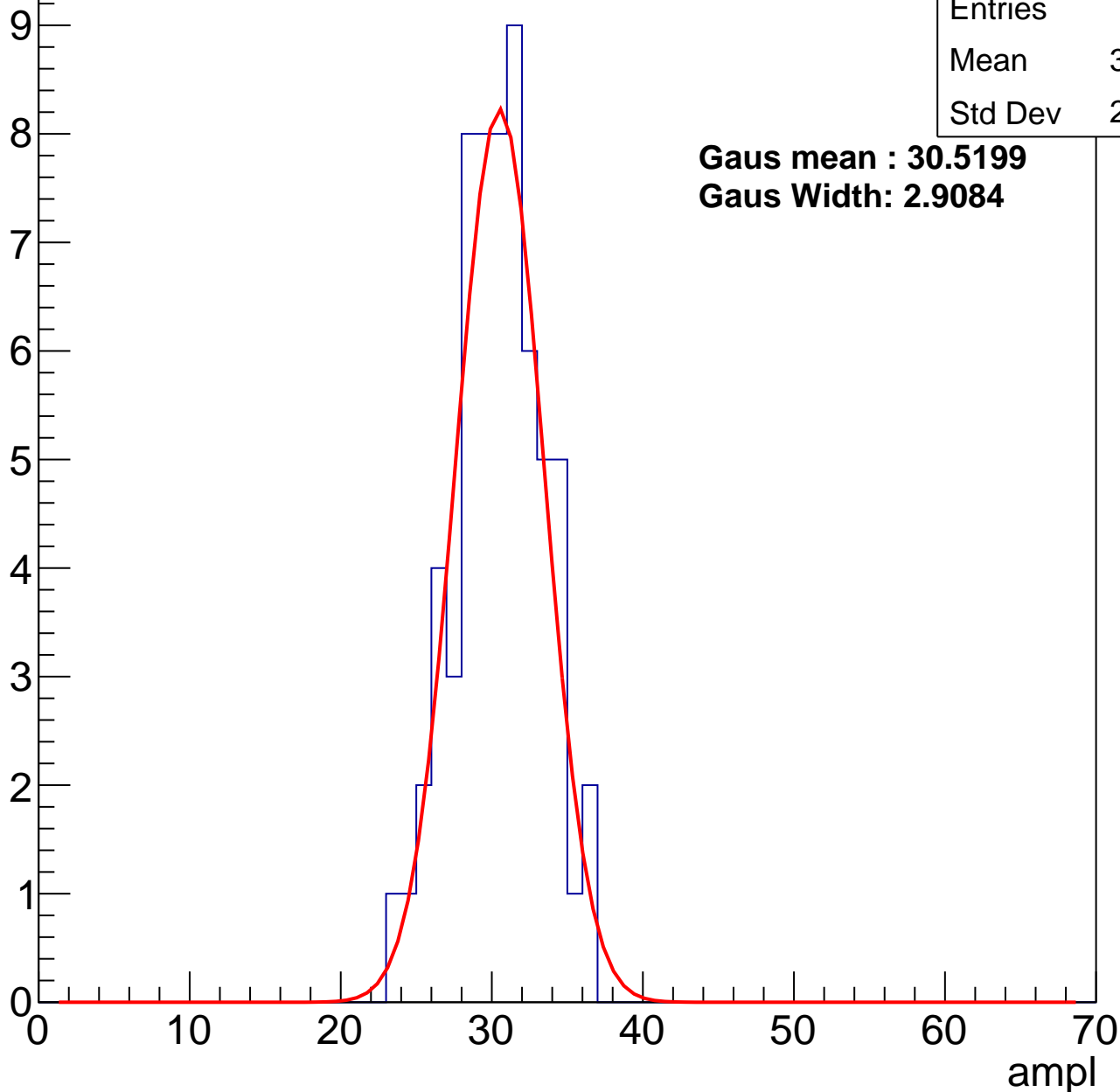
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	30.02
Std Dev	2.876

**Gaus mean : 30.5199**

**Gaus Width: 2.9084**



# B1L000S, U25-ch82, adc1

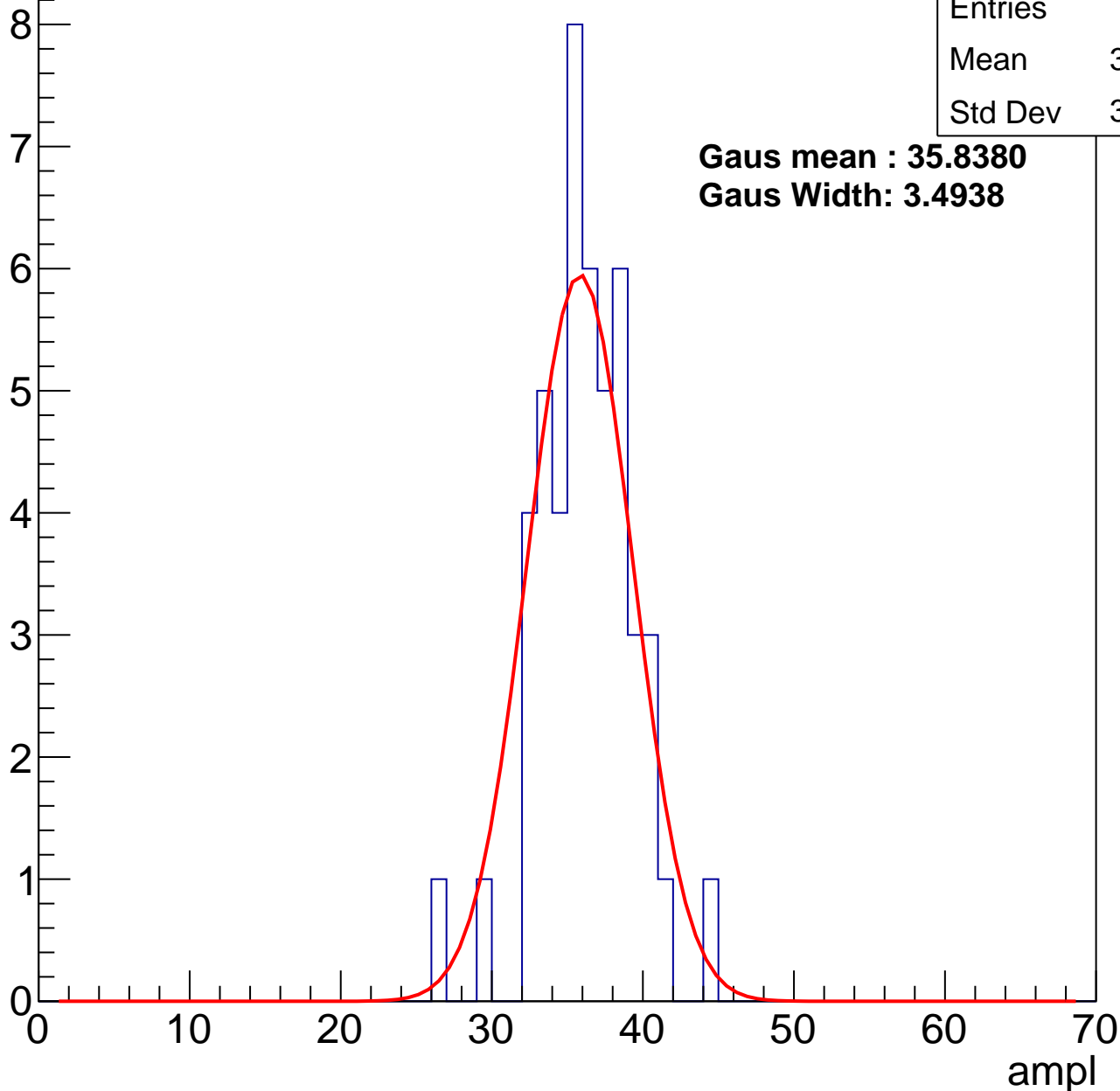
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	35.73
Std Dev	3.134

**Gaus mean : 35.8380**

**Gaus Width: 3.4938**



# B1L000S, U25-ch82, adc2

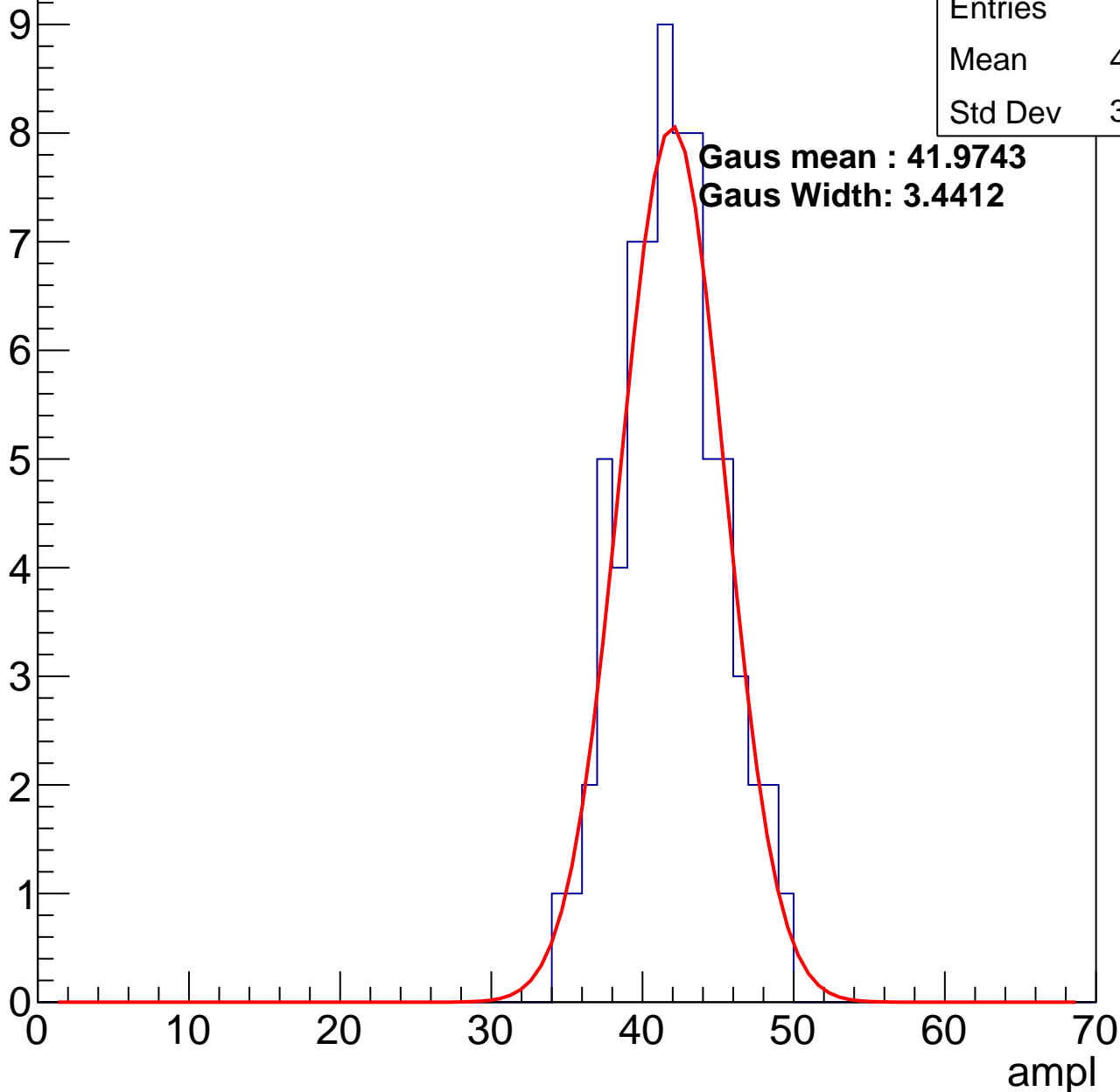
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	41.46
Std Dev	3.263

**Gaus mean : 41.9743**

**Gaus Width: 3.4412**

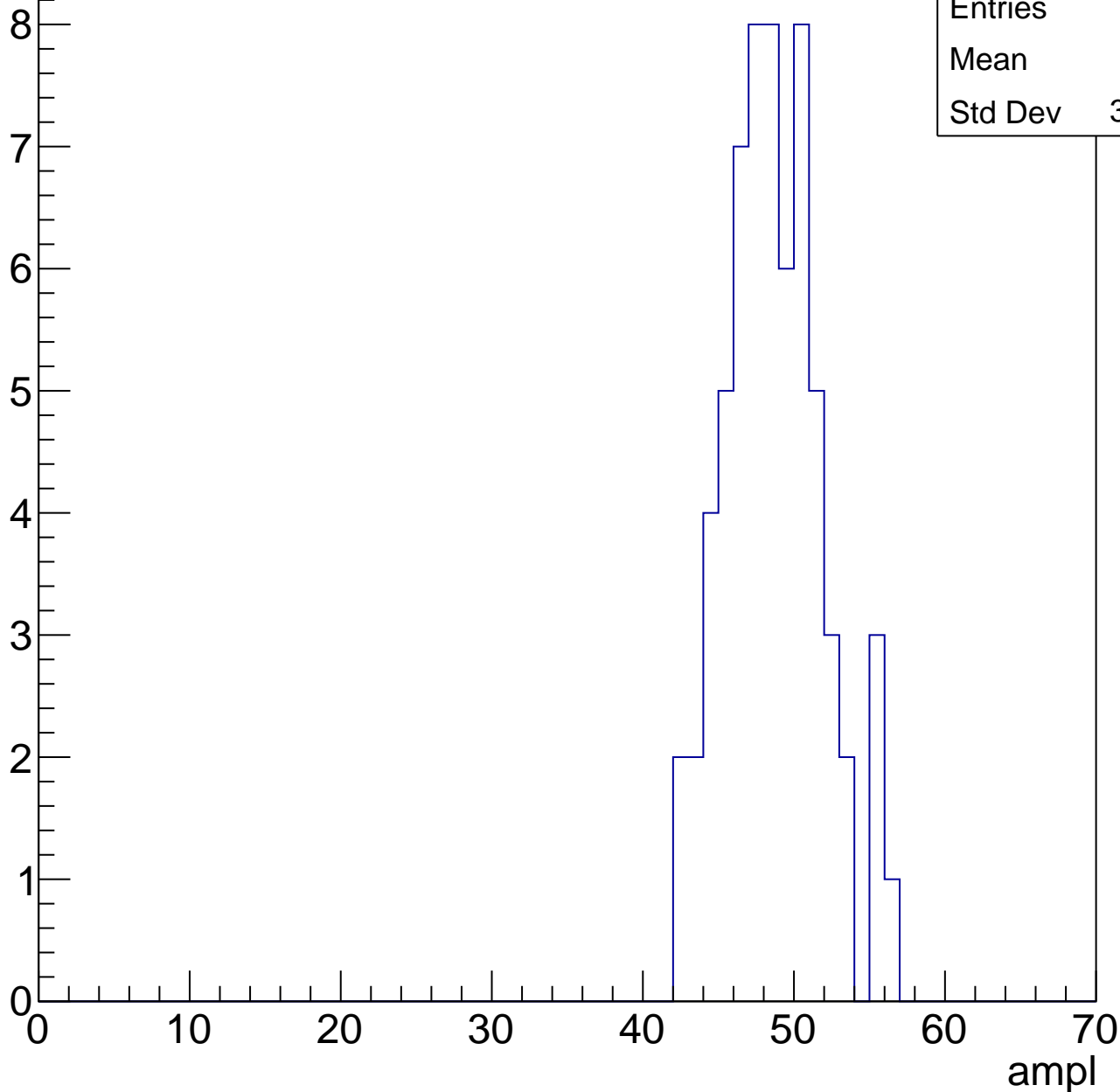


# B1L000S, U25-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	48.2
Std Dev	3.202

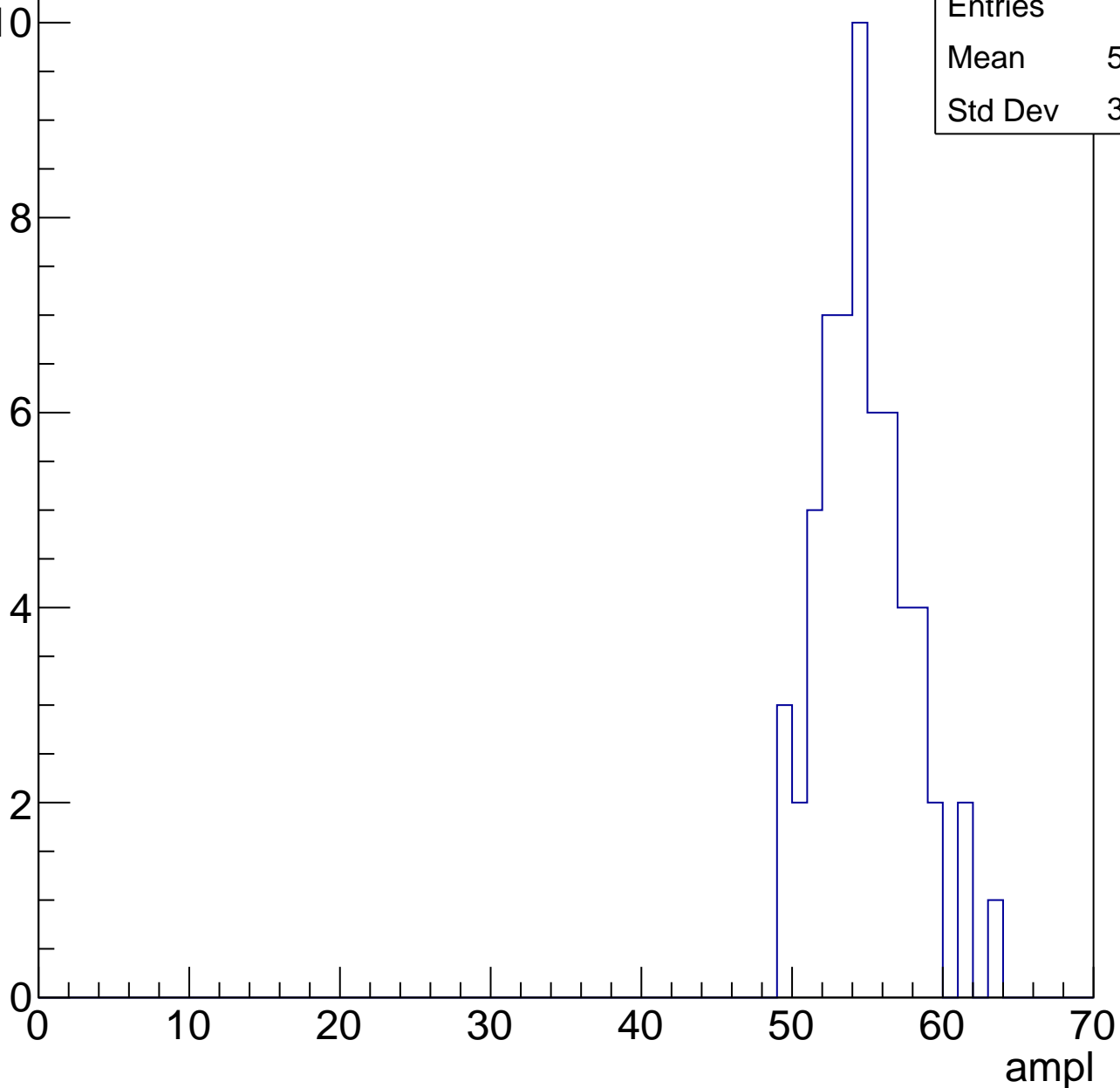


# B1L000S, U25-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	54.34
Std Dev	3.023

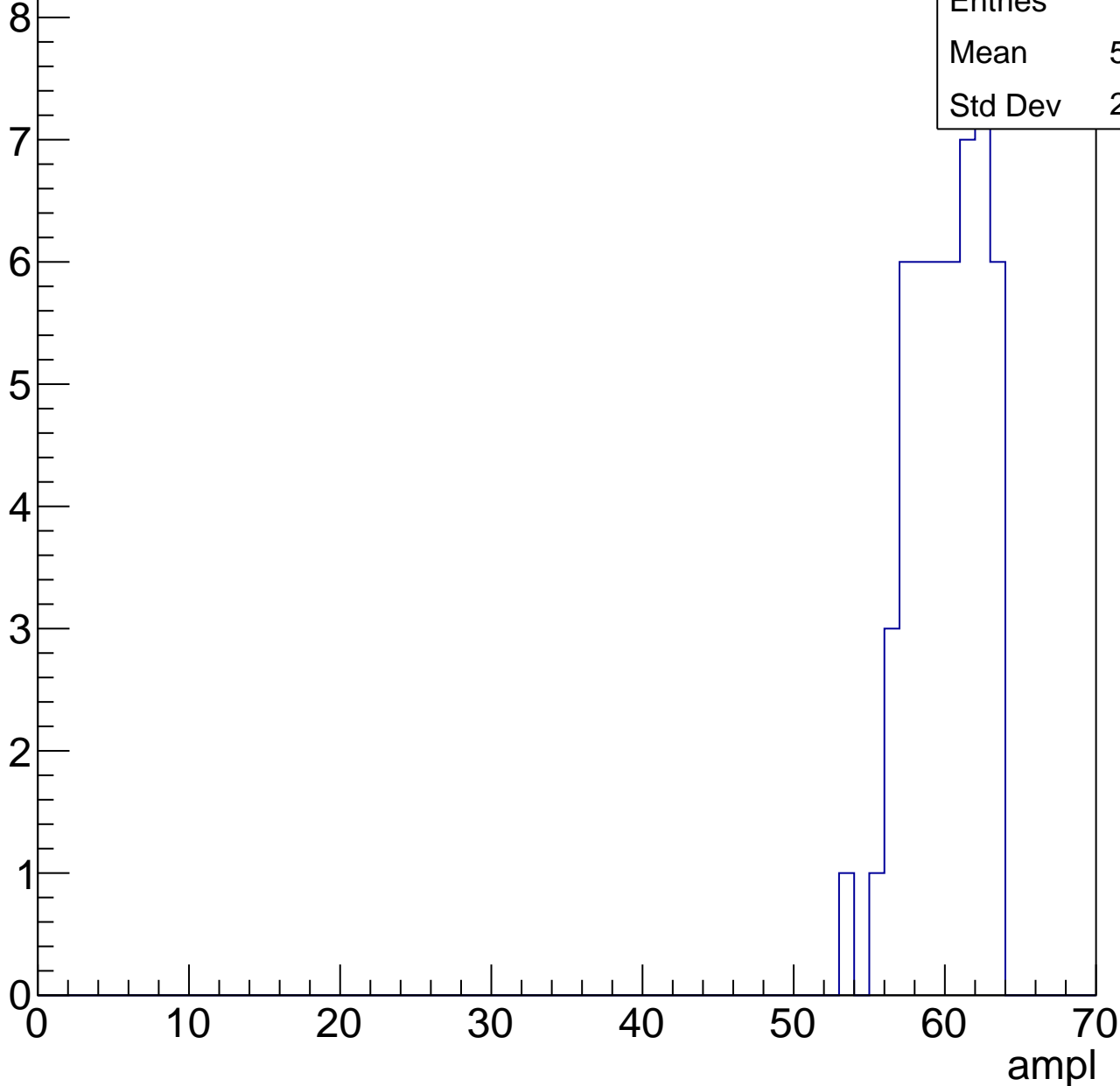


# B1L000S, U25-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

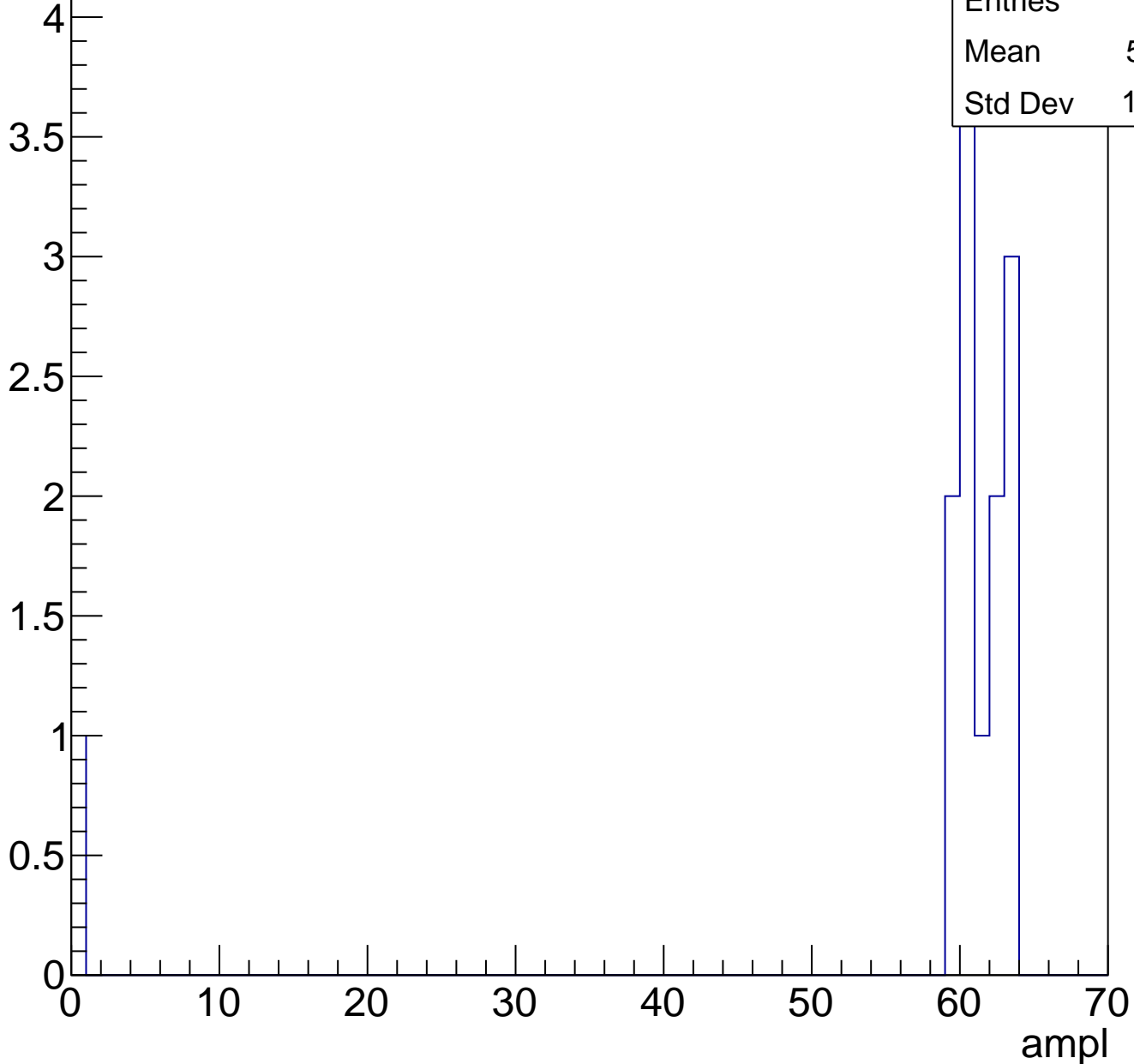
Entries	50
Mean	59.62
Std Dev	2.416



# B1L000S, U25-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch83, adc0

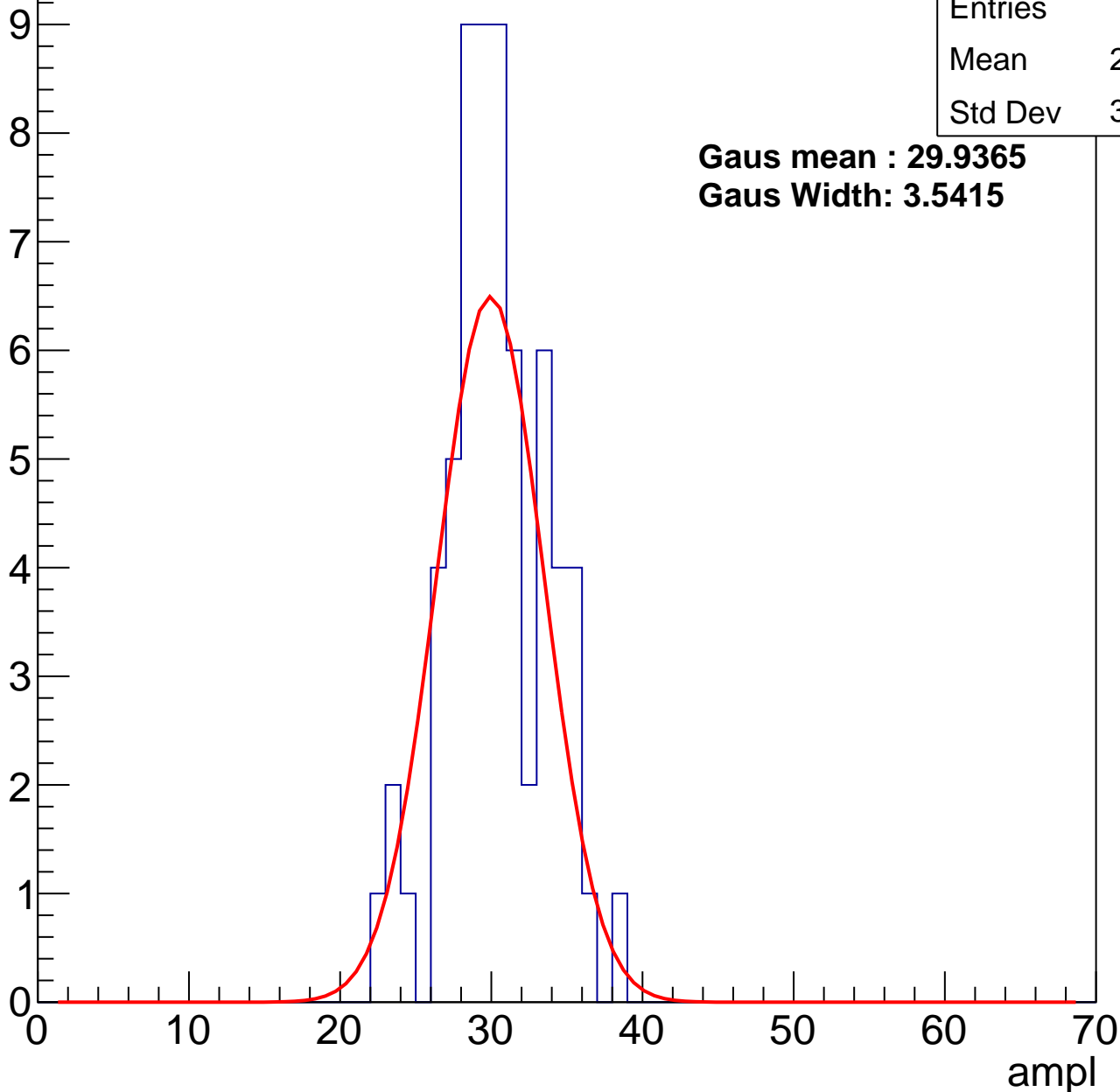
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	29.88
Std Dev	3.267

**Gaus mean : 29.9365**

**Gaus Width: 3.5415**



# B1L000S, U25-ch83, adc1

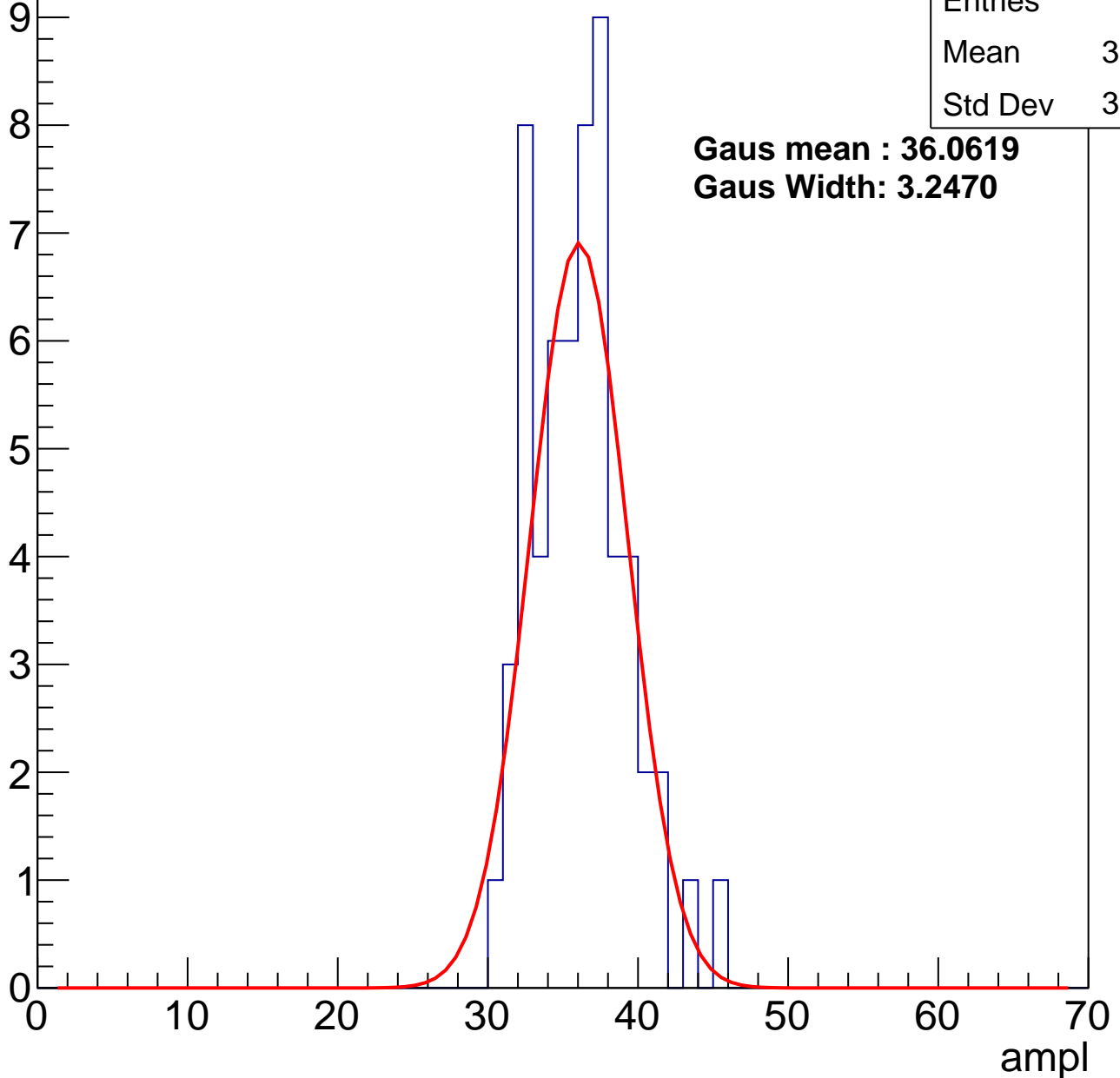
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	35.66
Std Dev	3.112

**Gaus mean : 36.0619**

**Gaus Width: 3.2470**



# B1L000S, U25-ch83, adc2

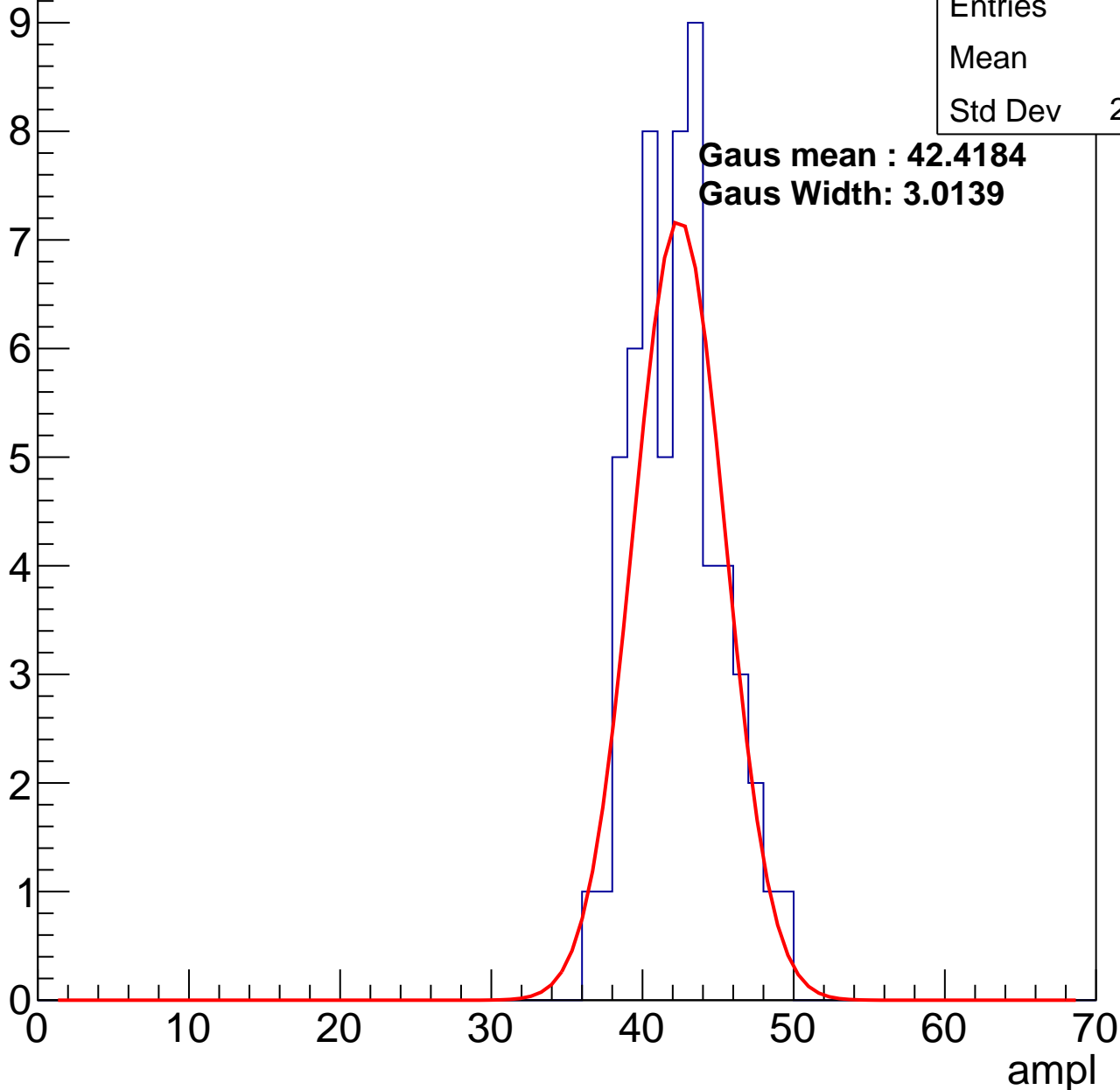
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	41.9
Std Dev	2.863

**Gaus mean : 42.4184**

**Gaus Width: 3.0139**

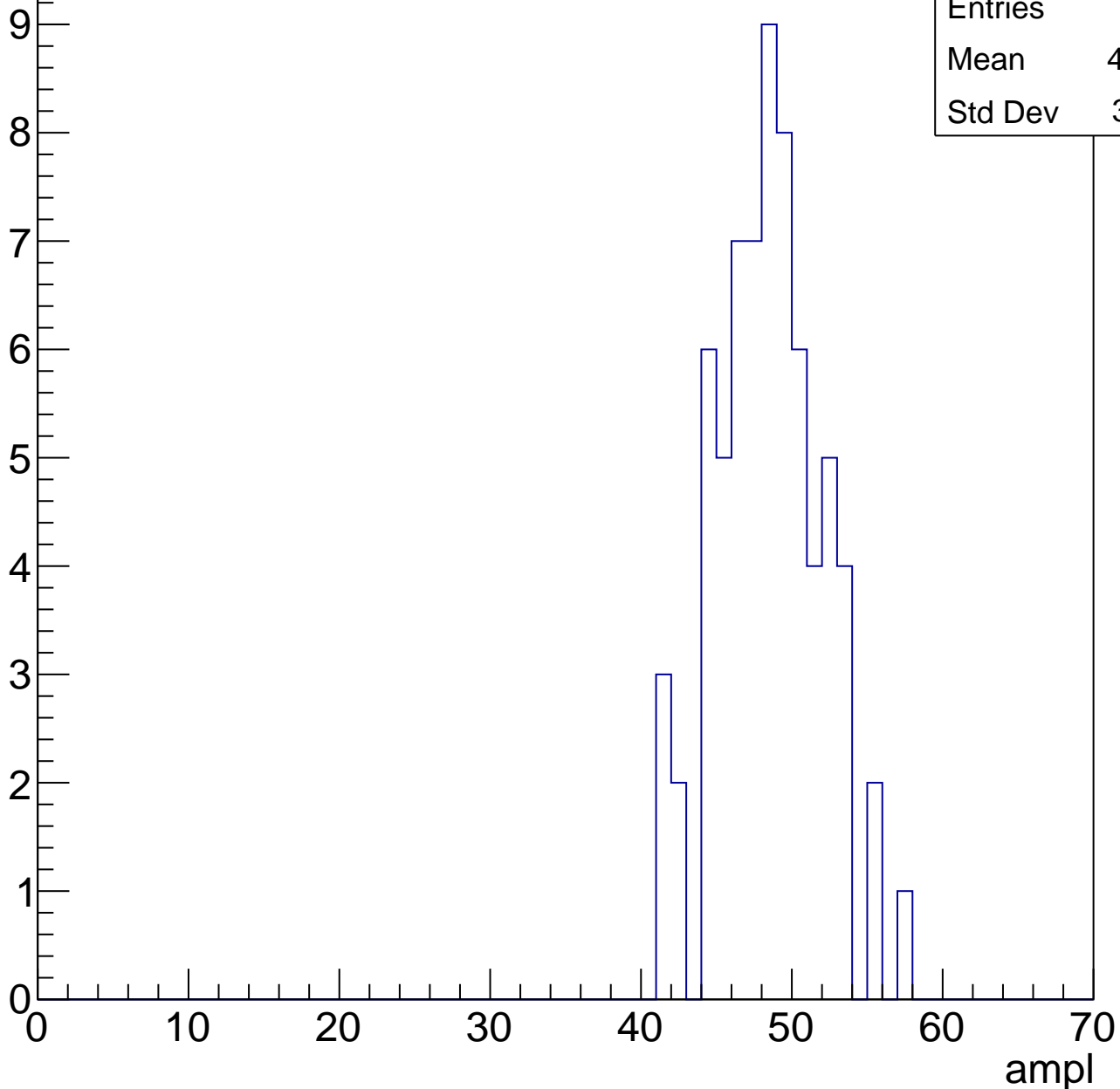


# B1L000S, U25-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	48.03
Std Dev	3.451

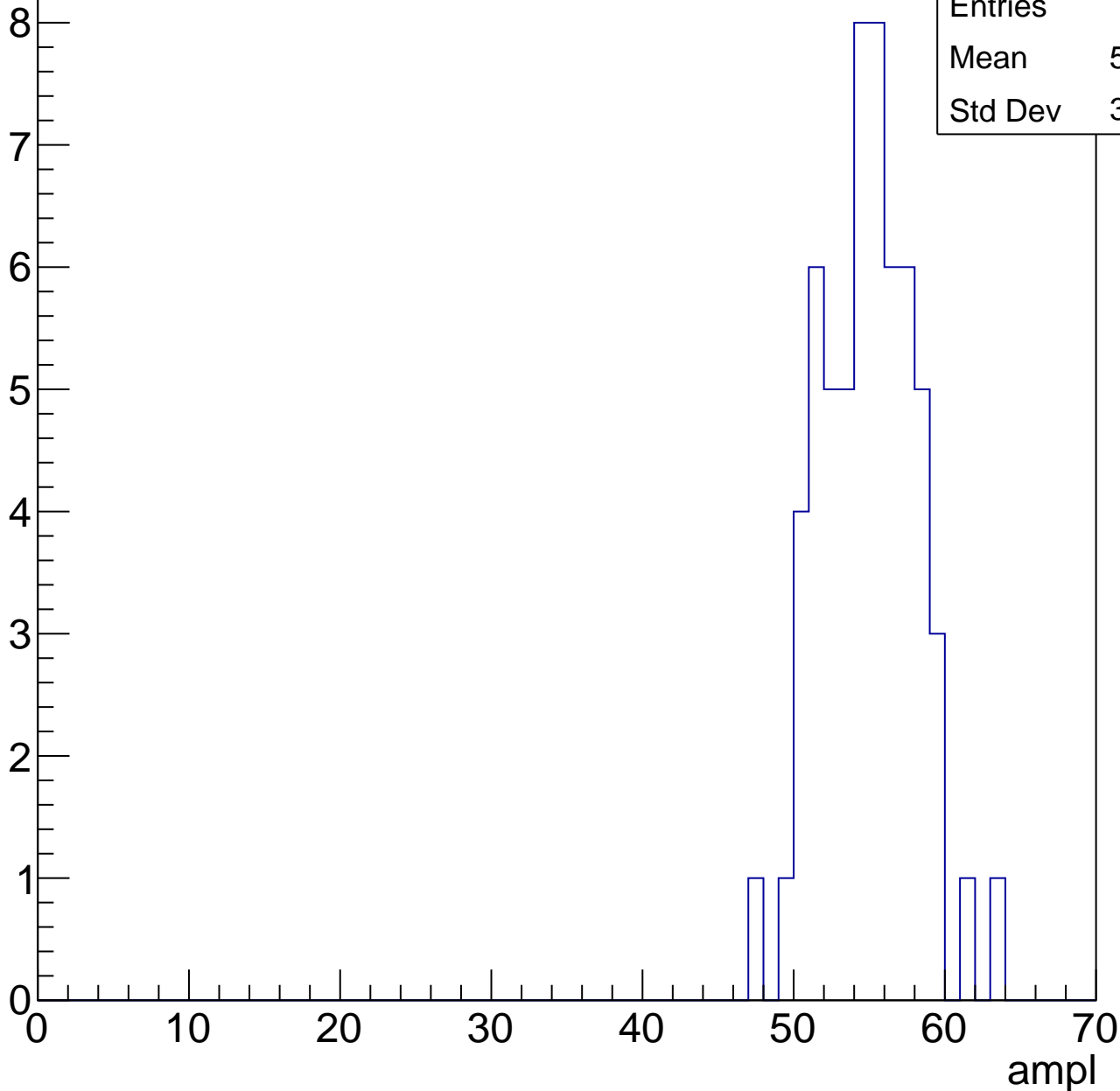


# B1L000S, U25-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.47
Std Dev	3.096

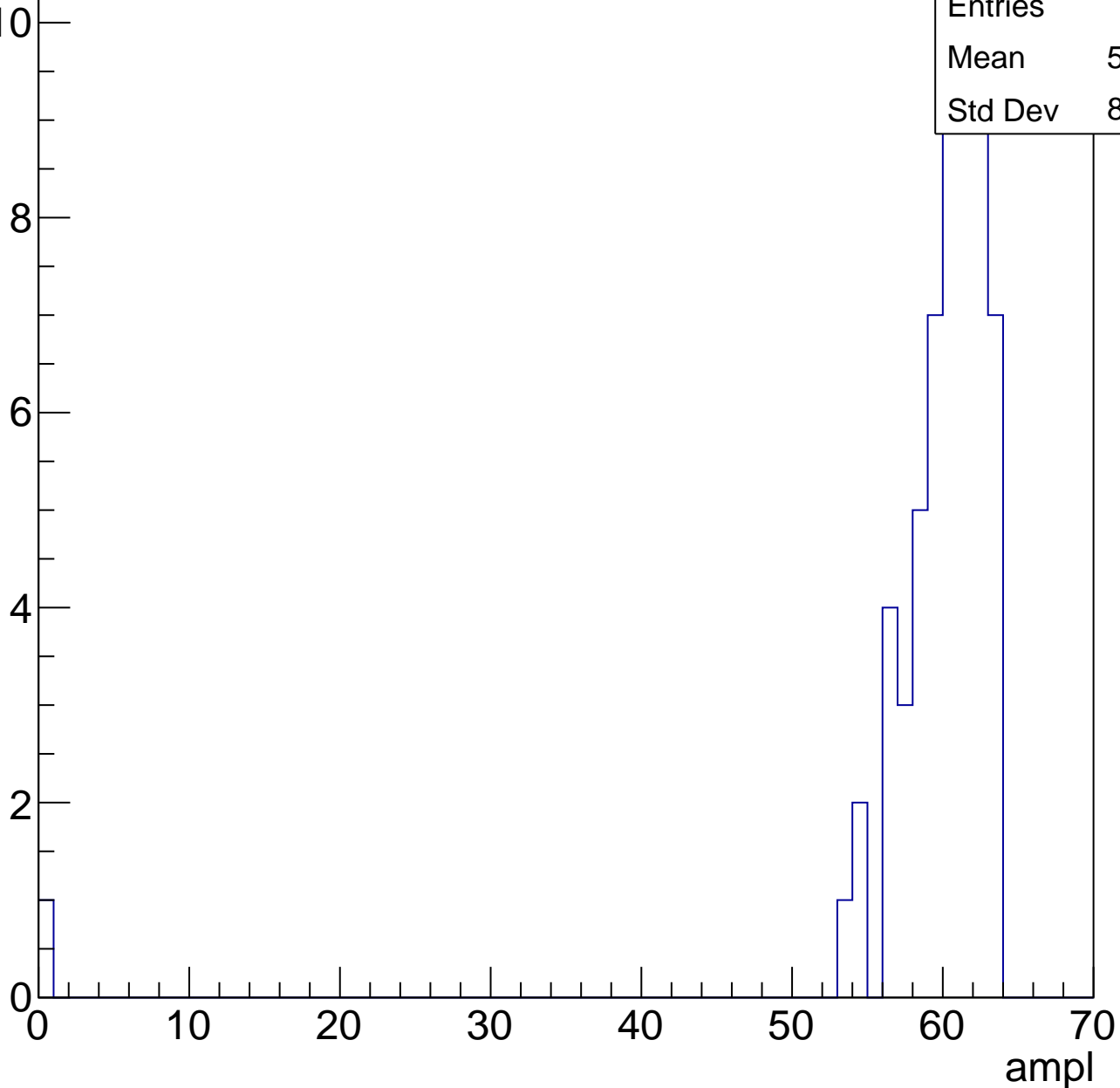


# B1L000S, U25-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	58.74
Std Dev	8.153



# B1L000S, U25-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch84, adc0

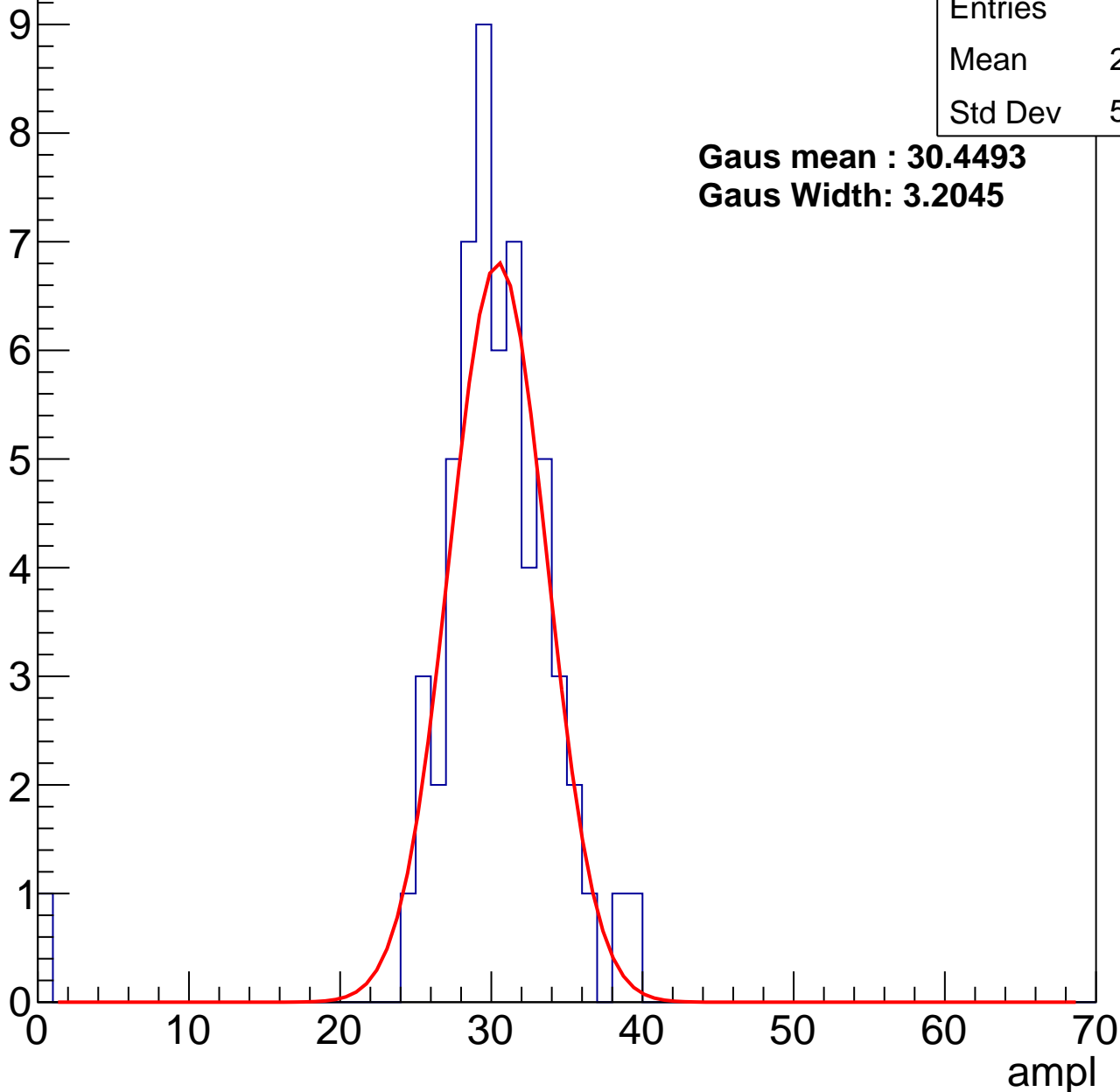
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	29.62
Std Dev	5.023

**Gaus mean : 30.4493**

**Gaus Width: 3.2045**



# B1L000S, U25-ch84, adc1

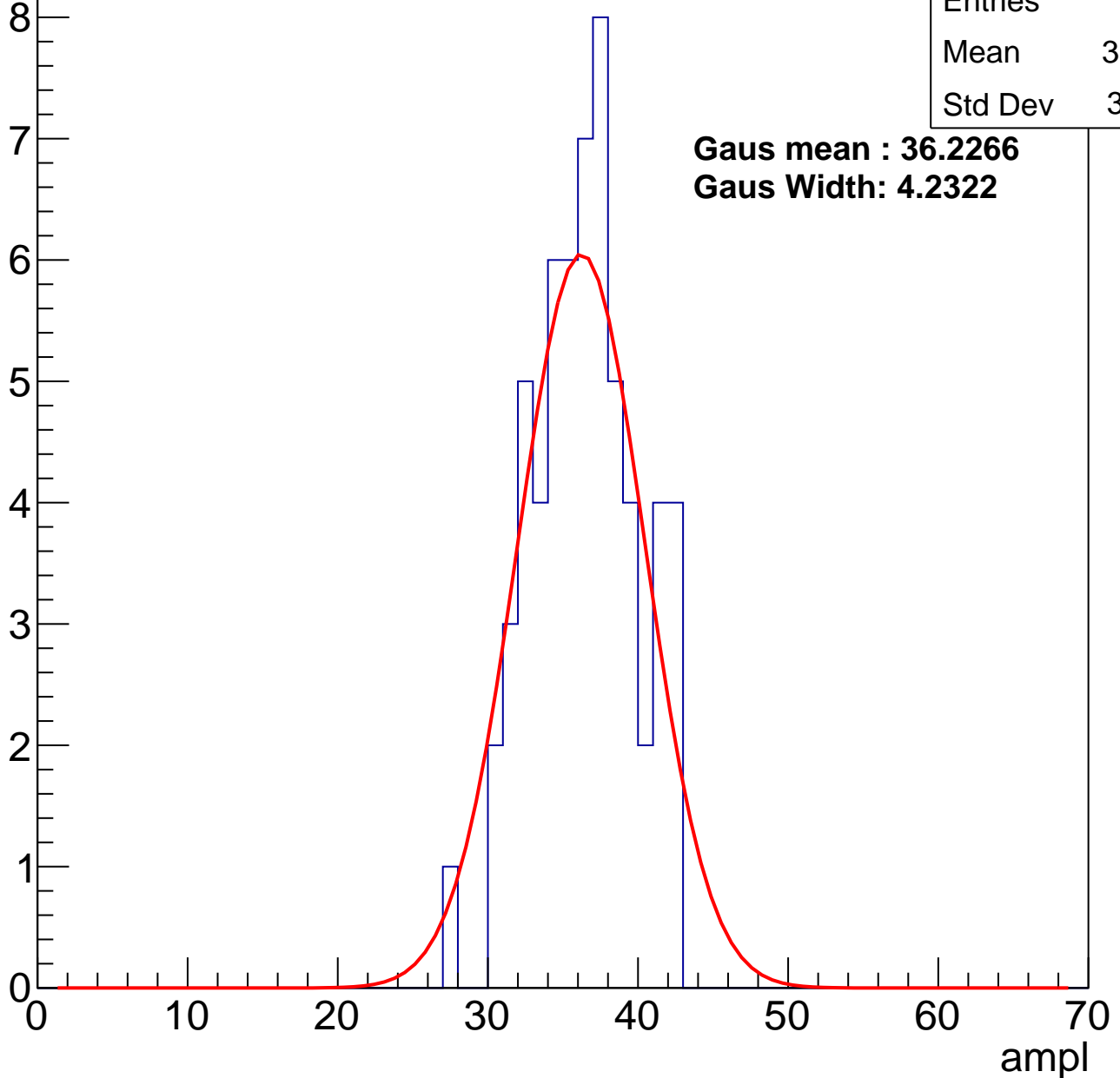
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.93
Std Dev	3.421

**Gaus mean : 36.2266**

**Gaus Width: 4.2322**



# B1L000S, U25-ch84, adc2

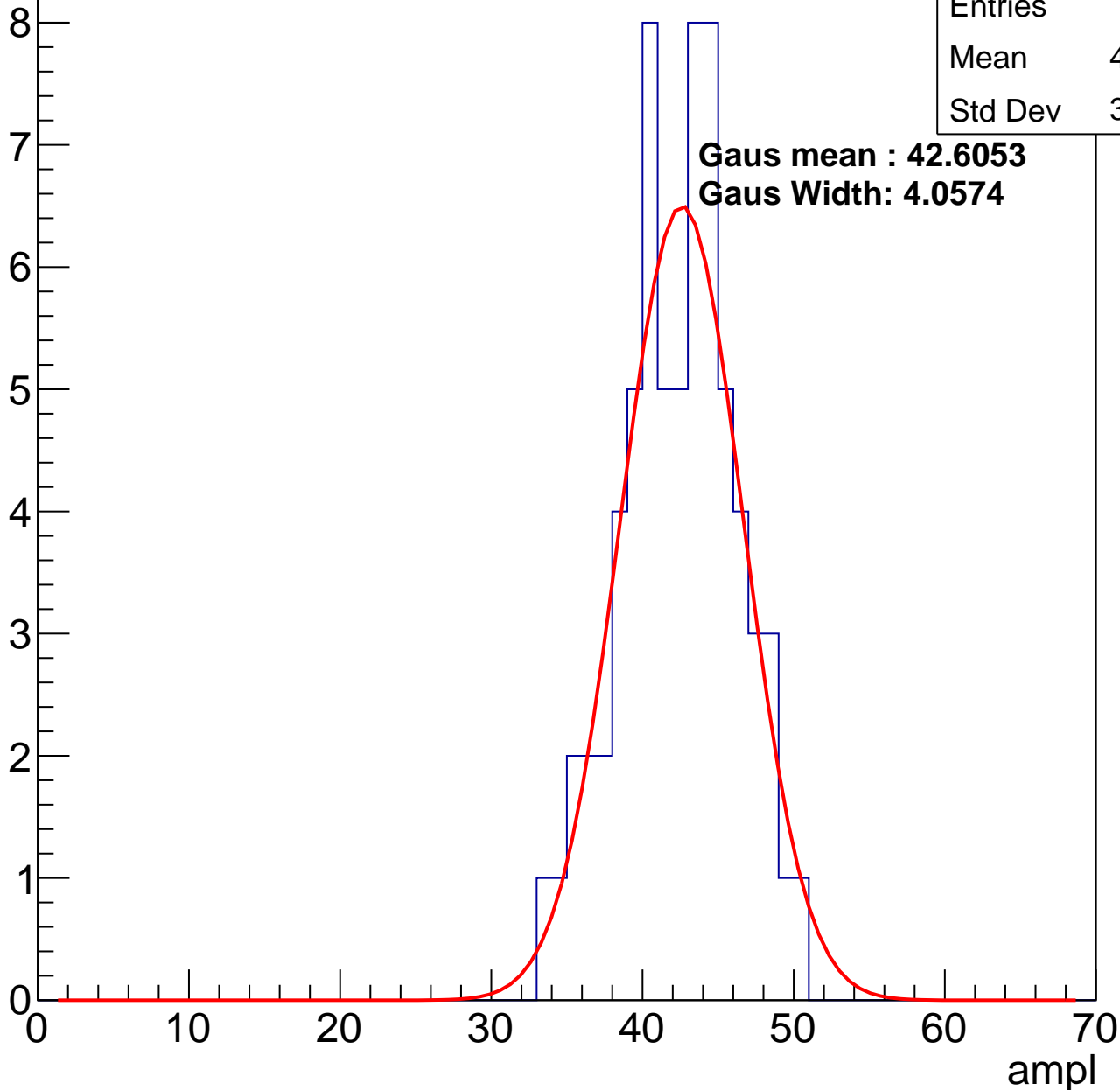
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	41.97
Std Dev	3.749

**Gaus mean : 42.6053**

**Gaus Width: 4.0574**

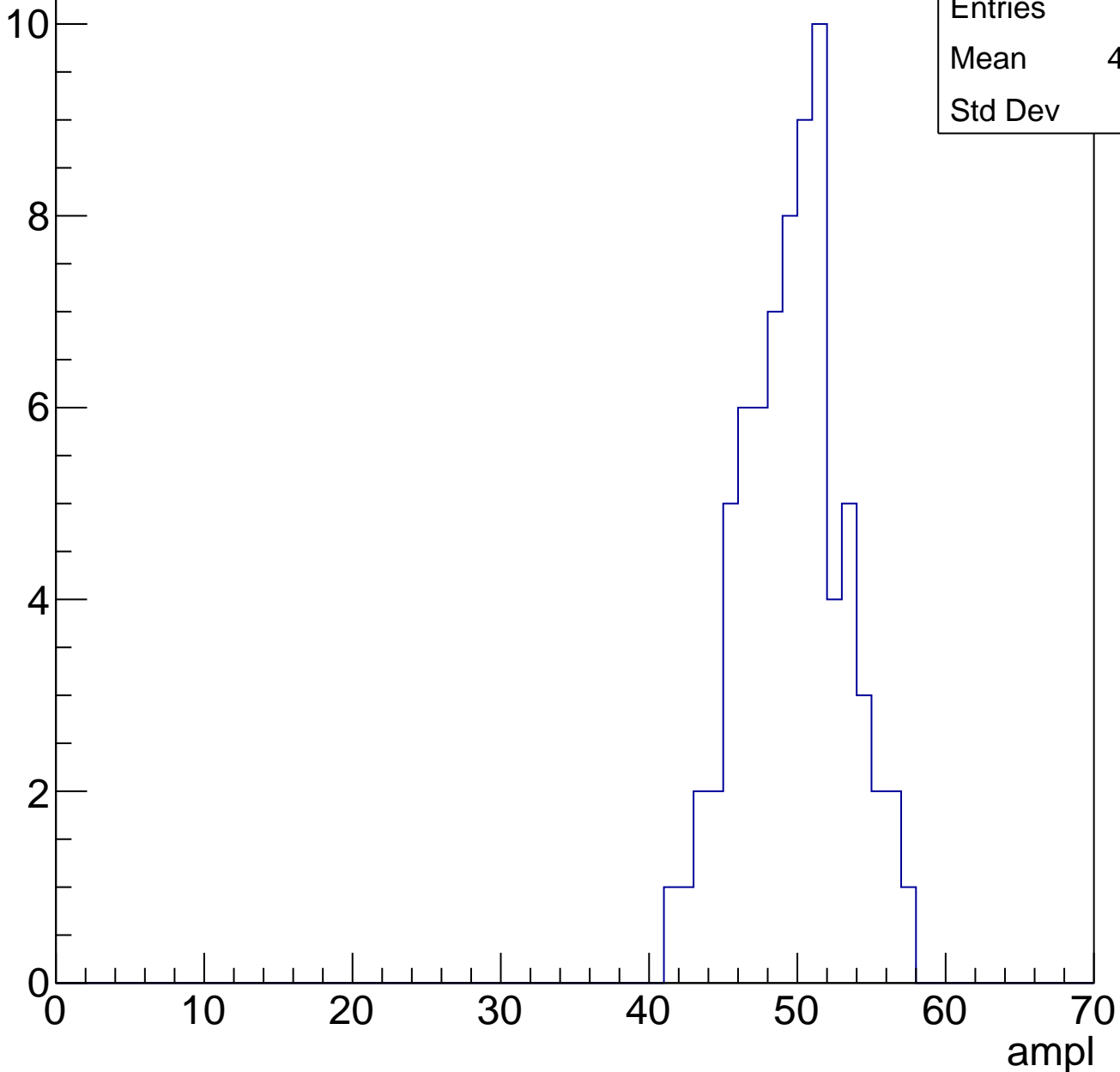


# B1L000S, U25-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	49.22
Std Dev	3.43

Entry

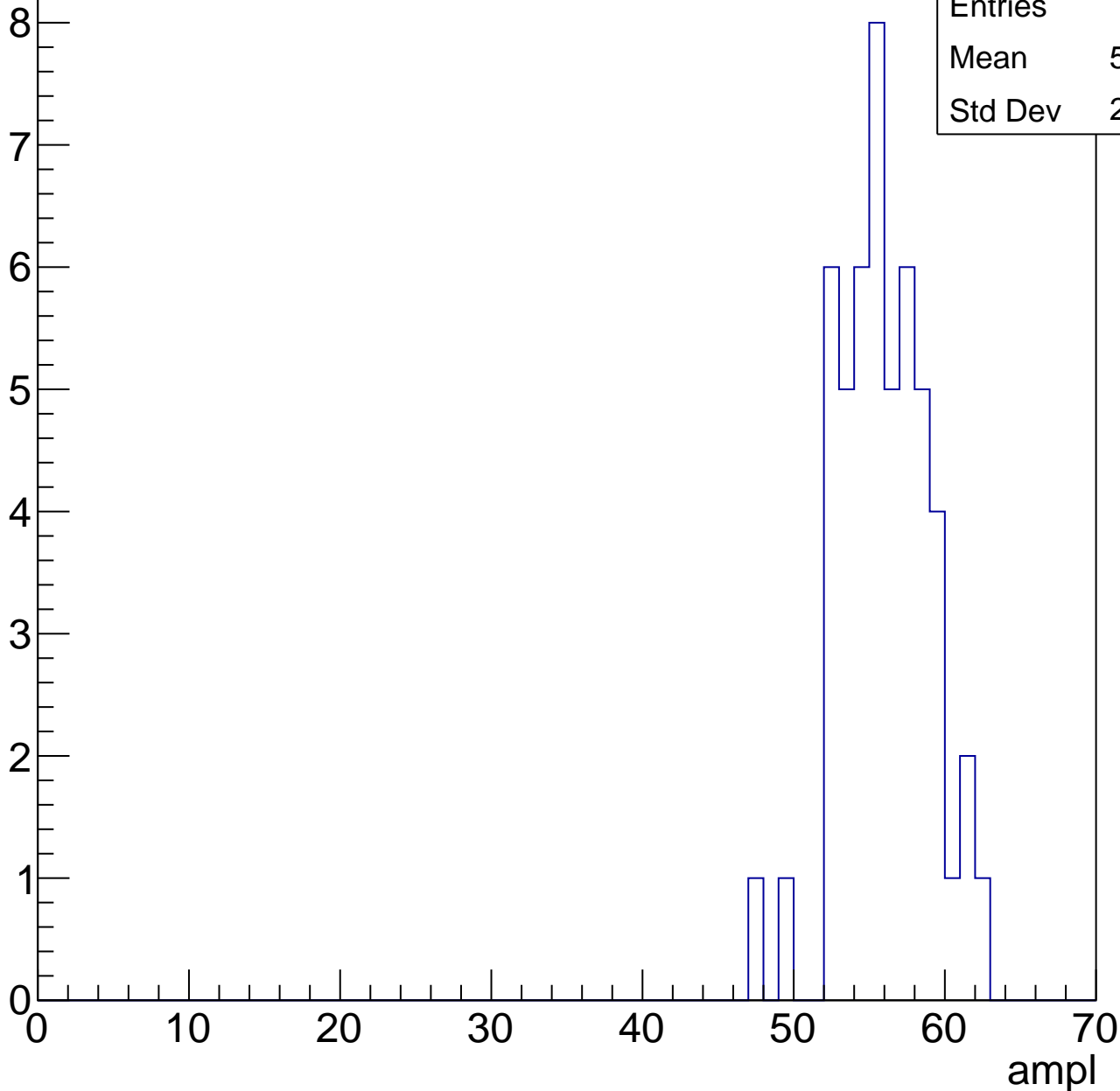


# B1L000S, U25-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	55.47
Std Dev	2.979

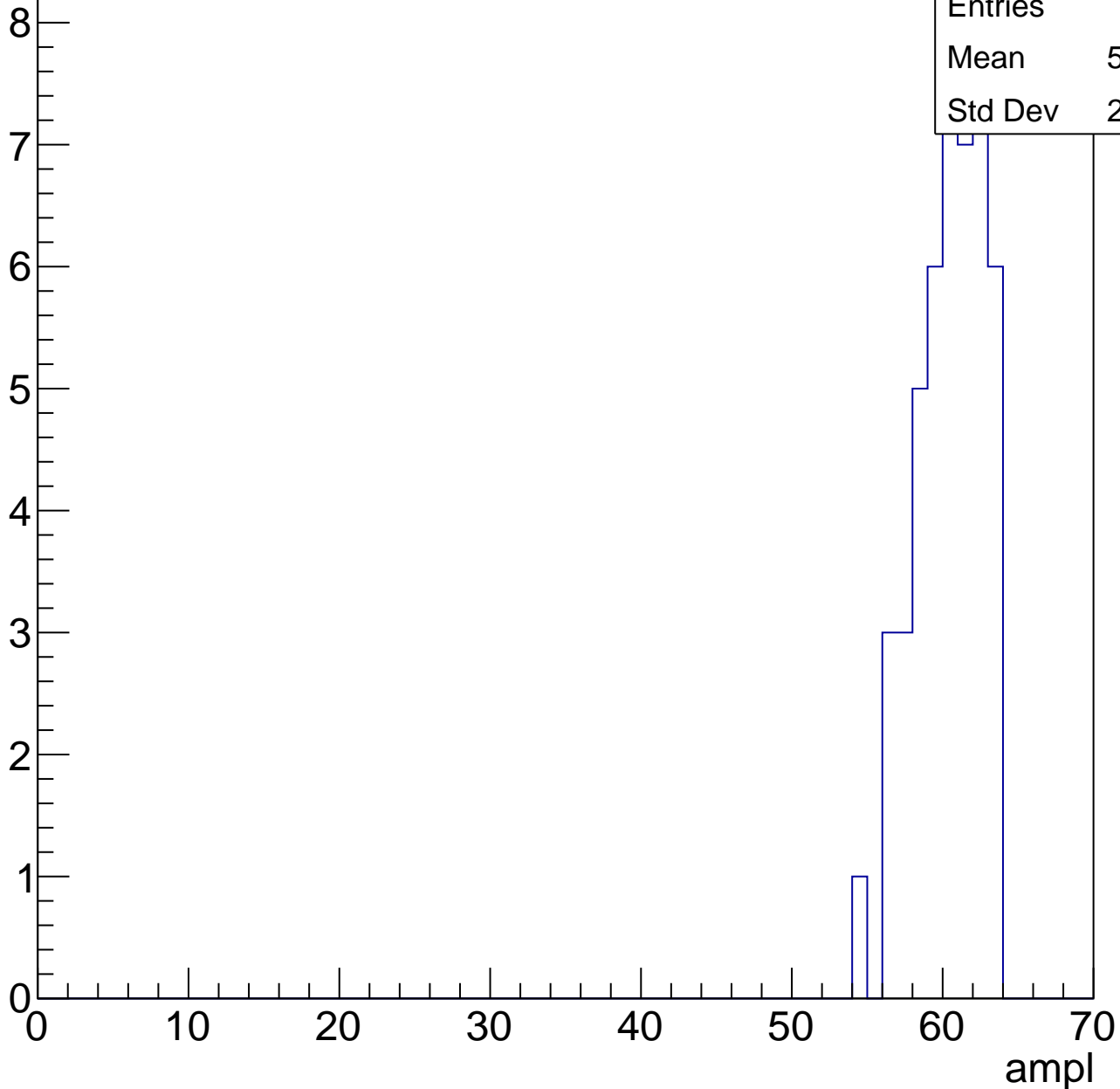


# B1L000S, U25-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

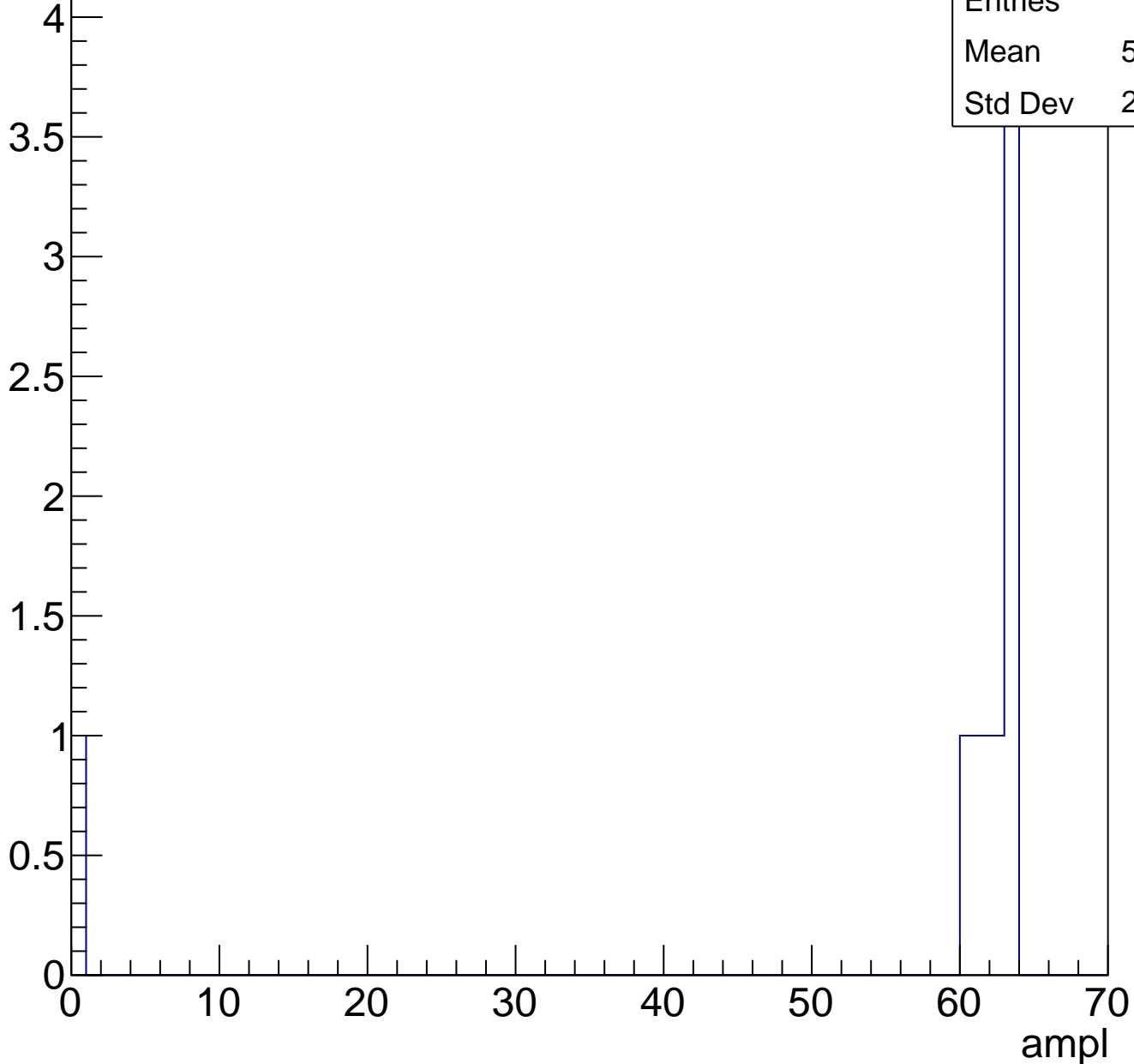
Entries	47
Mean	59.96
Std Dev	2.212



# B1L000S, U25-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch85, adc0

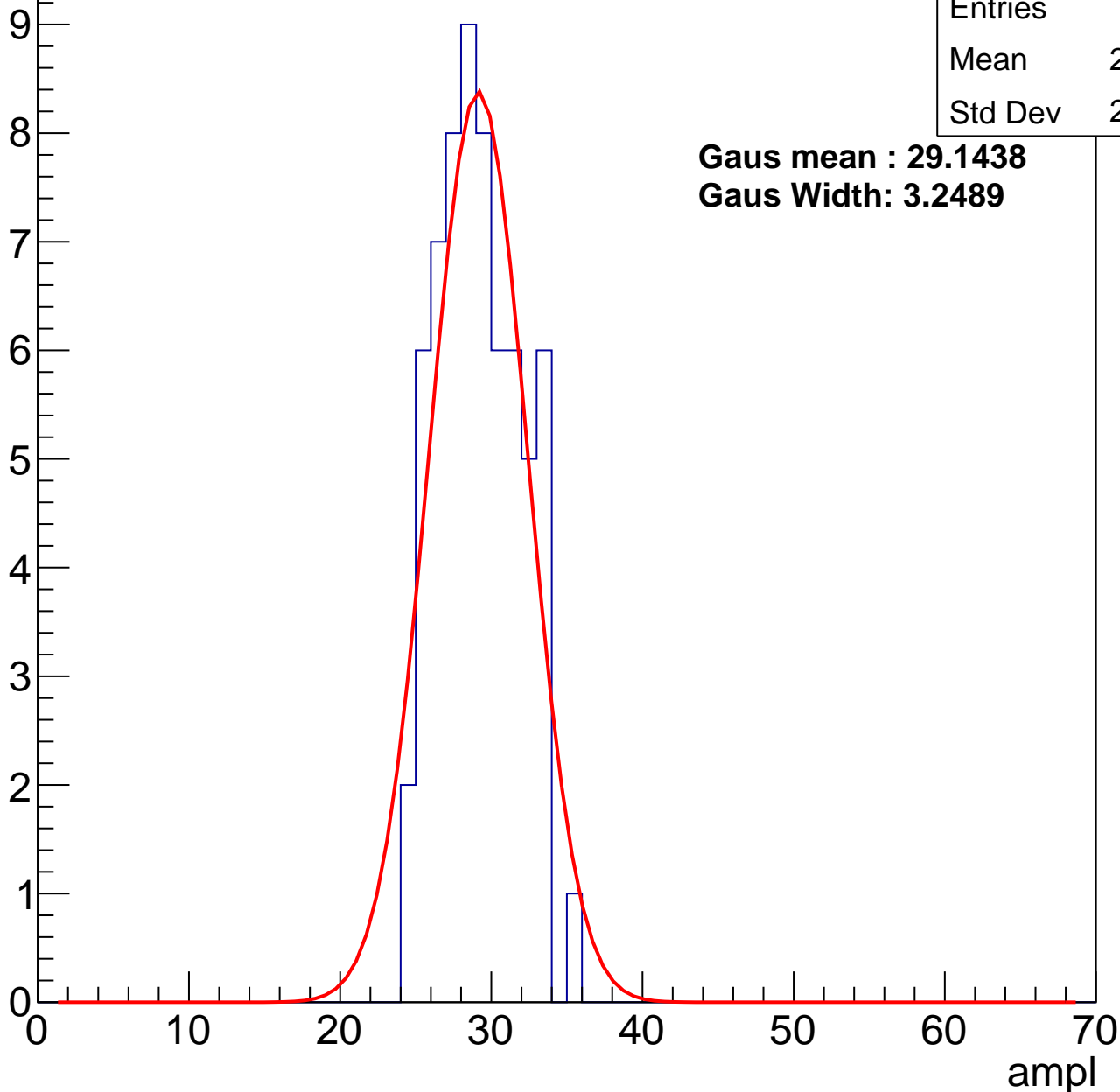
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.73
Std Dev	2.659

**Gaus mean : 29.1438**

**Gaus Width: 3.2489**



# B1L000S, U25-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	35.61
Std Dev	3.42

**Gaus mean : 36.0435**

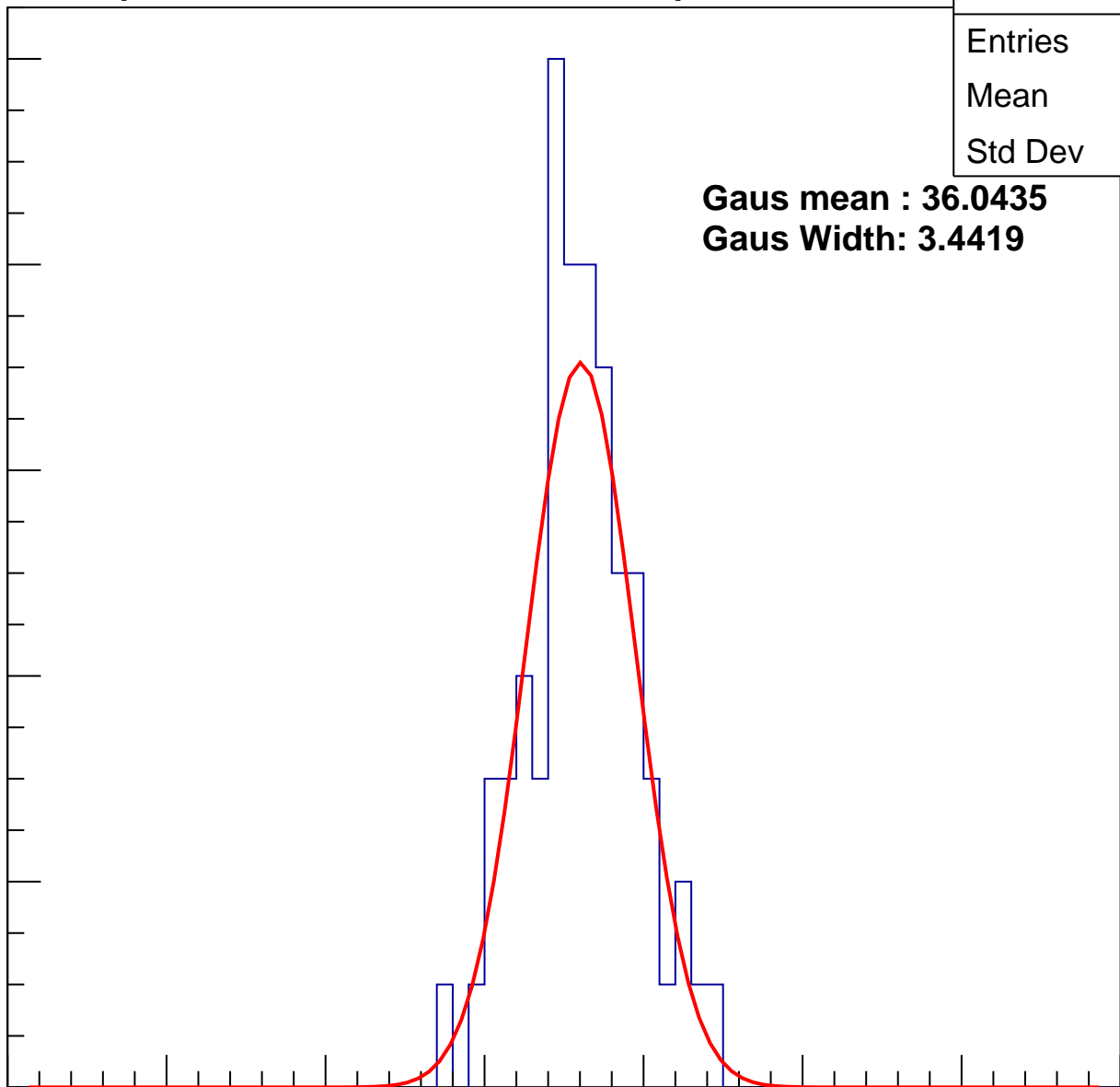
**Gaus Width: 3.4419**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch85, adc2

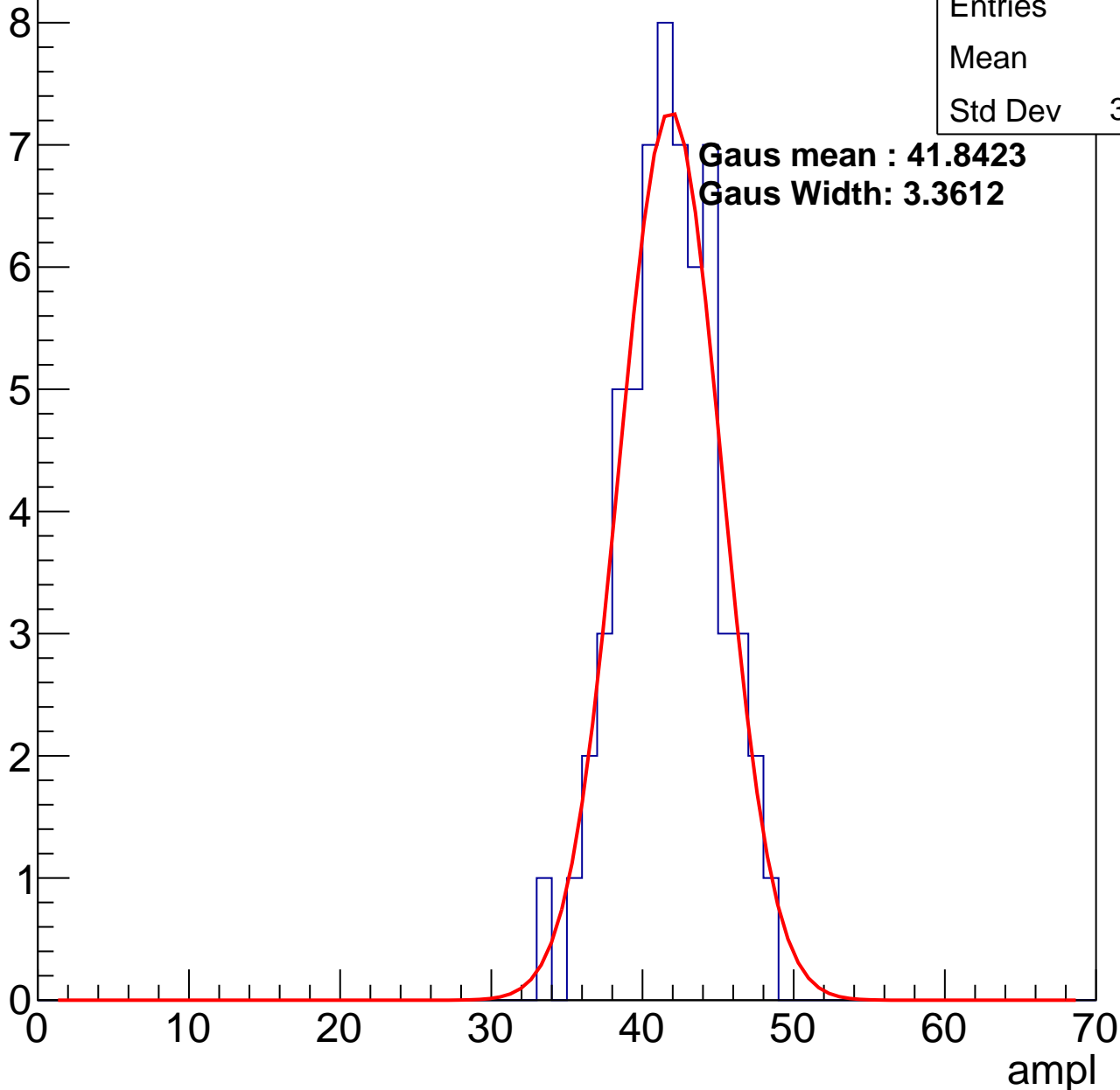
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.3
Std Dev	3.143

**Gaus mean : 41.8423**

**Gaus Width: 3.3612**

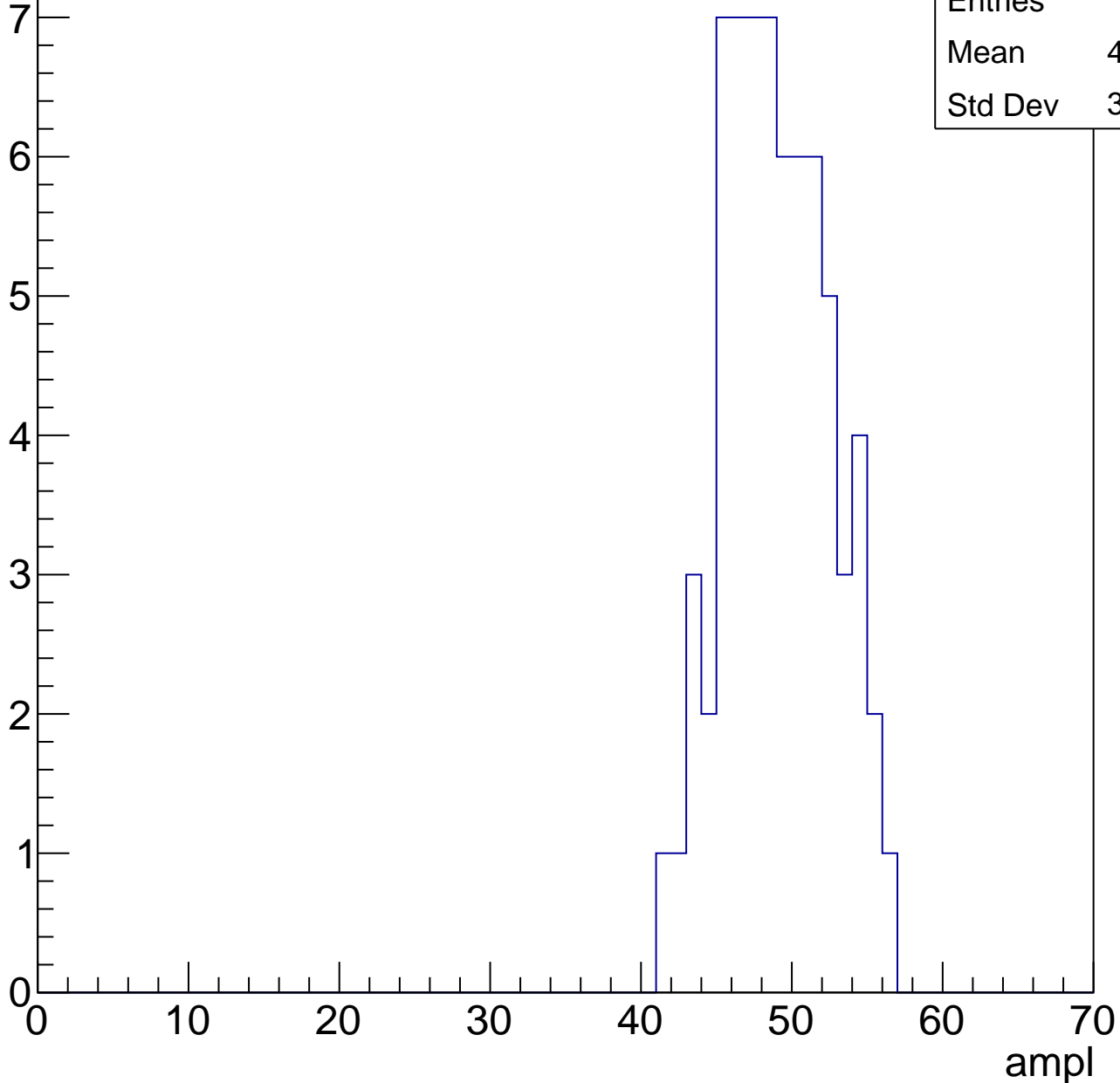


# B1L000S, U25-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

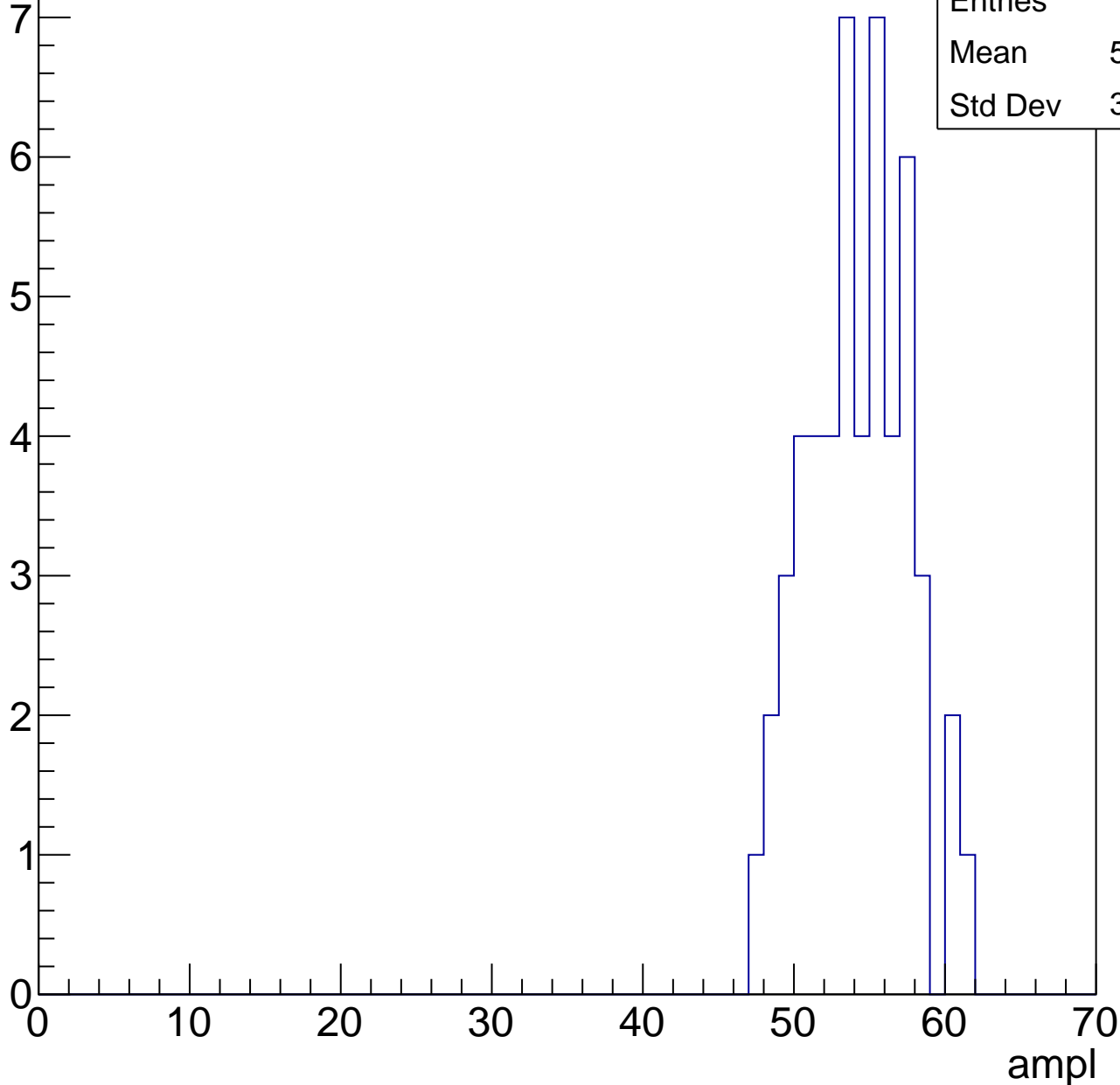
Entries	68
Mean	48.57
Std Dev	3.457



# B1L000S, U25-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

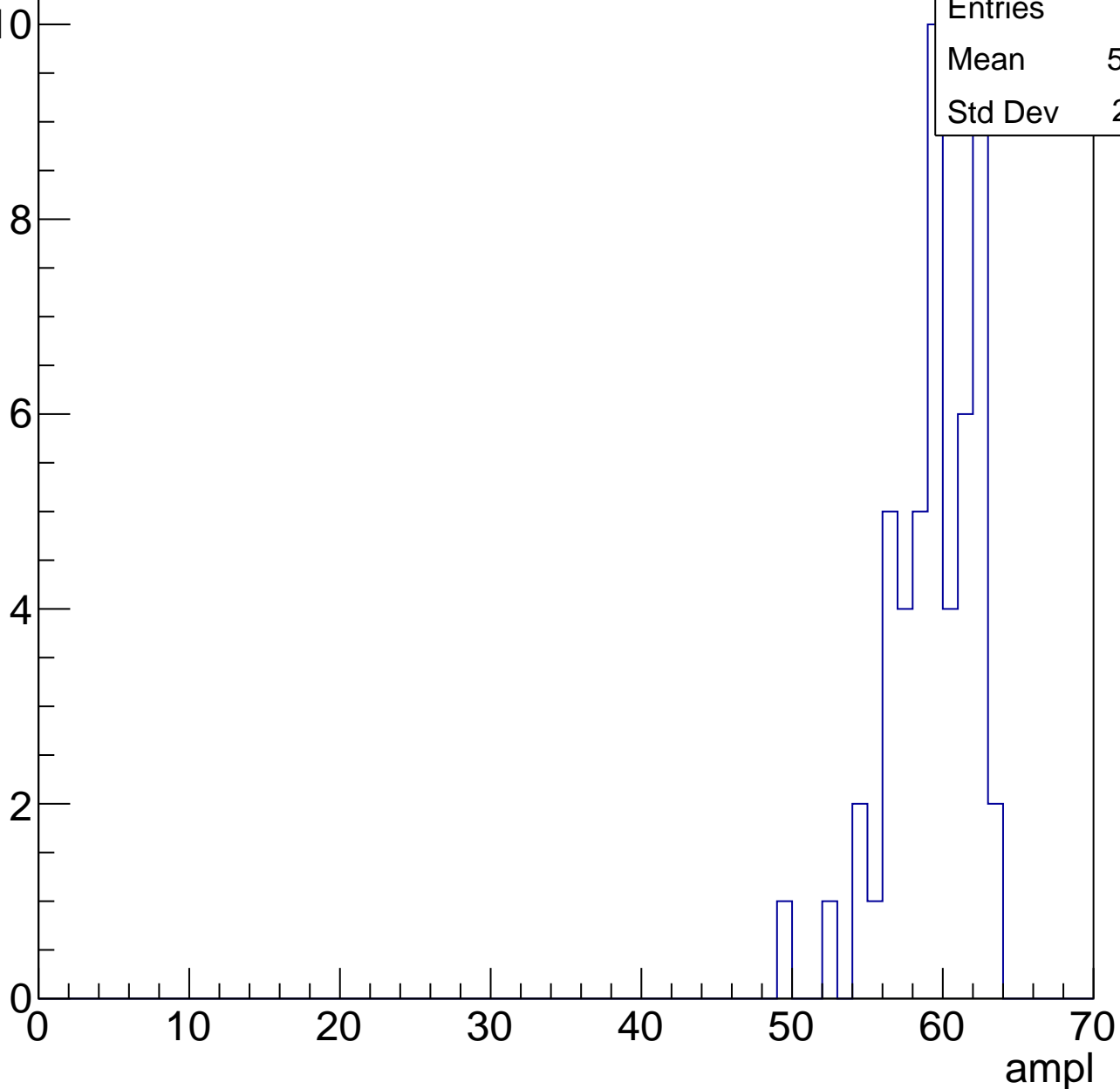


# B1L000S, U25-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	58.84
Std Dev	2.901

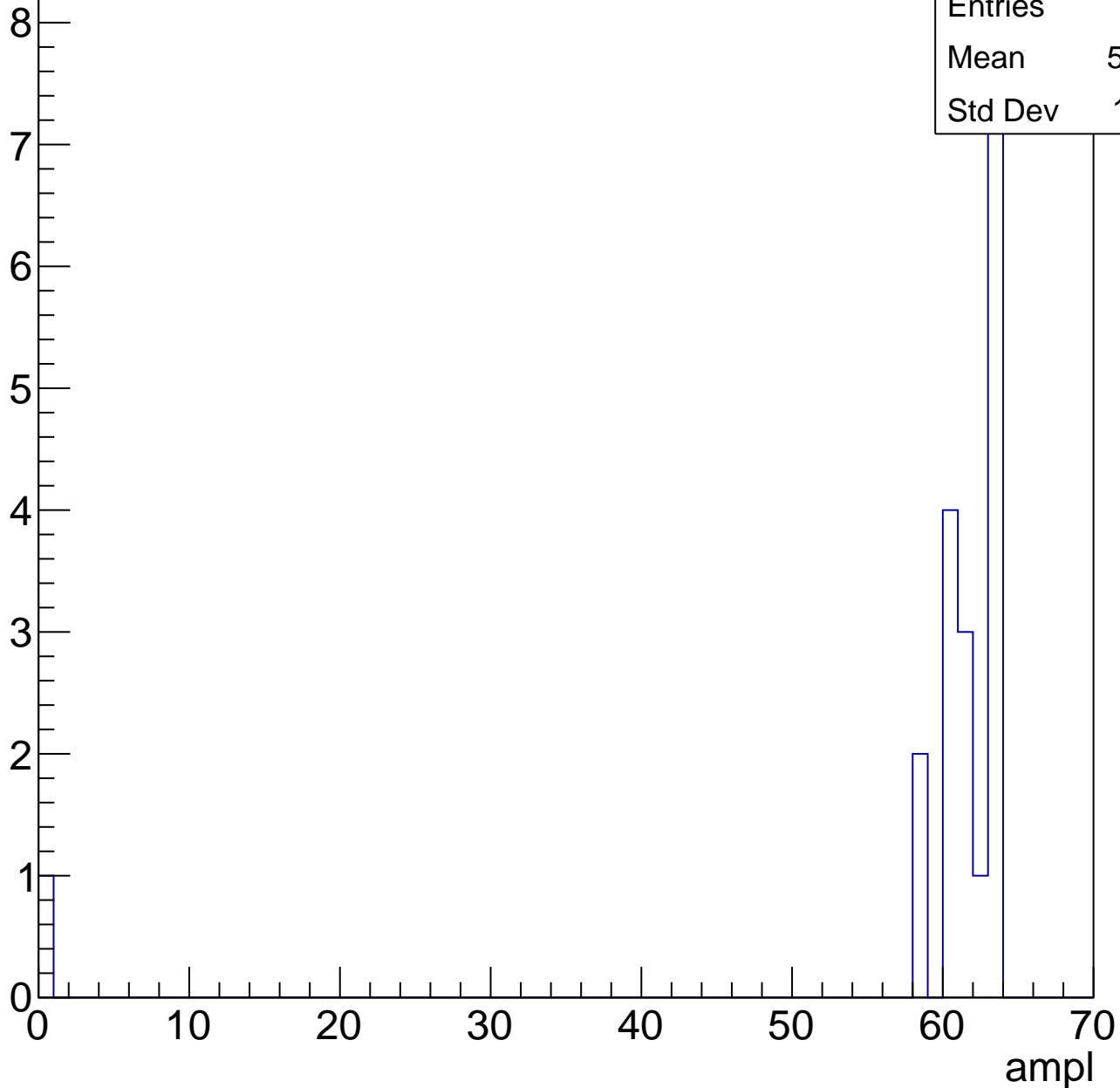


# B1L000S, U25-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	58.16
Std Dev	13.81





# B1L000S, U25-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U25-ch86, adc0

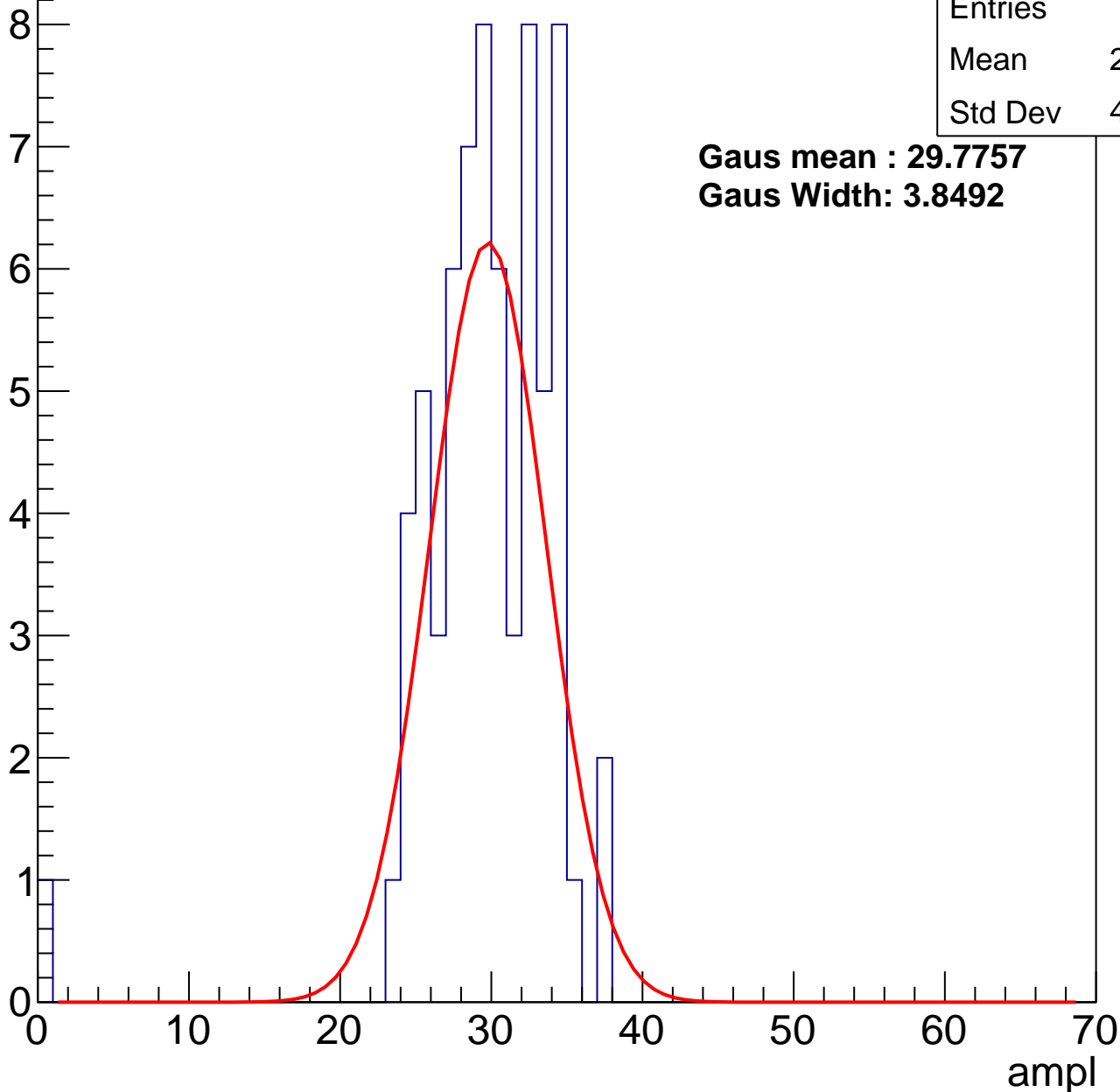
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	29.22
Std Dev	4.919

**Gaus mean : 29.7757**

**Gaus Width: 3.8492**



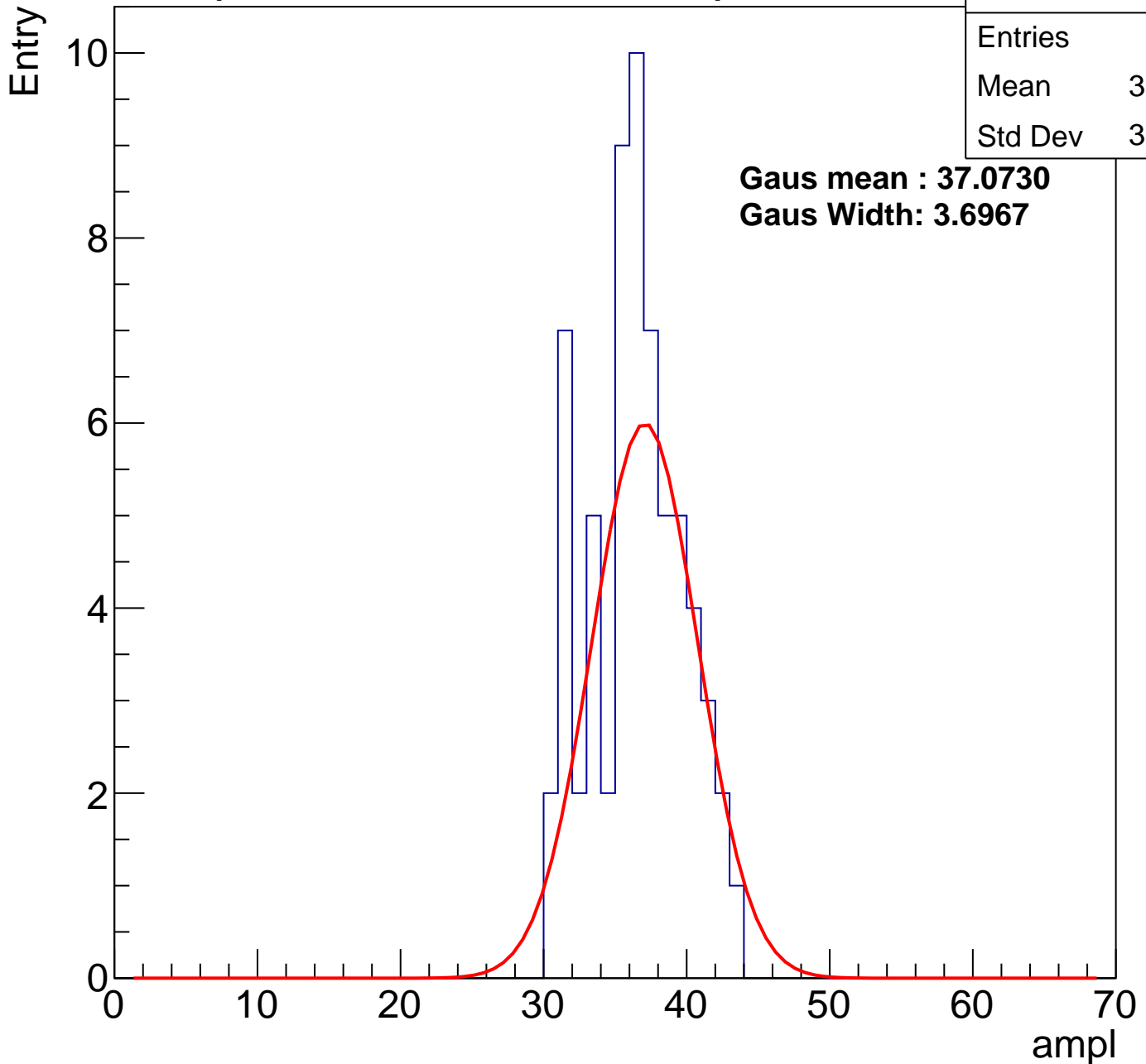
# B1L000S, U25-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	35.98
Std Dev	3.243

**Gaus mean : 37.0730**

**Gaus Width: 3.6967**



# B1L000S, U25-ch86, adc2

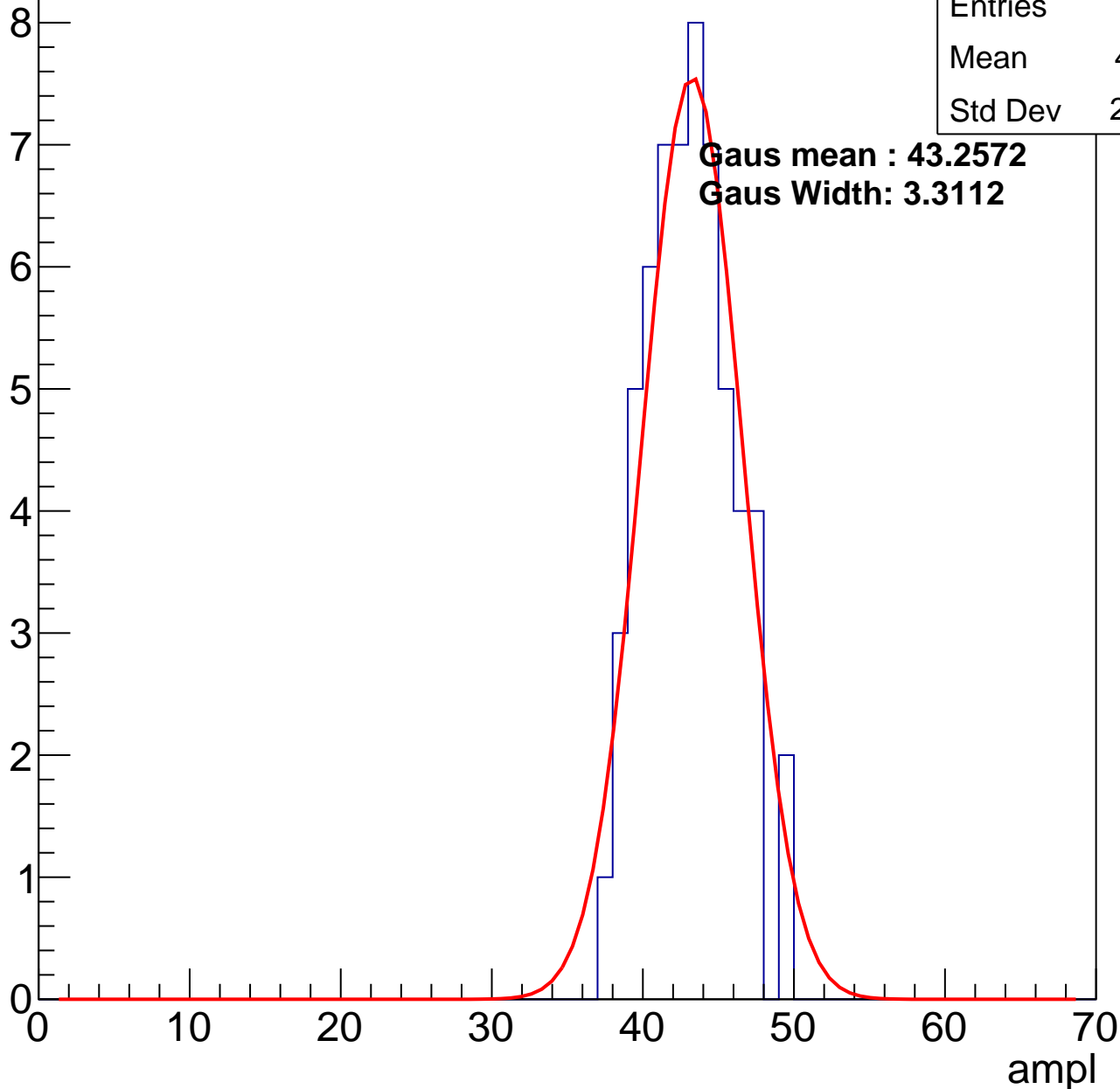
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	42.61
Std Dev	2.823

**Gaus mean : 43.2572**

**Gaus Width: 3.3112**

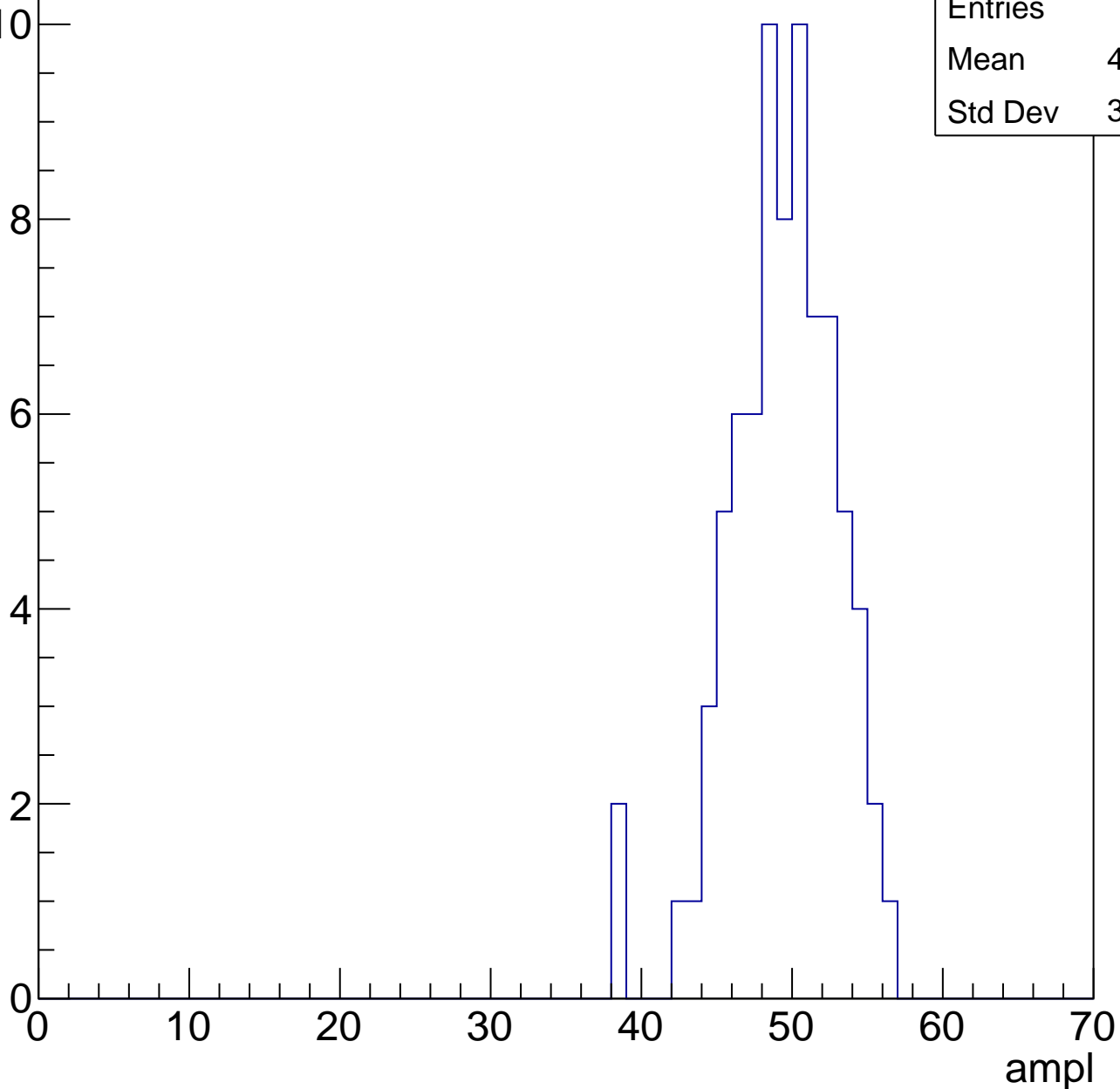


# B1L000S, U25-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

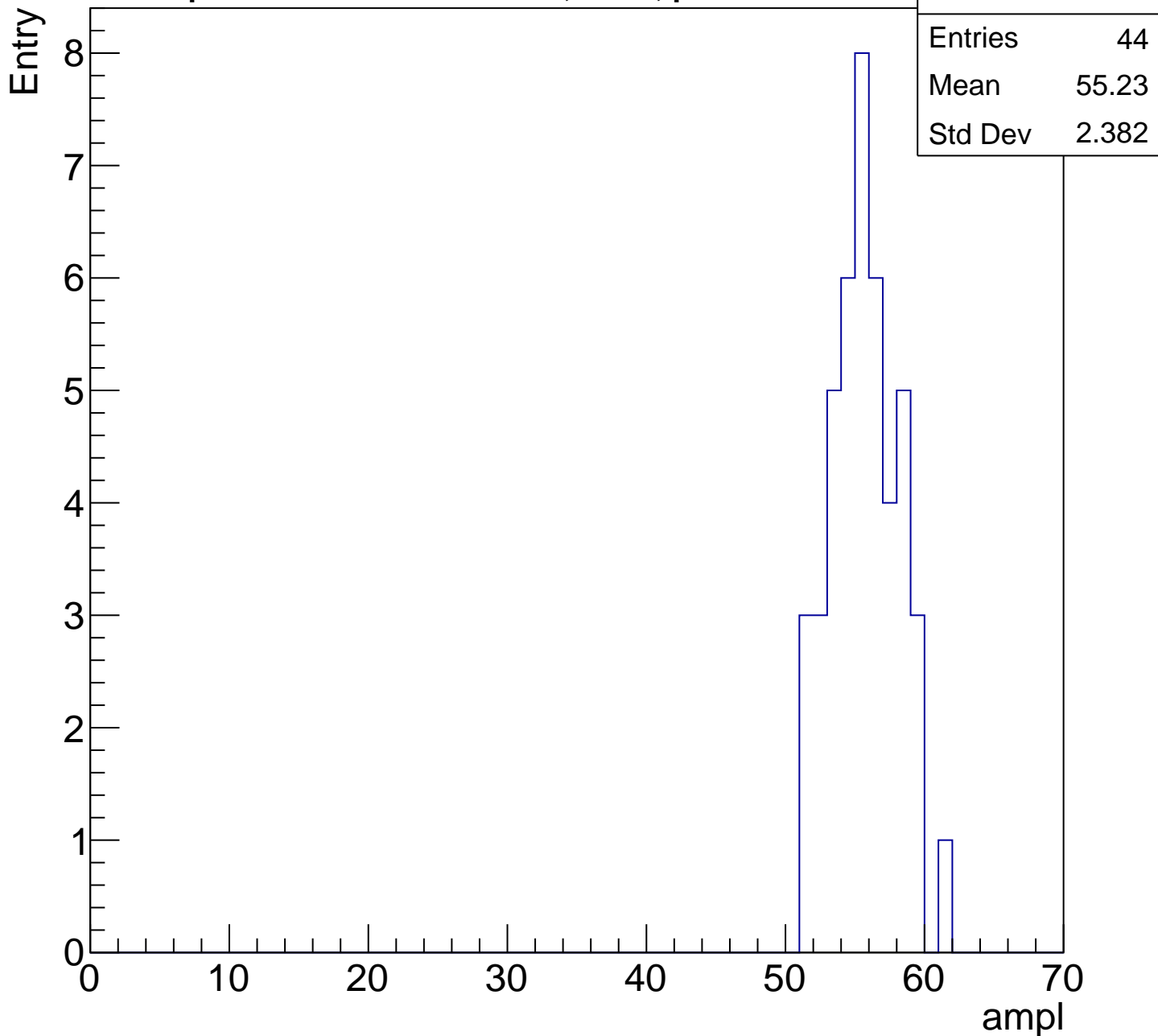
Entry

Entries	78
Mean	48.92
Std Dev	3.529



# B1L000S, U25-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

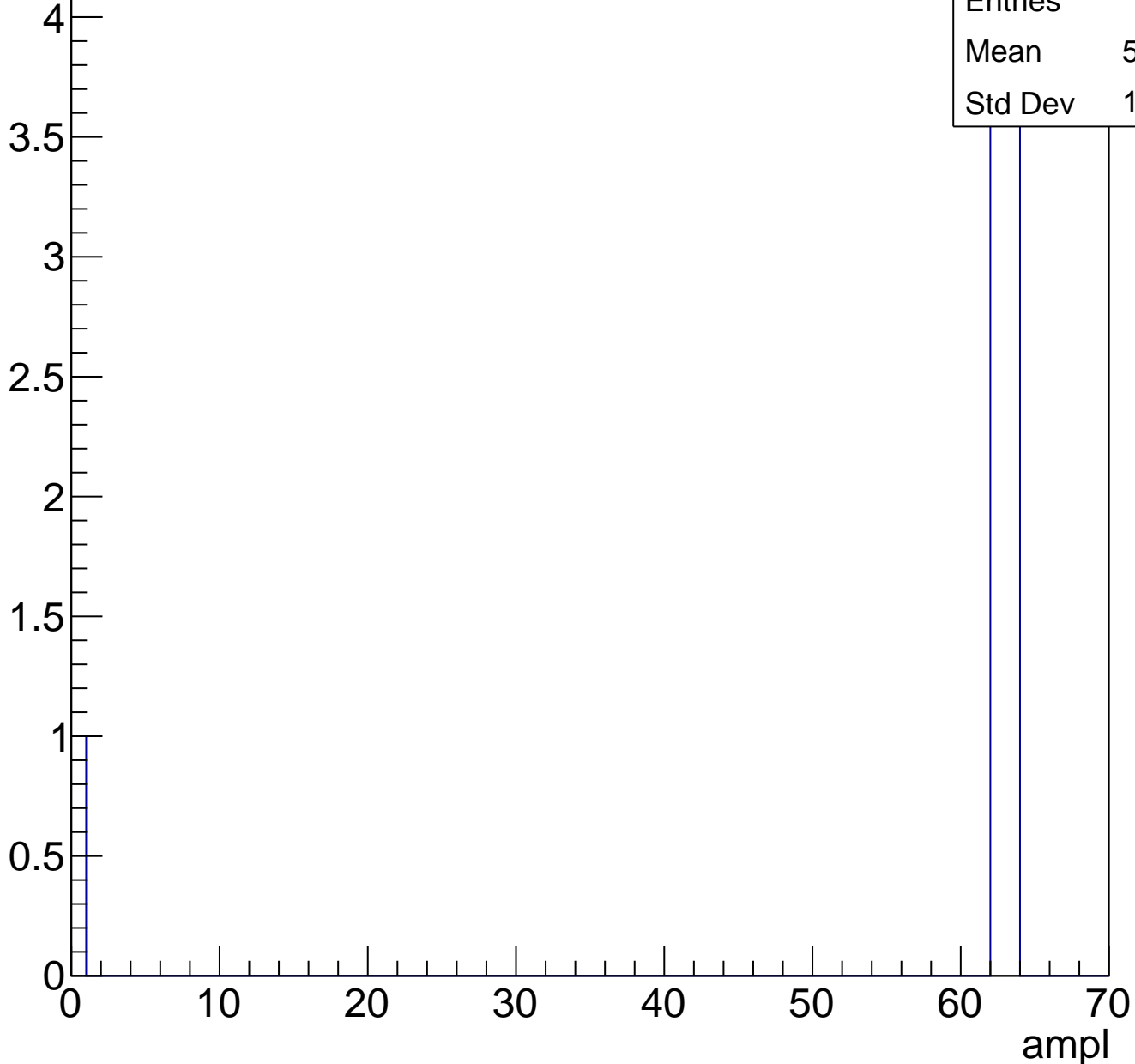
ampl

Entries	52
Mean	59.85
Std Dev	2.032

# B1L000S, U25-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

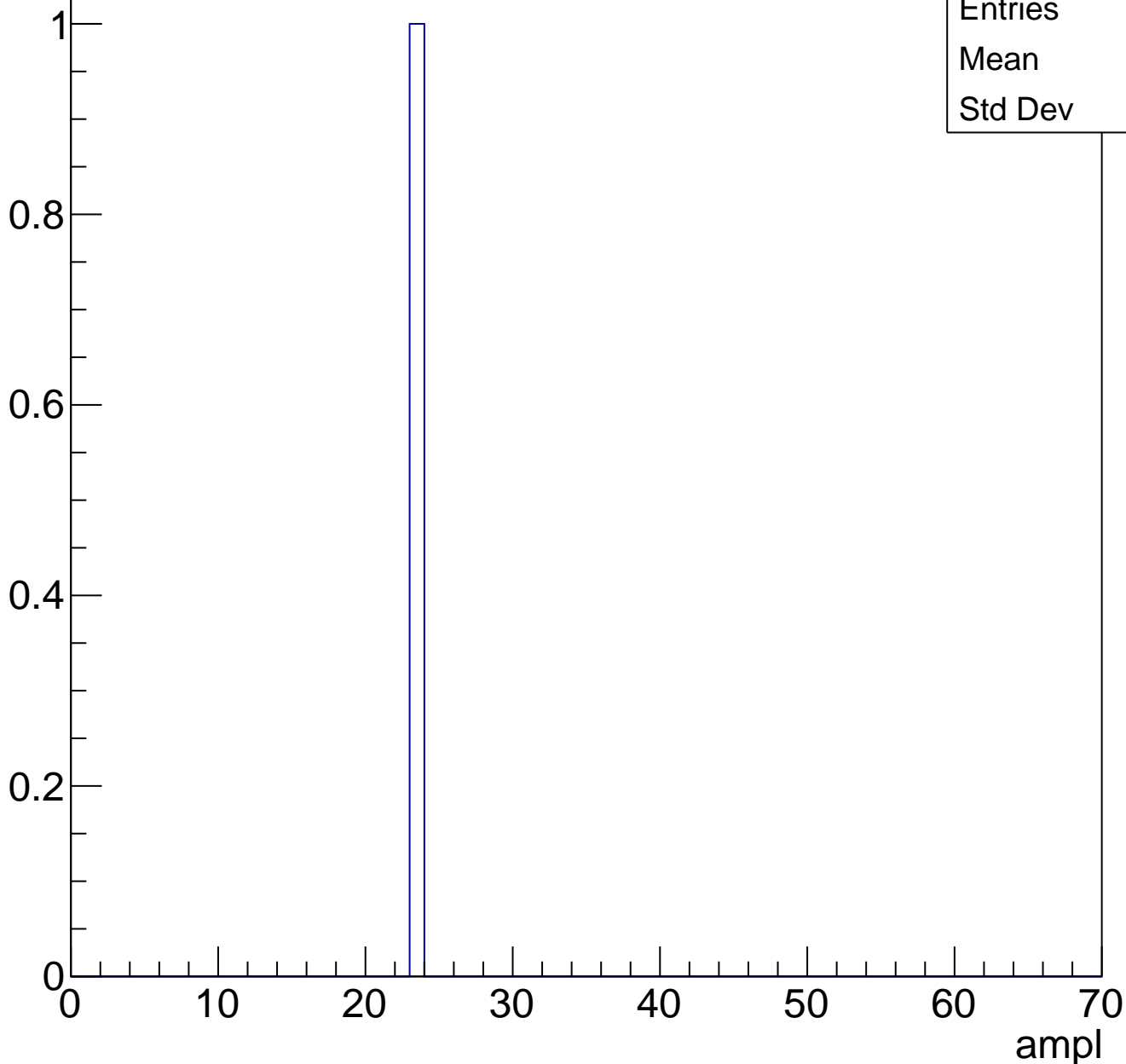




# B1L000S, U25-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	23
Std Dev	0

# B1L000S, U25-ch87, adc0

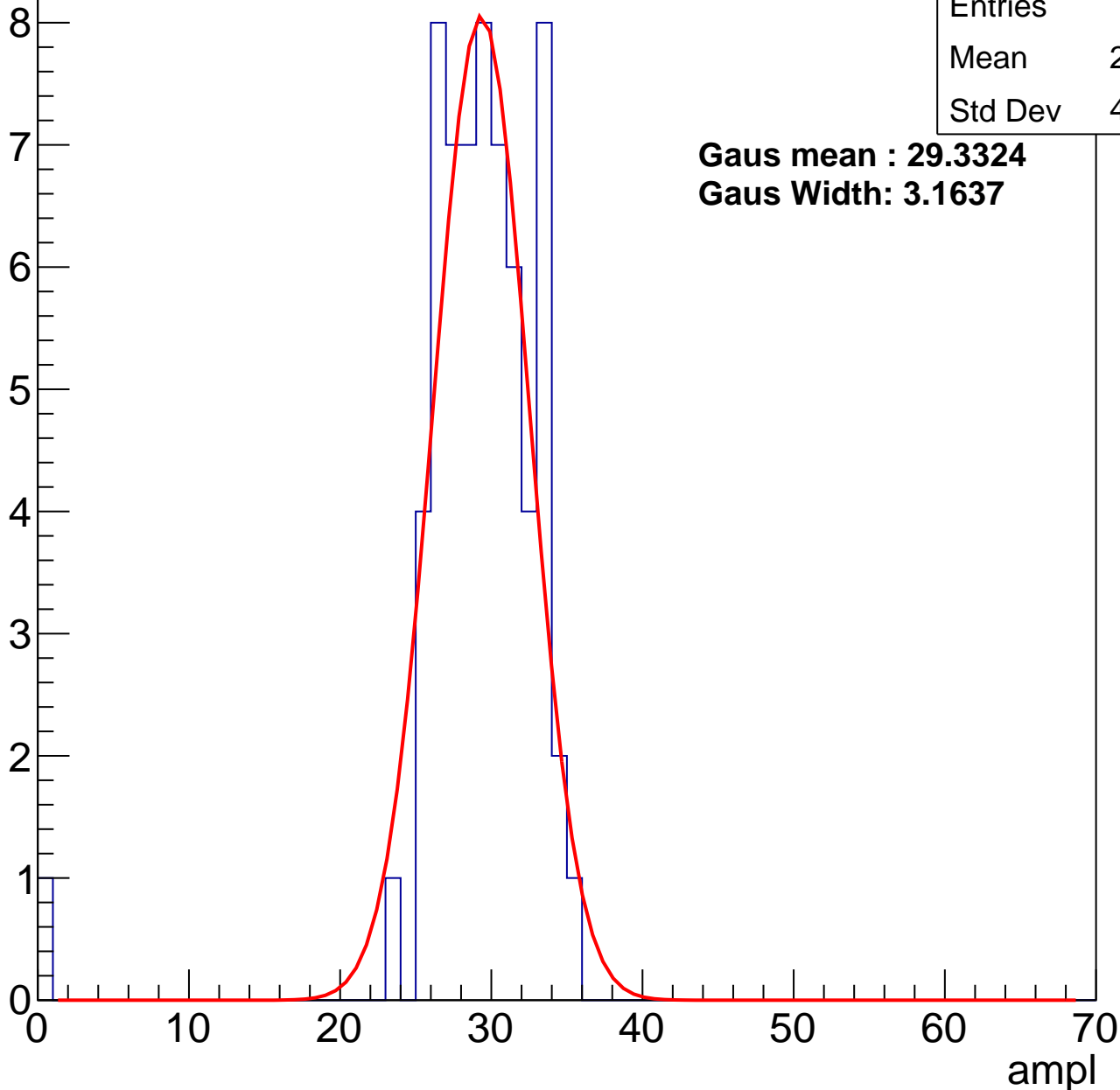
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	28.73
Std Dev	4.549

**Gaus mean : 29.3324**

**Gaus Width: 3.1637**



# B1L000S, U25-ch87, adc1

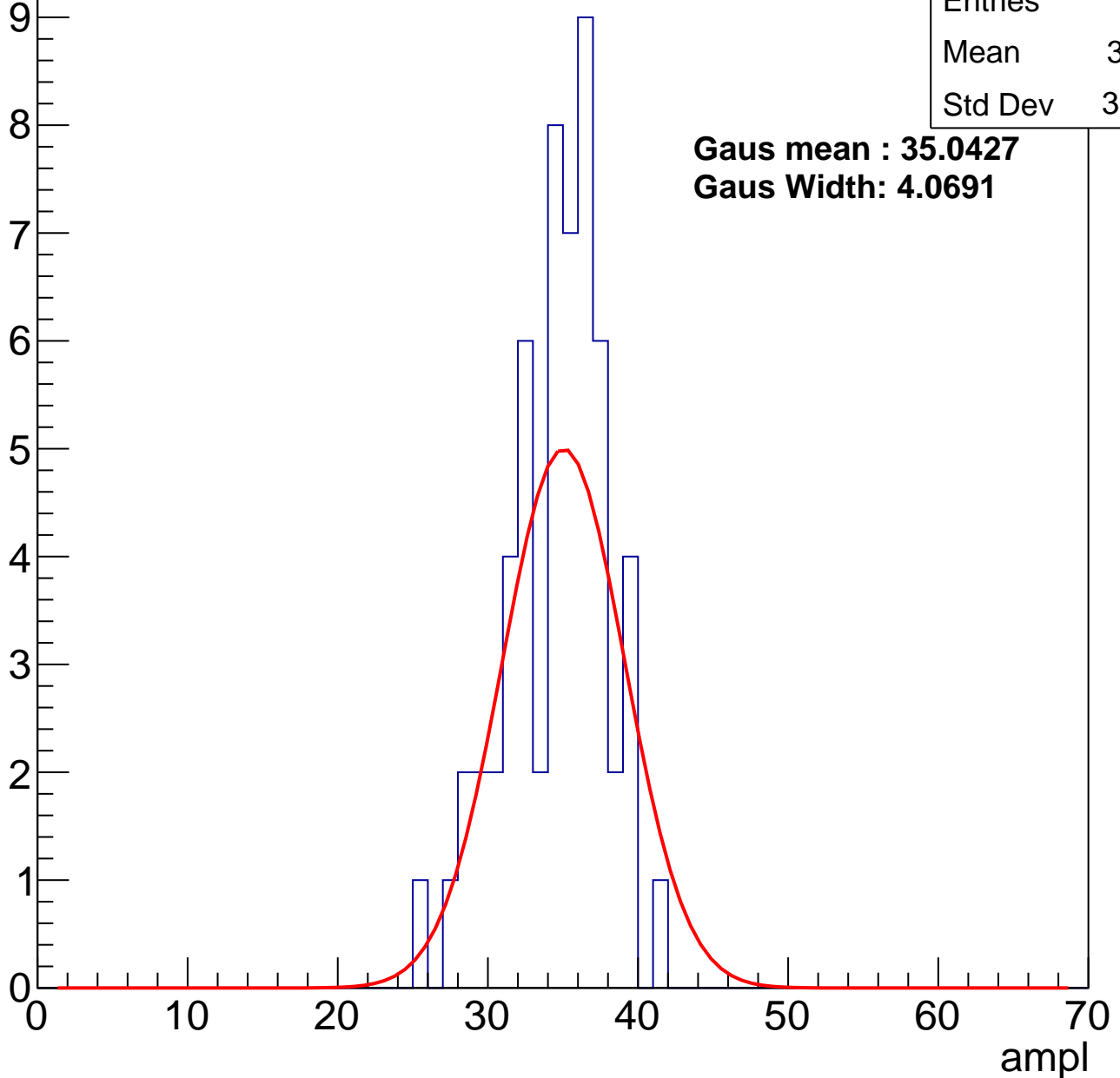
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	34.11
Std Dev	3.307

**Gaus mean : 35.0427**

**Gaus Width: 4.0691**



# B1L000S, U25-ch87, adc2

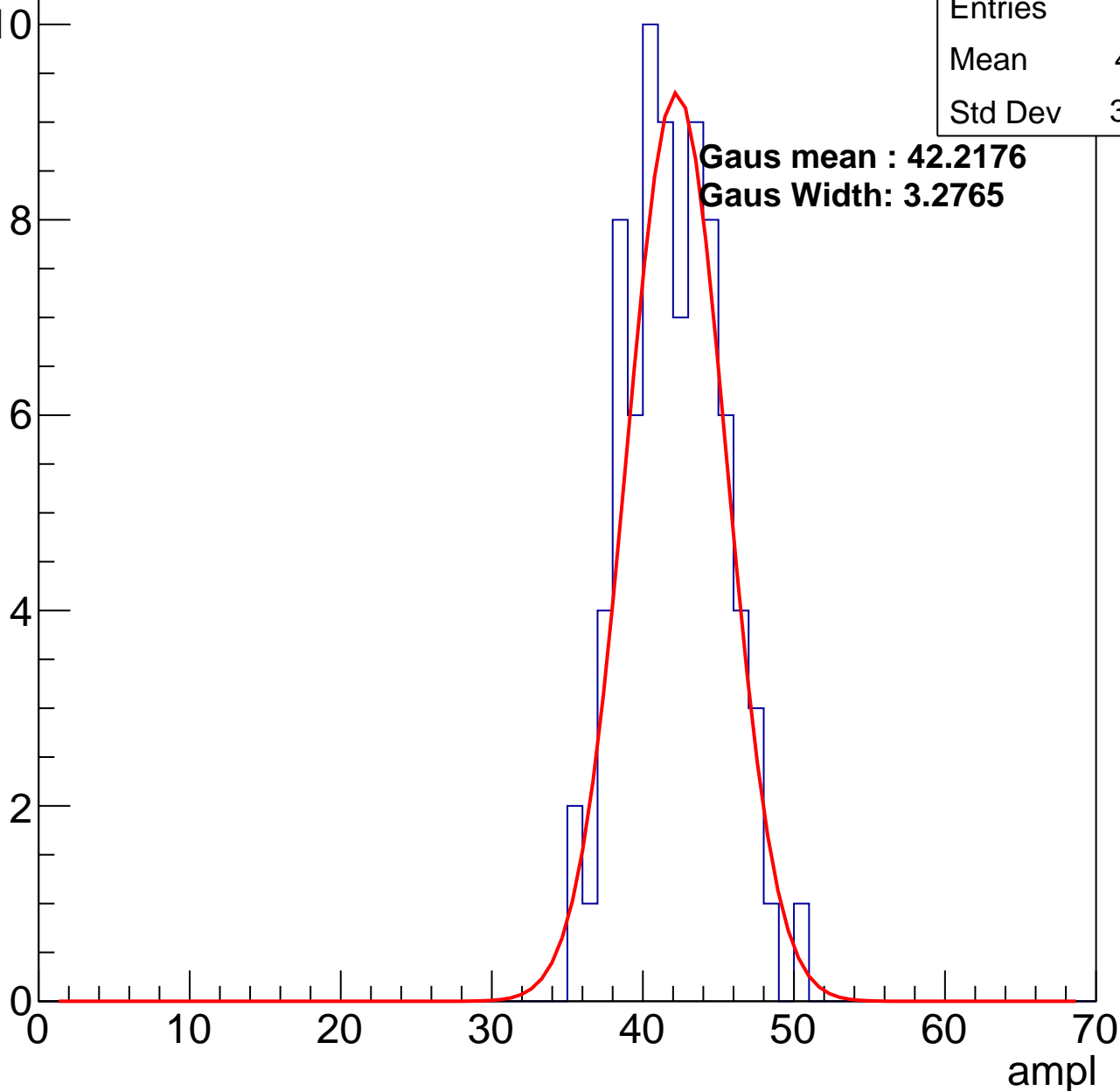
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	41.61
Std Dev	3.156

**Gaus mean : 42.2176**

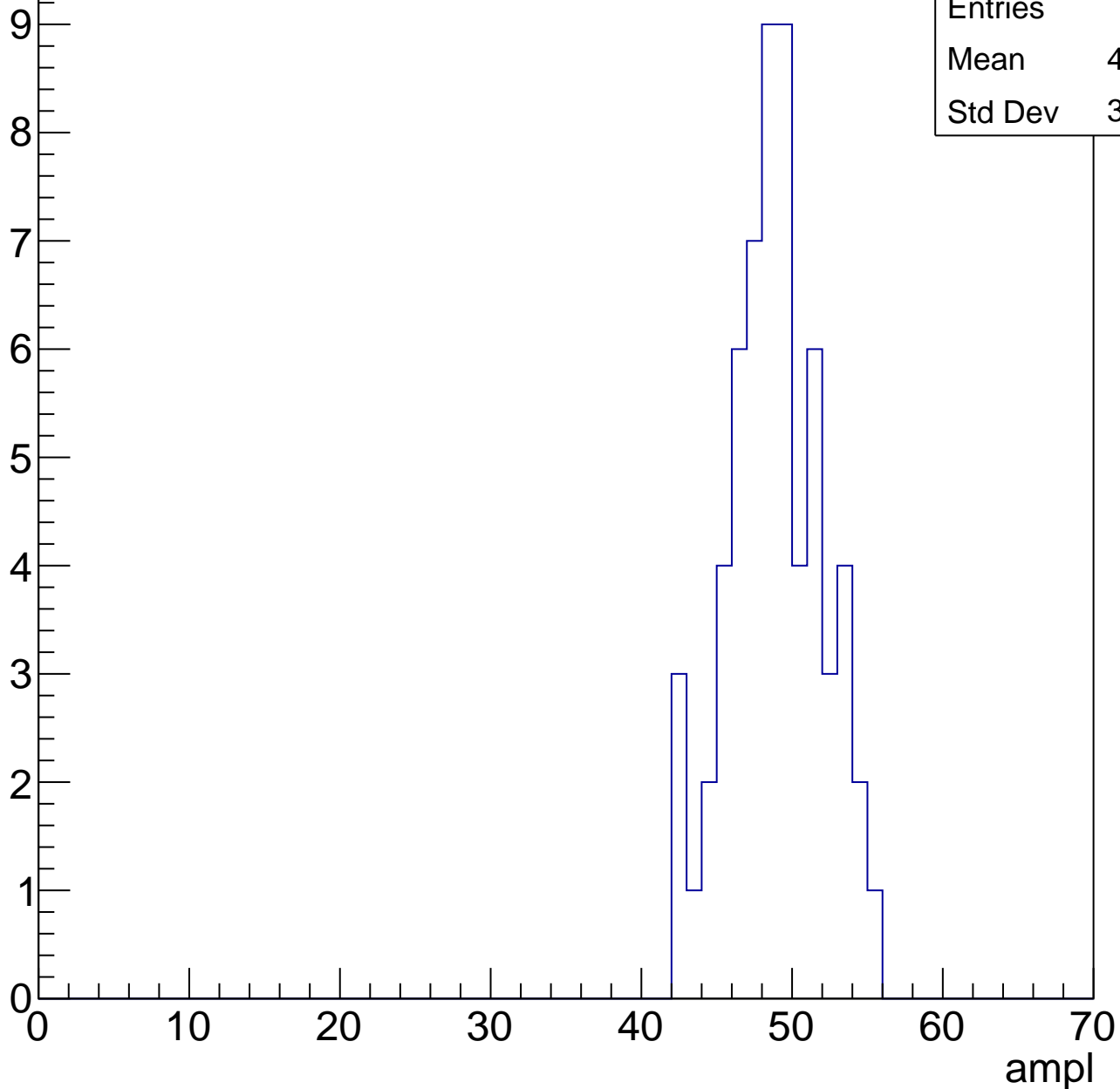
**Gaus Width: 3.2765**



# B1L000S, U25-ch87, adc3

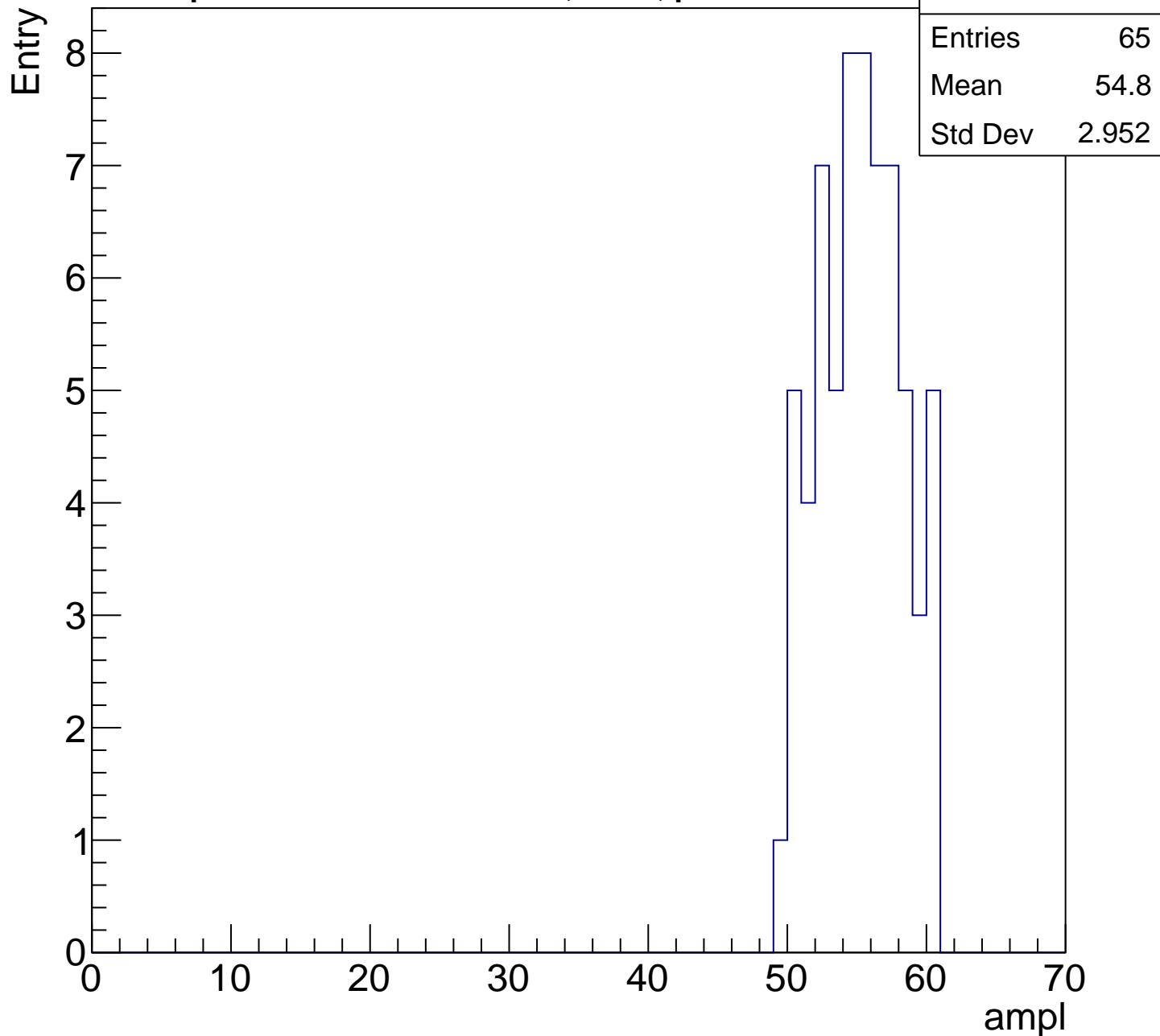
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

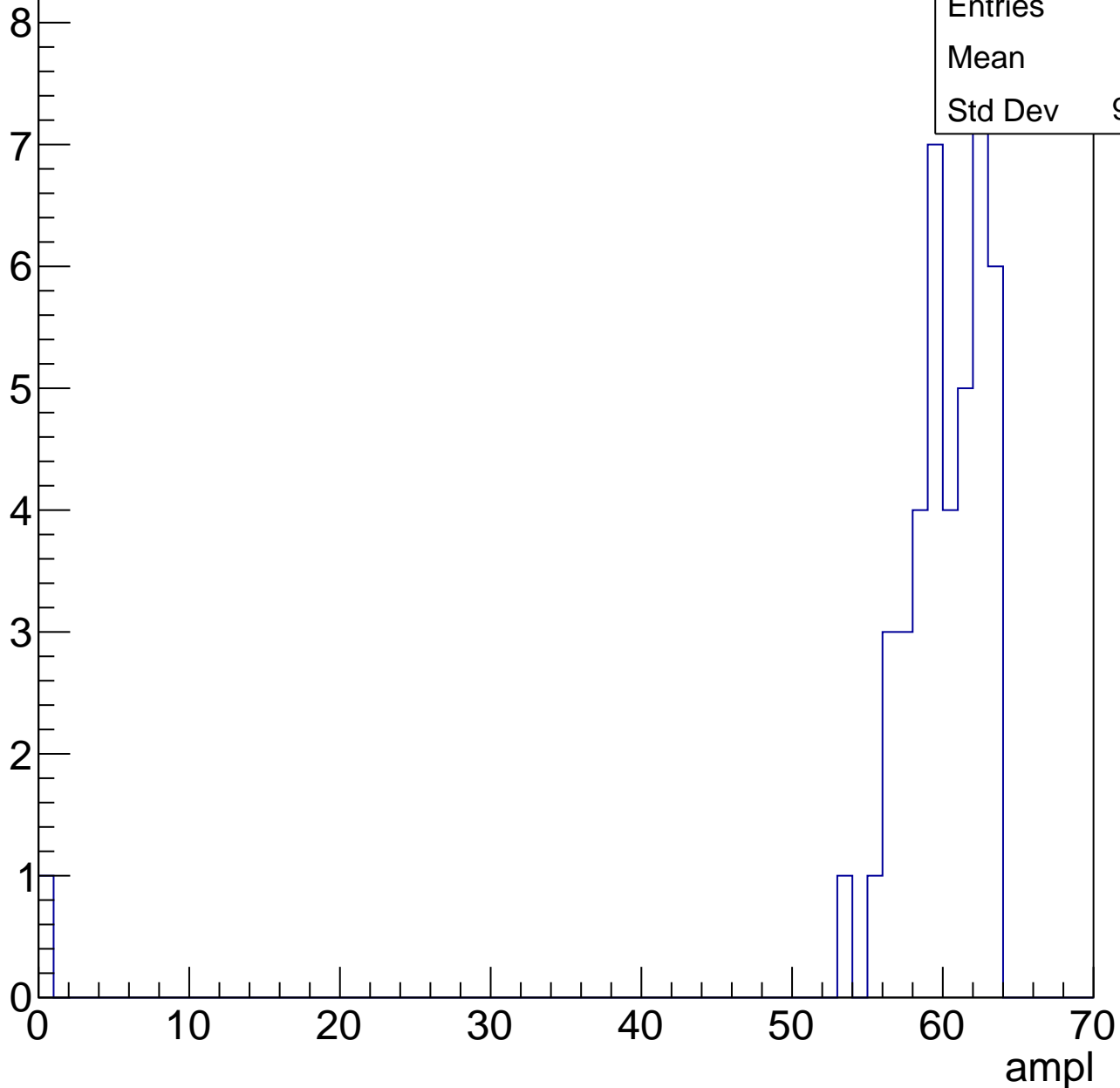


# B1L000S, U25-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	58.4
Std Dev	9.341

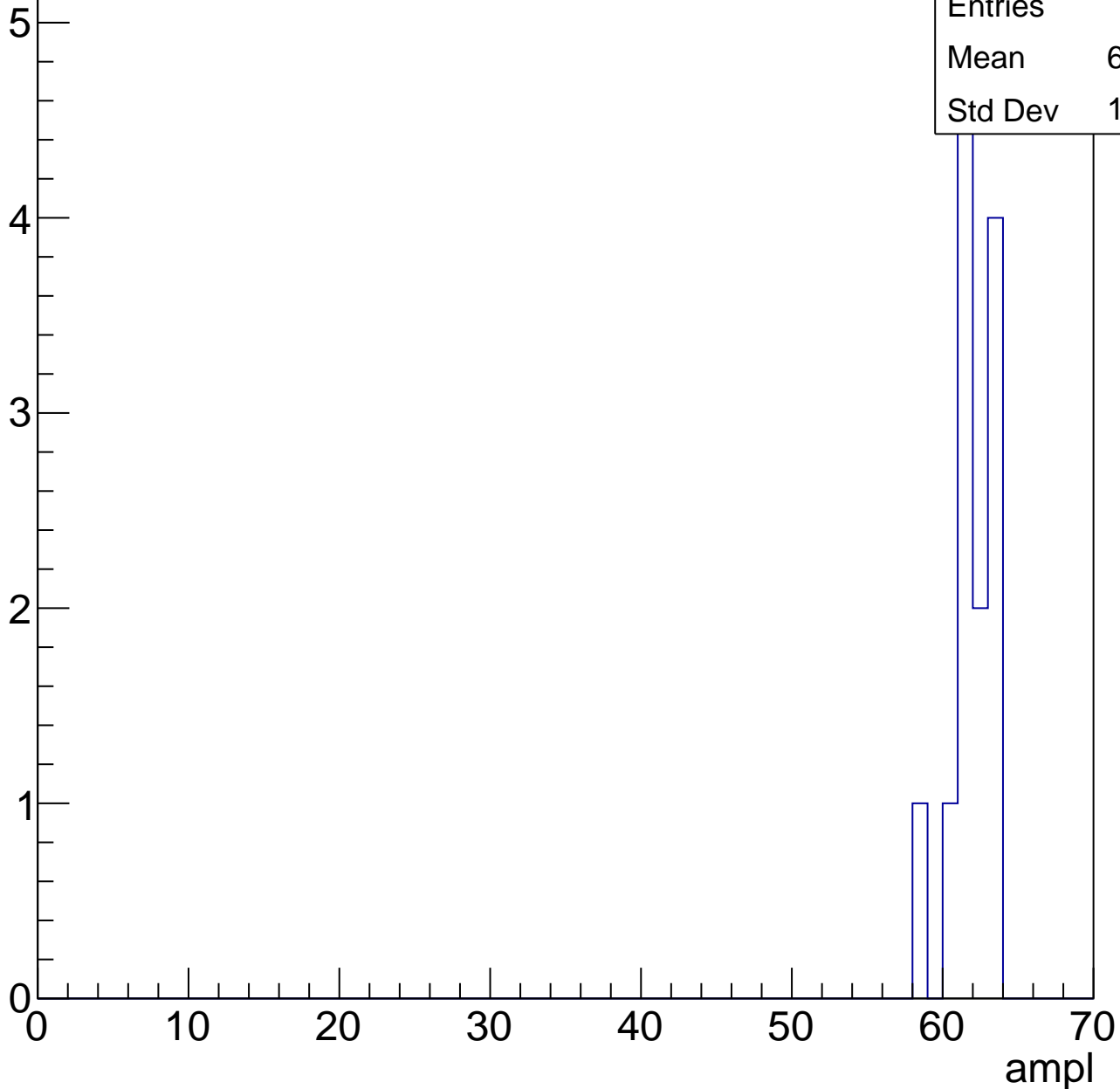


# B1L000S, U25-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.46
Std Dev	1.393





# B1L000S, U25-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch88, adc0

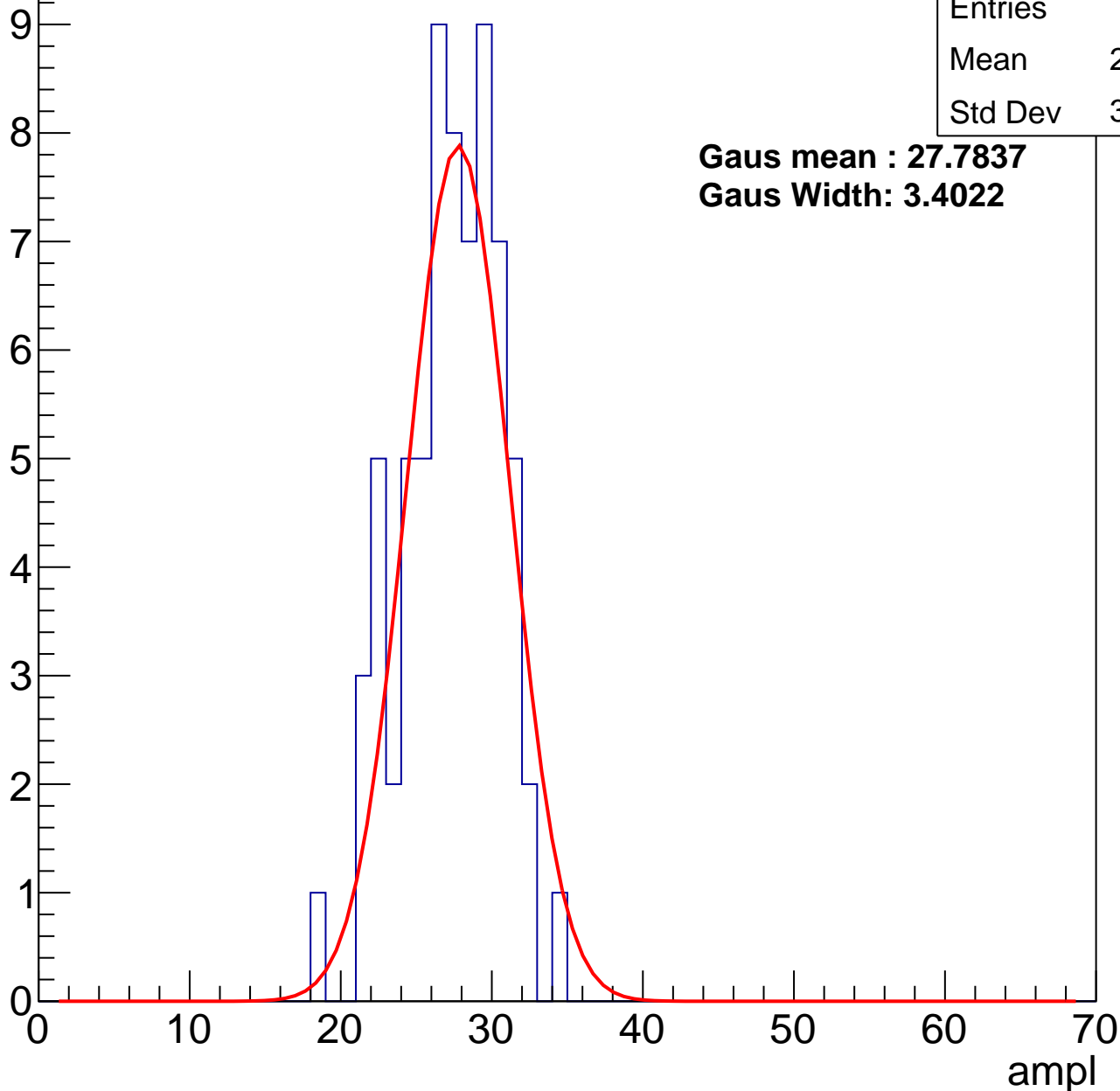
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	26.84
Std Dev	3.192

**Gaus mean : 27.7837**

**Gaus Width: 3.4022**



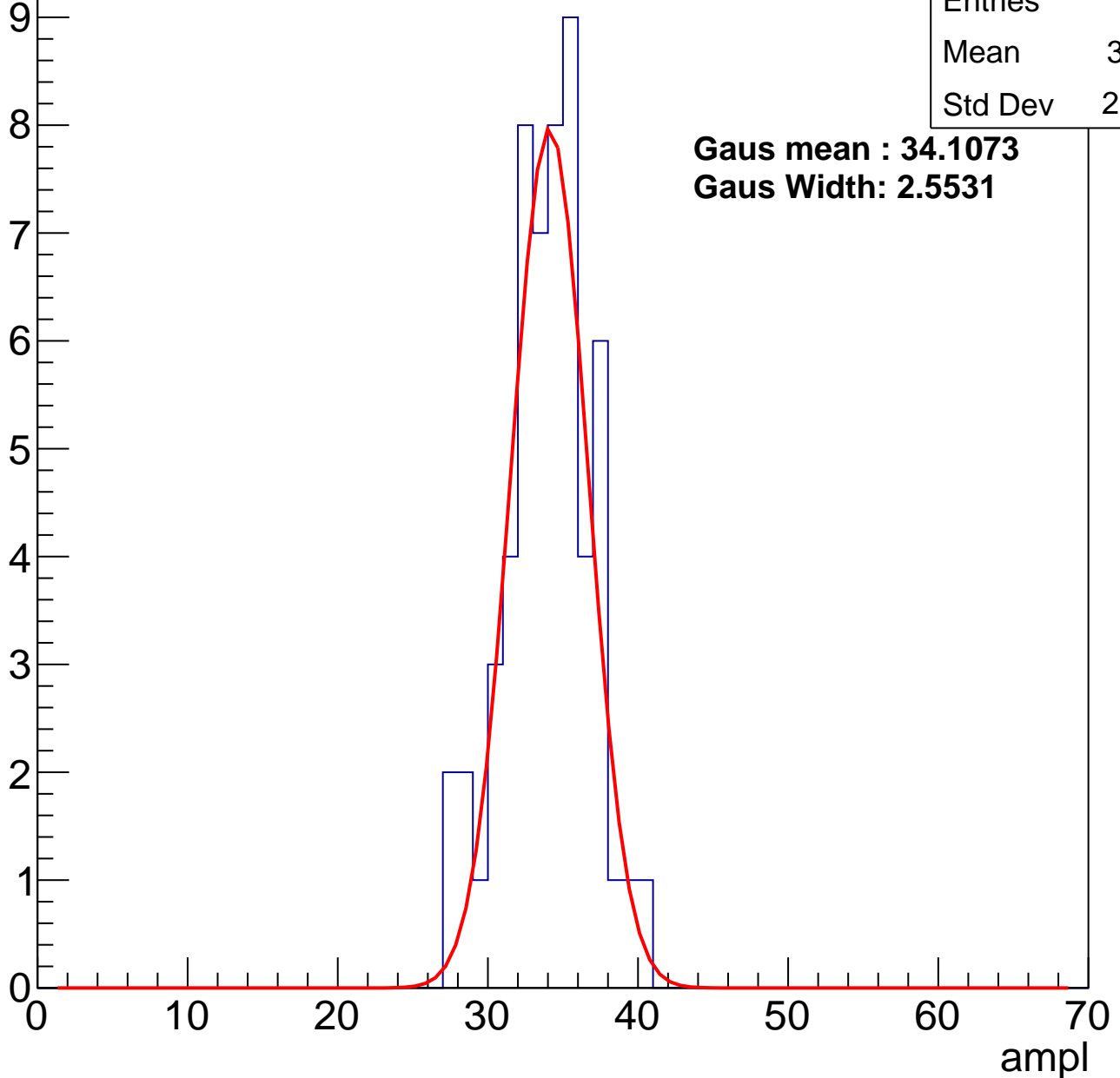
# B1L000S, U25-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	33.51
Std Dev	2.842

**Gaus mean : 34.1073**  
**Gaus Width: 2.5531**



# B1L000S, U25-ch88, adc2

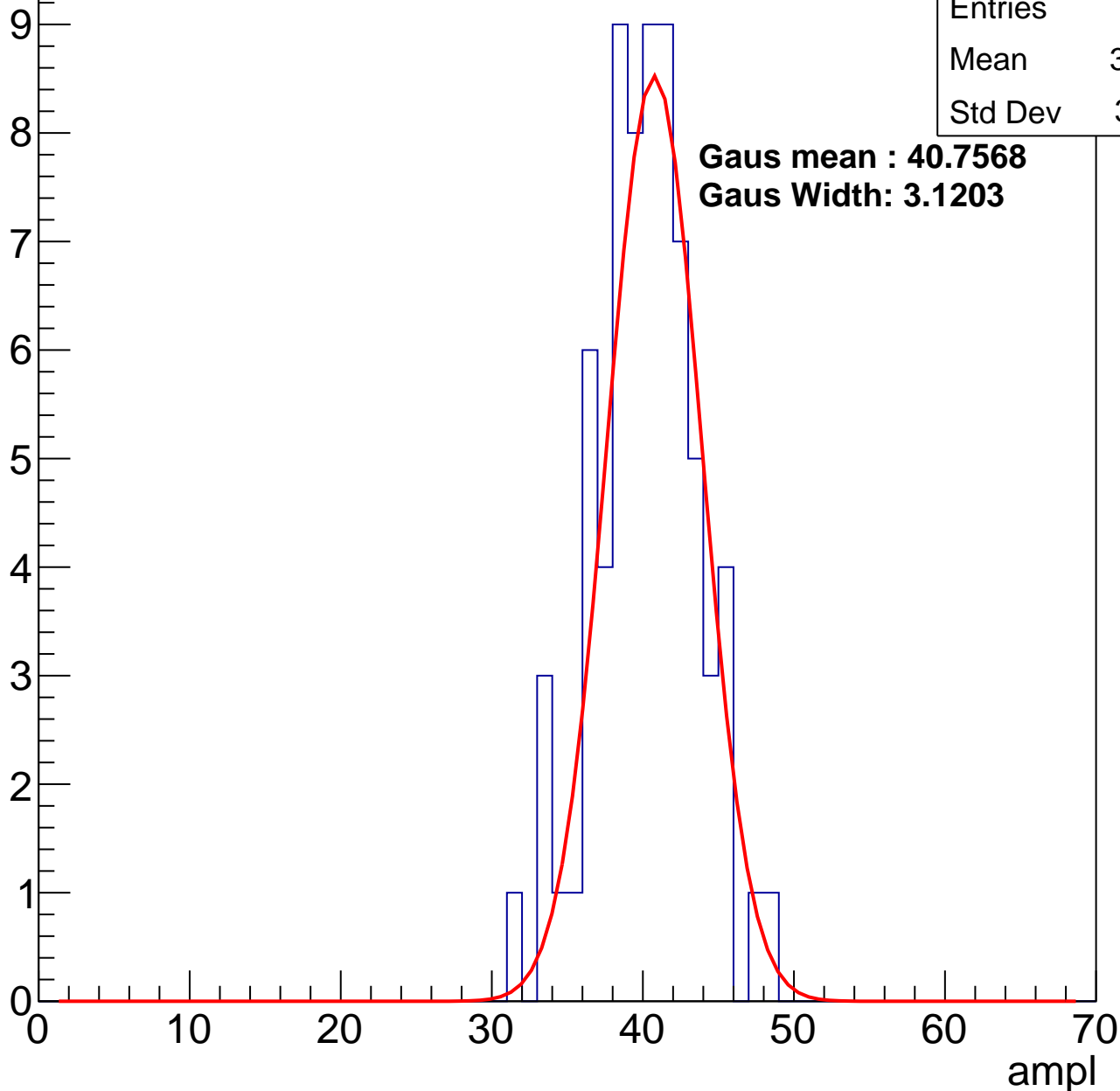
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	39.75
Std Dev	3.341

**Gaus mean : 40.7568**

**Gaus Width: 3.1203**

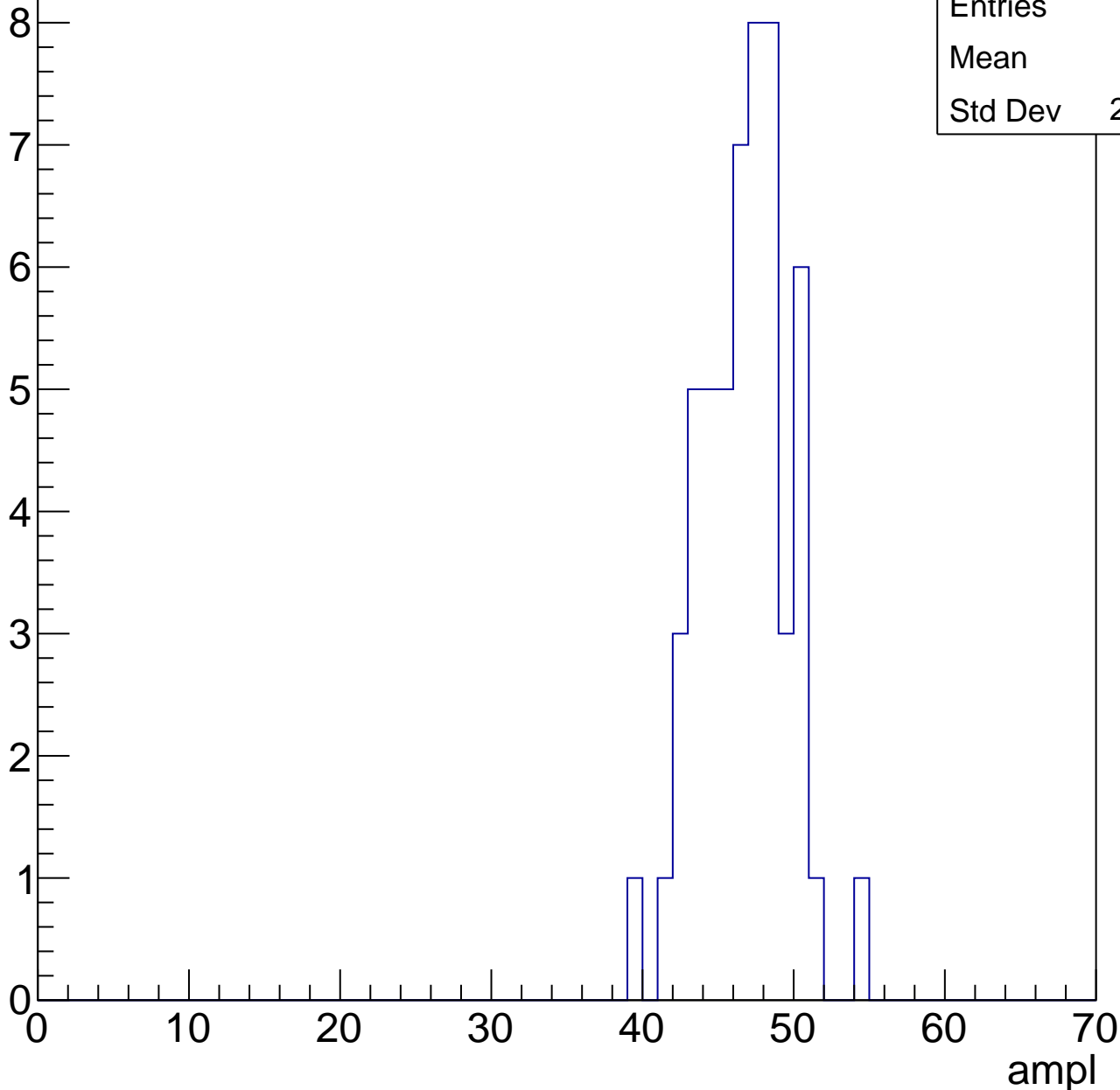


# B1L000S, U25-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

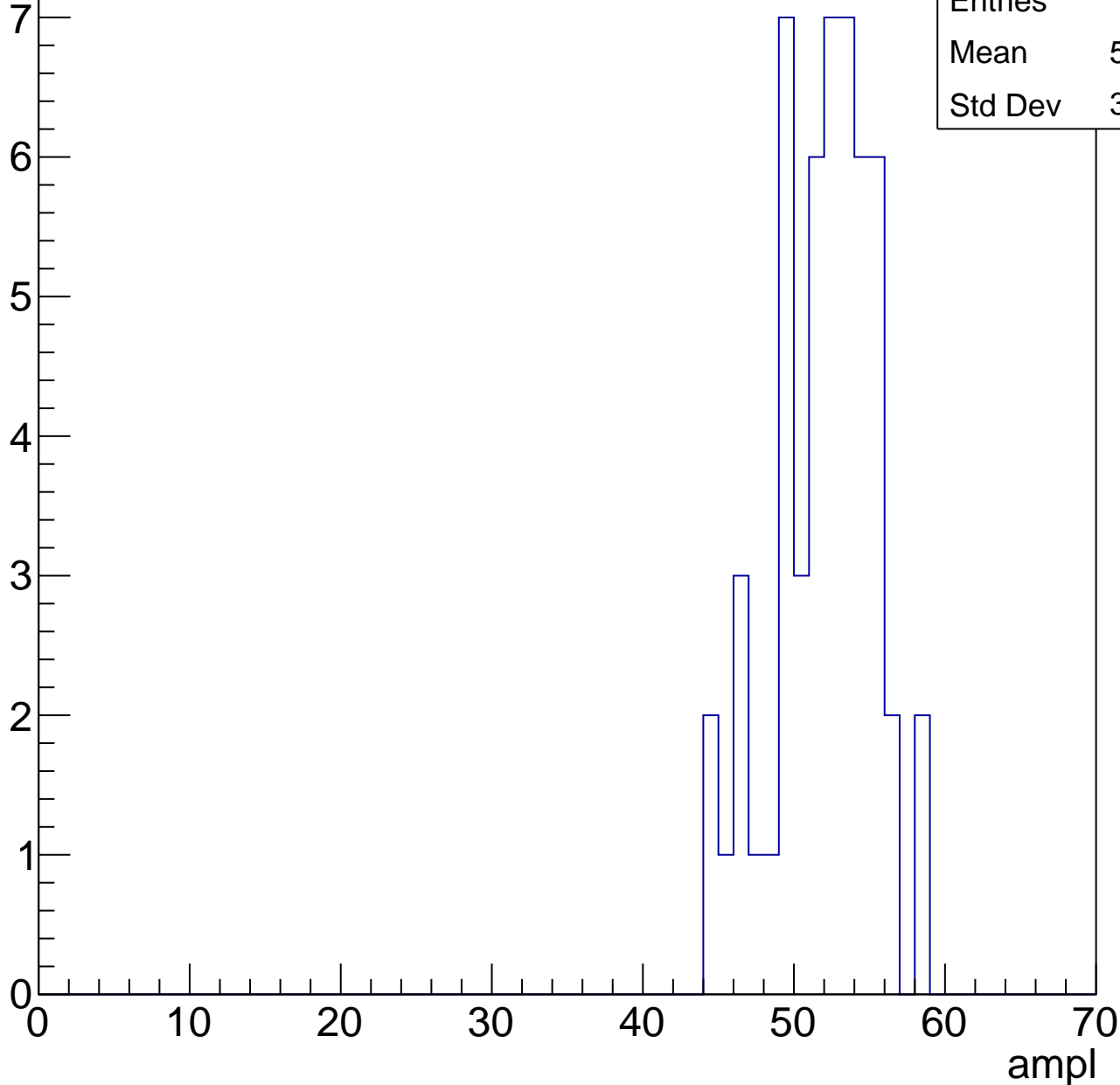
Entries	54
Mean	46.3
Std Dev	2.852



# B1L000S, U25-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

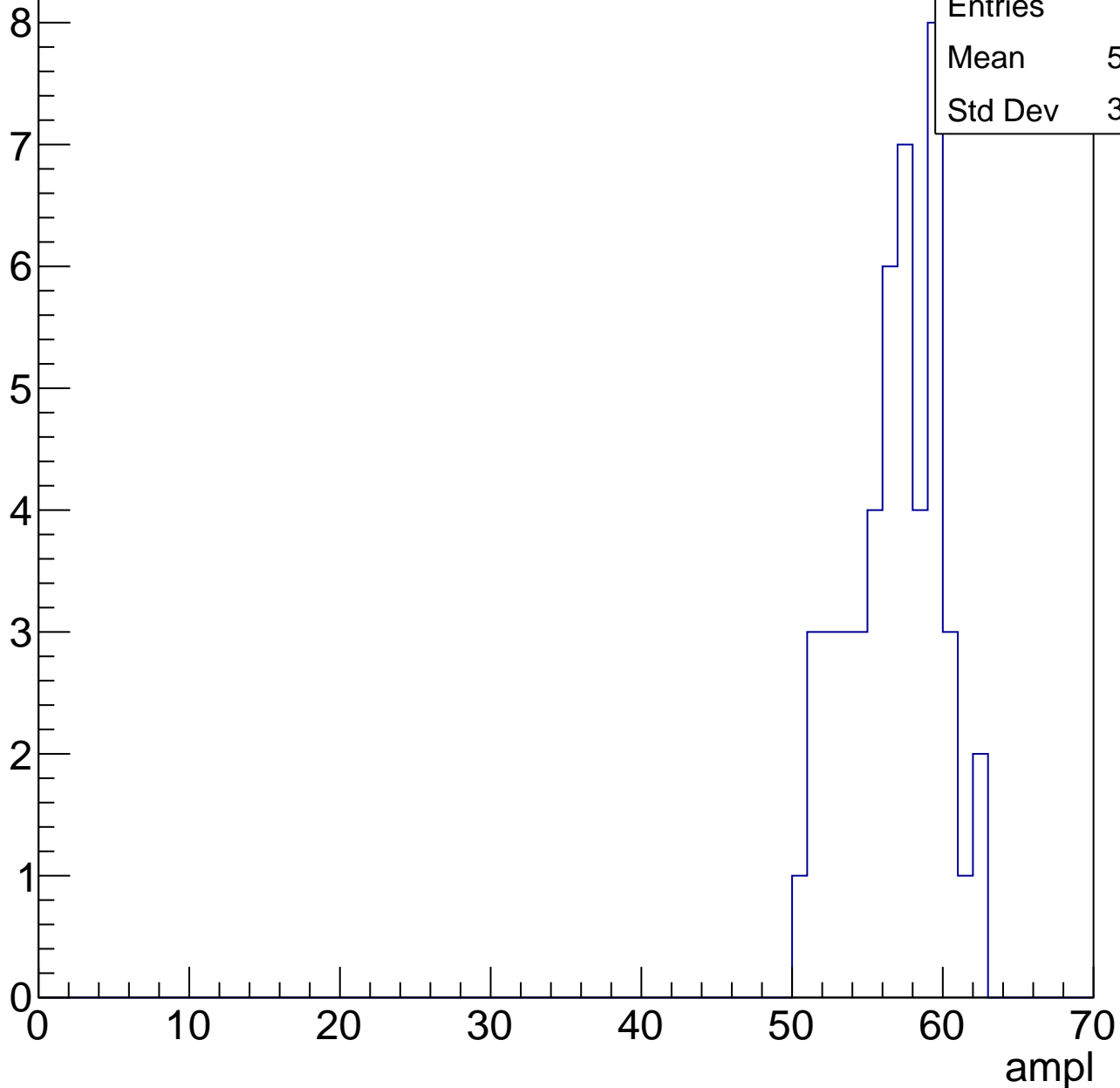


Entries	54
Mean	51.52
Std Dev	3.287

# B1L000S, U25-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

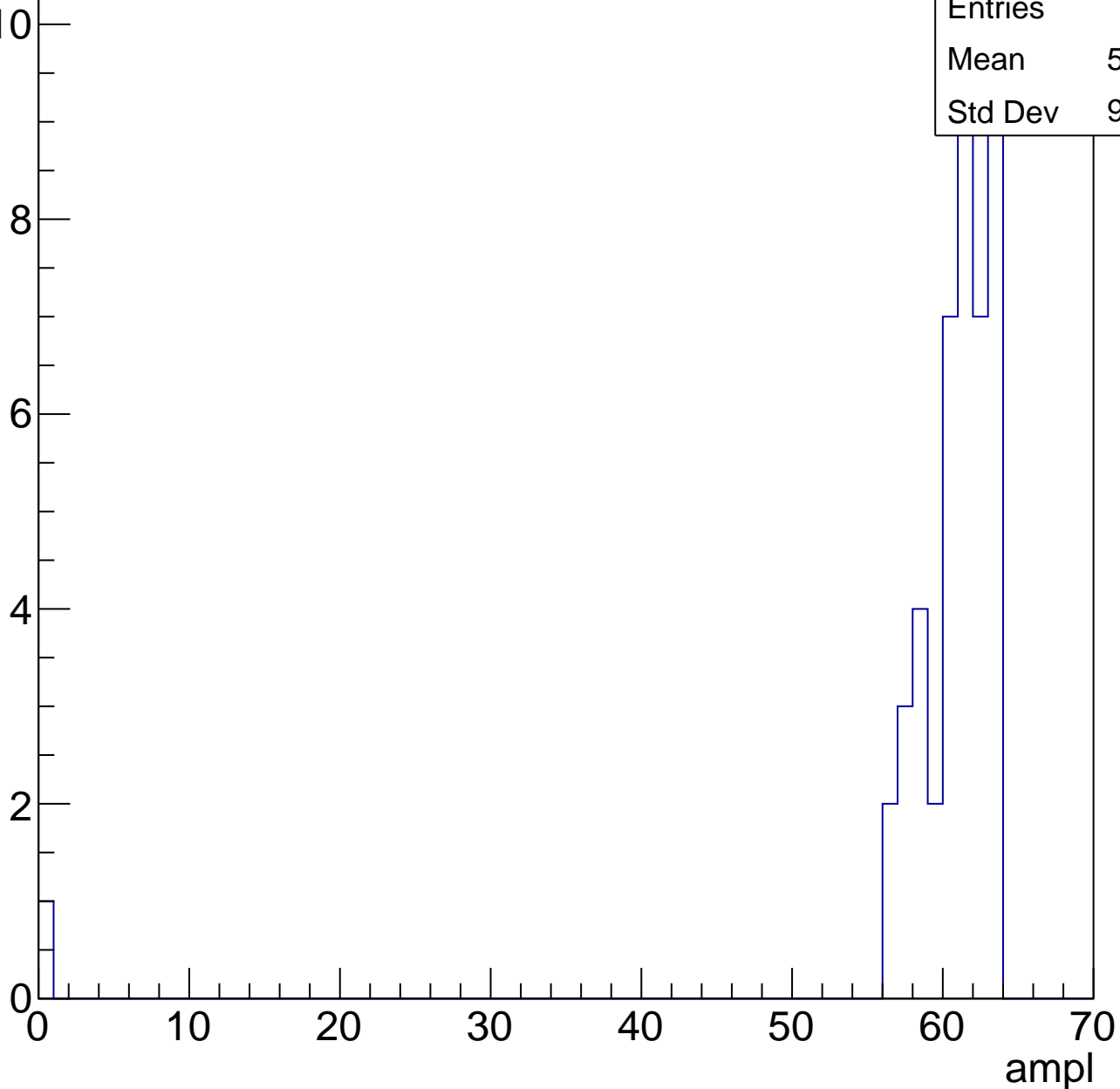


# B1L000S, U25-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	45
Mean	59.24
Std Dev	9.163





# B1L000S, U25-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch89, adc0

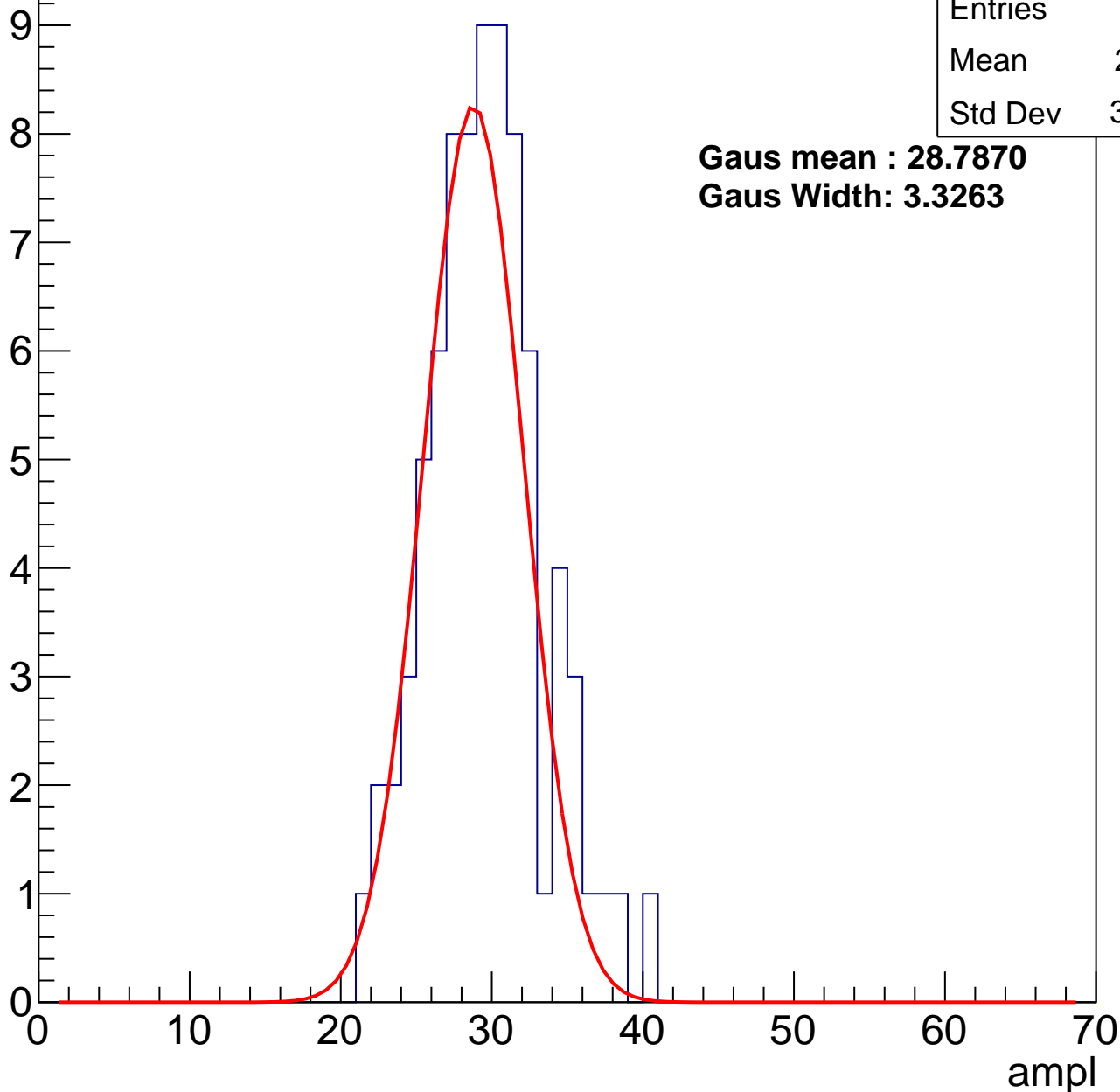
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	79
Mean	29.11
Std Dev	3.758

**Gaus mean : 28.7870**

**Gaus Width: 3.3263**



# B1L000S, U25-ch89, adc1

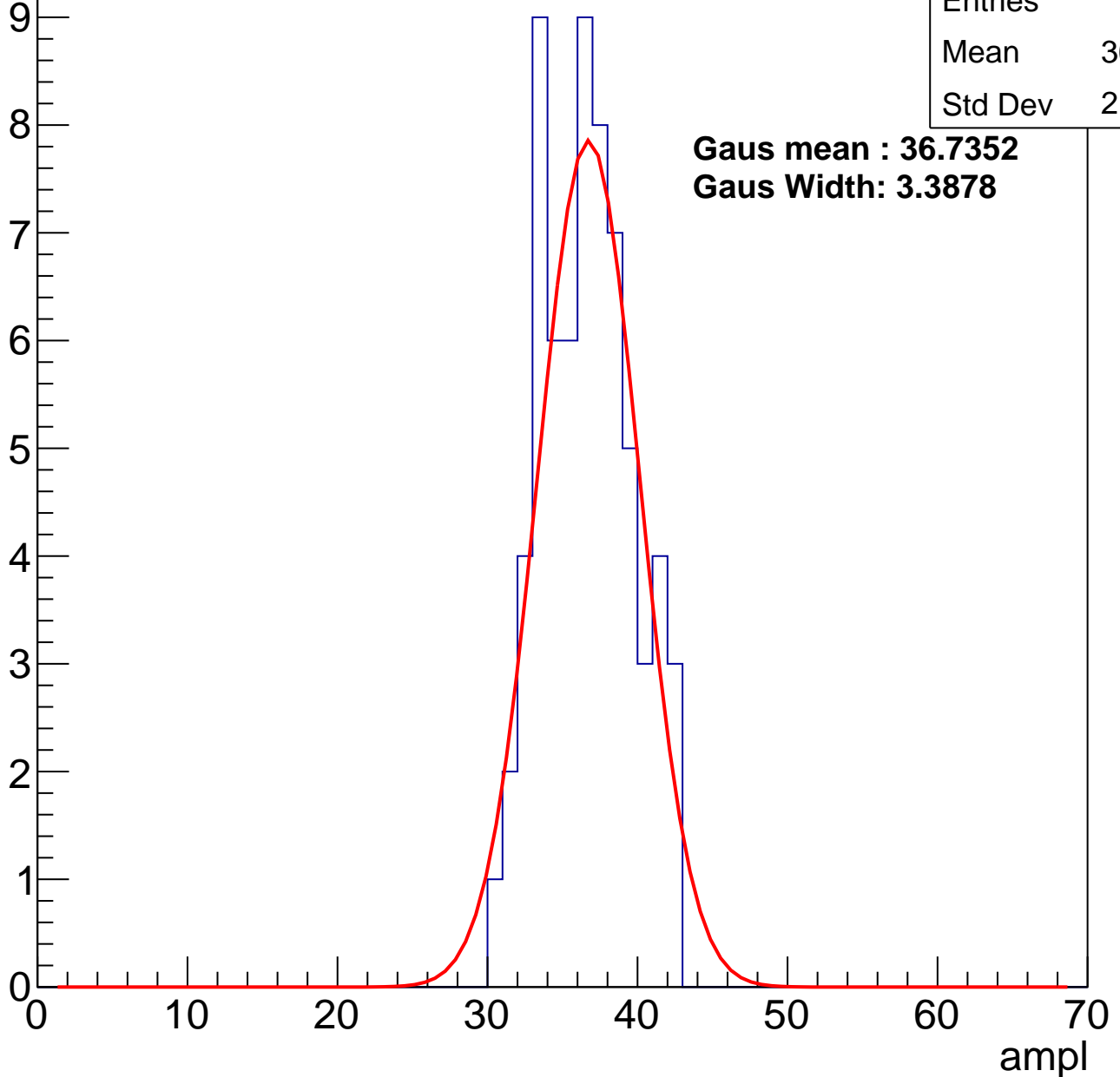
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	36.15
Std Dev	2.984

**Gaus mean : 36.7352**

**Gaus Width: 3.3878**



# B1L000S, U25-ch89, adc2

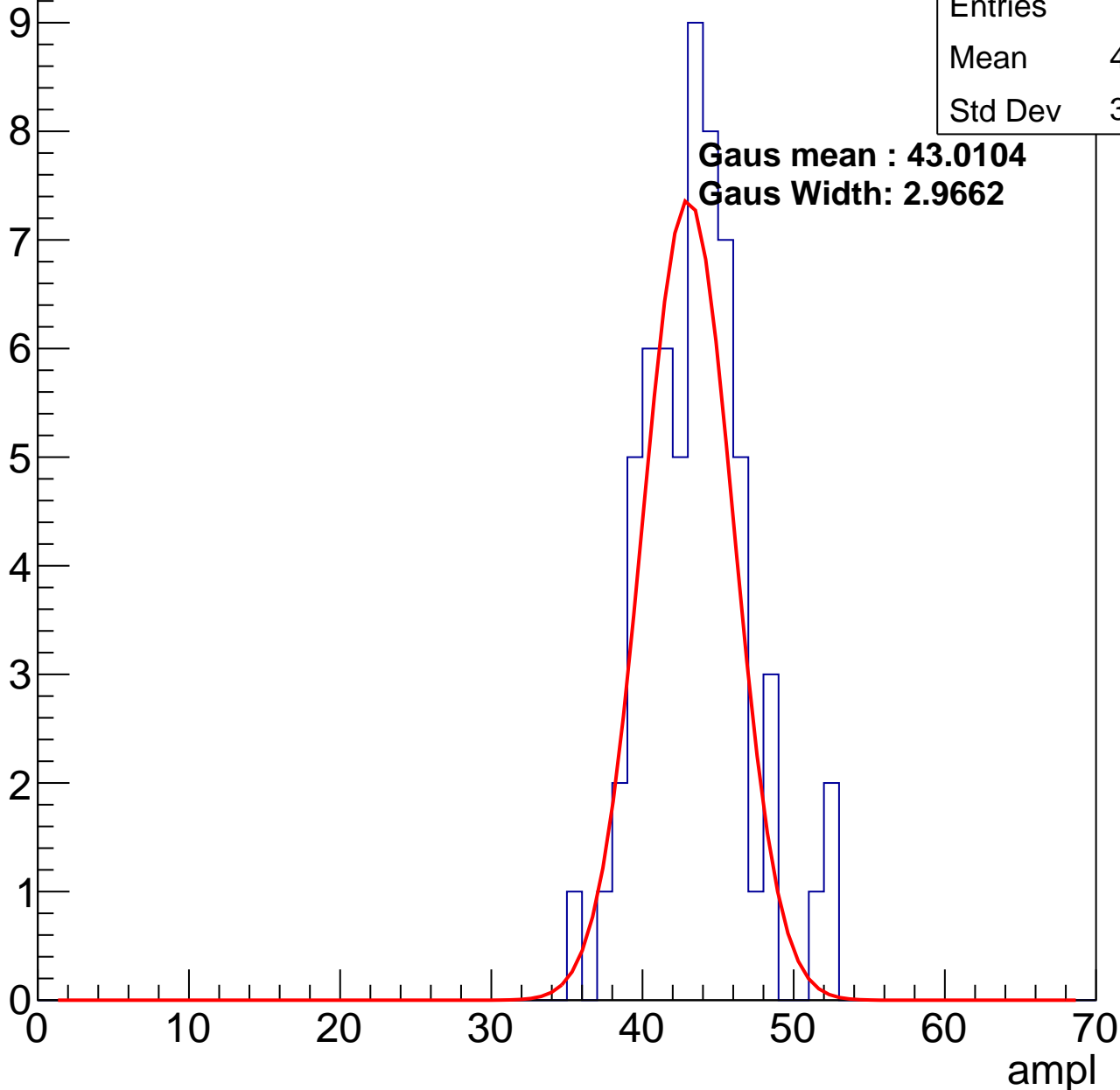
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	43.05
Std Dev	3.386

**Gaus mean : 43.0104**

**Gaus Width: 2.9662**

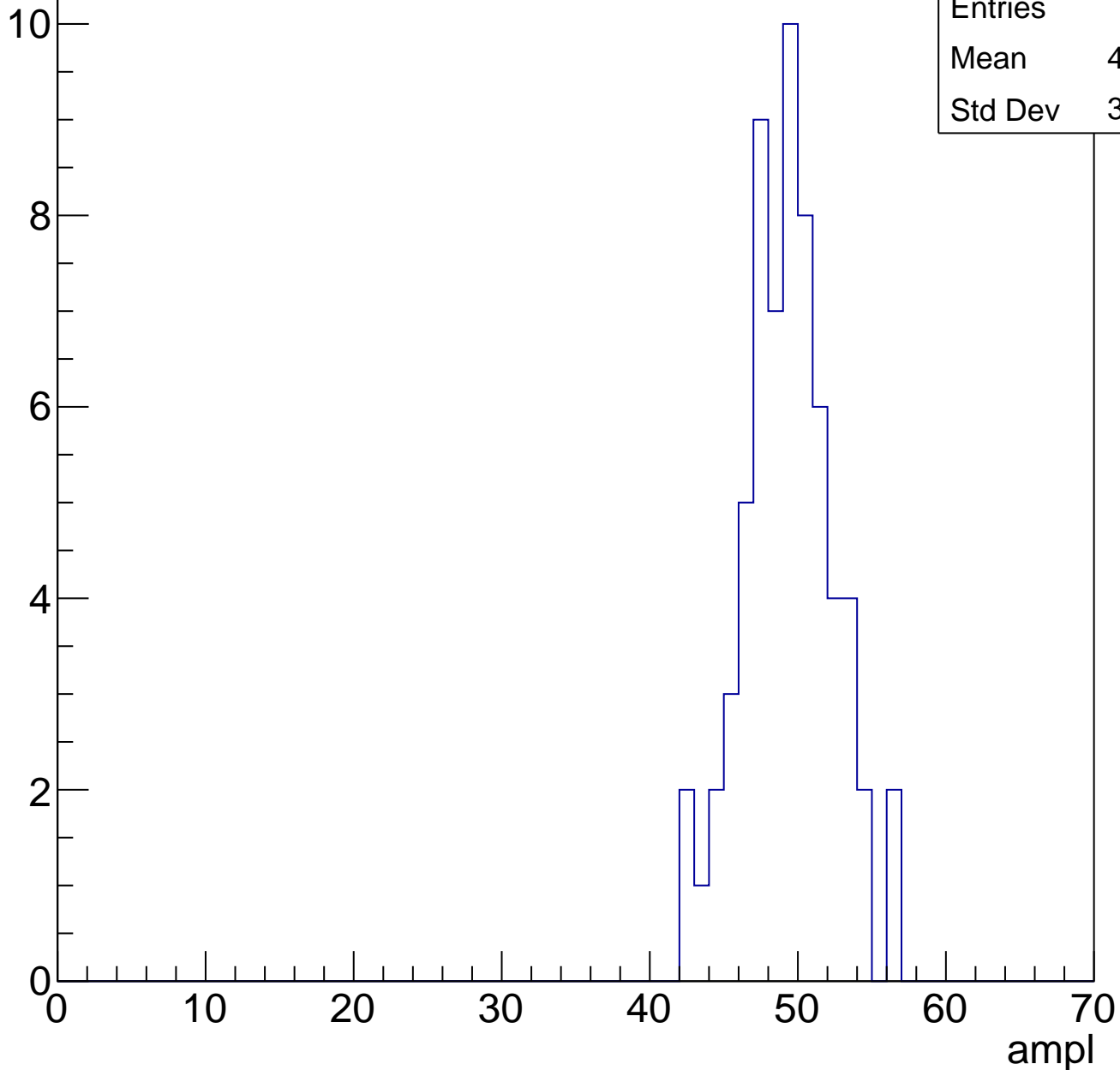


# B1L000S, U25-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	48.85
Std Dev	3.034

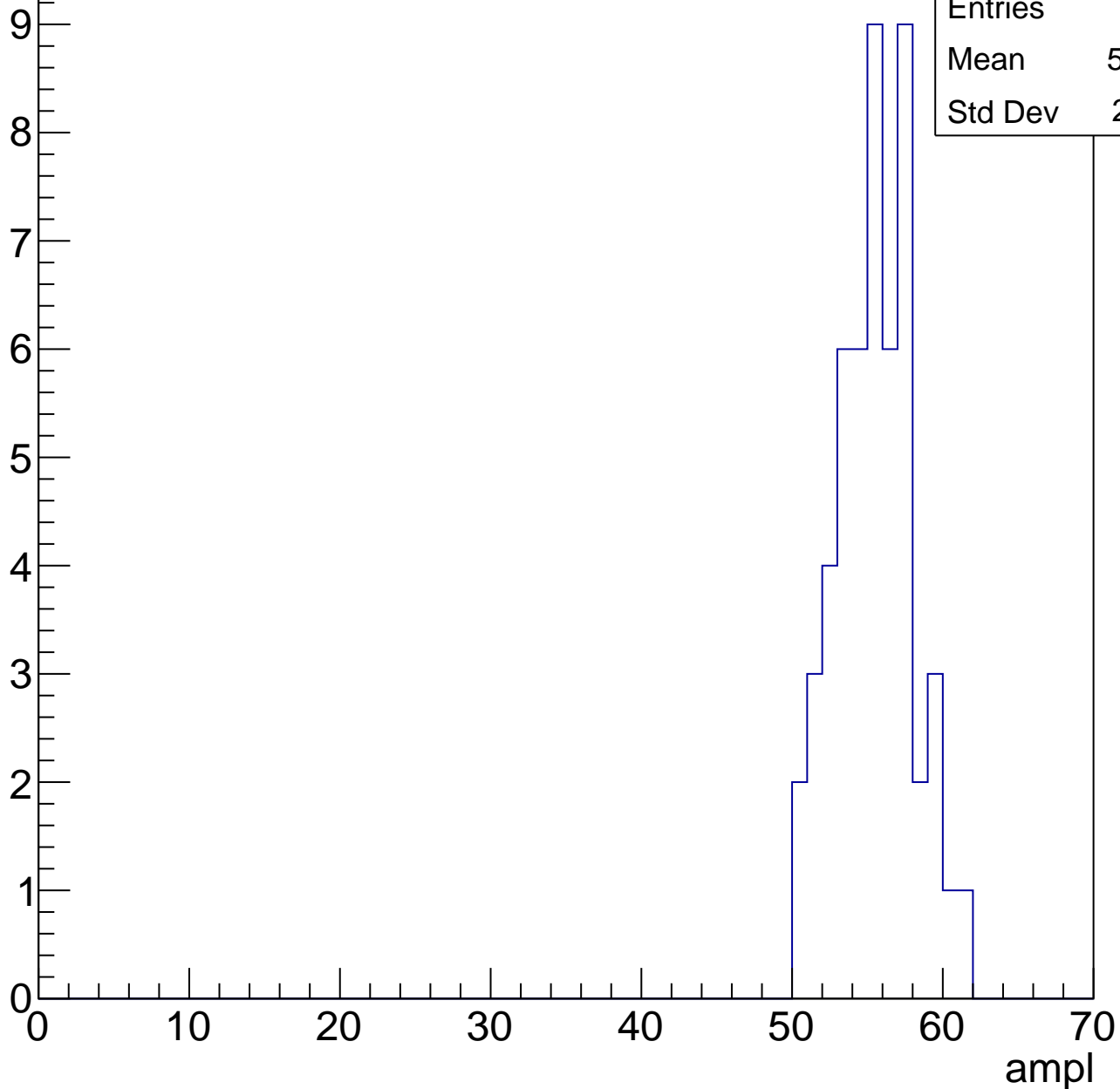
Entry



# B1L000S, U25-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

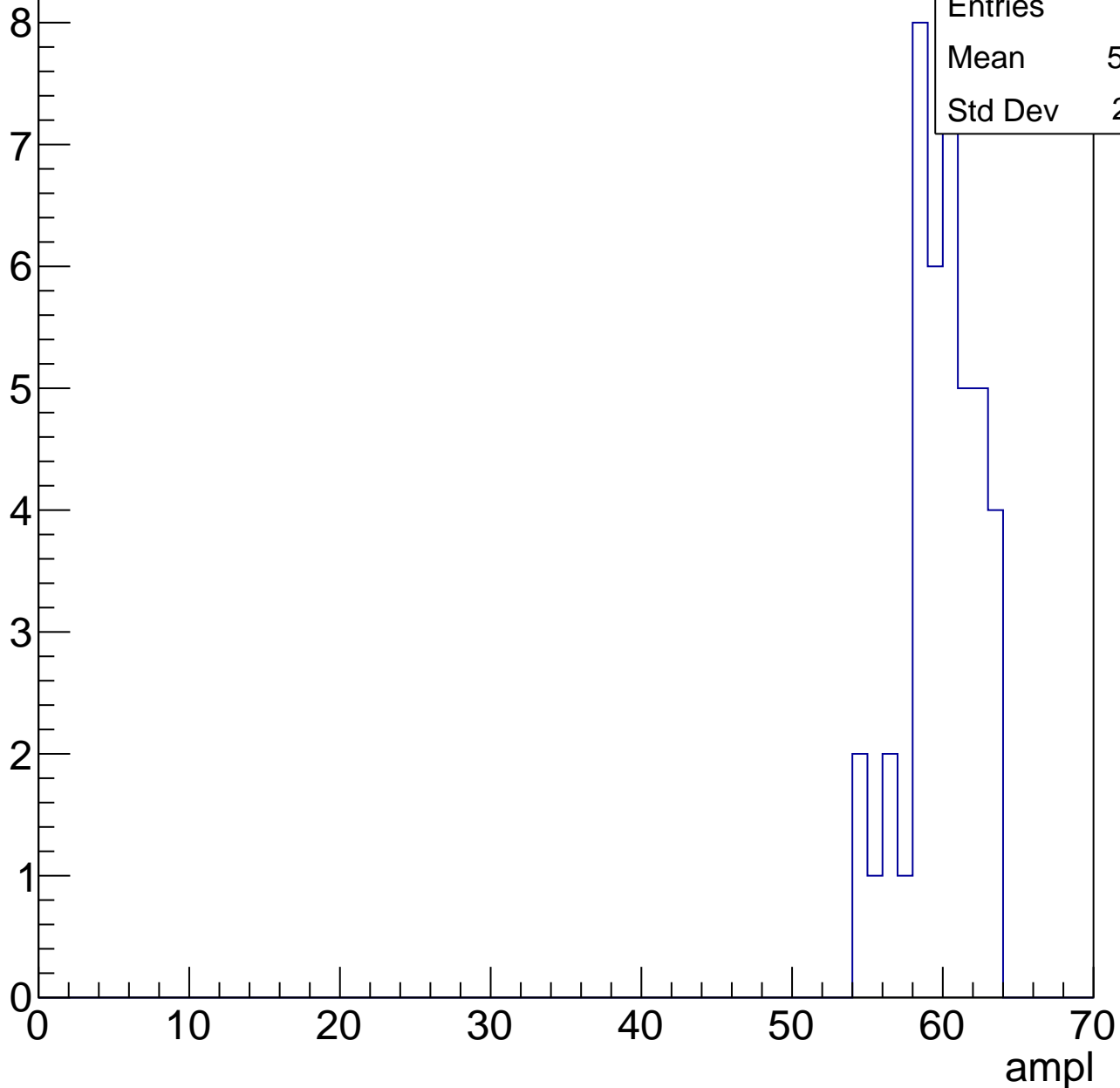


Entries	52
Mean	55.02
Std Dev	2.531

# B1L000S, U25-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

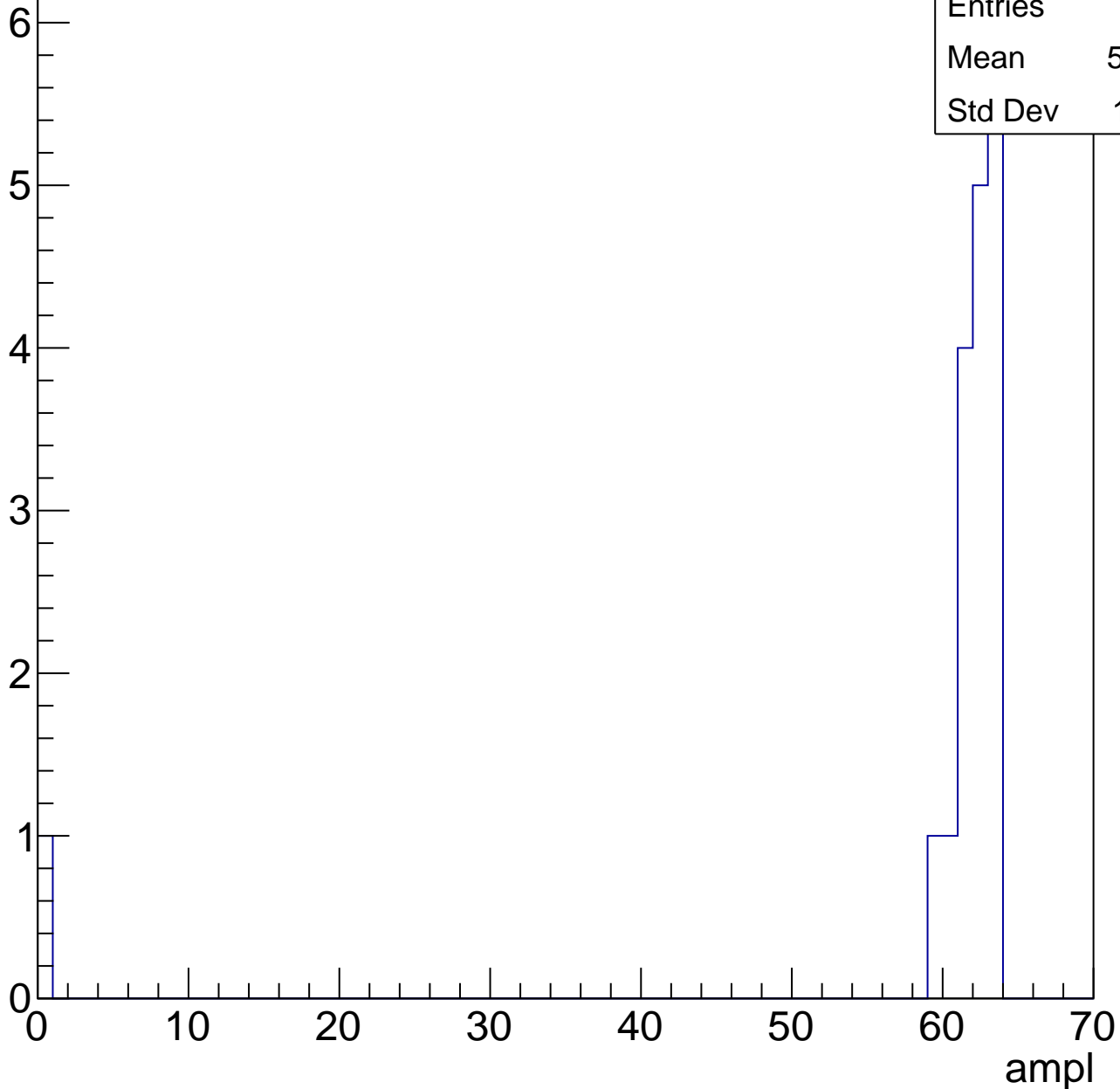


# B1L000S, U25-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	58.39
Std Dev	14.21





# B1L000S, U25-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch90, adc0

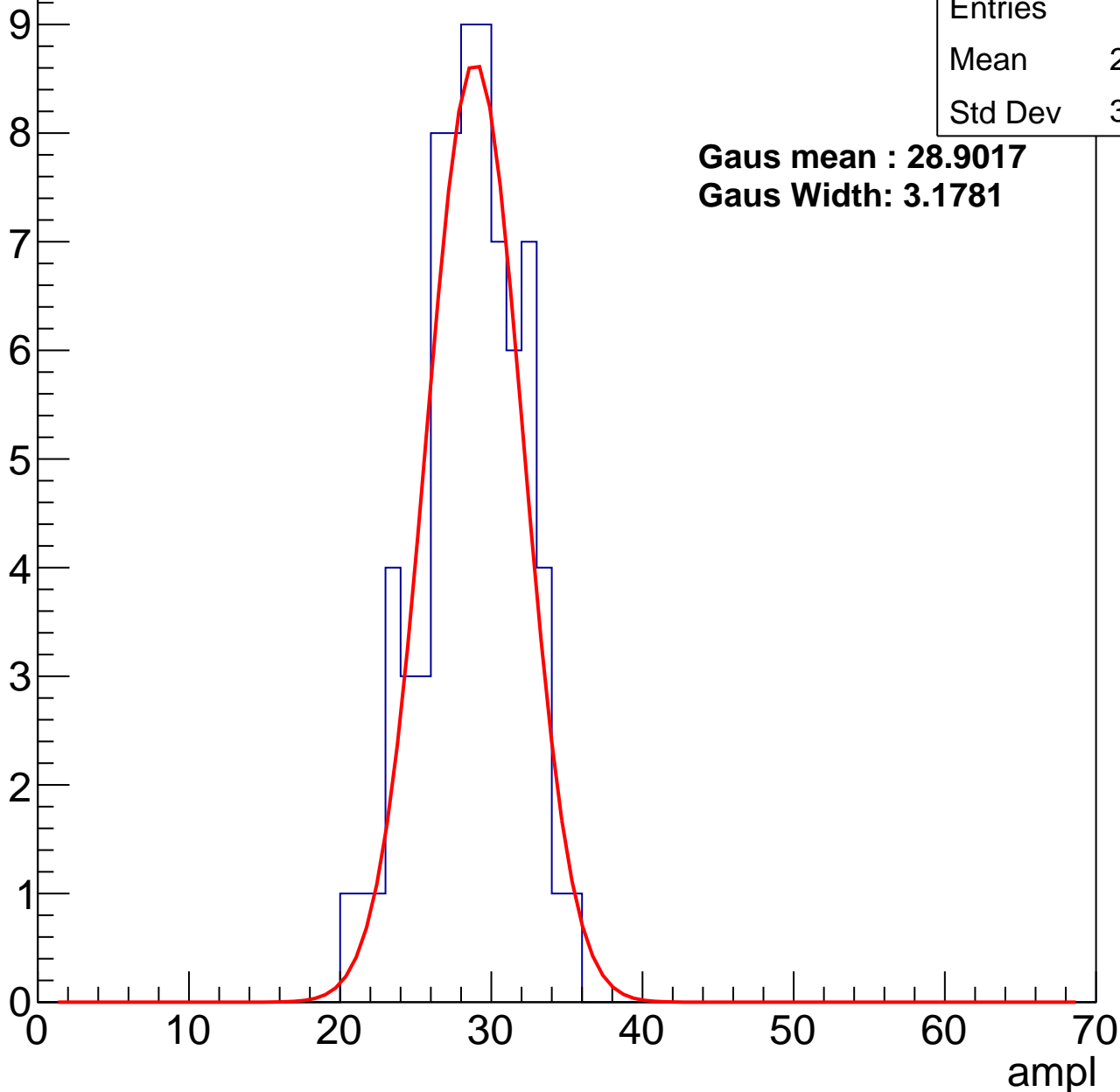
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.22
Std Dev	3.202

**Gaus mean : 28.9017**

**Gaus Width: 3.1781**



# B1L000S, U25-ch90, adc1

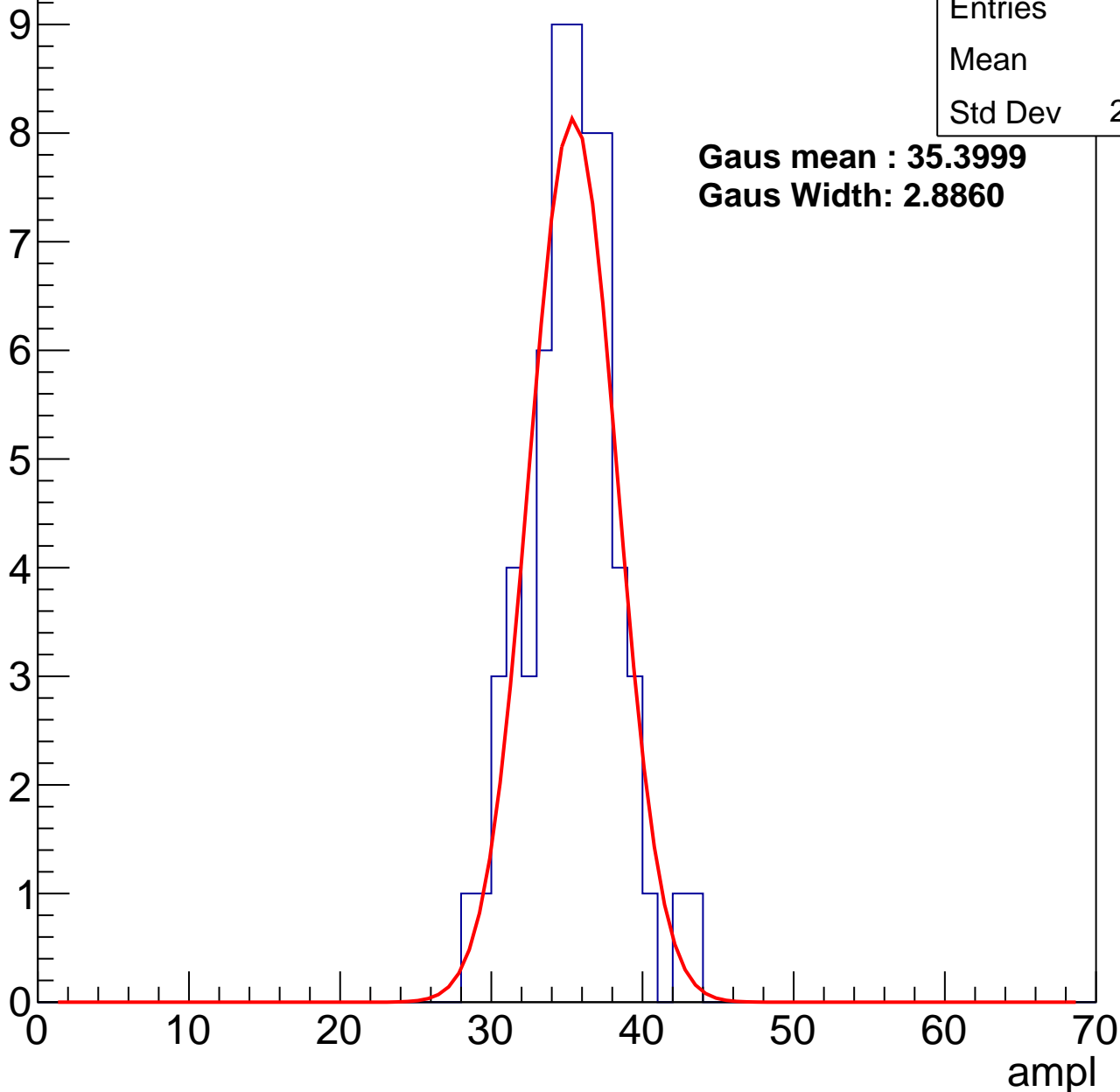
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	34.9
Std Dev	2.966

**Gaus mean : 35.3999**

**Gaus Width: 2.8860**



# B1L000S, U25-ch90, adc2

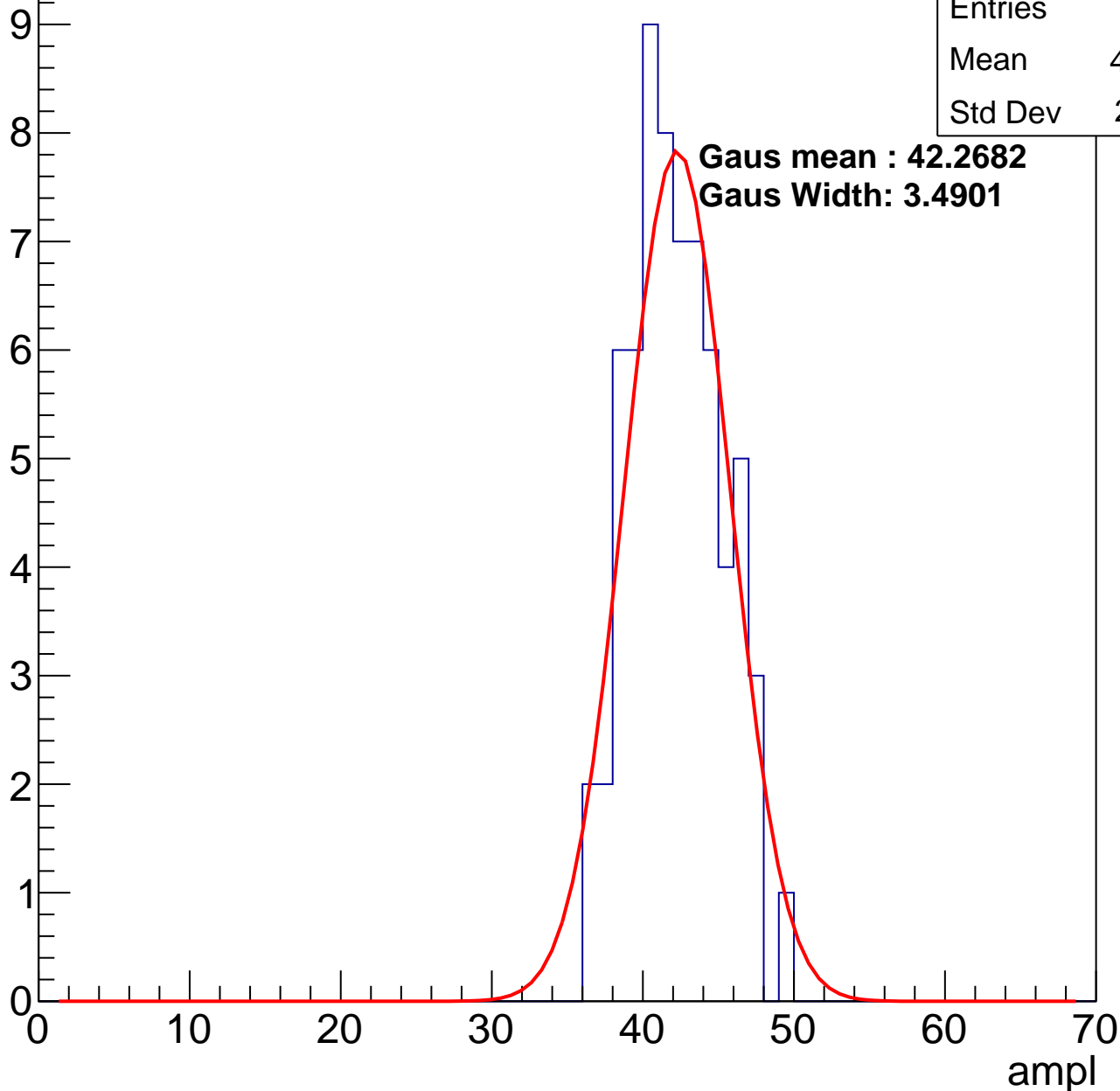
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	41.74
Std Dev	2.971

**Gaus mean : 42.2682**

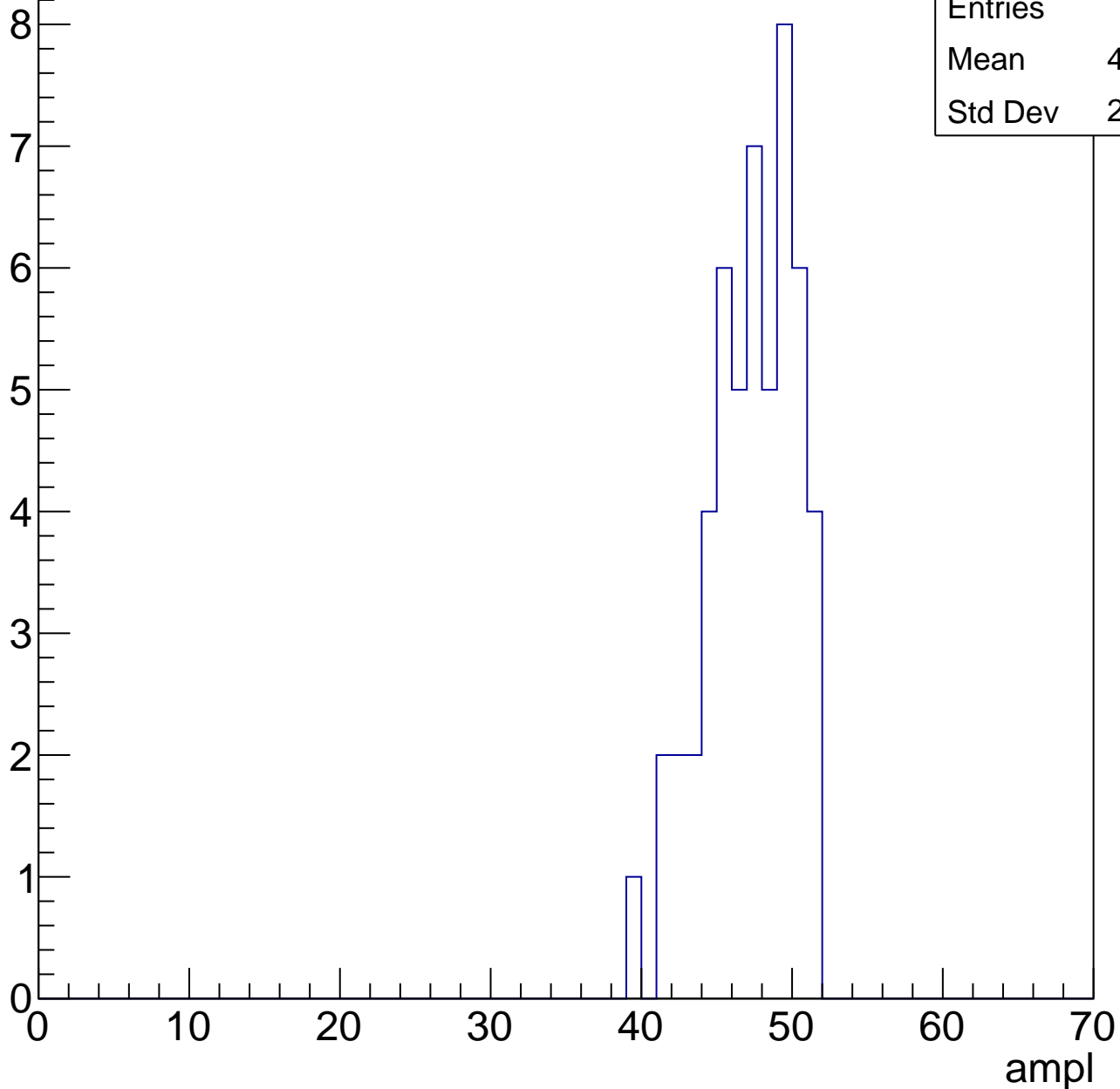
**Gaus Width: 3.4901**



# B1L000S, U25-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



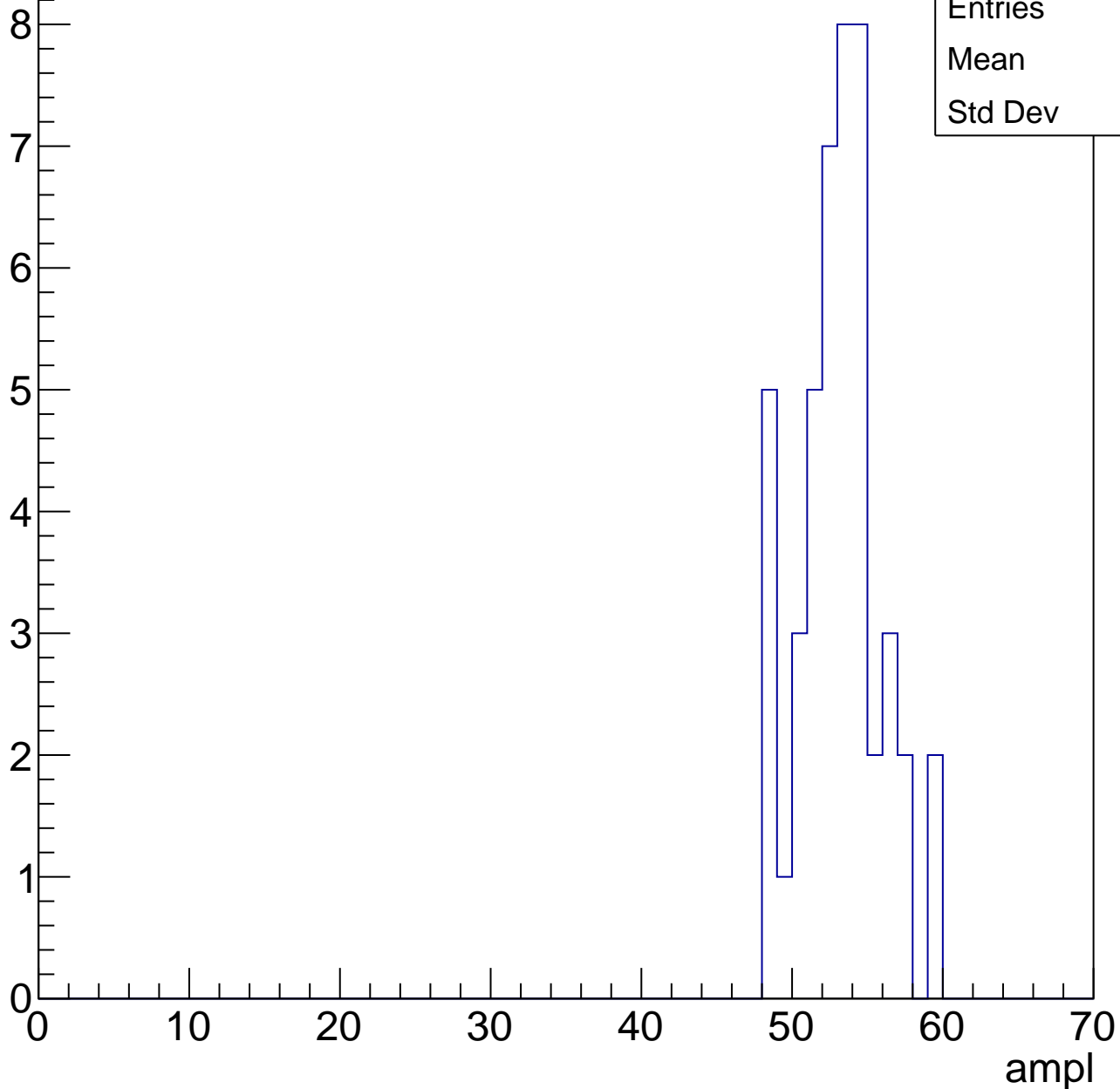
Entries	52
Mean	46.77
Std Dev	2.893

# B1L000S, U25-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	46
Mean	52.7
Std Dev	2.71

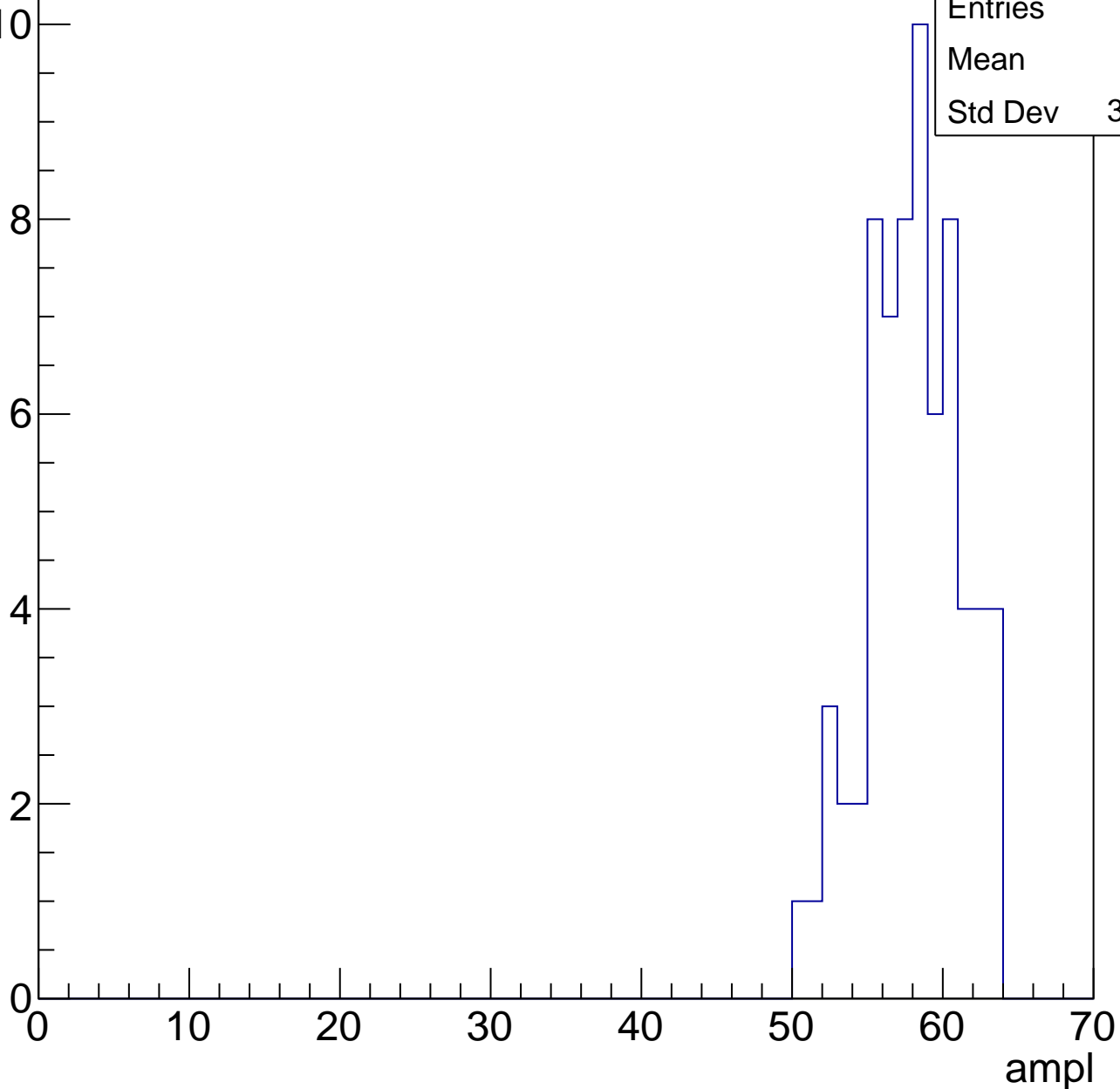


# B1L000S, U25-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	57.6
Std Dev	3.064

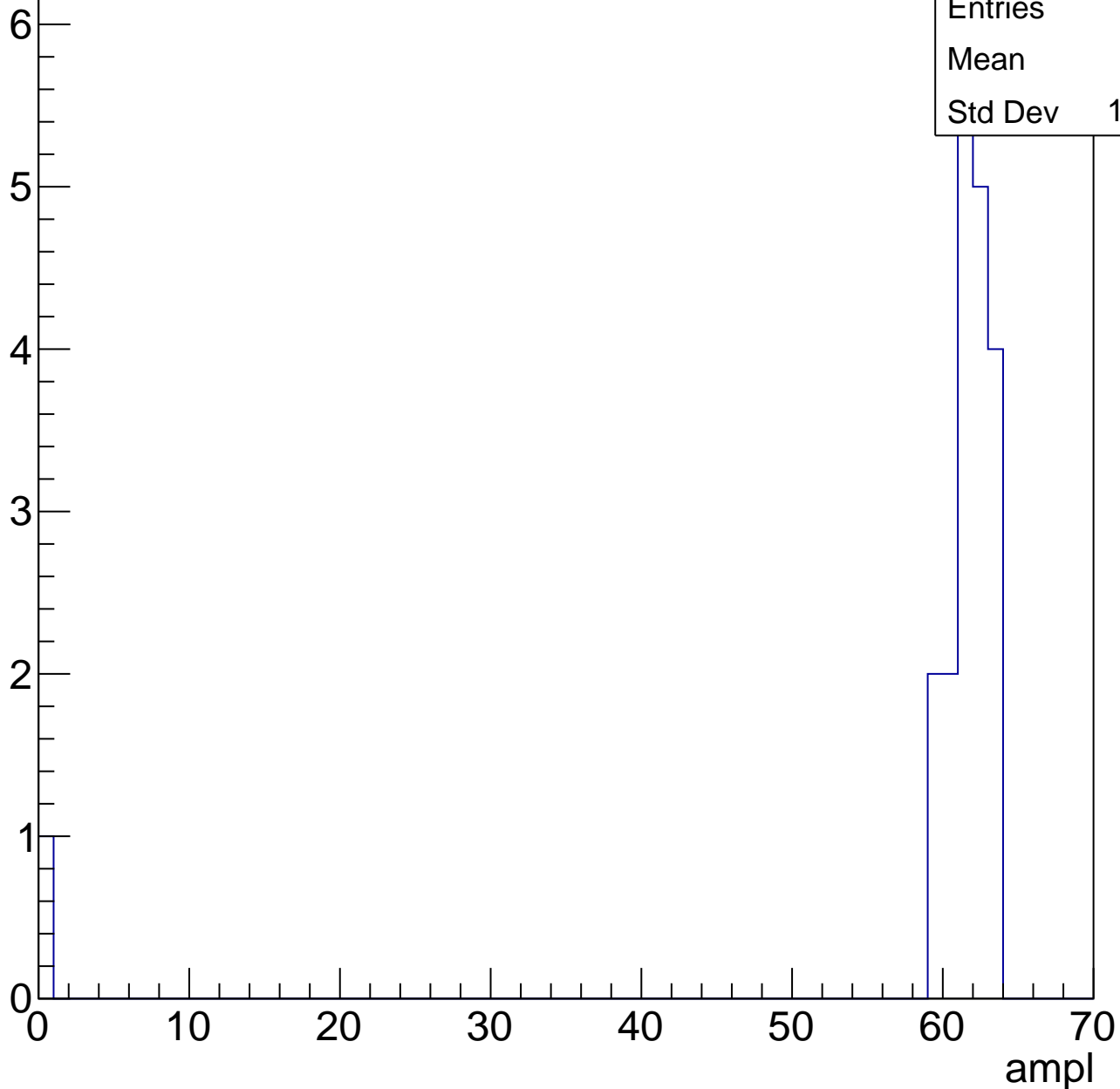


# B1L000S, U25-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.3
Std Dev	13.43

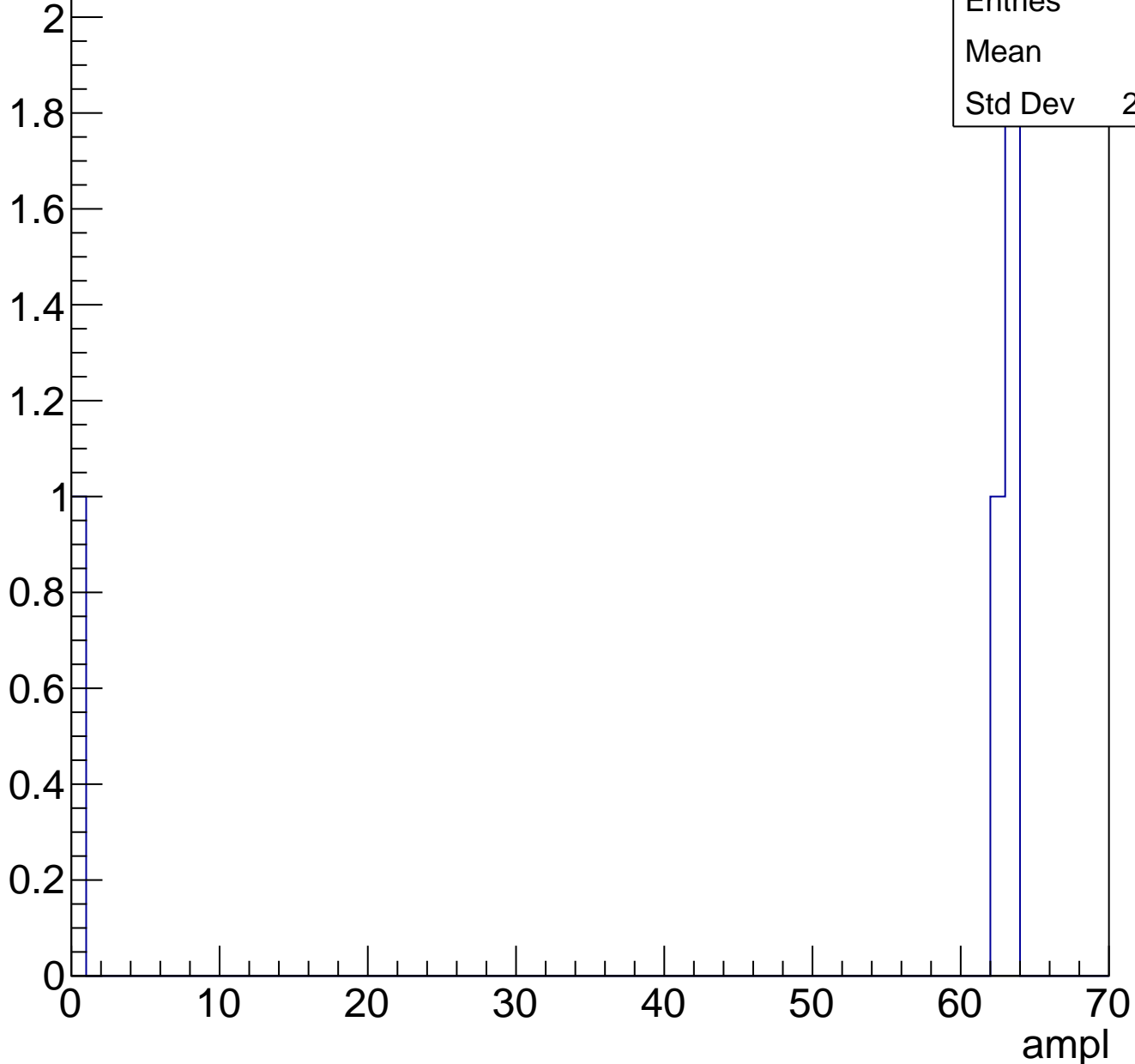




# B1L000S, U25-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch91, adc0

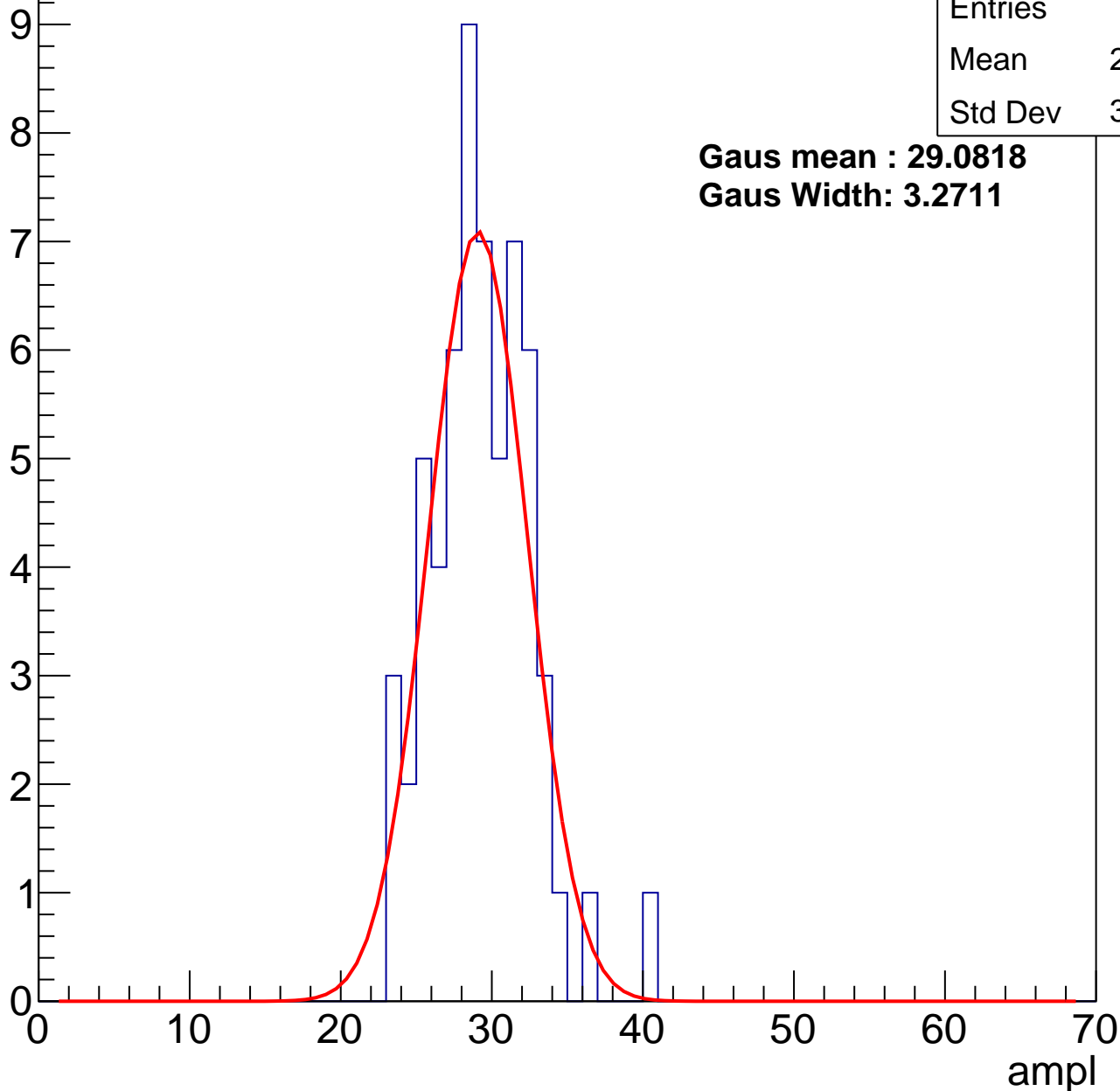
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	28.85
Std Dev	3.255

**Gaus mean : 29.0818**

**Gaus Width: 3.2711**



# B1L000S, U25-ch91, adc1

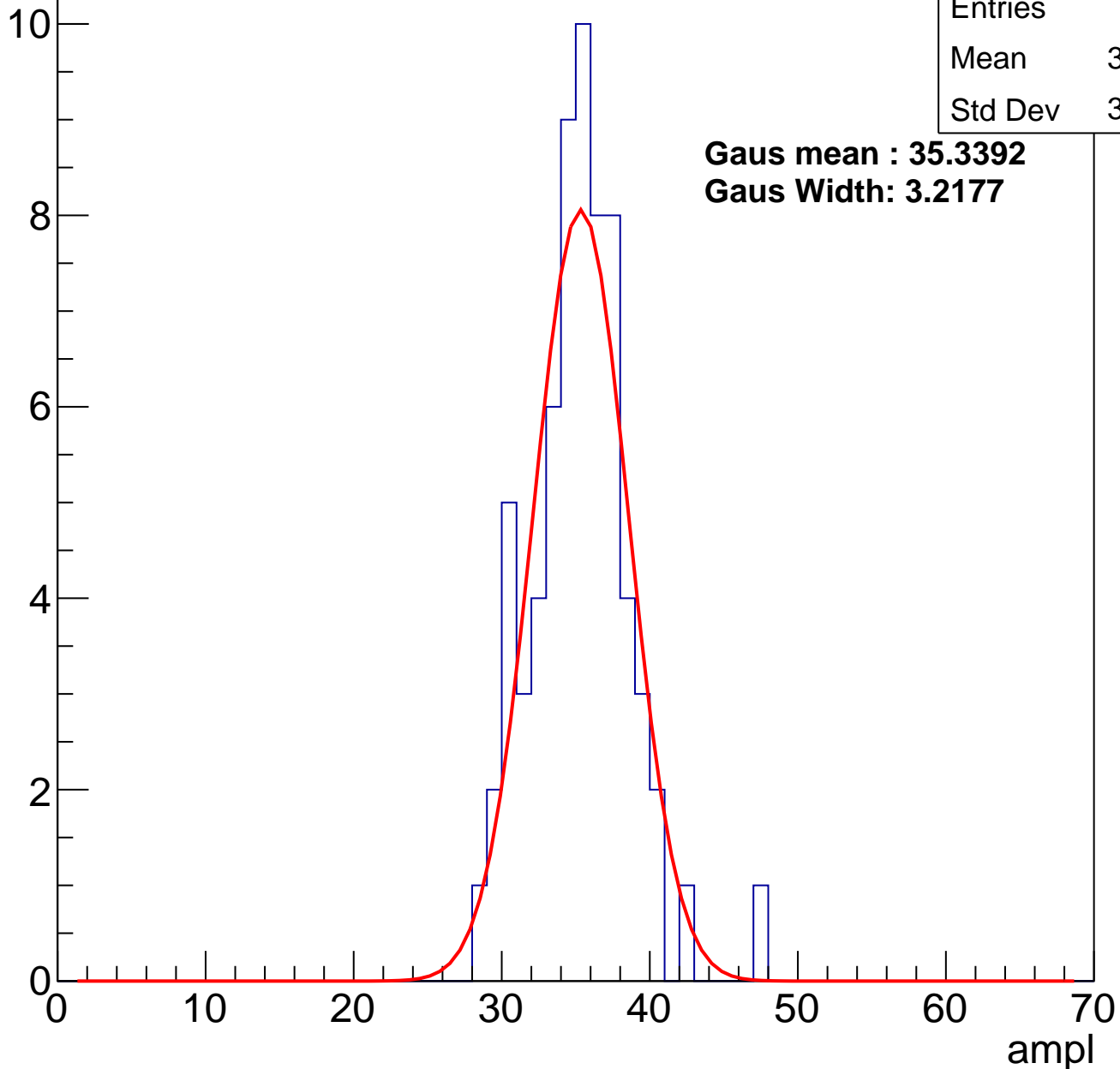
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	34.82
Std Dev	3.296

**Gaus mean : 35.3392**

**Gaus Width: 3.2177**

Entry



# B1L000S, U25-ch91, adc2

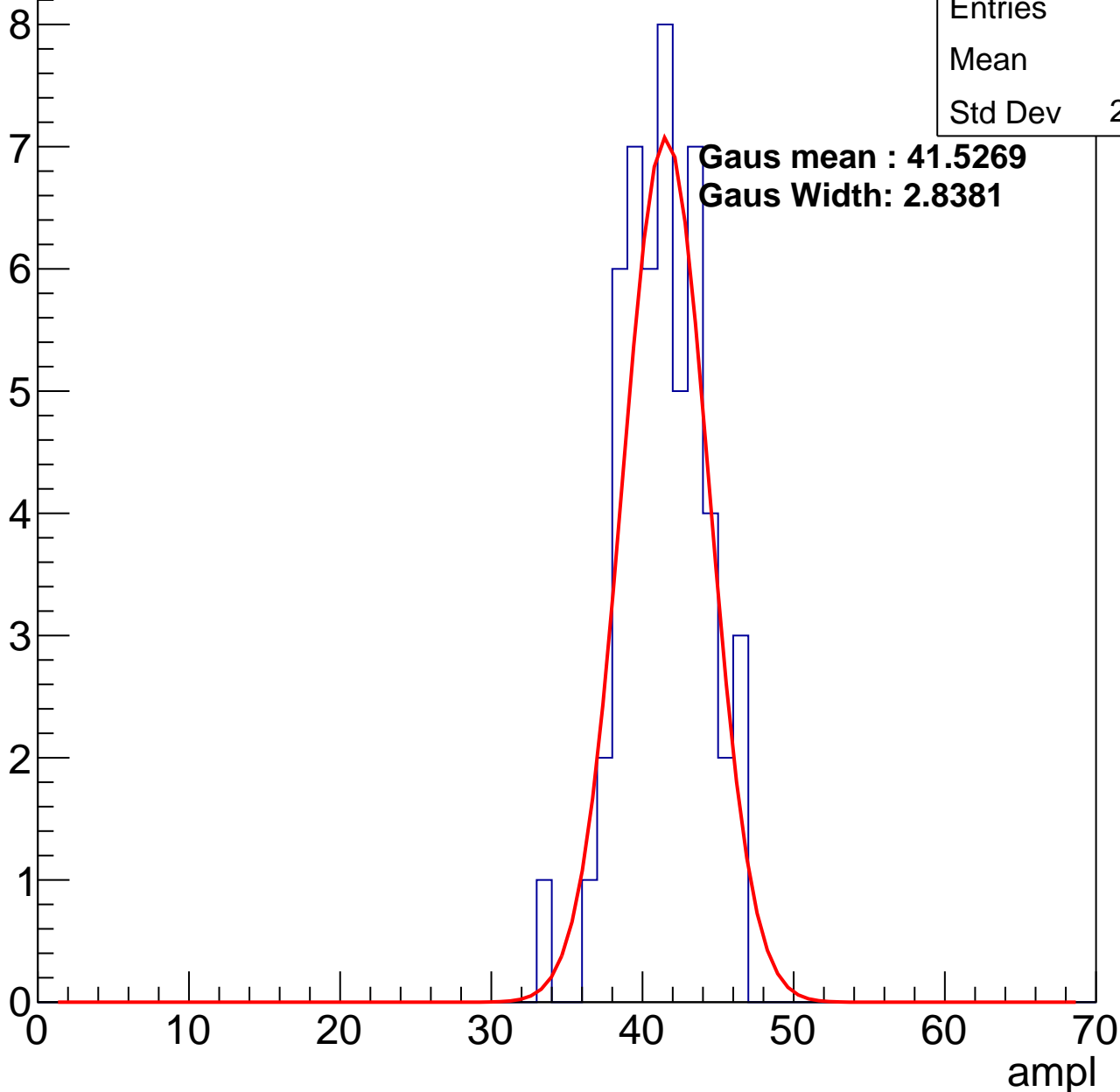
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	40.9
Std Dev	2.719

**Gaus mean : 41.5269**

**Gaus Width: 2.8381**



# B1L000S, U25-ch91, adc3

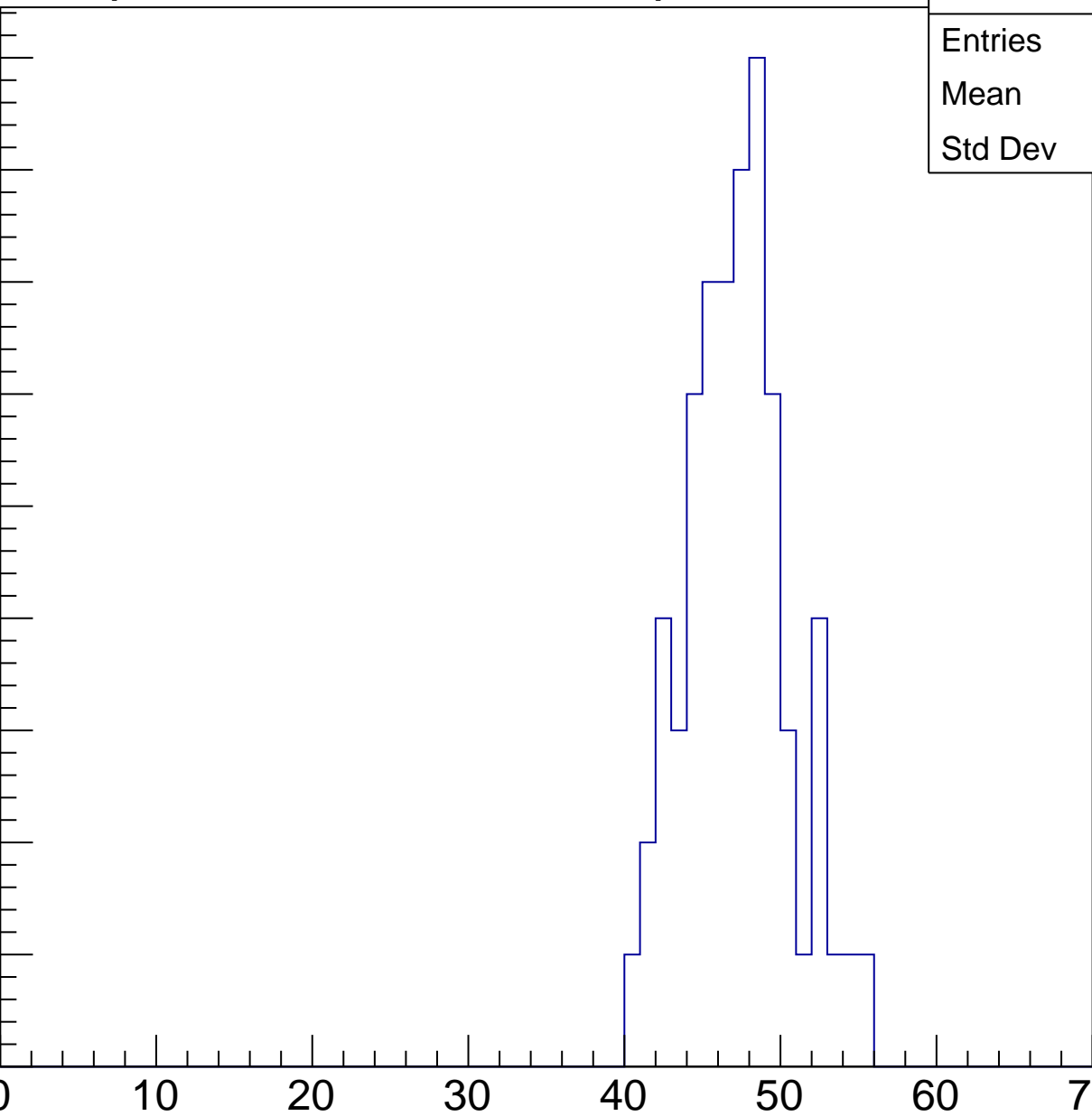
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	64
Mean	46.77
Std Dev	3.258

ampl

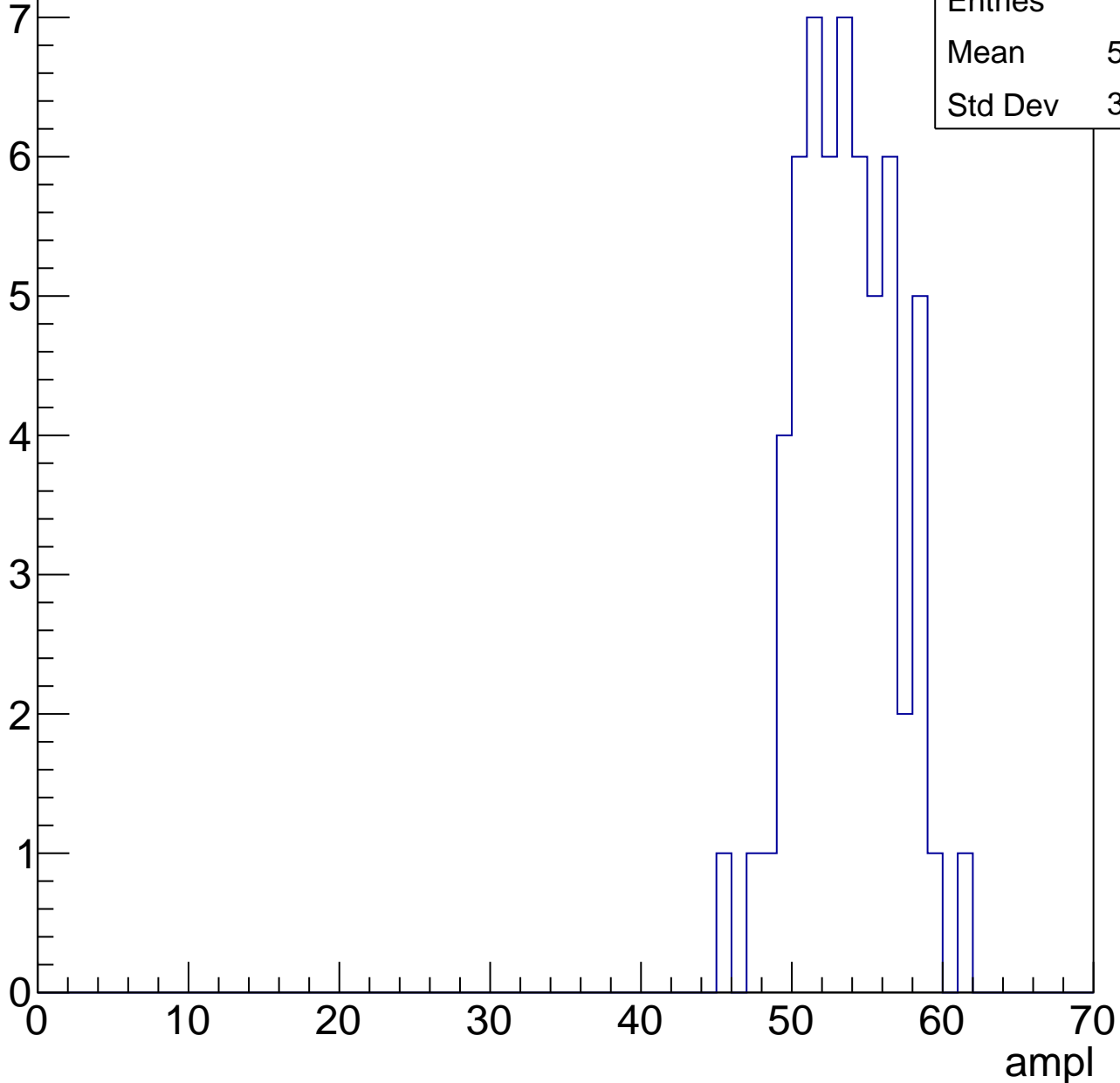


# B1L000S, U25-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.14
Std Dev	3.223

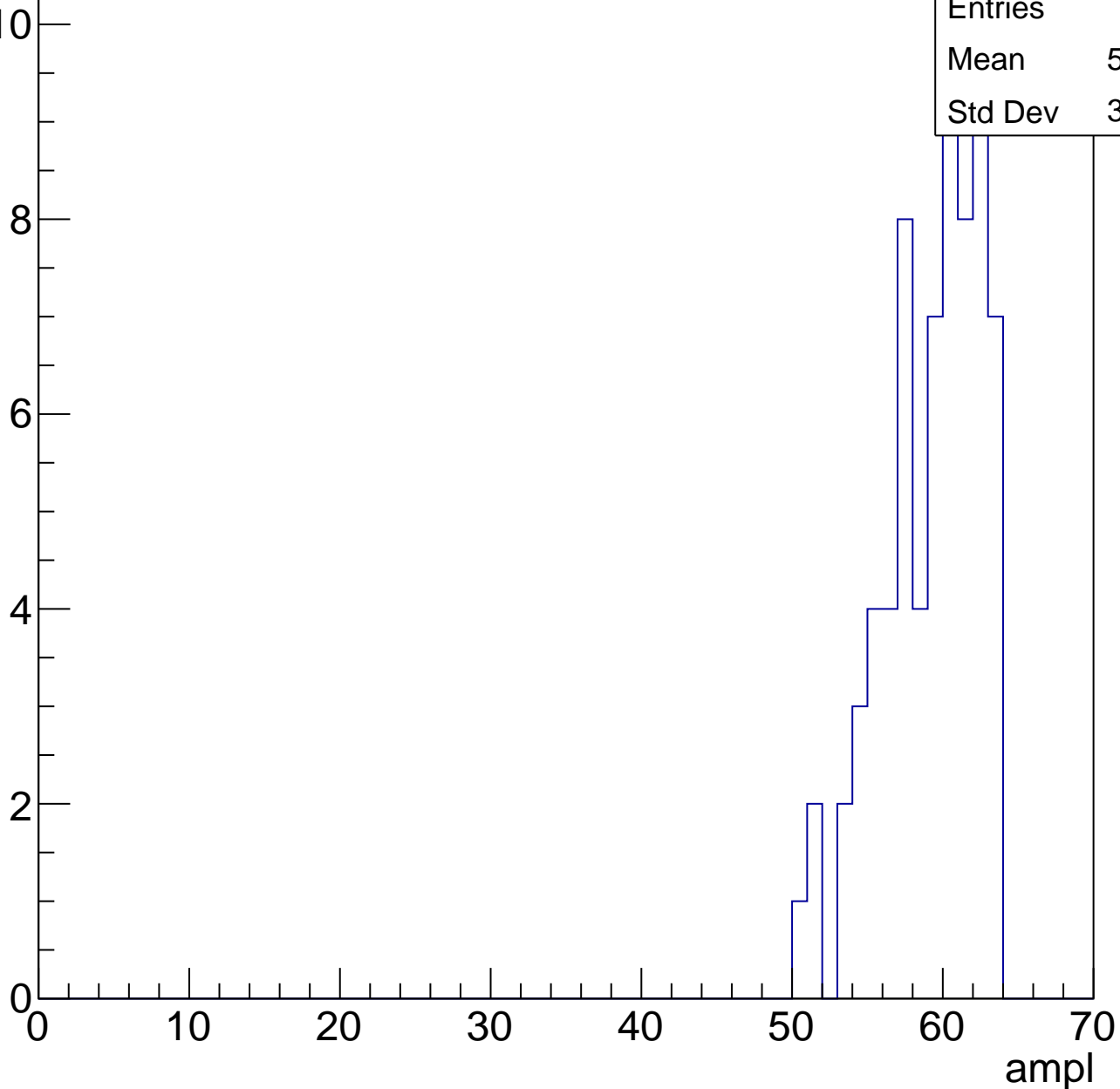


# B1L000S, U25-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

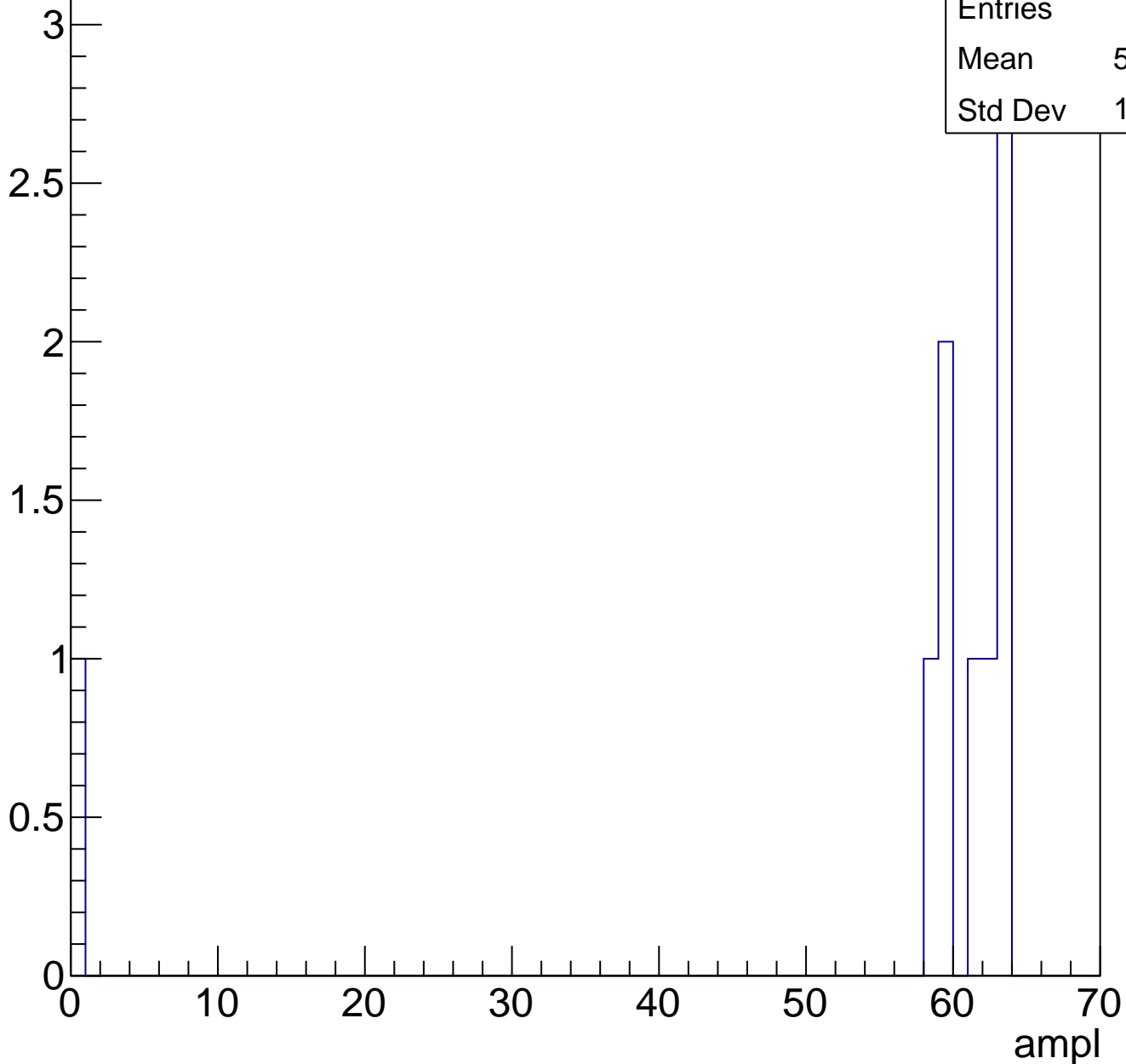
Entries	69
Mean	58.72
Std Dev	3.234



# B1L000S, U25-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch92, adc0

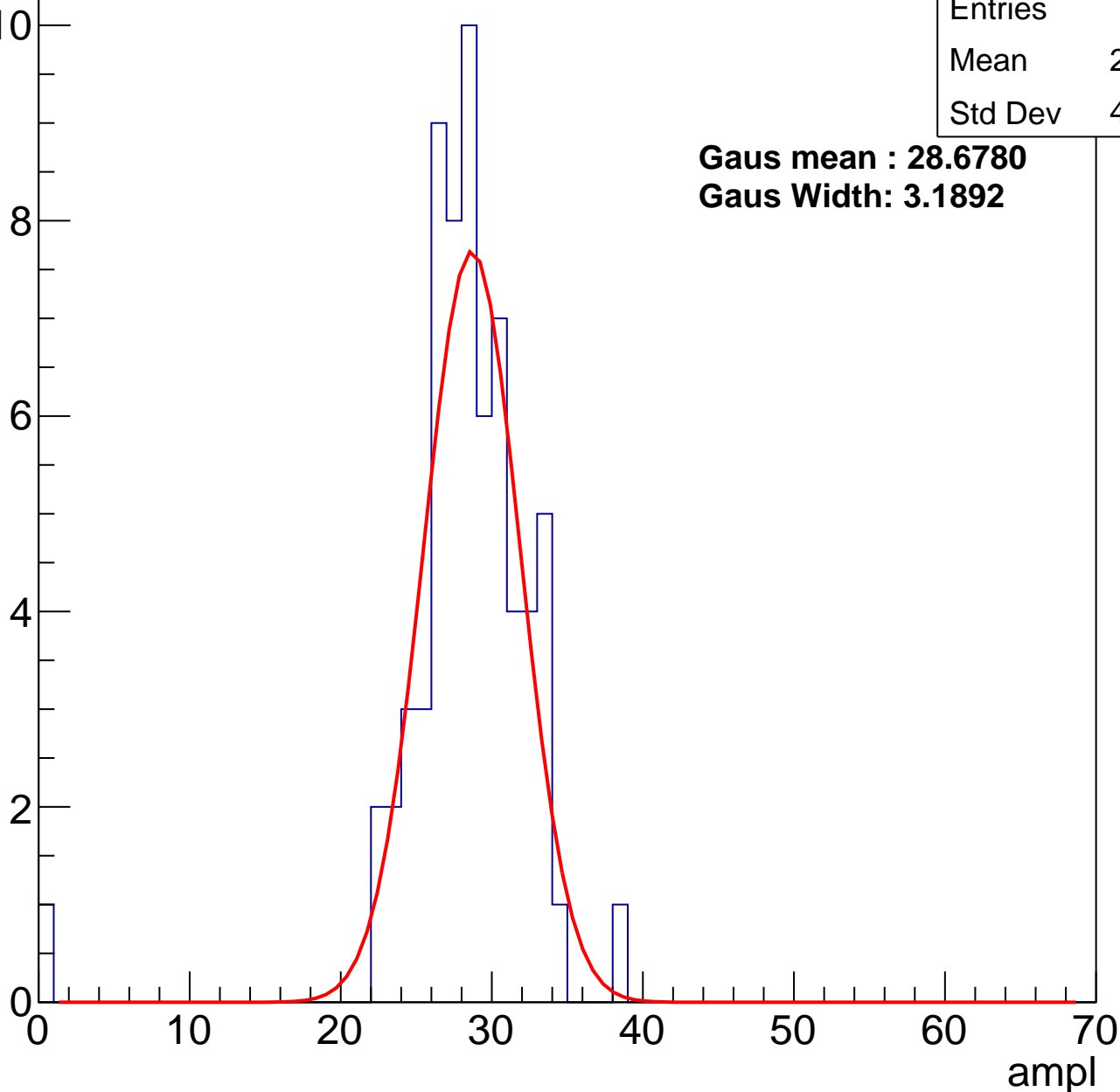
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	27.88
Std Dev	4.637

**Gaus mean : 28.6780**

**Gaus Width: 3.1892**



# B1L000S, U25-ch92, adc1

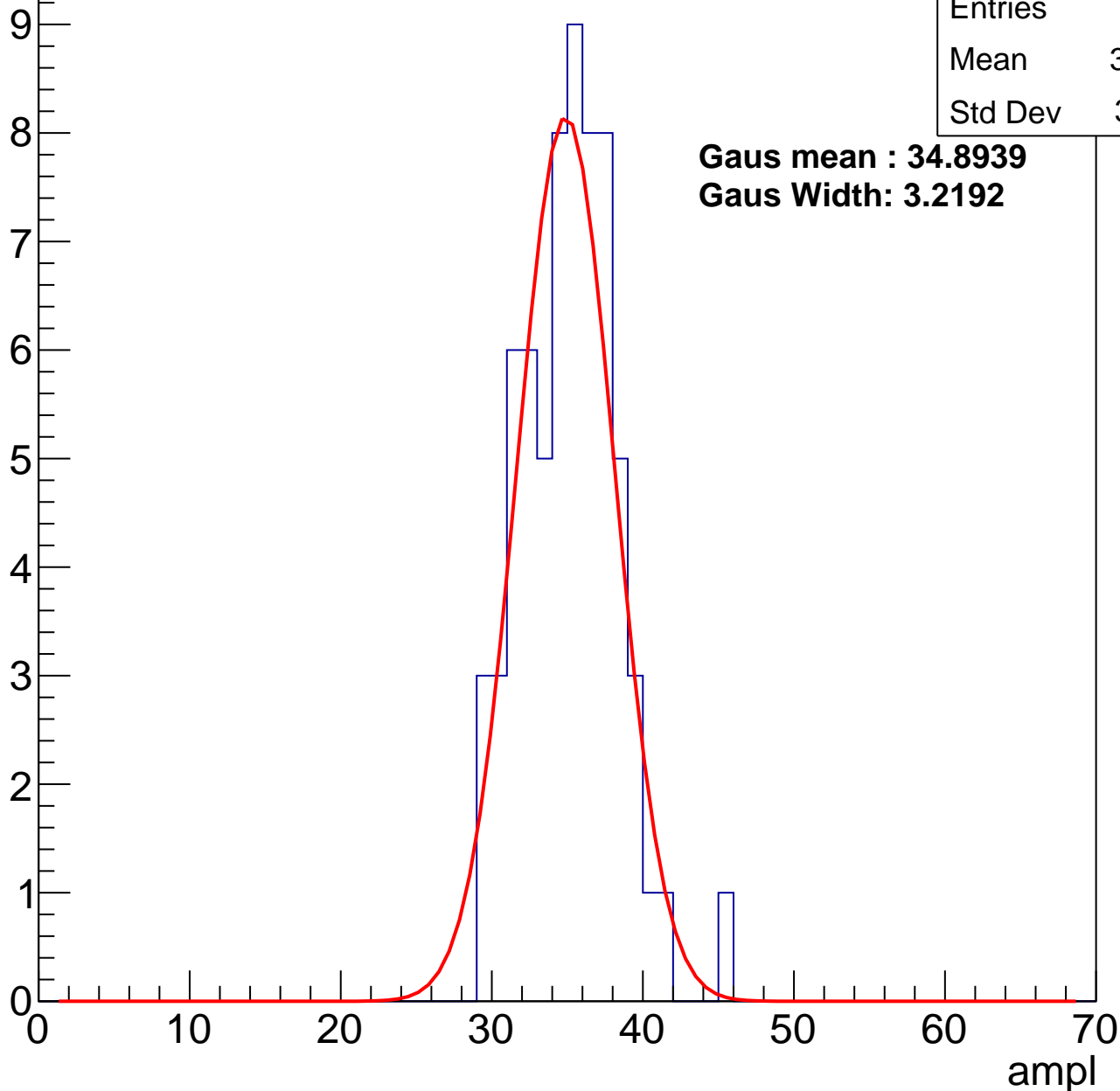
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.69
Std Dev	3.111

**Gaus mean : 34.8939**

**Gaus Width: 3.2192**



# B1L000S, U25-ch92, adc2

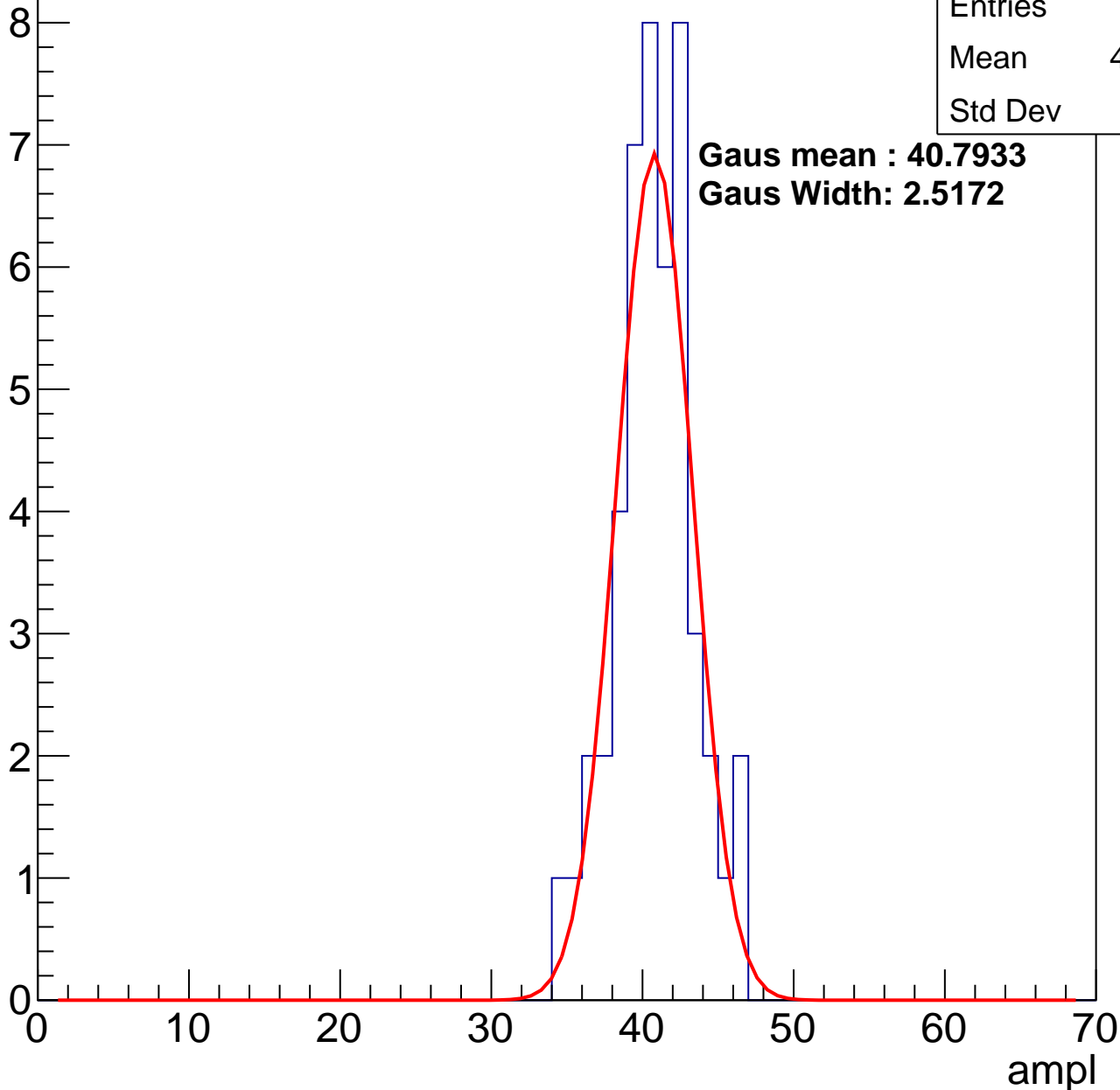
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	40.34
Std Dev	2.62

**Gaus mean : 40.7933**

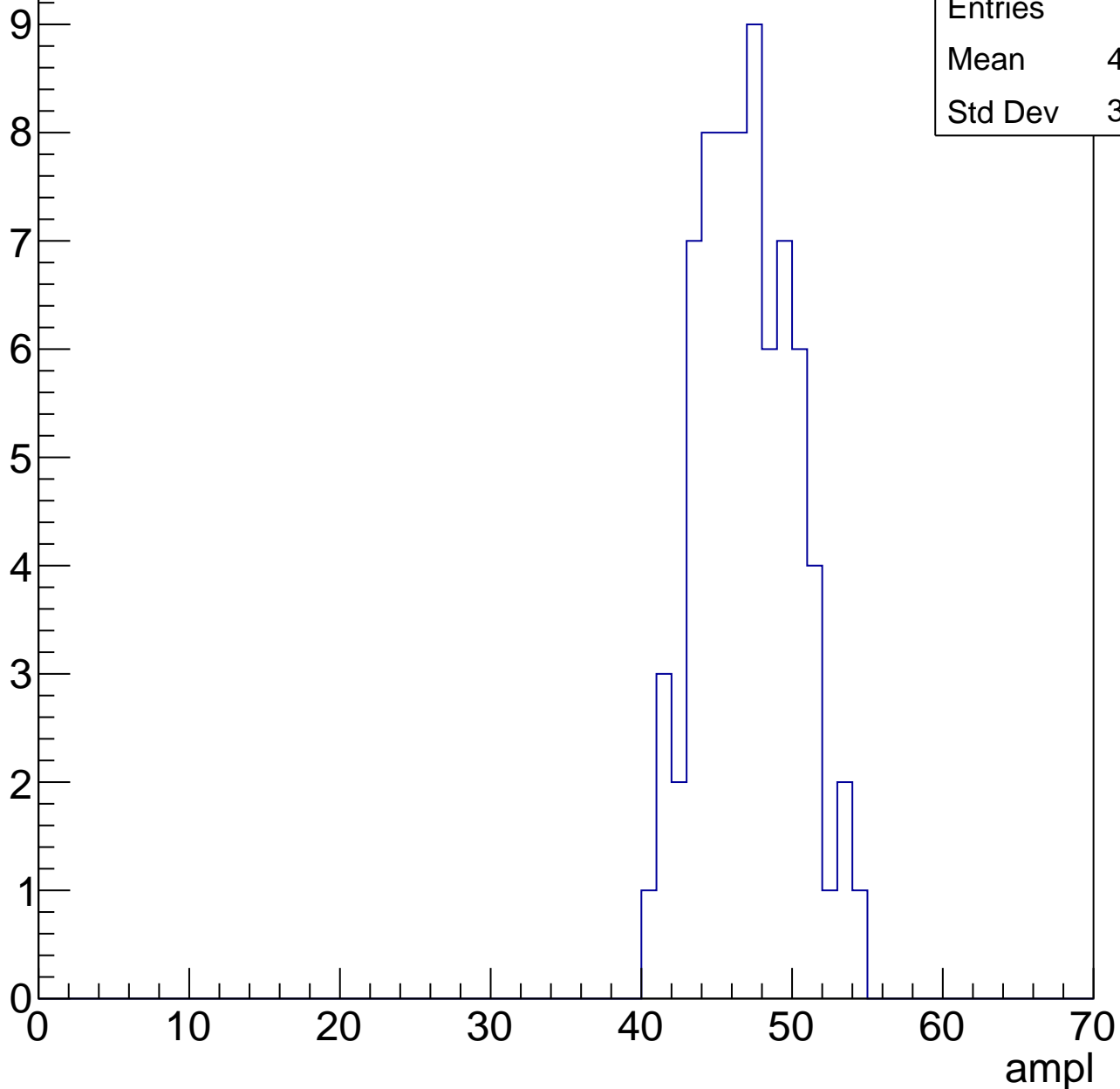
**Gaus Width: 2.5172**



# B1L000S, U25-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



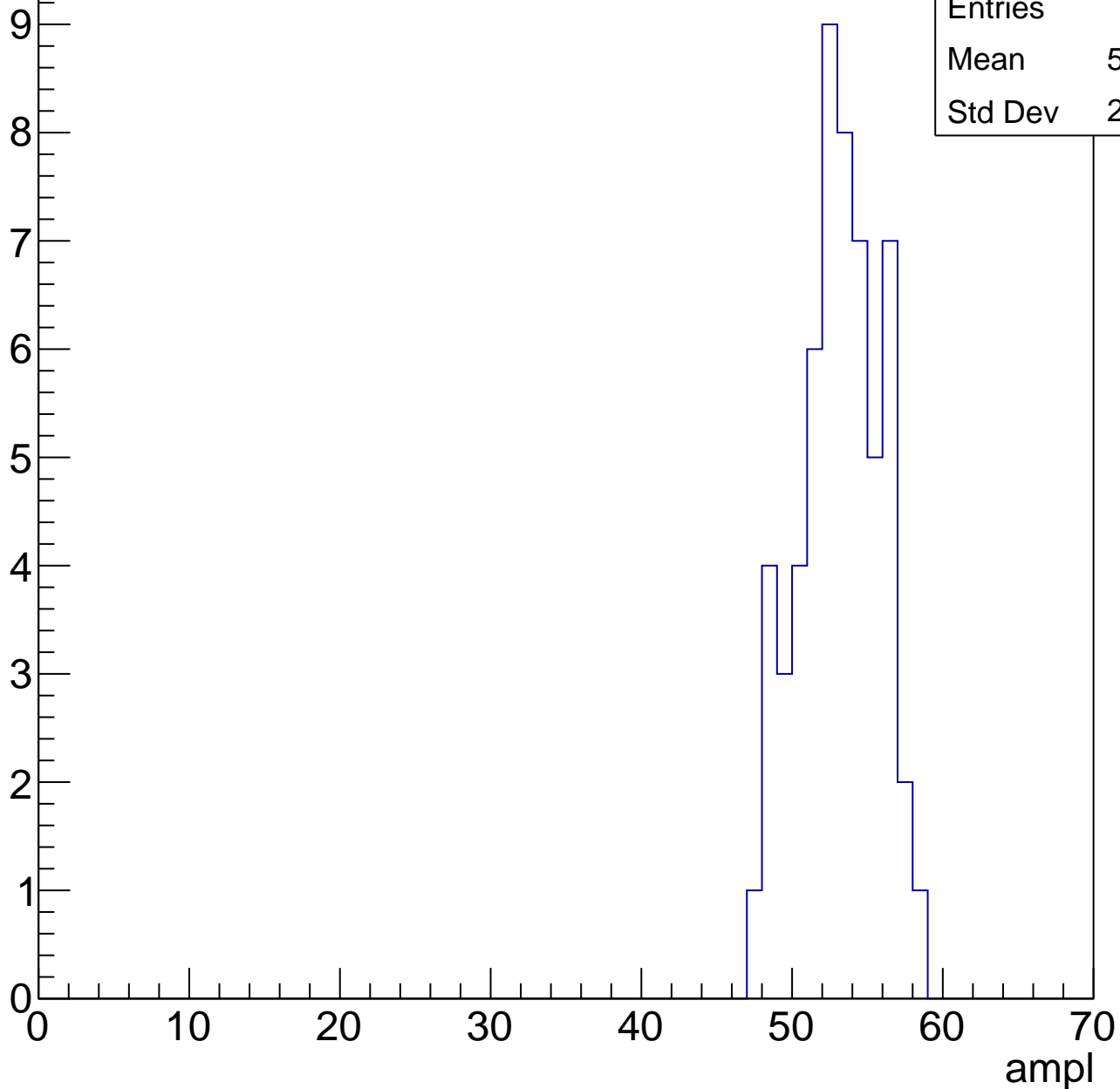
Entries	73
Mean	46.55
Std Dev	3.128

# B1L000S, U25-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

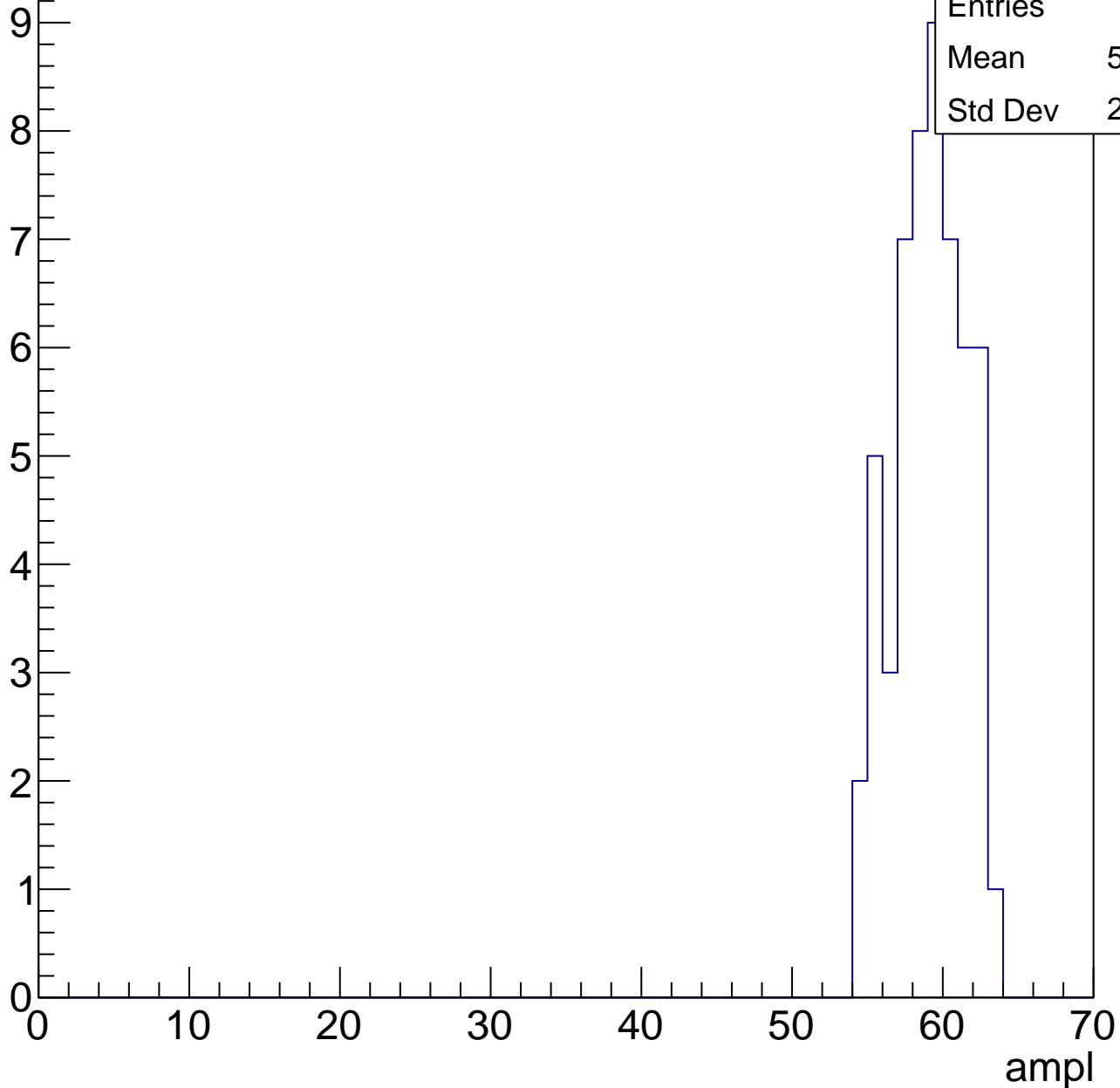
Entries	57
Mean	52.65
Std Dev	2.626



# B1L000S, U25-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

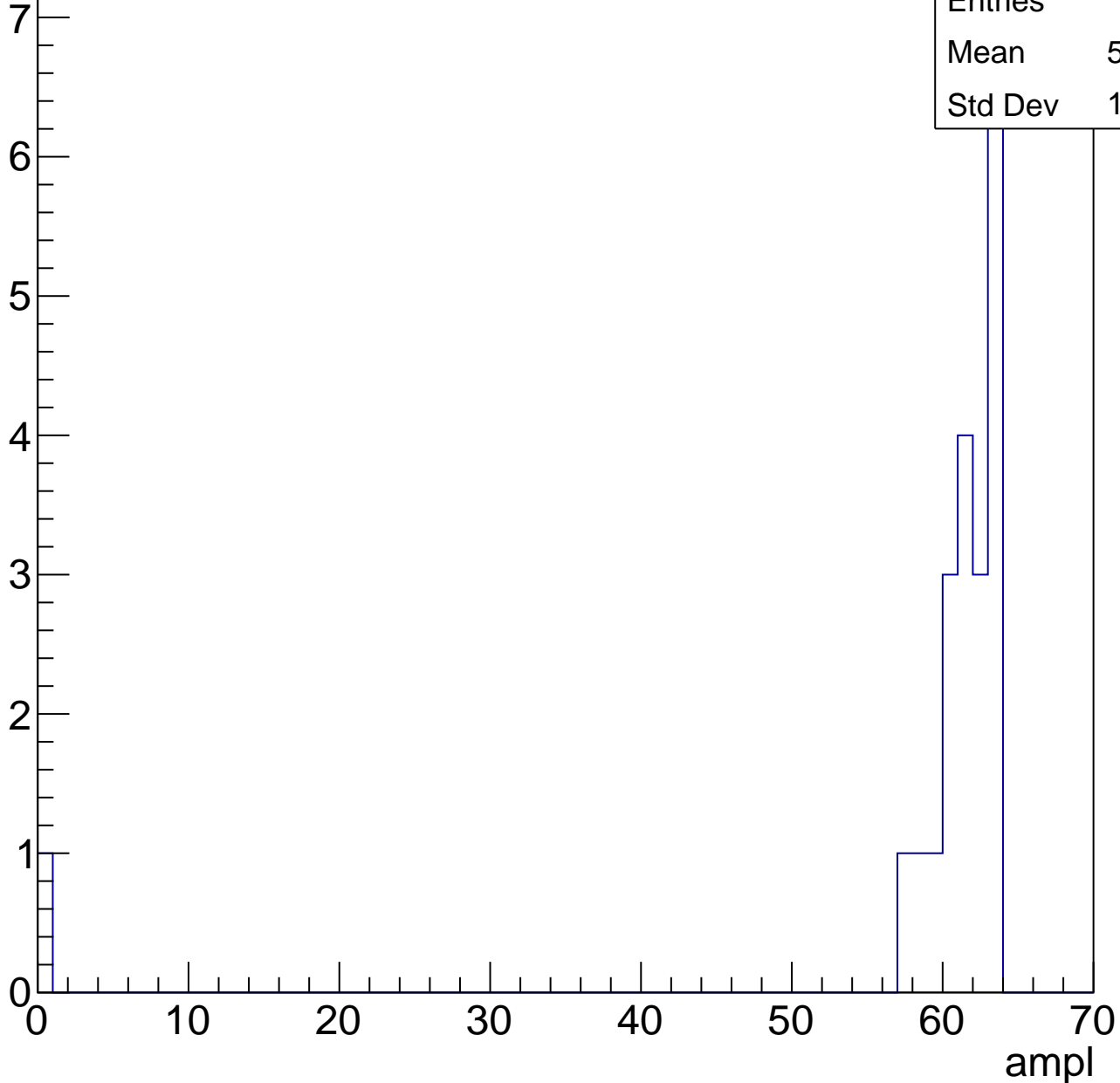


# B1L000S, U25-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.33
Std Dev	13.16

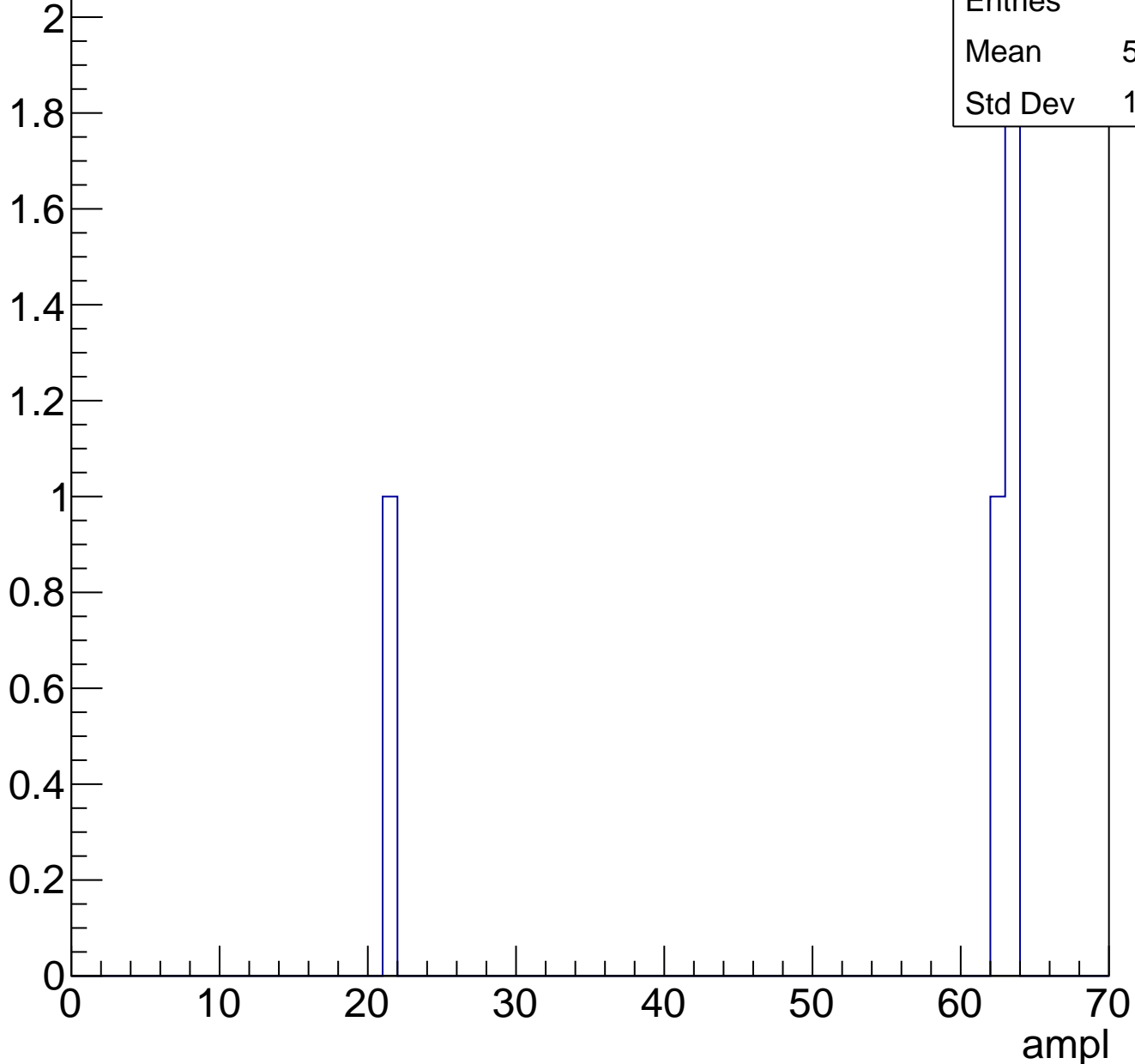




# B1L000S, U25-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch93, adc0

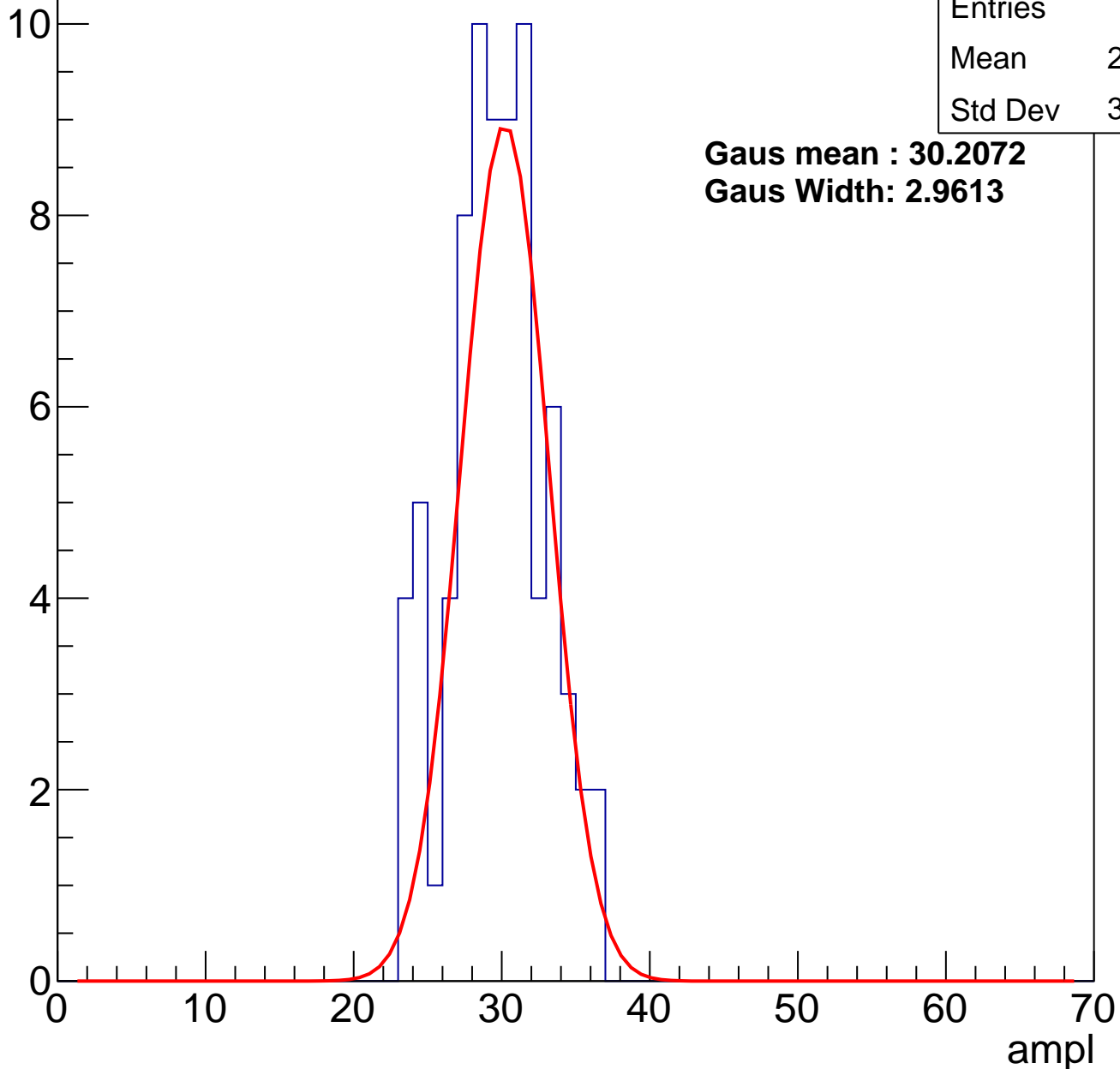
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	77
Mean	29.19
Std Dev	3.195

**Gaus mean : 30.2072**

**Gaus Width: 2.9613**

Entry



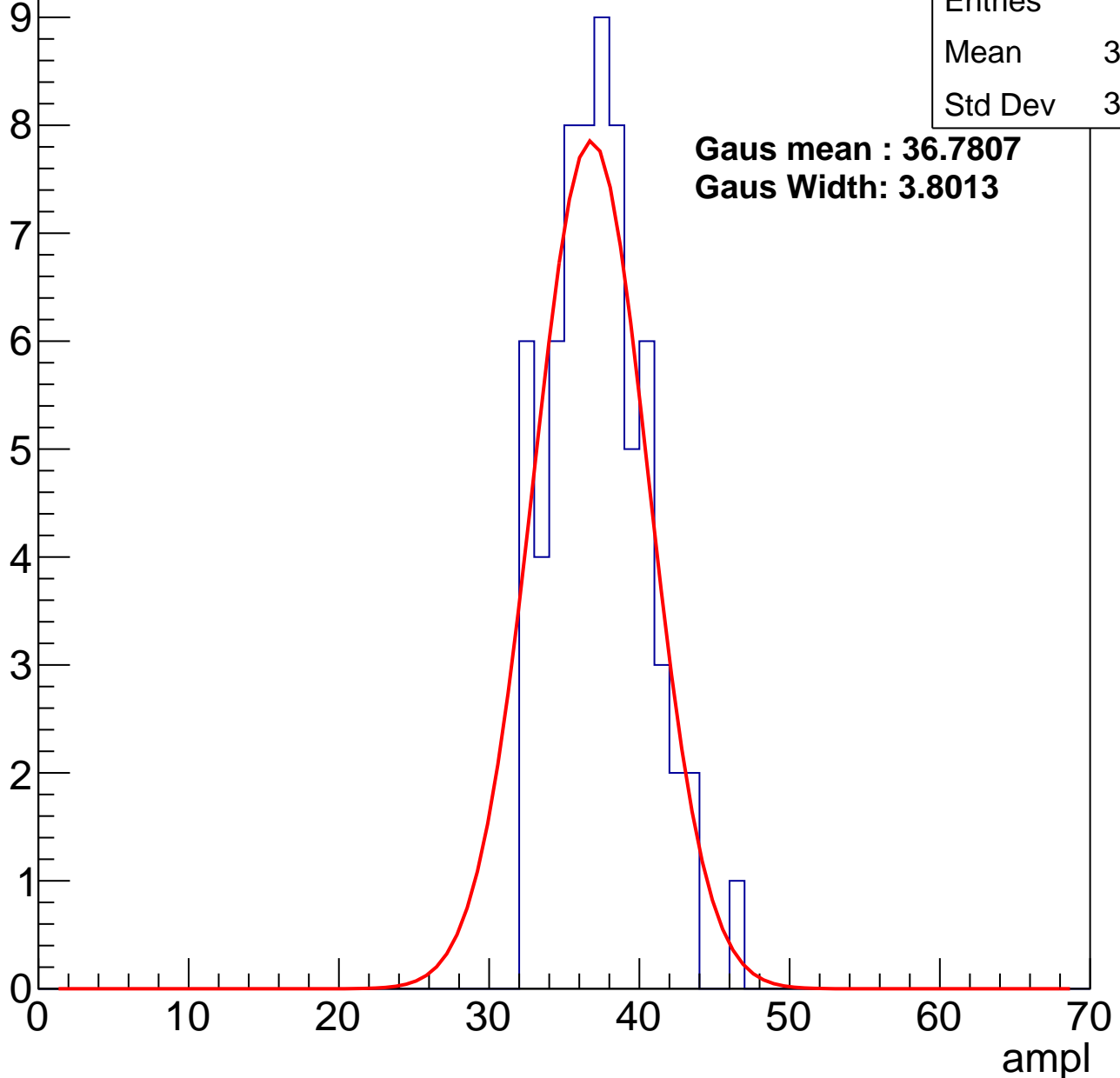
# B1L000S, U25-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.87
Std Dev	3.067

**Gaus mean : 36.7807**  
**Gaus Width: 3.8013**



# B1L000S, U25-ch93, adc2

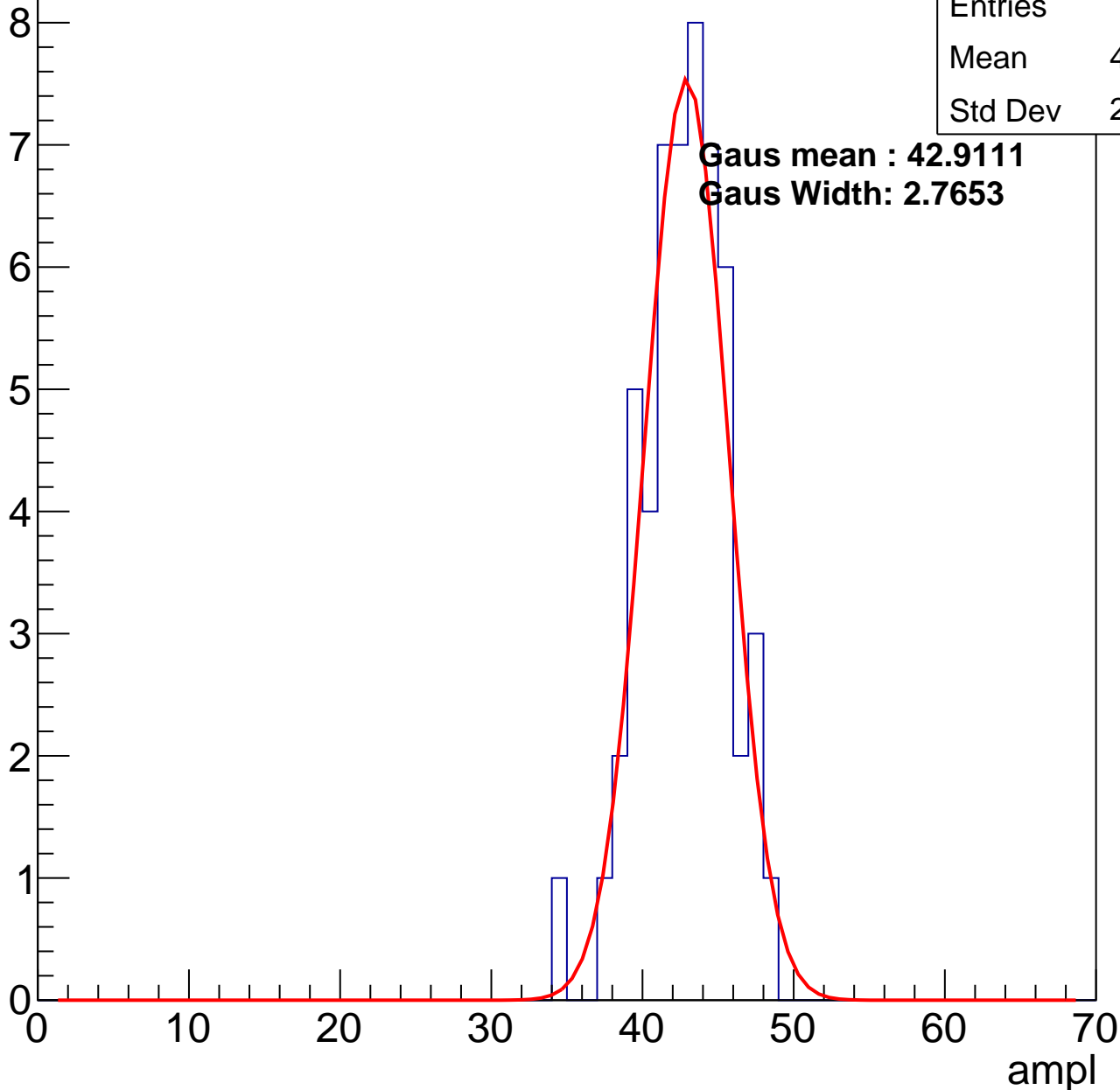
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.33
Std Dev	2.776

**Gaus mean : 42.9111**

**Gaus Width: 2.7653**



# B1L000S, U25-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

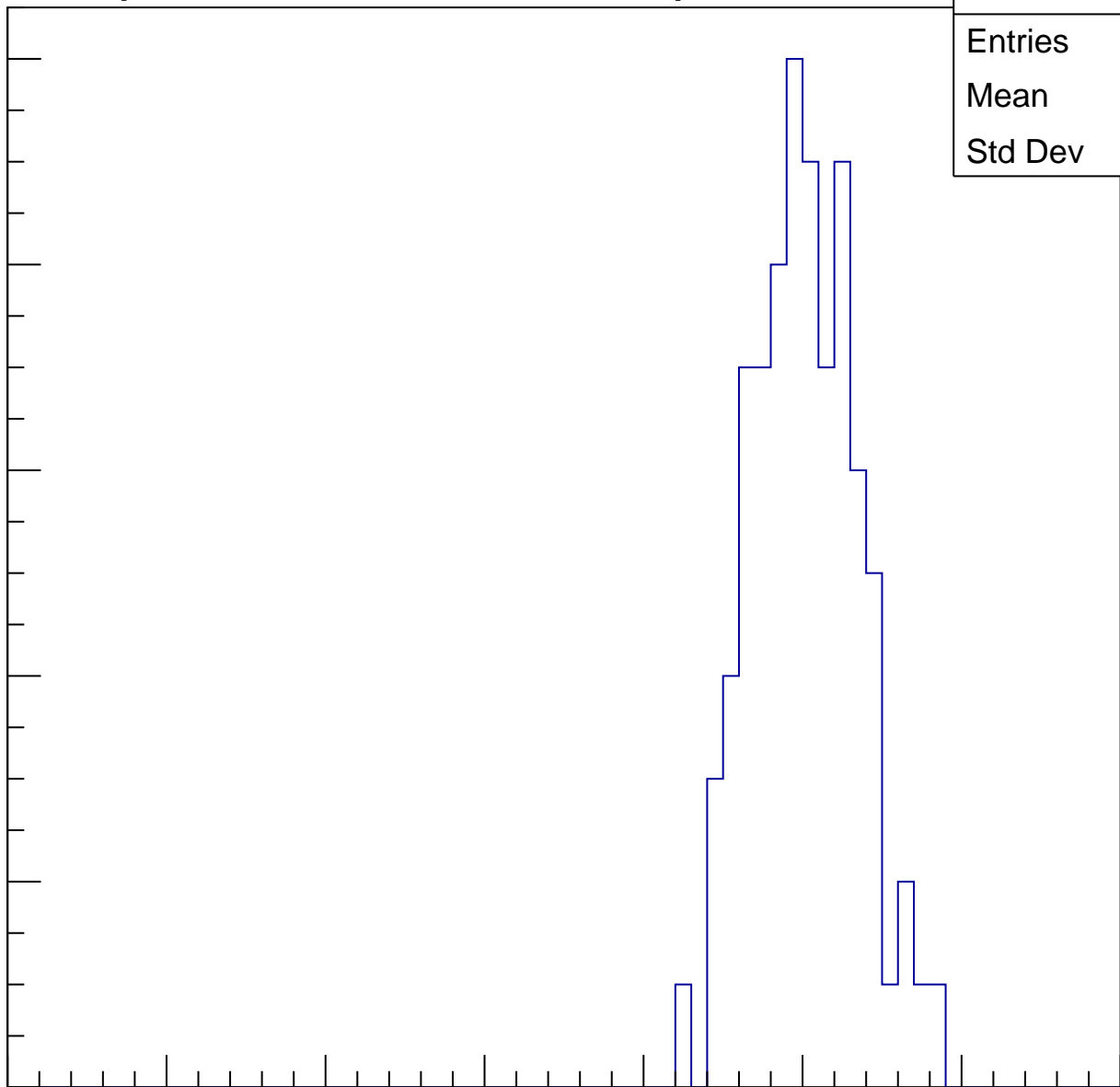
Entries	81
Mean	49.68
Std Dev	3.273

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch93, adc4

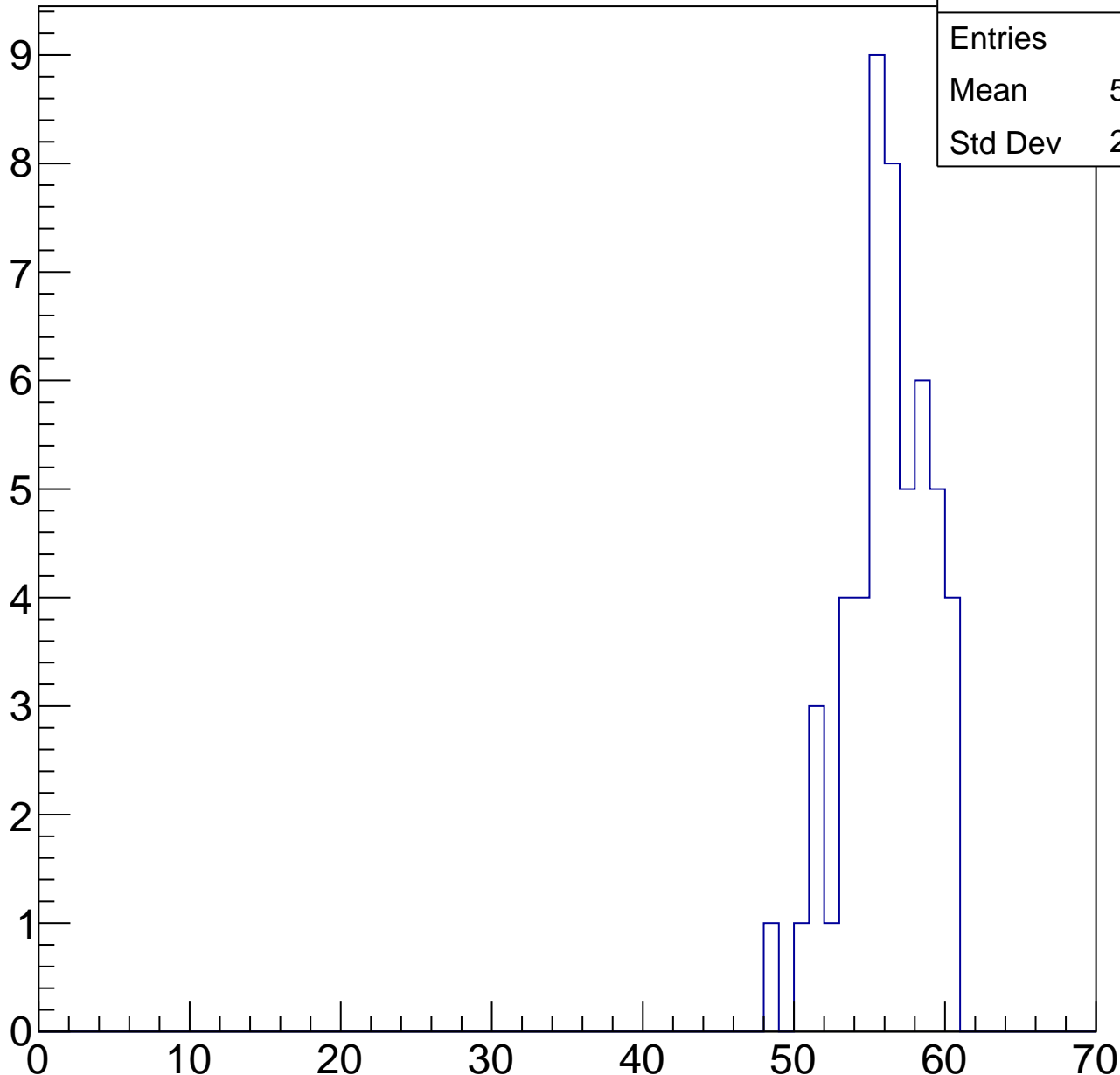
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	55.73
Std Dev	2.752

ampl

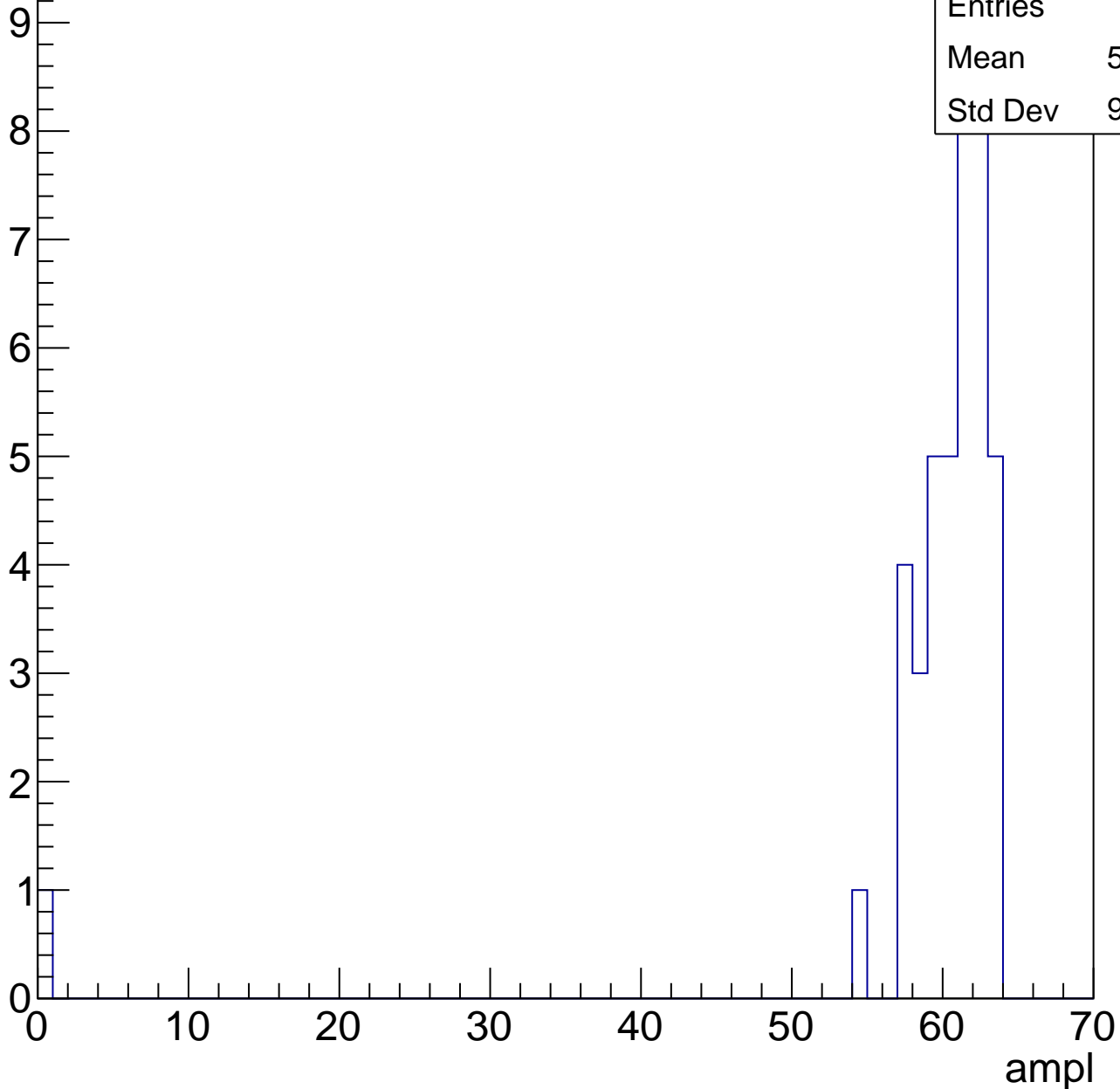


# B1L000S, U25-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	58.88
Std Dev	9.419

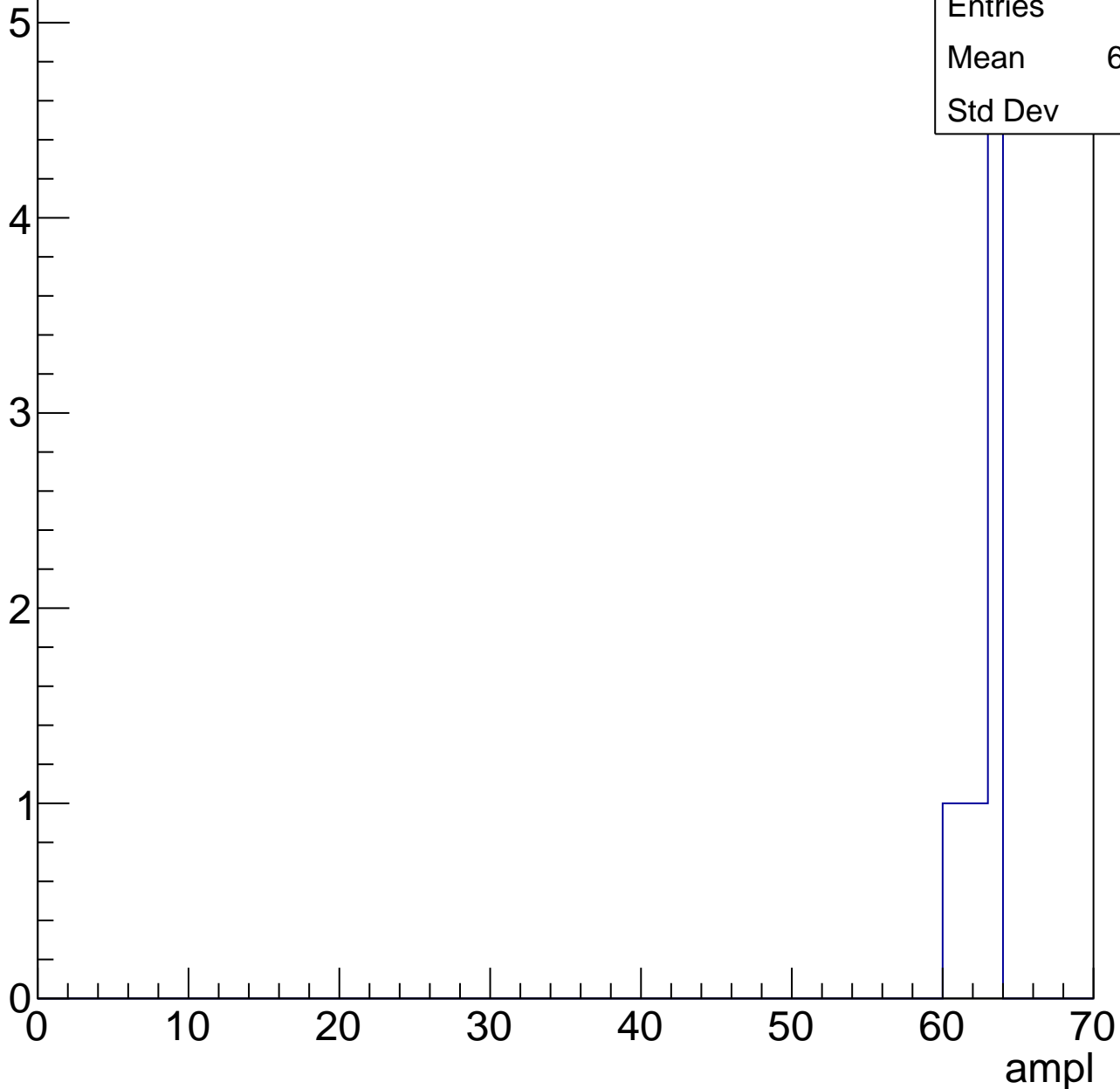


# B1L000S, U25-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	8
Mean	62.25
Std Dev	1.09





# B1L000S, U25-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B1L000S, U25-ch94, adc0

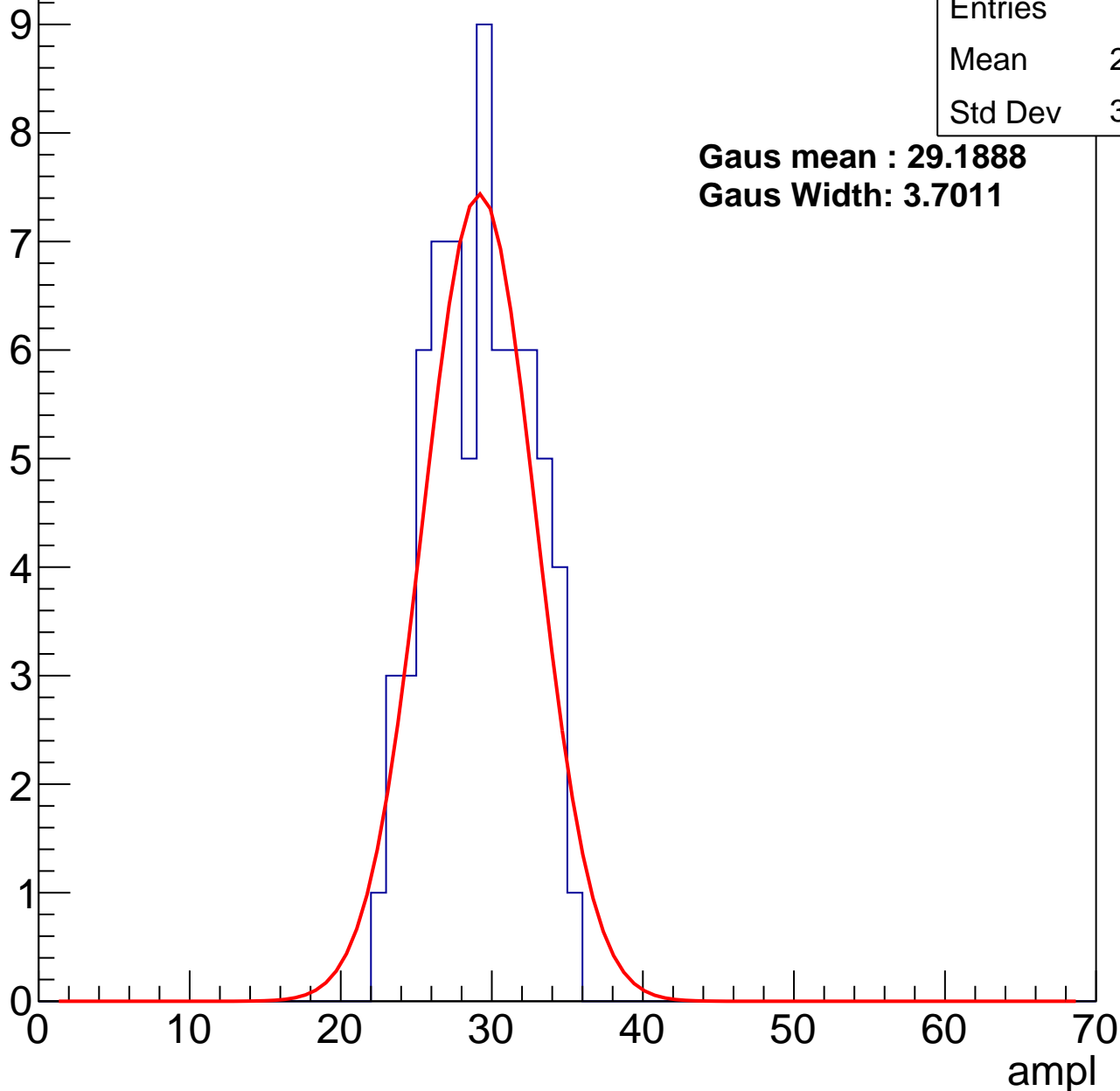
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	28.68
Std Dev	3.224

**Gaus mean : 29.1888**

**Gaus Width: 3.7011**



# B1L000S, U25-ch94, adc1

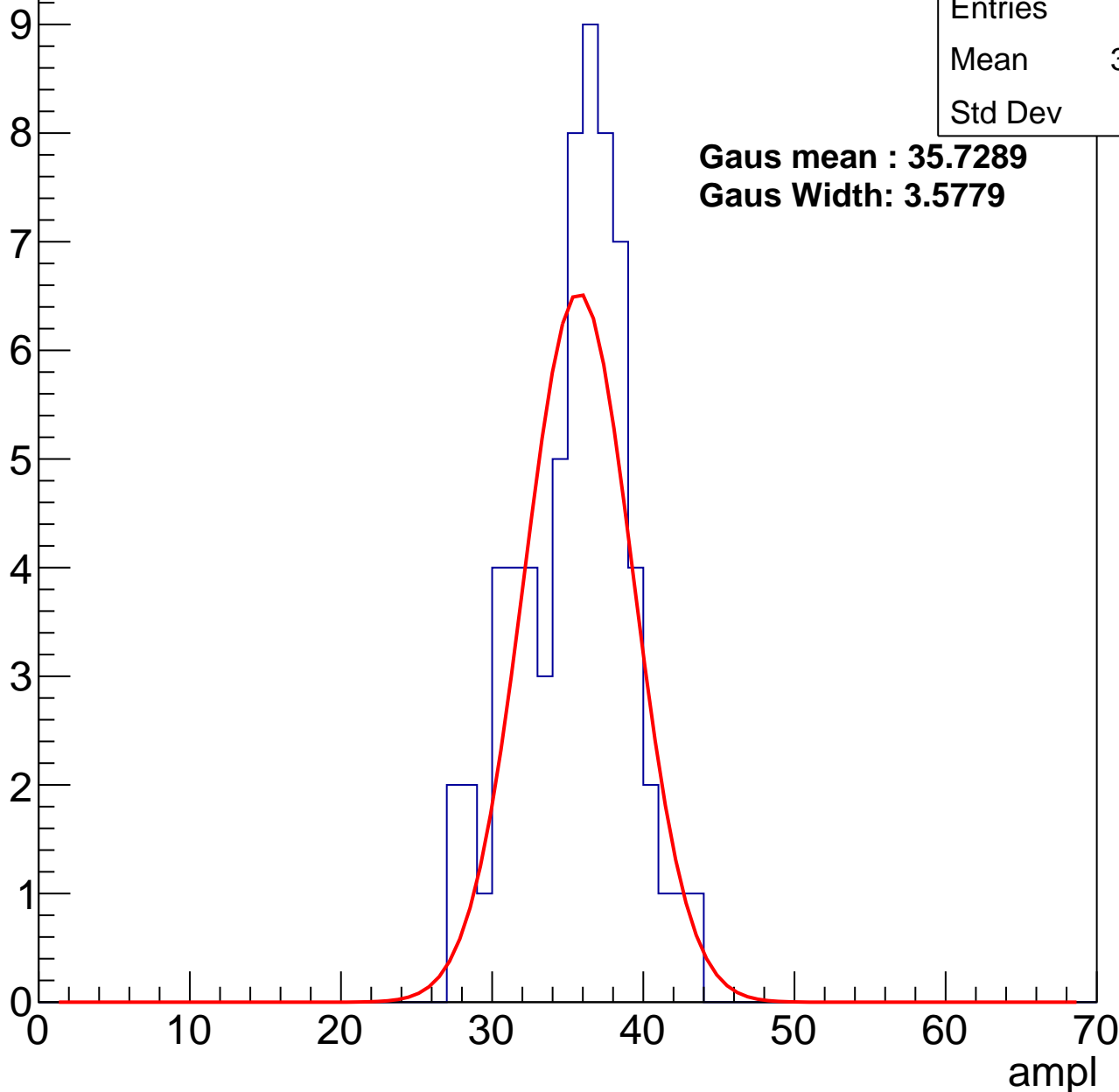
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	34.97
Std Dev	3.58

**Gaus mean : 35.7289**

**Gaus Width: 3.5779**



# B1L000S, U25-ch94, adc2

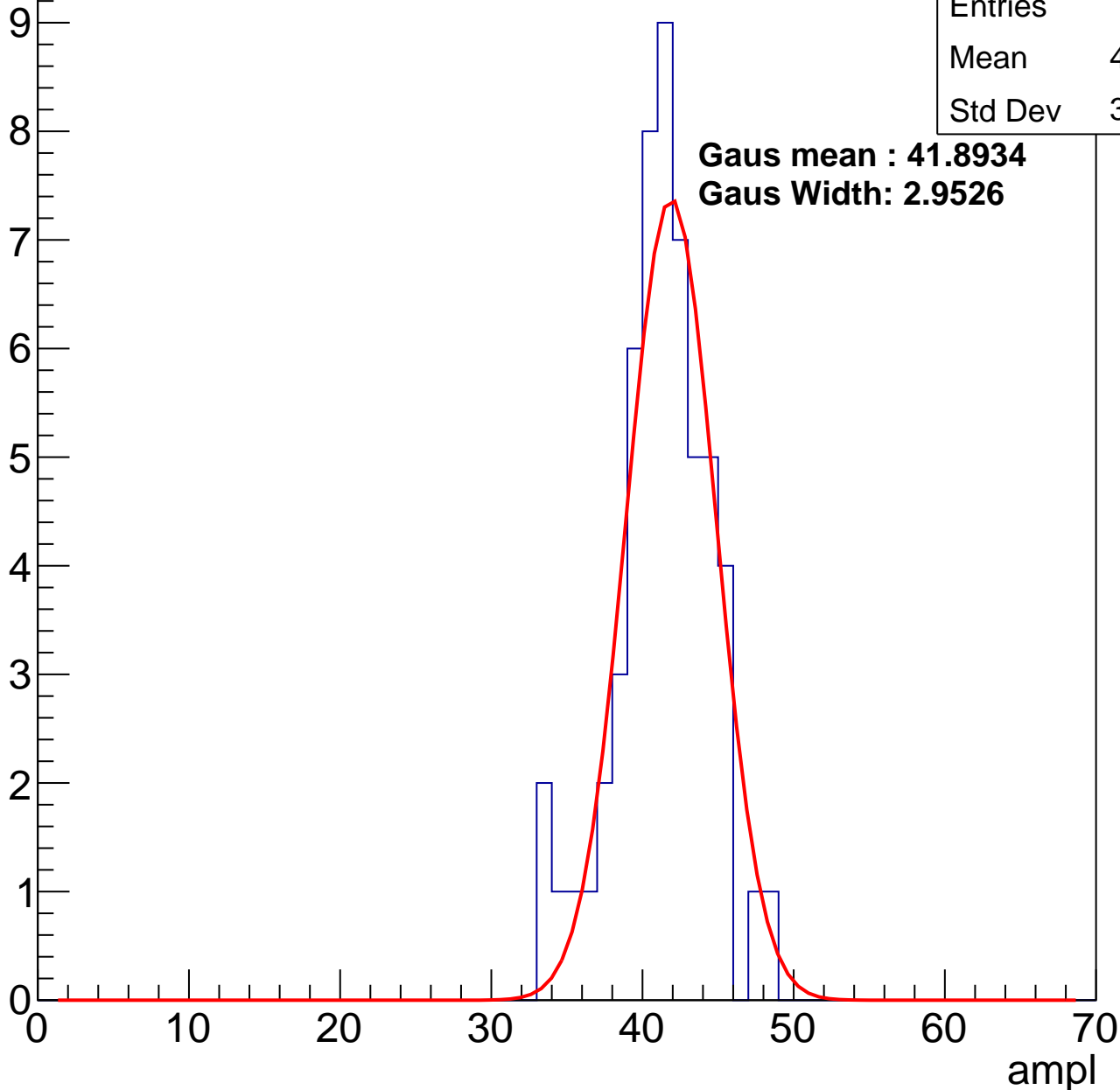
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.82
Std Dev	3.129

**Gaus mean : 41.8934**

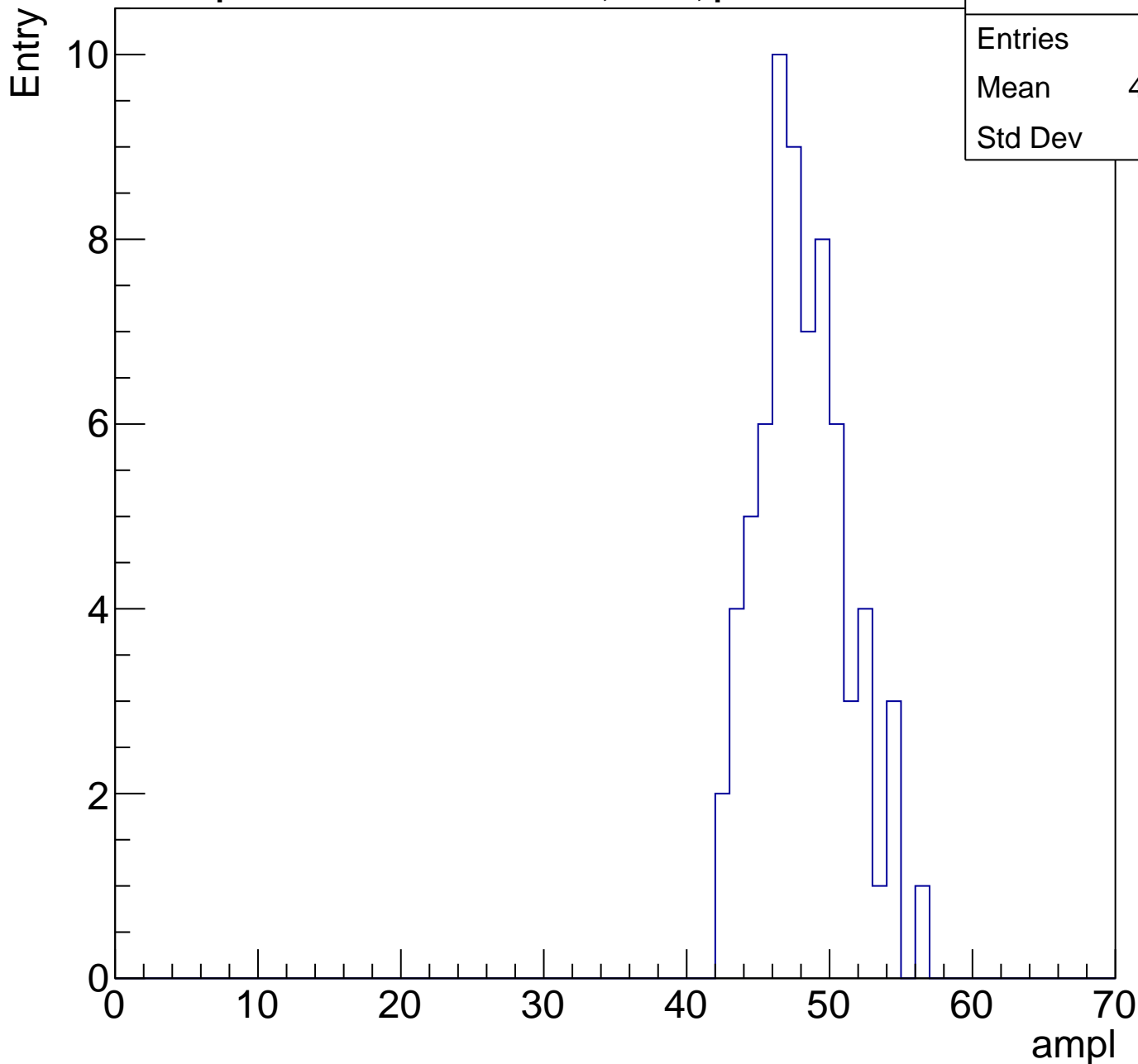
**Gaus Width: 2.9526**



# B1L000S, U25-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	47.67
Std Dev	3.11



# B1L000S, U25-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

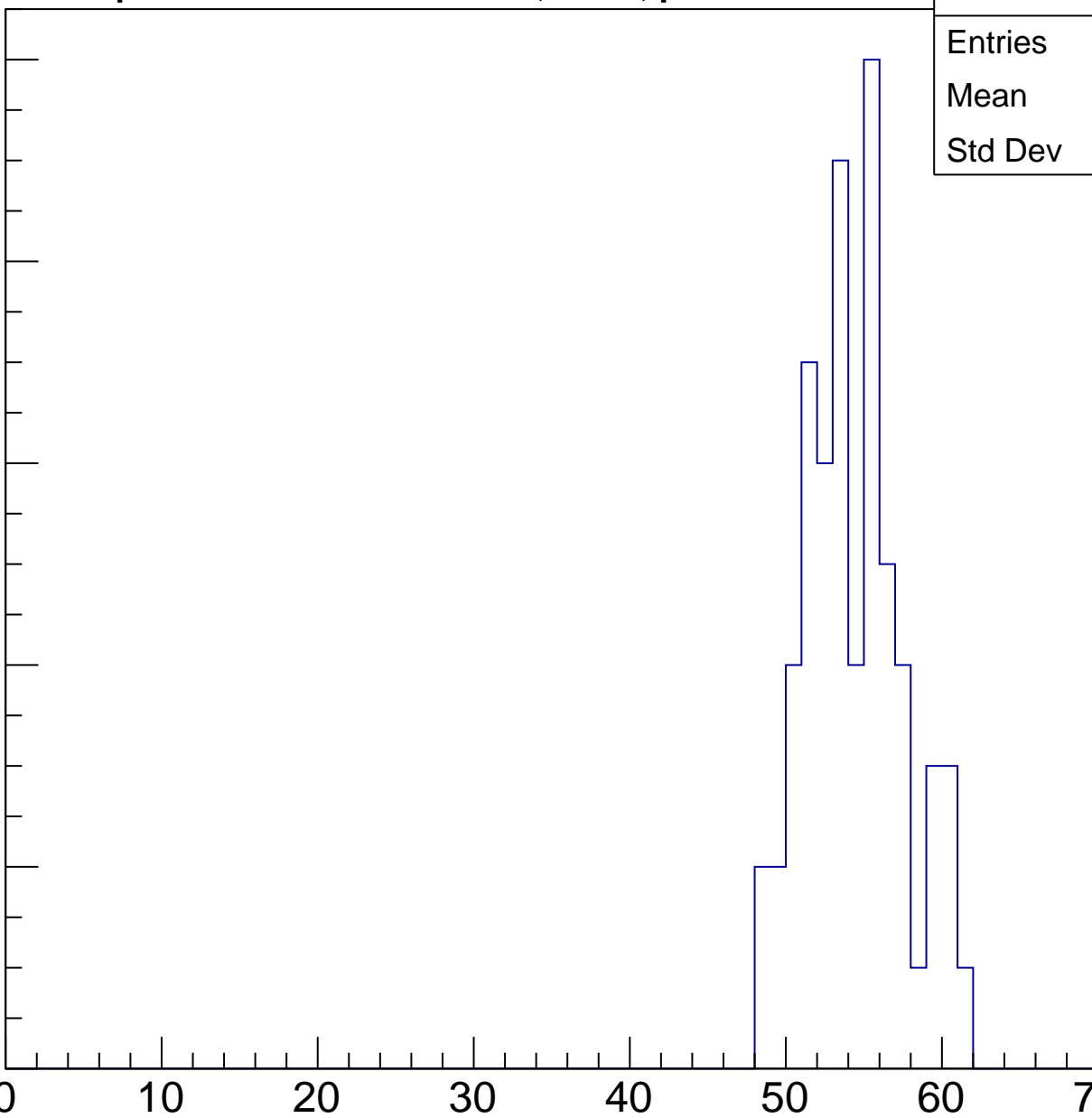
Entries	61
Mean	53.93
Std Dev	3.125

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

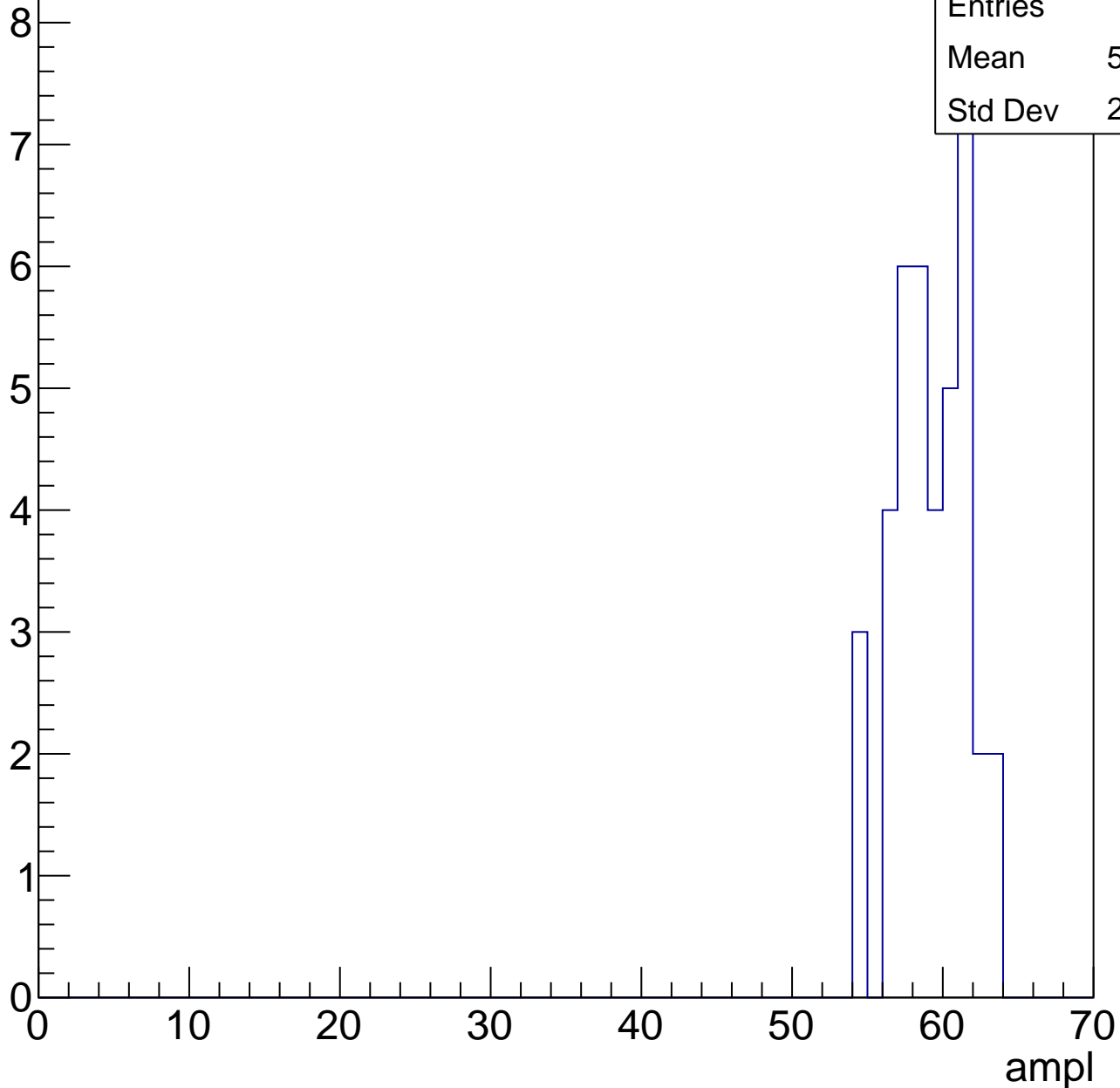


# B1L000S, U25-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	40
Mean	58.75
Std Dev	2.374

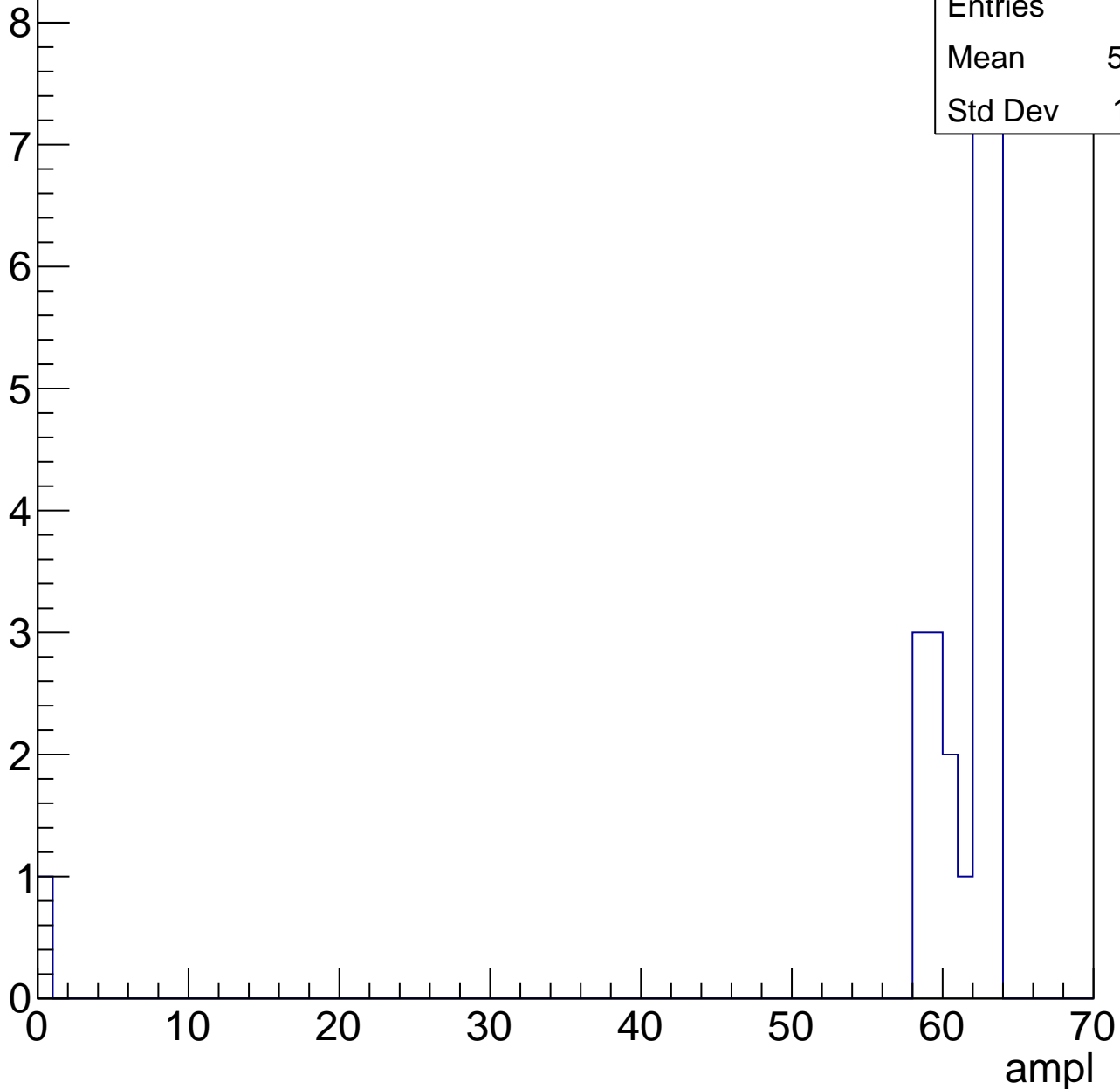


# B1L000S, U25-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	26
Mean	58.92
Std Dev	11.91





# B1L000S, U25-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch95, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	74
Mean	28.38
Std Dev	4.695

**Gaus mean : 29.6135**

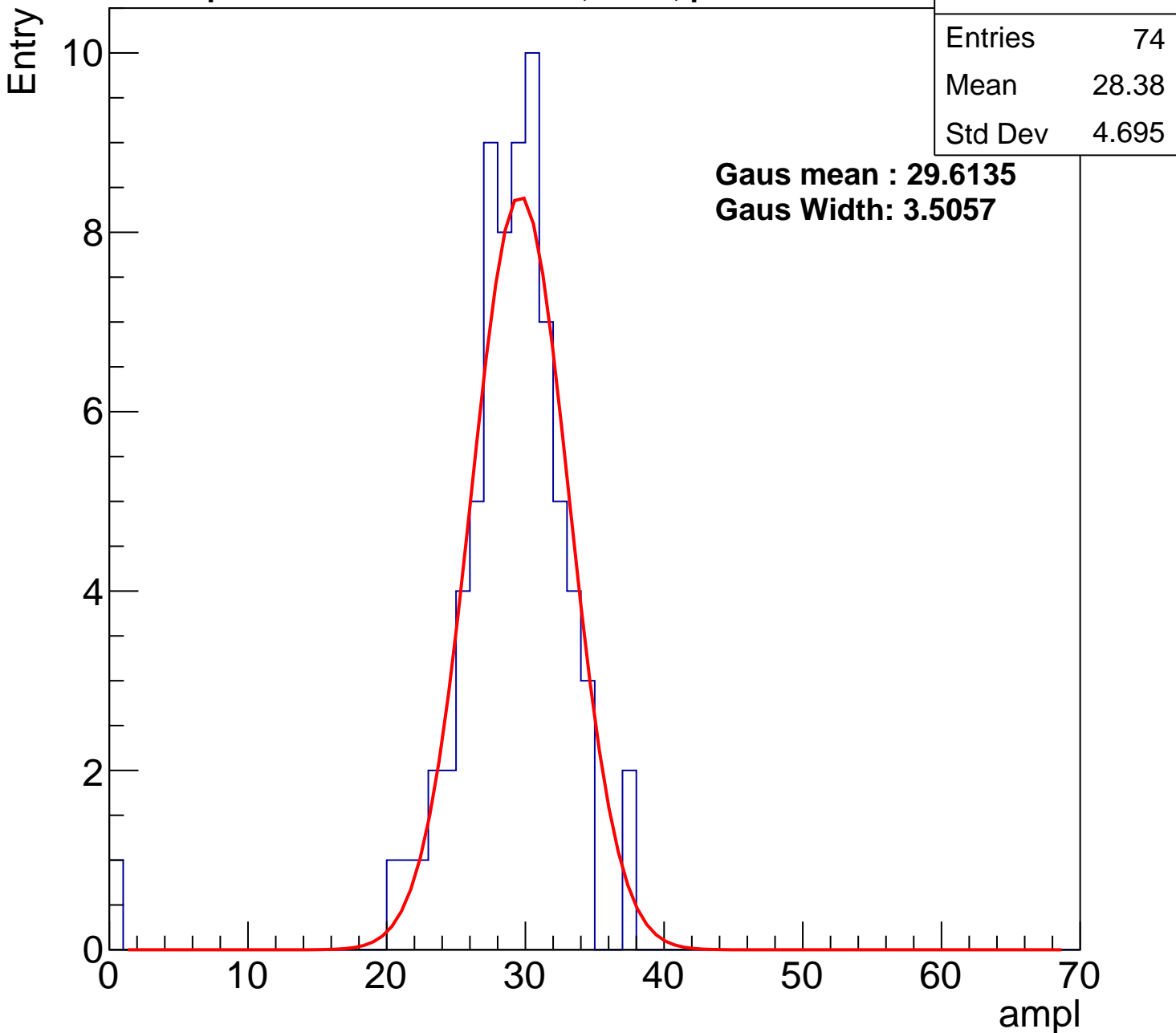
**Gaus Width: 3.5057**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch95, adc1

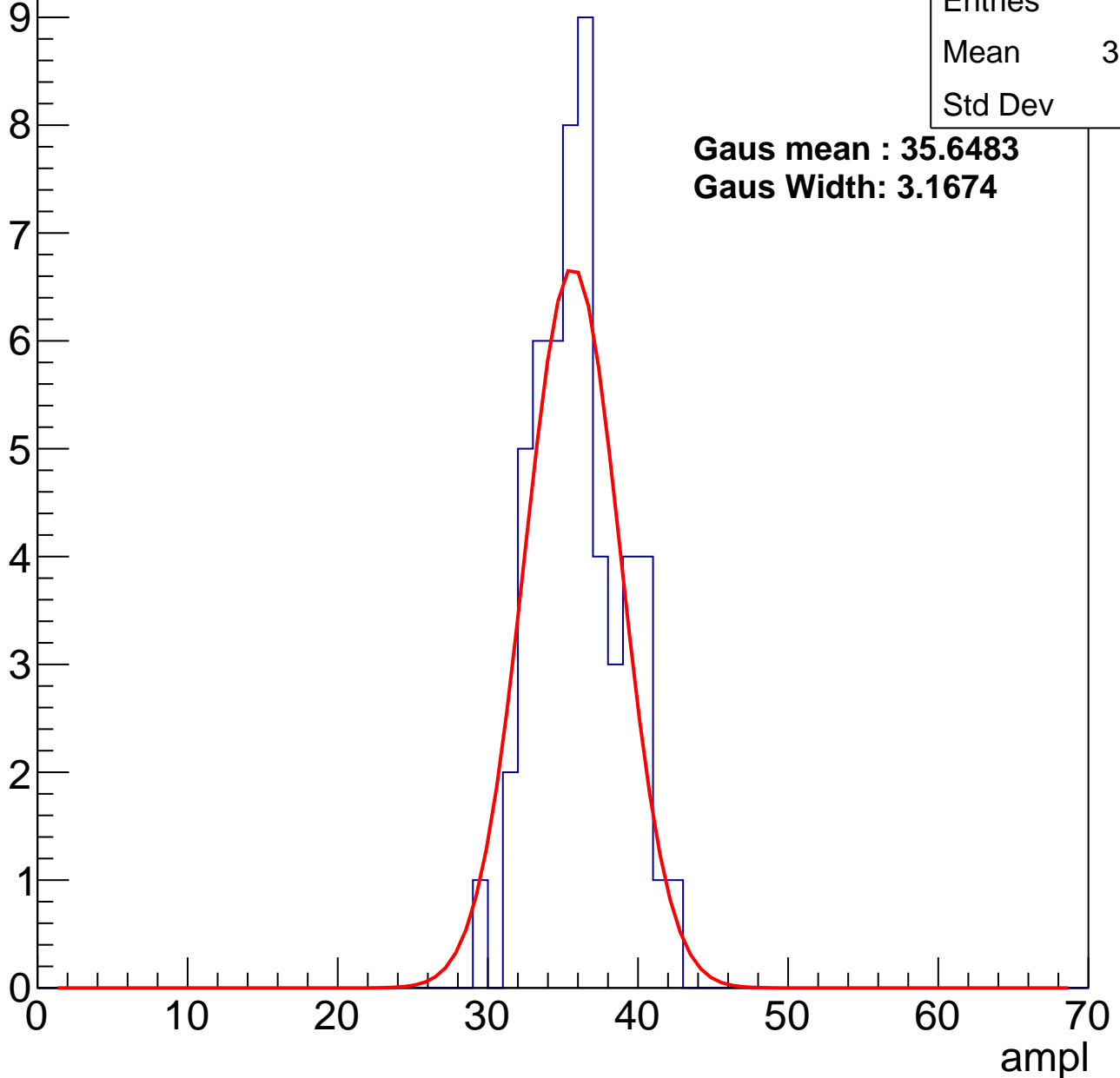
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	35.52
Std Dev	2.82

**Gaus mean : 35.6483**

**Gaus Width: 3.1674**



# B1L000S, U25-ch95, adc2

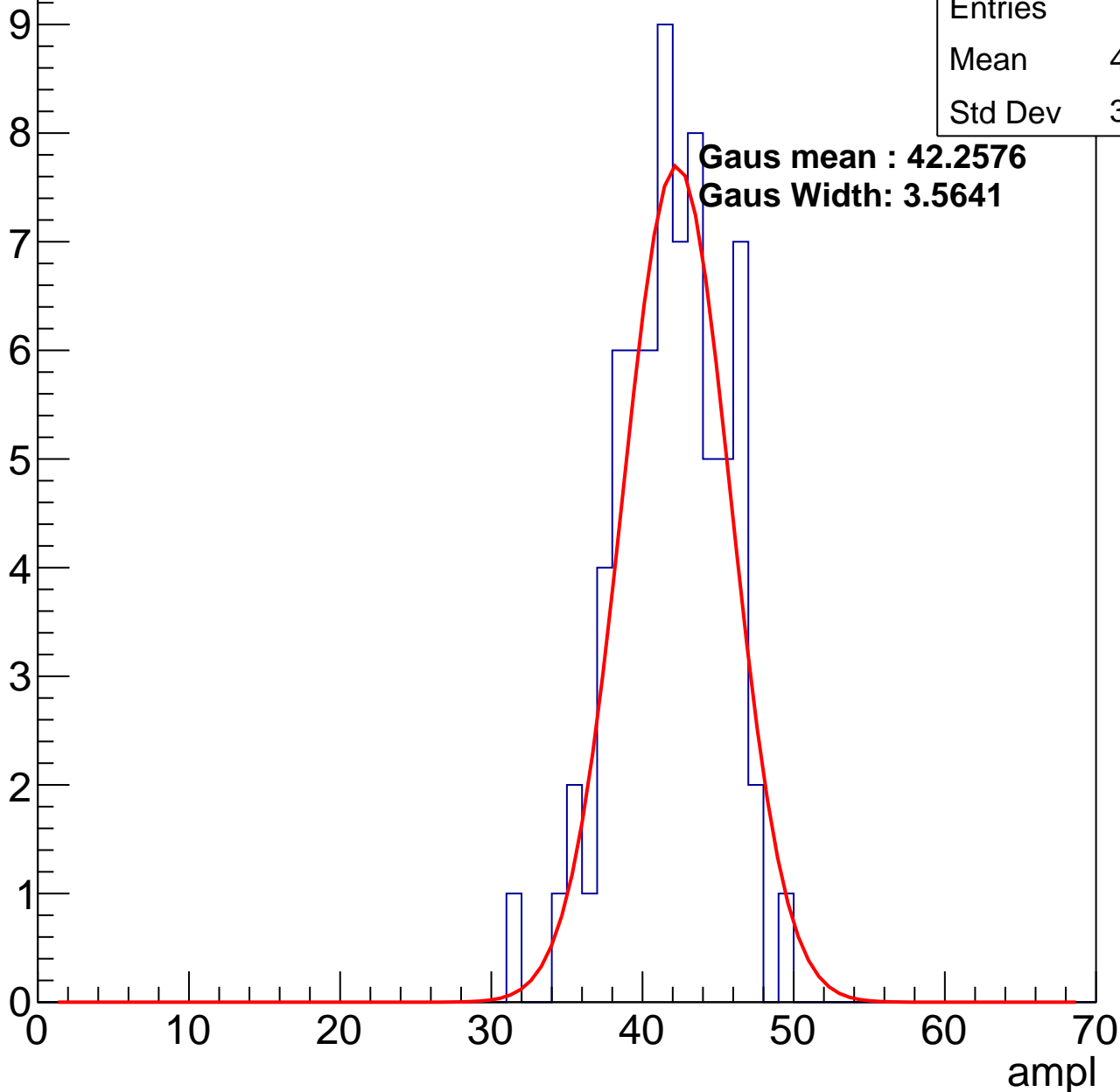
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	41.38
Std Dev	3.478

**Gaus mean : 42.2576**

**Gaus Width: 3.5641**



# B1L000S, U25-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

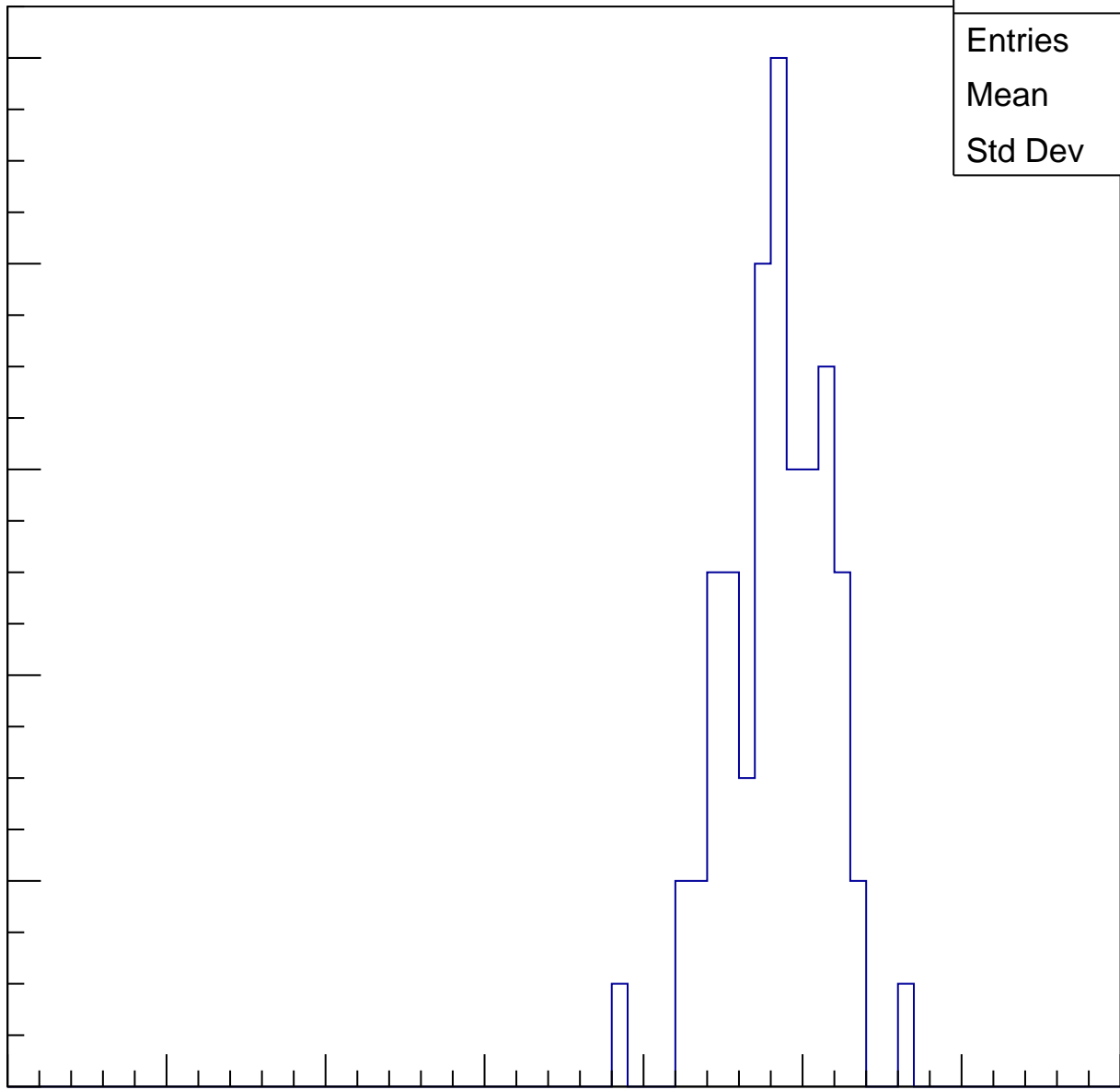
Entries	63
Mean	47.94
Std Dev	3.221

Entry

10  
8  
6  
4  
2  
0

ampl

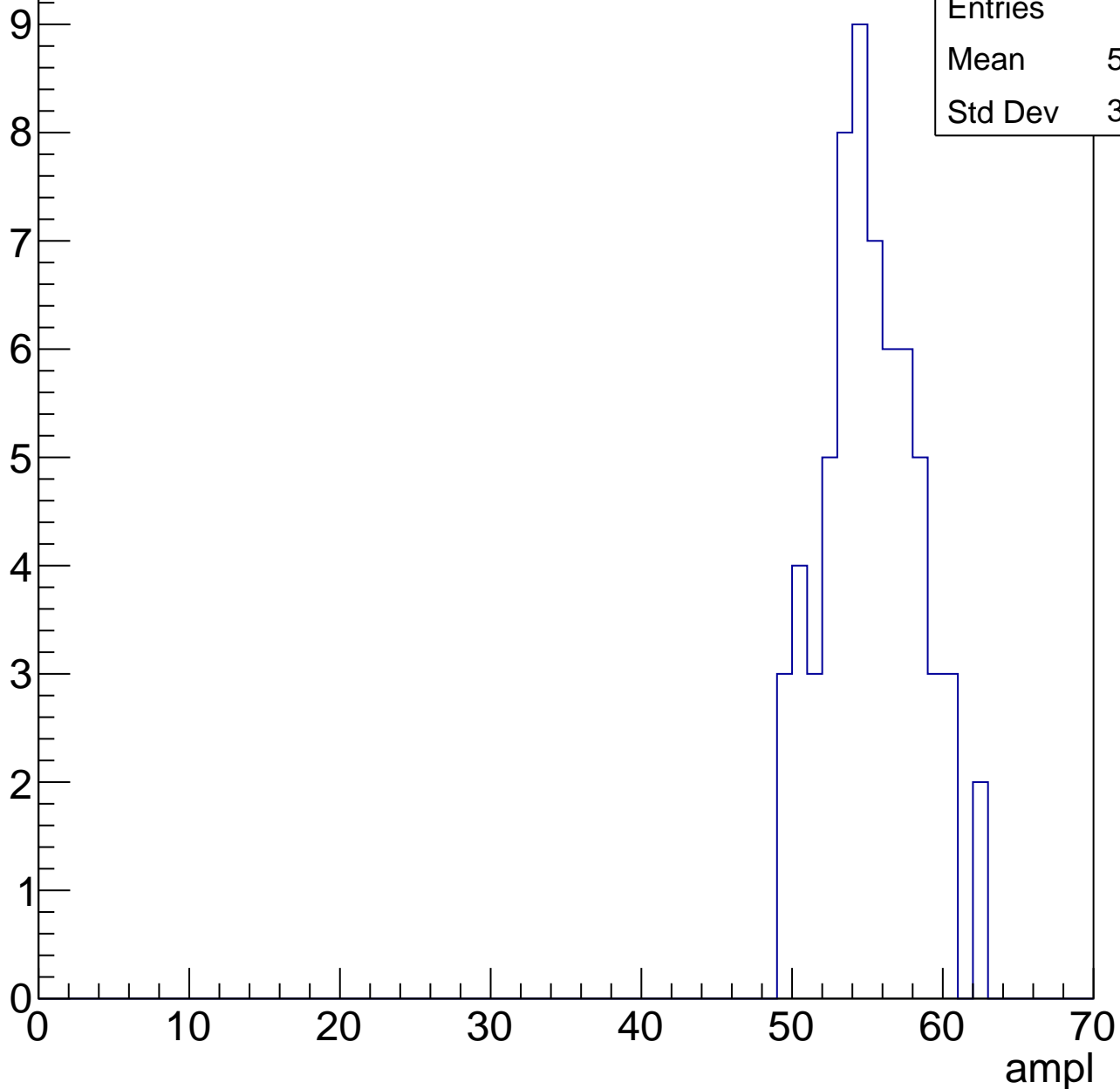
0 10 20 30 40 50 60 70



# B1L000S, U25-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	64
Mean	54.75
Std Dev	3.147

# B1L000S, U25-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	59.45
Std Dev	2.499

ampl

10

20

30

40

50

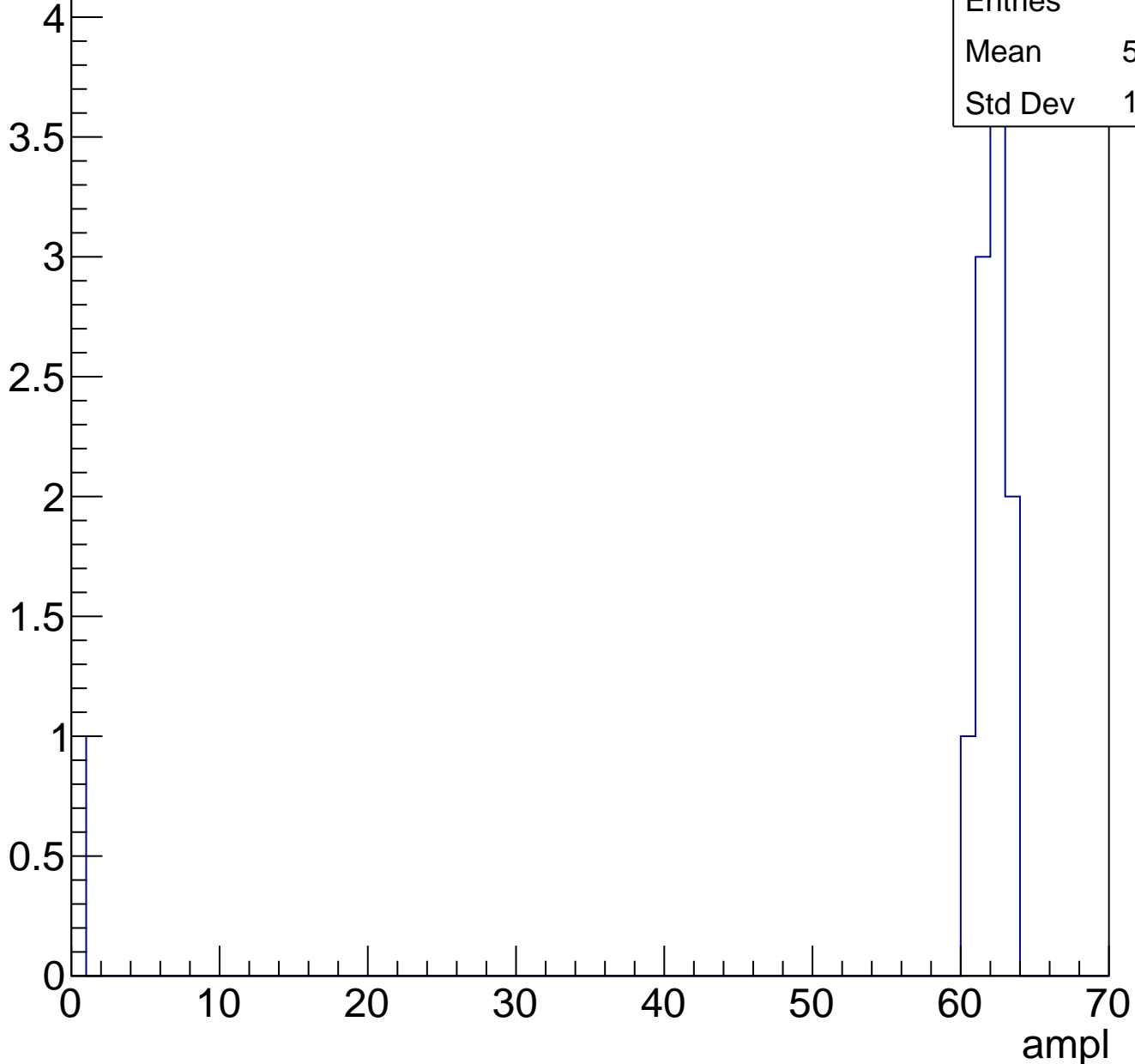
60

70

# B1L000S, U25-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch96, adc0

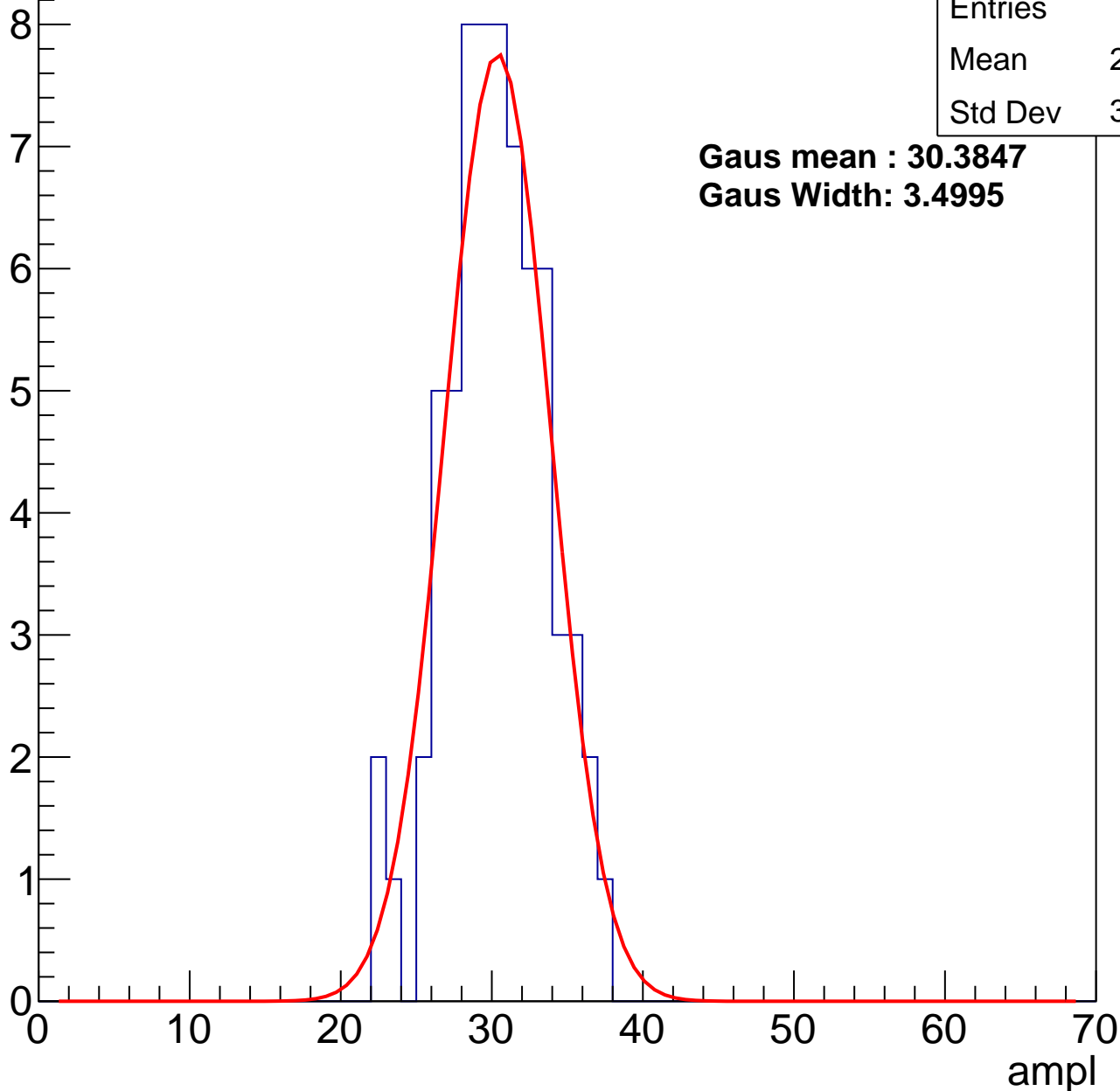
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	29.87
Std Dev	3.278

**Gaus mean : 30.3847**

**Gaus Width: 3.4995**



# B1L000S, U25-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	67
Mean	36.16
Std Dev	3.179

**Gaus mean : 36.3861**

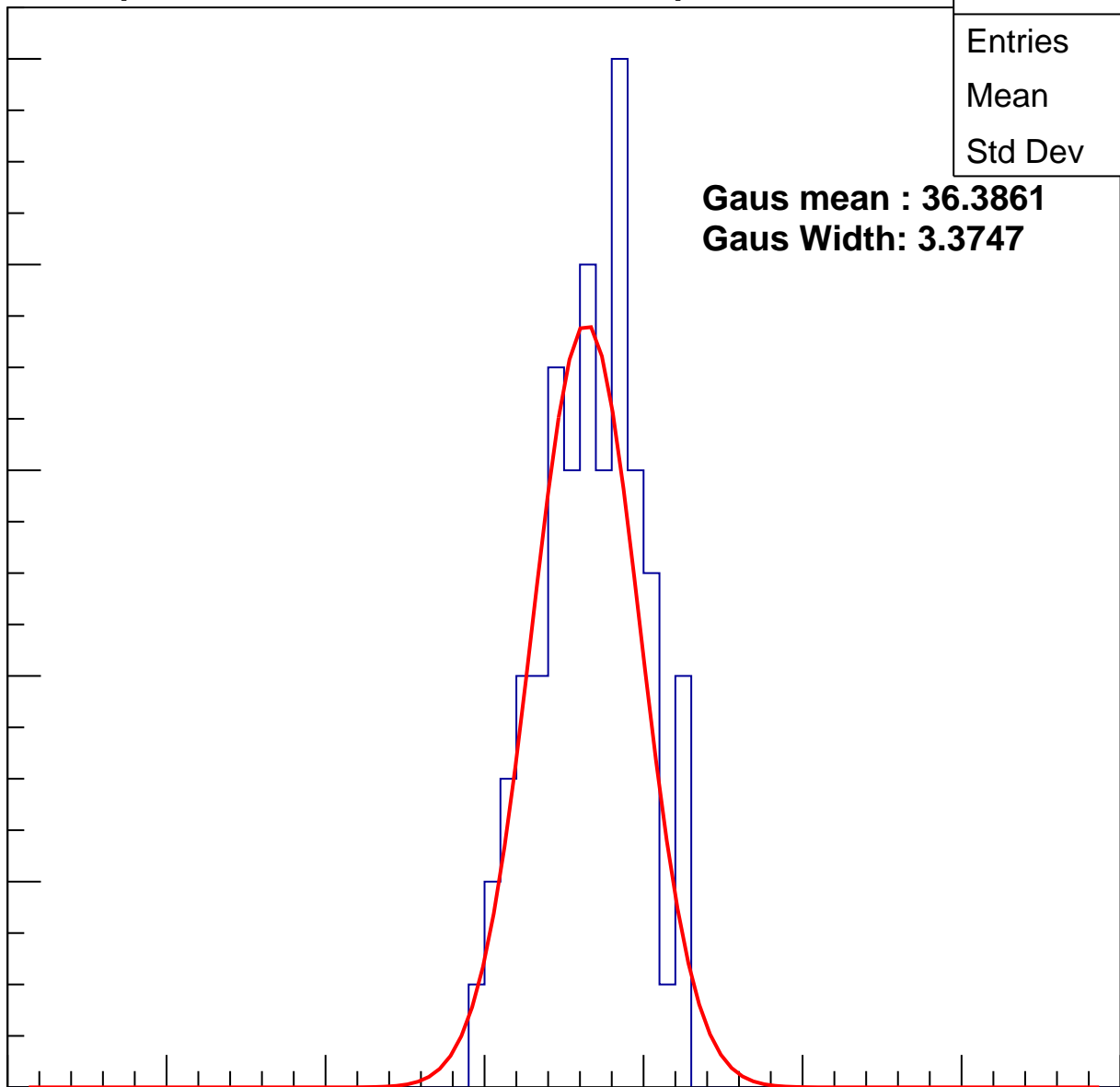
**Gaus Width: 3.3747**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch96, adc2

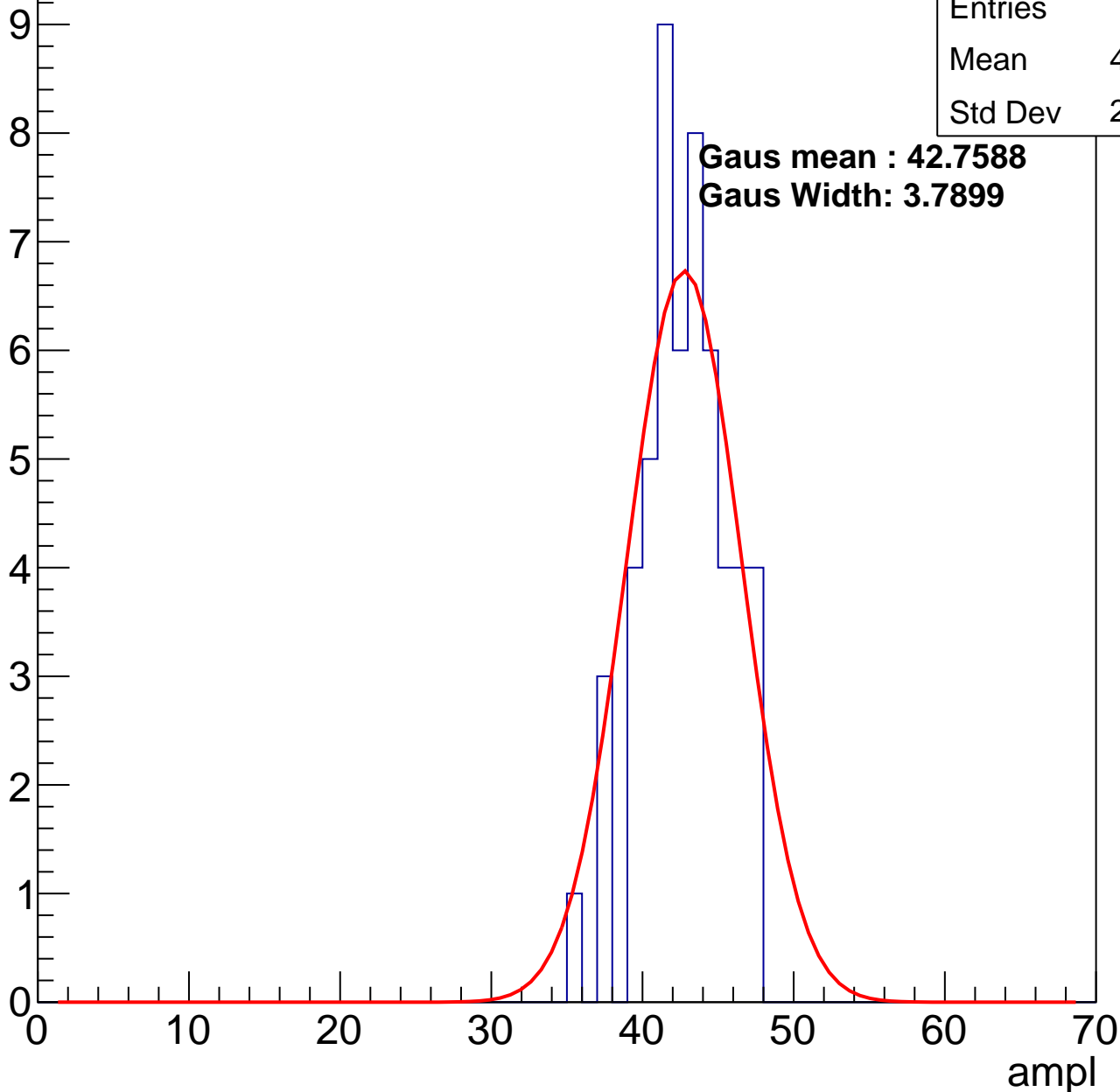
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.28
Std Dev	2.778

**Gaus mean : 42.7588**

**Gaus Width: 3.7899**



# B1L000S, U25-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	48.49
Std Dev	2.754

Entry

10

8

6

4

2

0

0

10

20

30

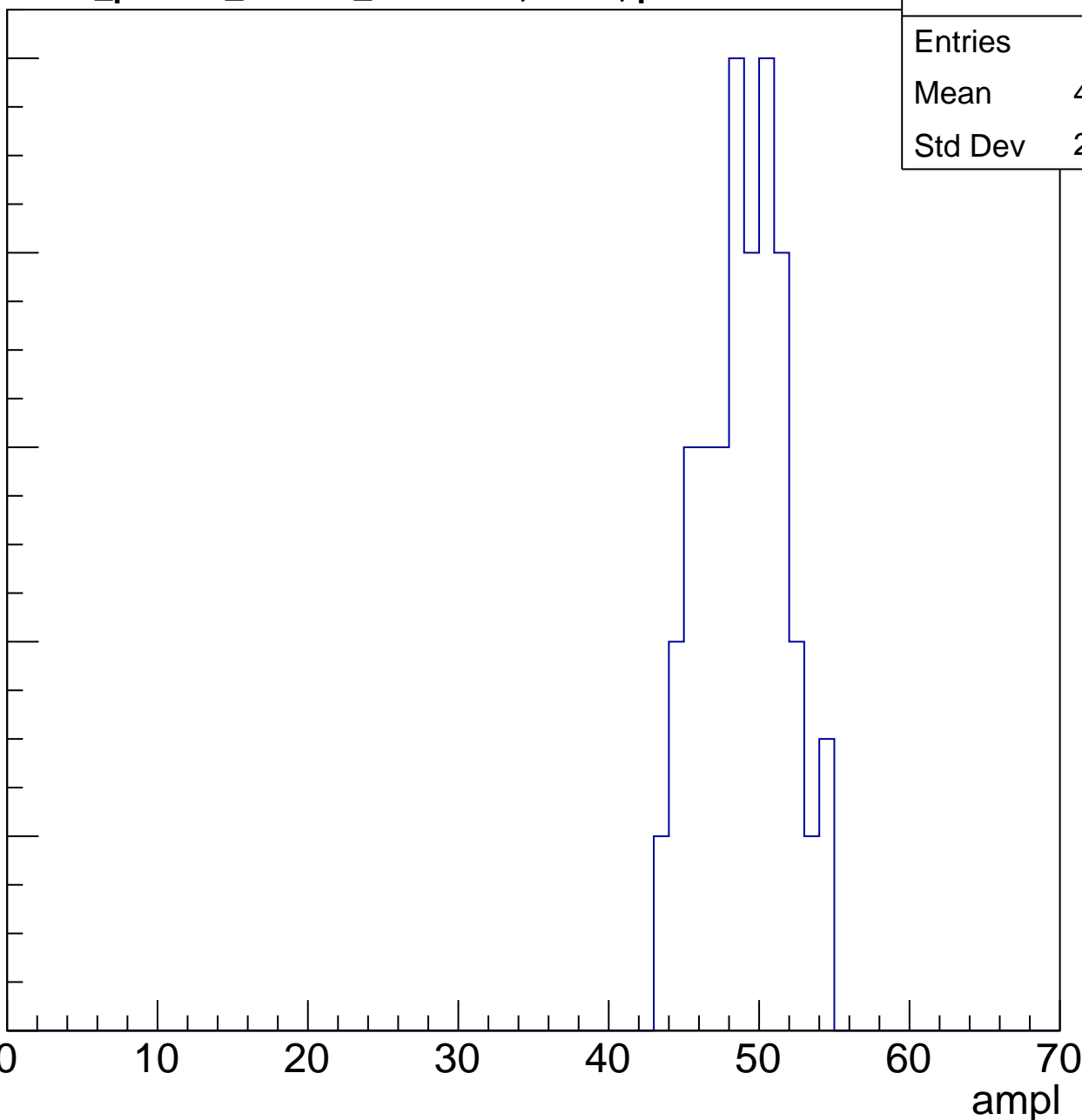
40

50

60

70

ampl

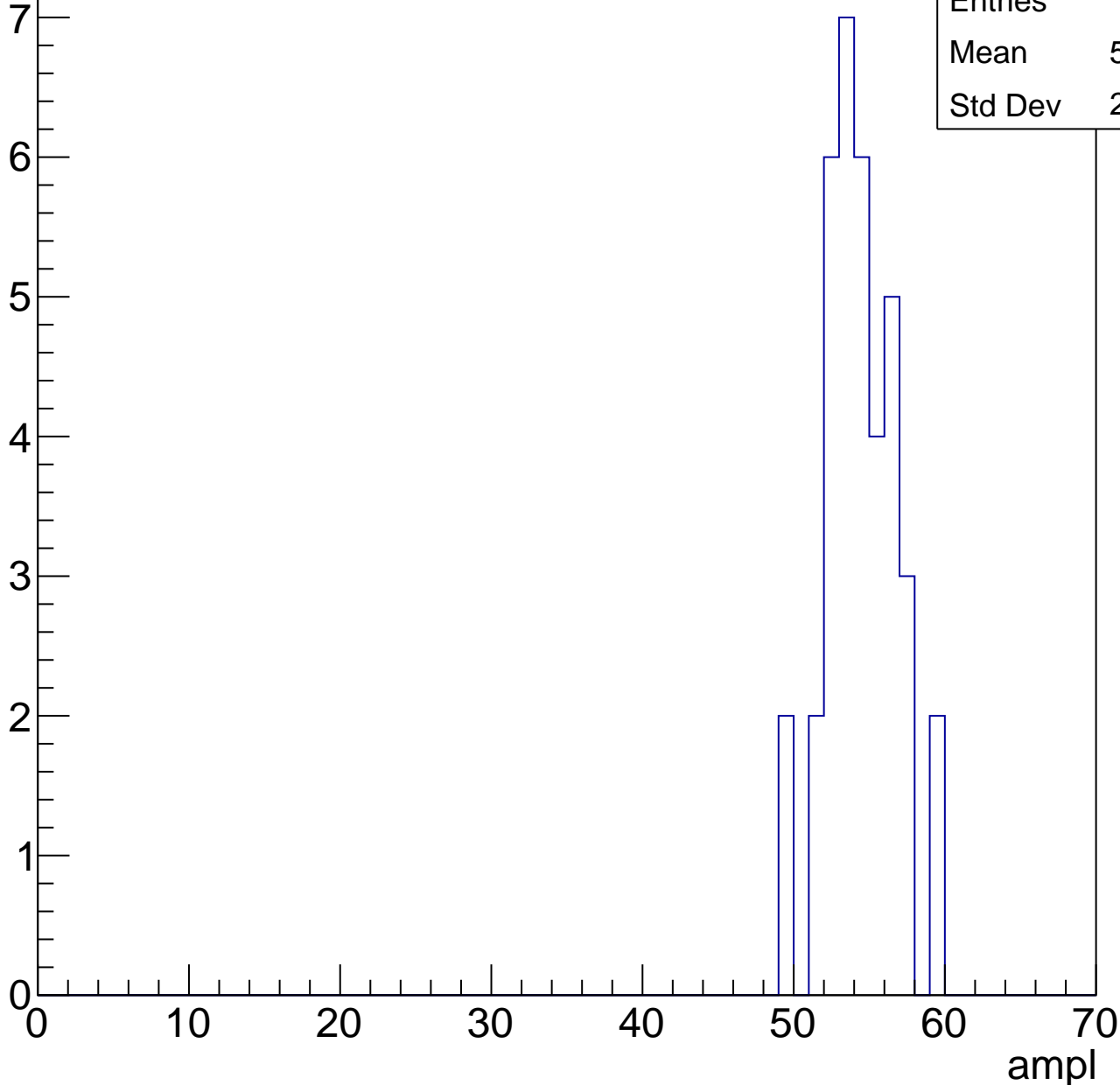


# B1L000S, U25-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

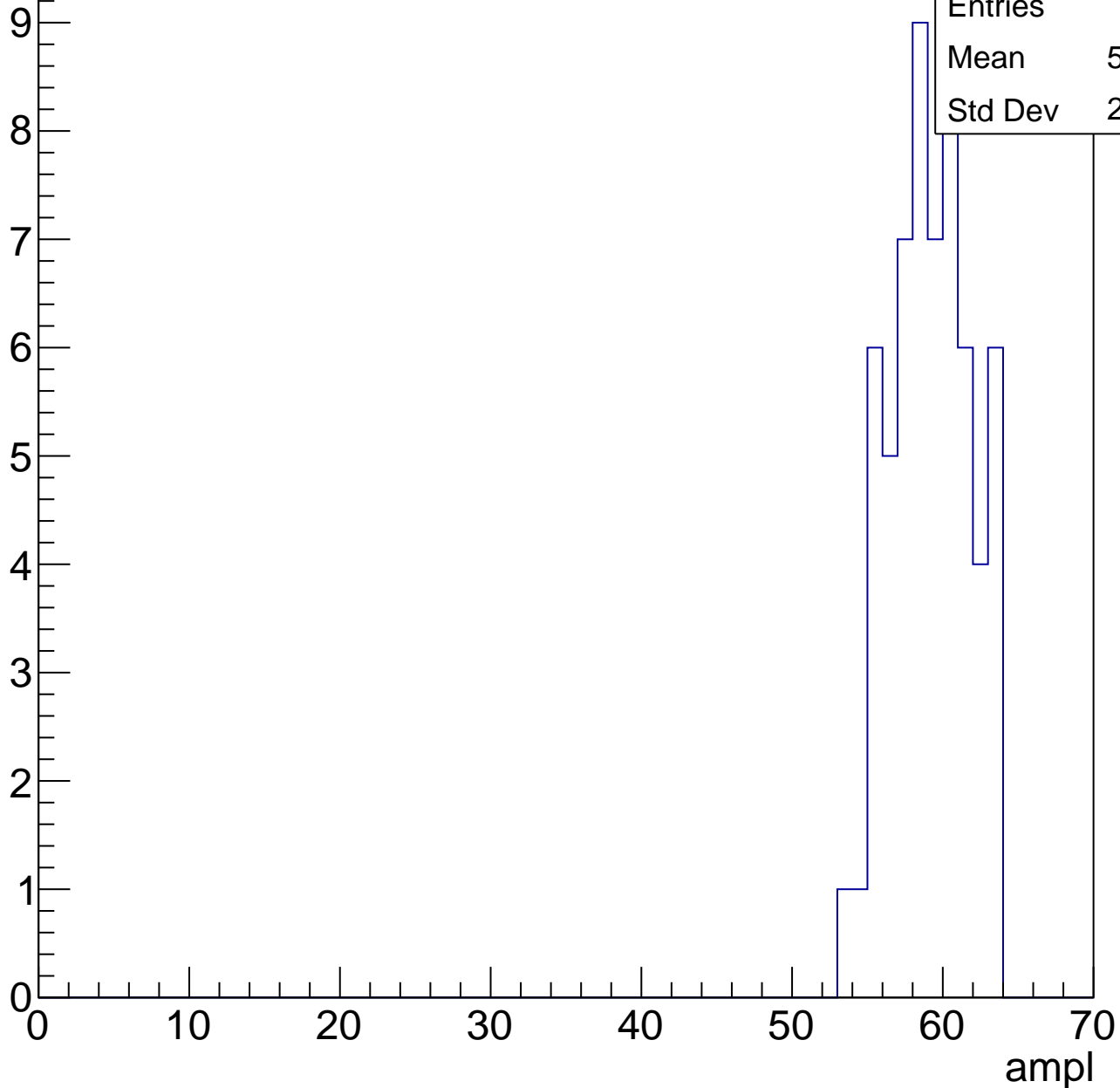
Entries	37
Mean	53.95
Std Dev	2.324



# B1L000S, U25-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

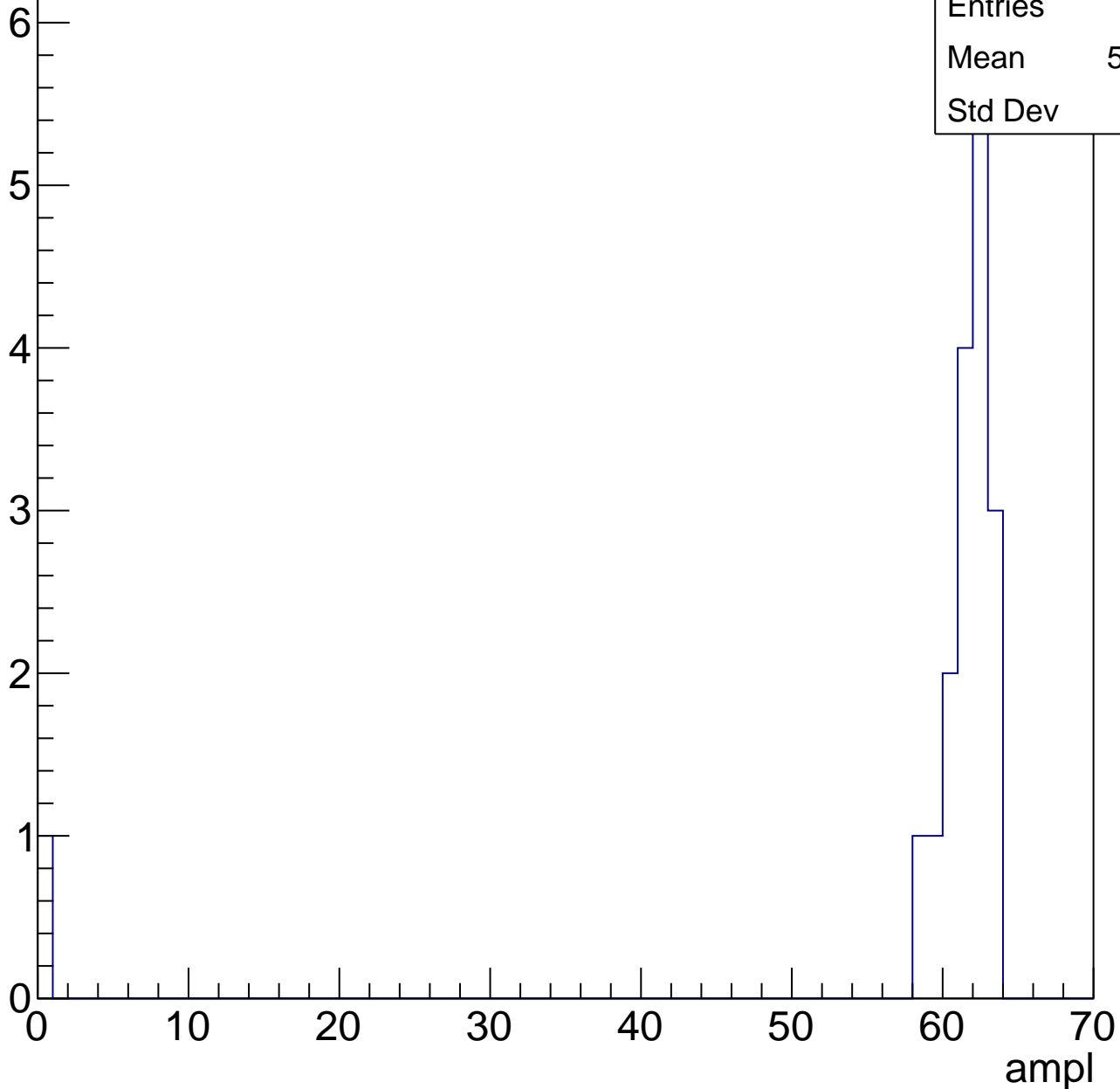


# B1L000S, U25-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	57.89
Std Dev	14.1





# B1L000S, U25-ch96, adc7

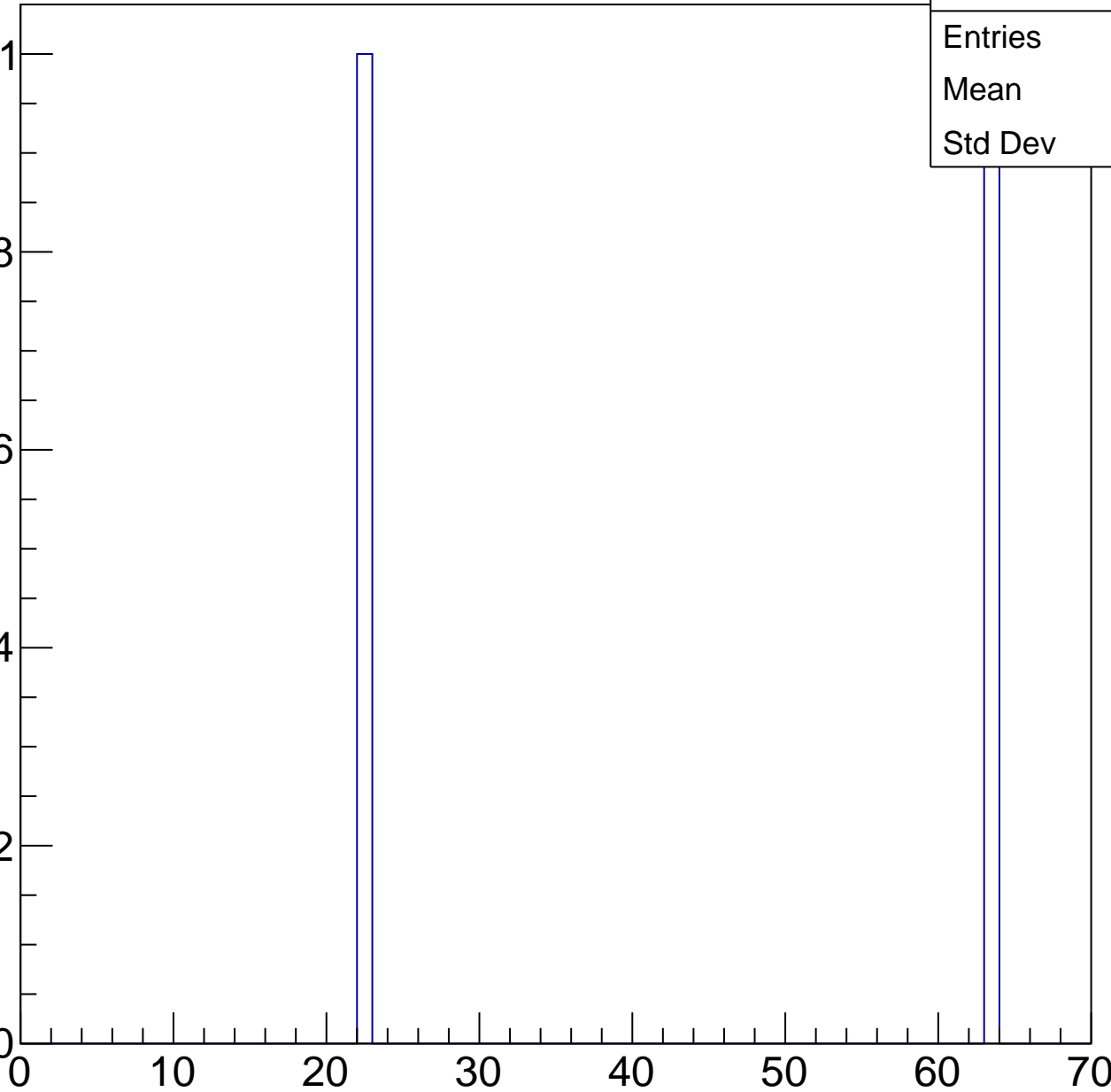
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl



# B1L000S, U25-ch97, adc0

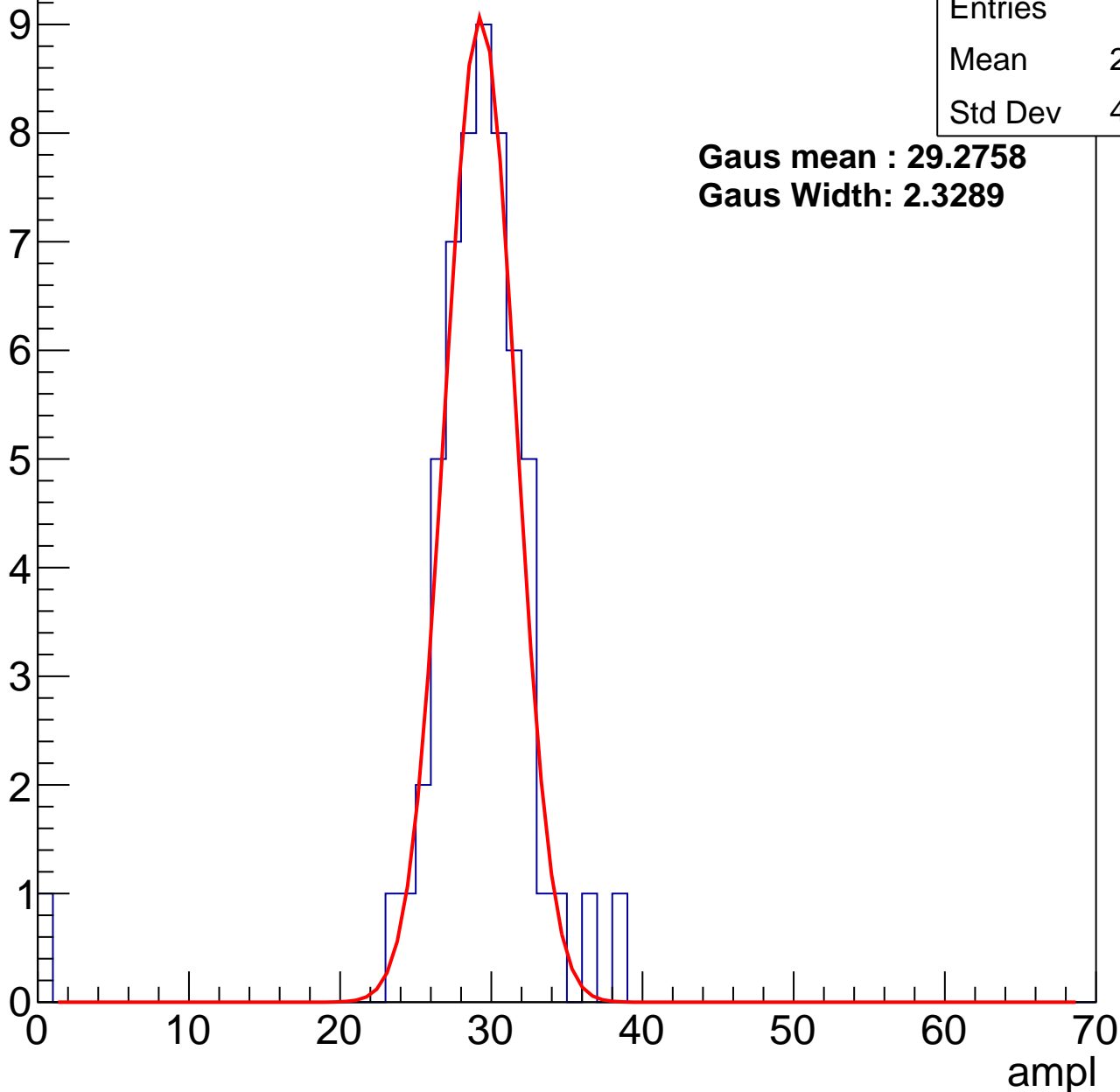
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	28.56
Std Dev	4.687

**Gaus mean : 29.2758**

**Gaus Width: 2.3289**



# B1L000S, U25-ch97, adc1

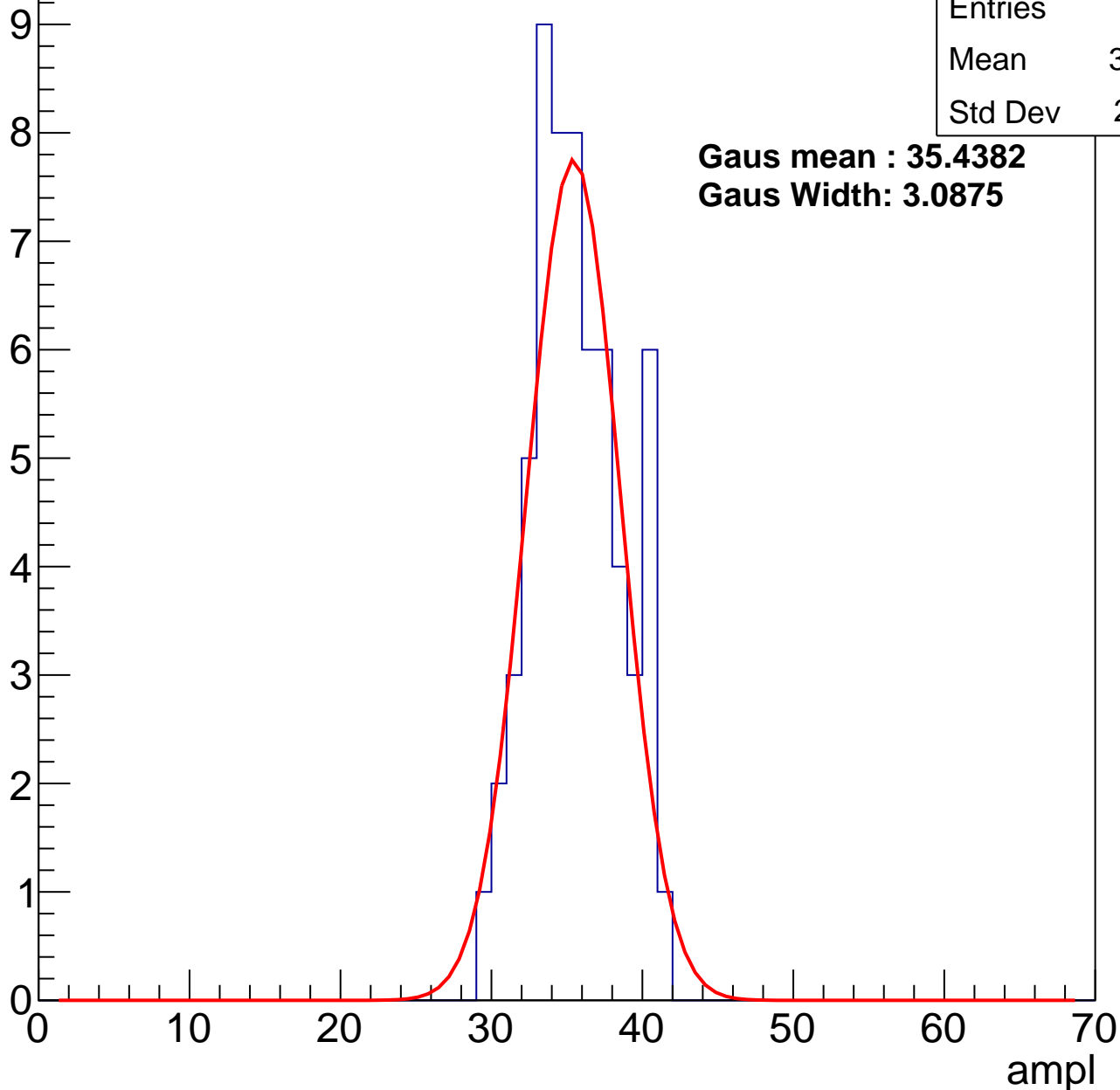
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	35.15
Std Dev	2.901

**Gaus mean : 35.4382**

**Gaus Width: 3.0875**



# B1L000S, U25-ch97, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	42.33
Std Dev	3.839

**Gaus mean : 43.1186**

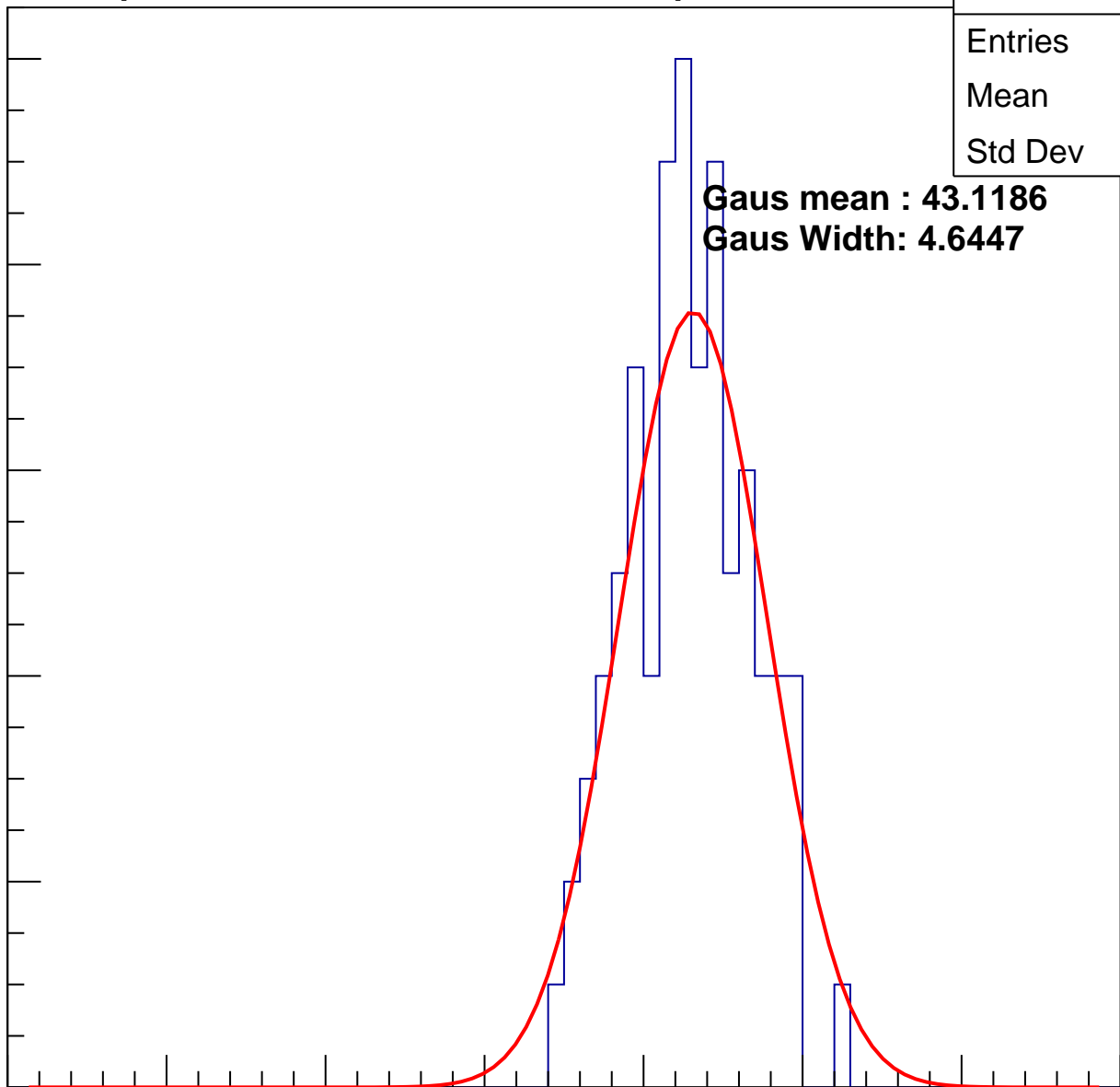
**Gaus Width: 4.6447**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

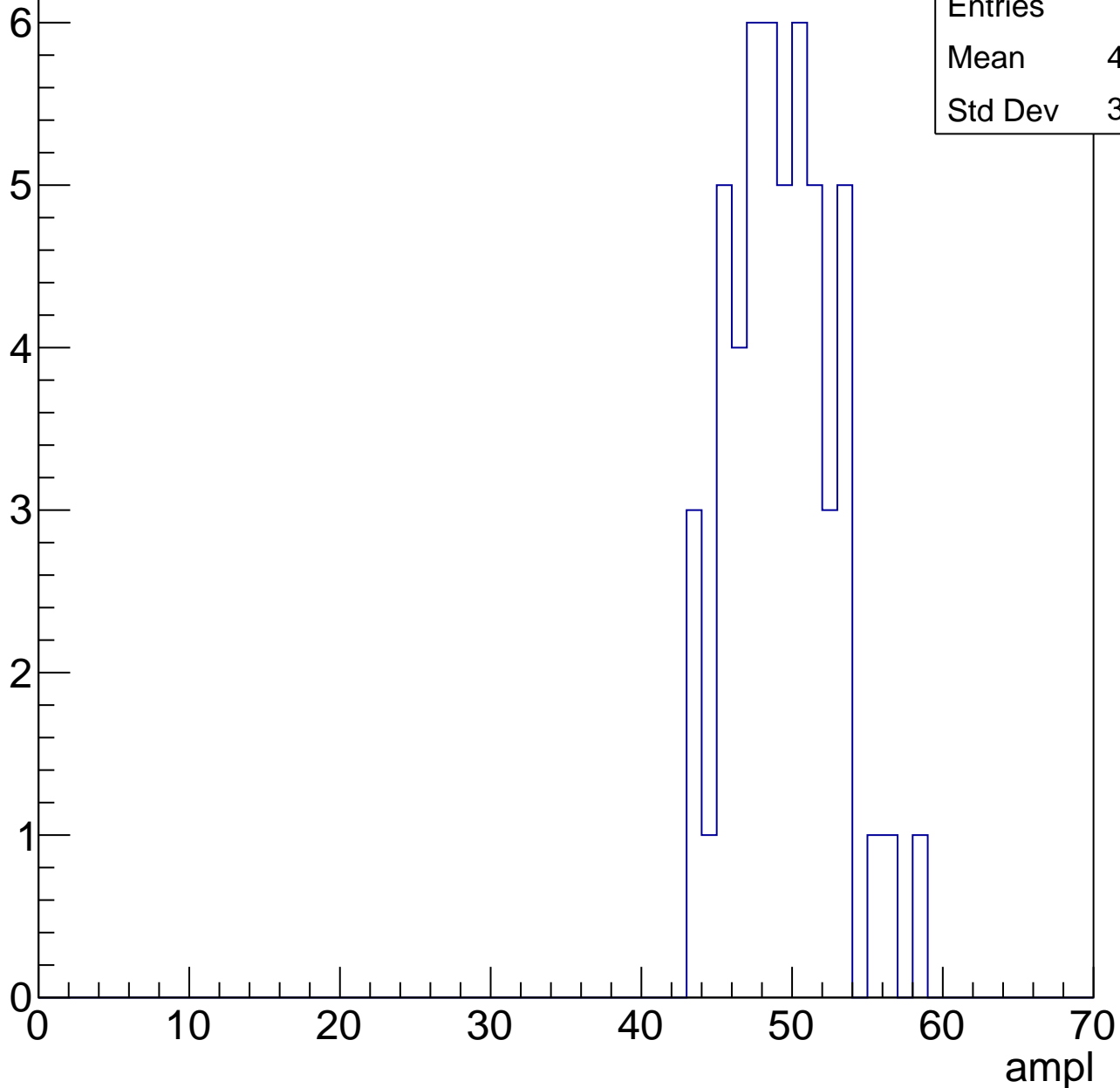


# B1L000S, U25-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	48.88
Std Dev	3.332

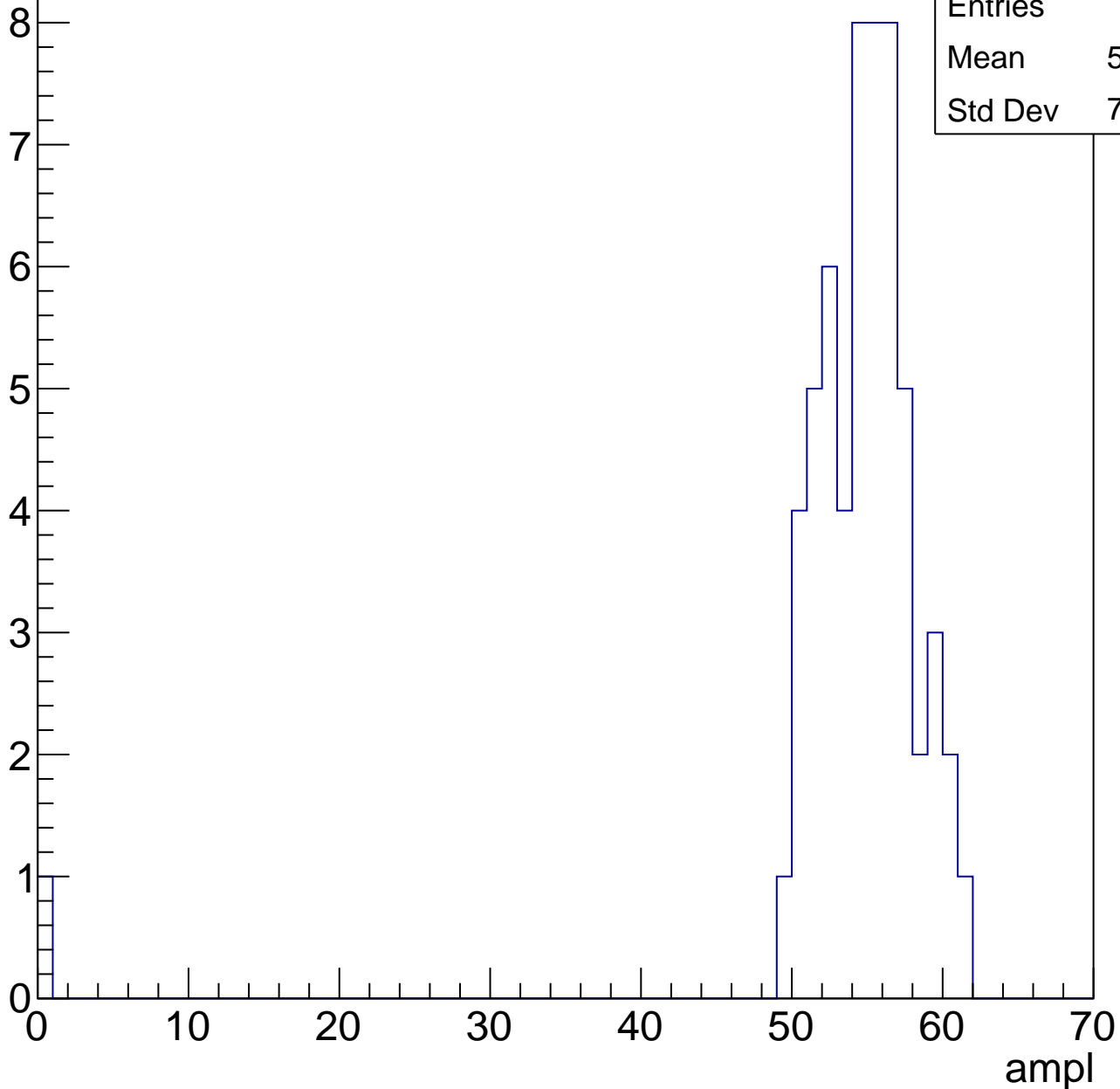


# B1L000S, U25-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	53.57
Std Dev	7.634

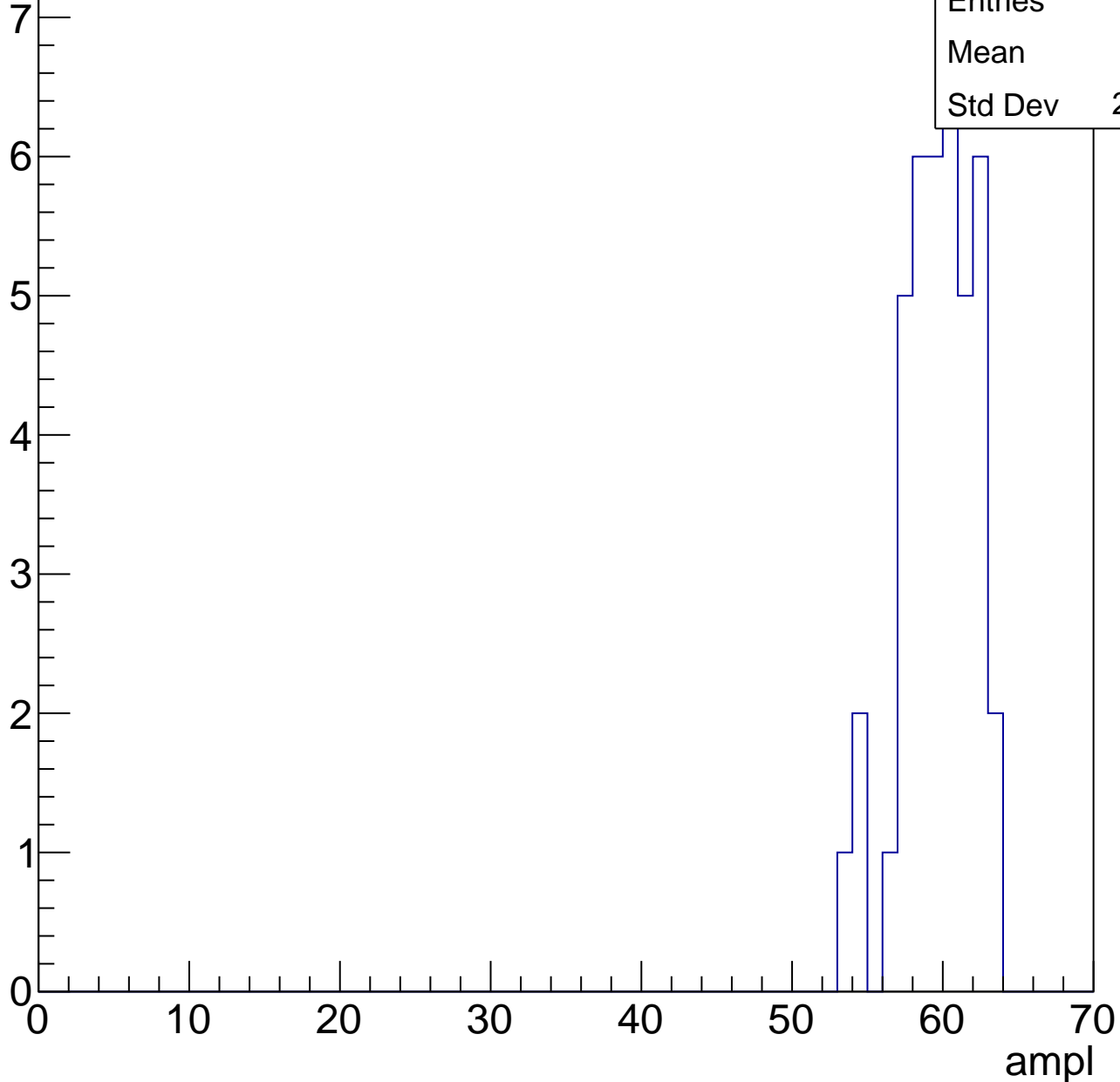


# B1L000S, U25-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	41
Mean	59.2
Std Dev	2.381

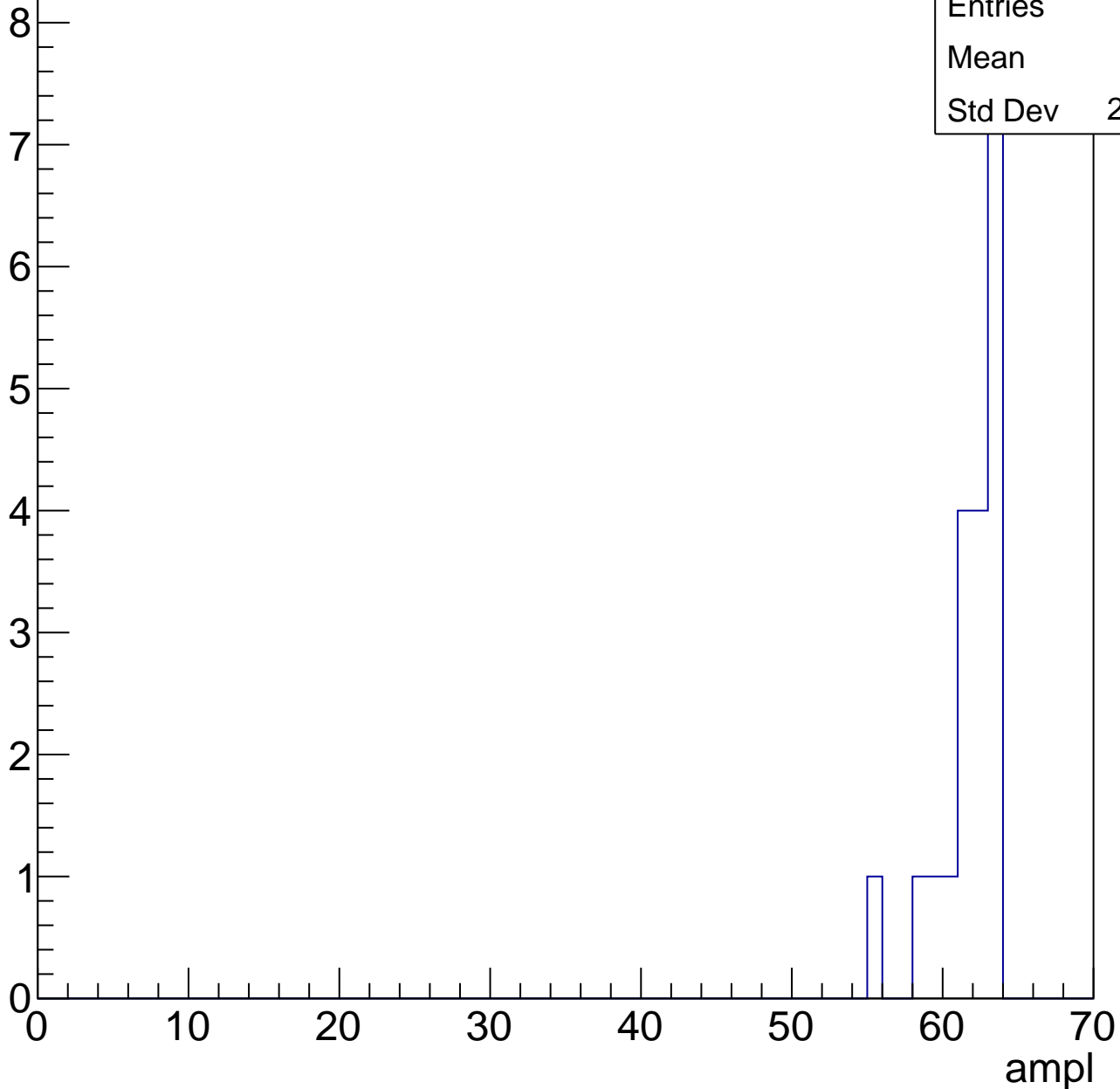


# B1L000S, U25-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	61.4
Std Dev	2.035





# B1L000S, U25-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch98, adc0

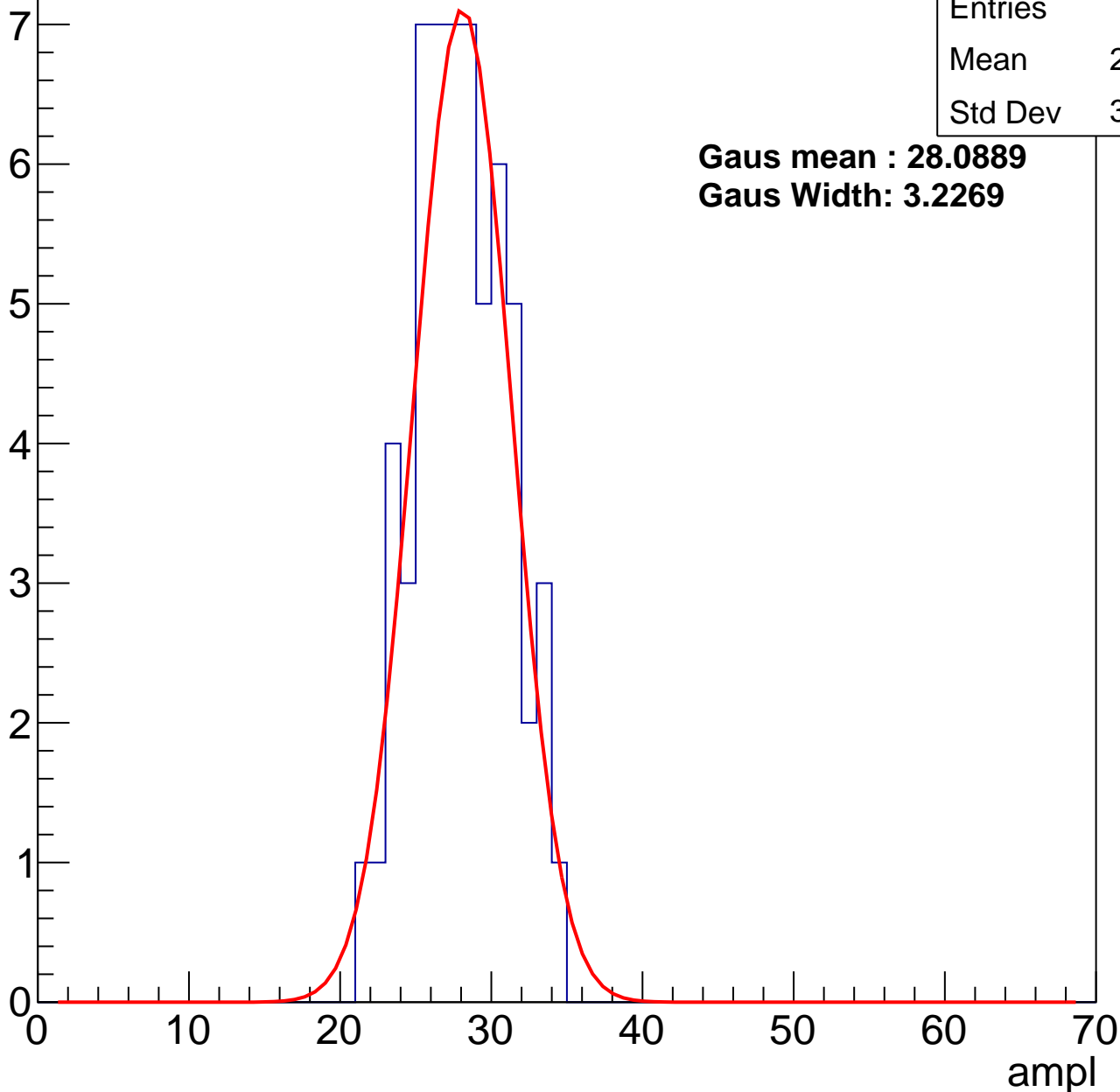
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	27.56
Std Dev	3.016

**Gaus mean : 28.0889**

**Gaus Width: 3.2269**



# B1L000S, U25-ch98, adc1

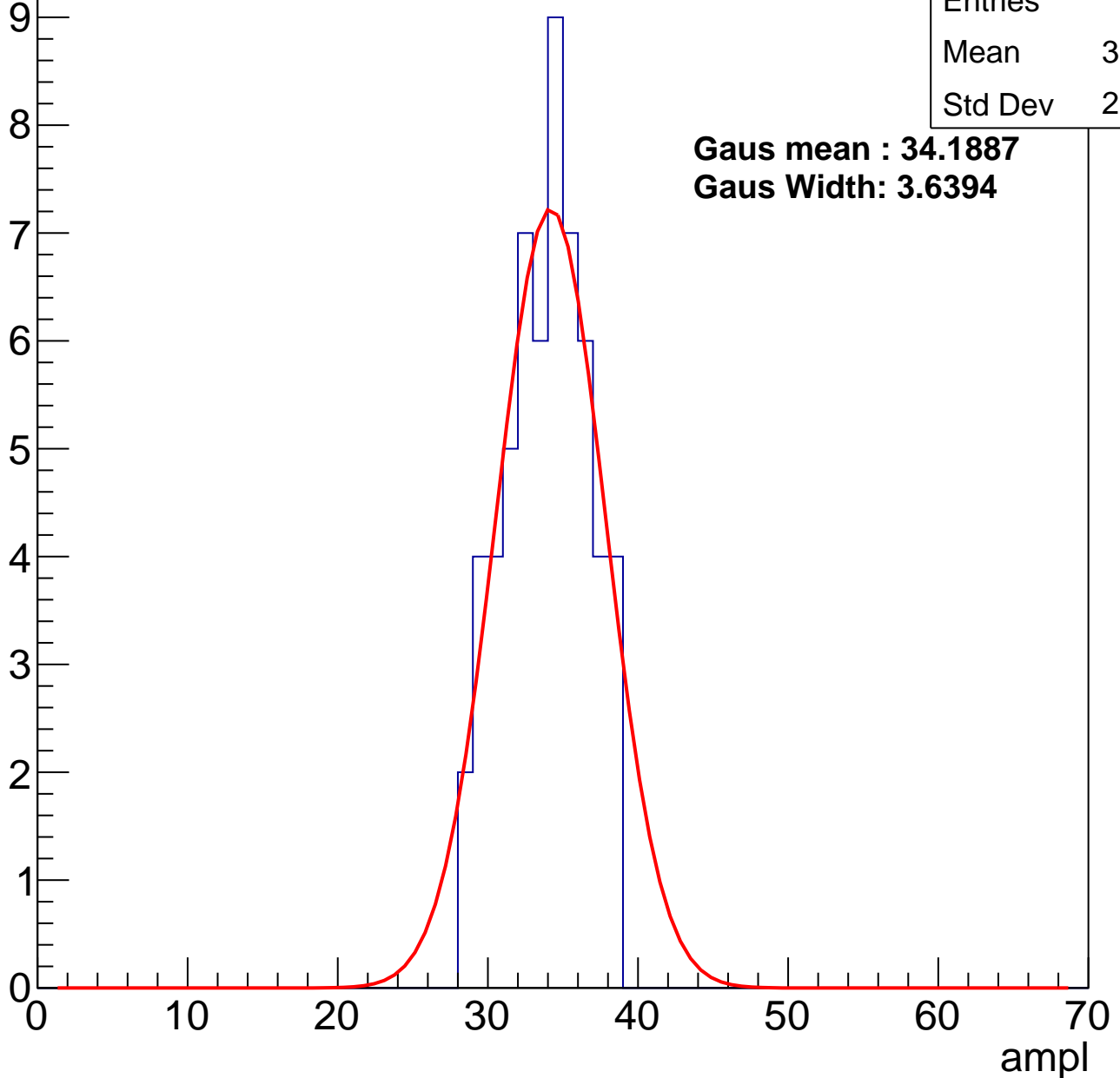
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	33.38
Std Dev	2.703

**Gaus mean : 34.1887**

**Gaus Width: 3.6394**



# B1L000S, U25-ch98, adc2

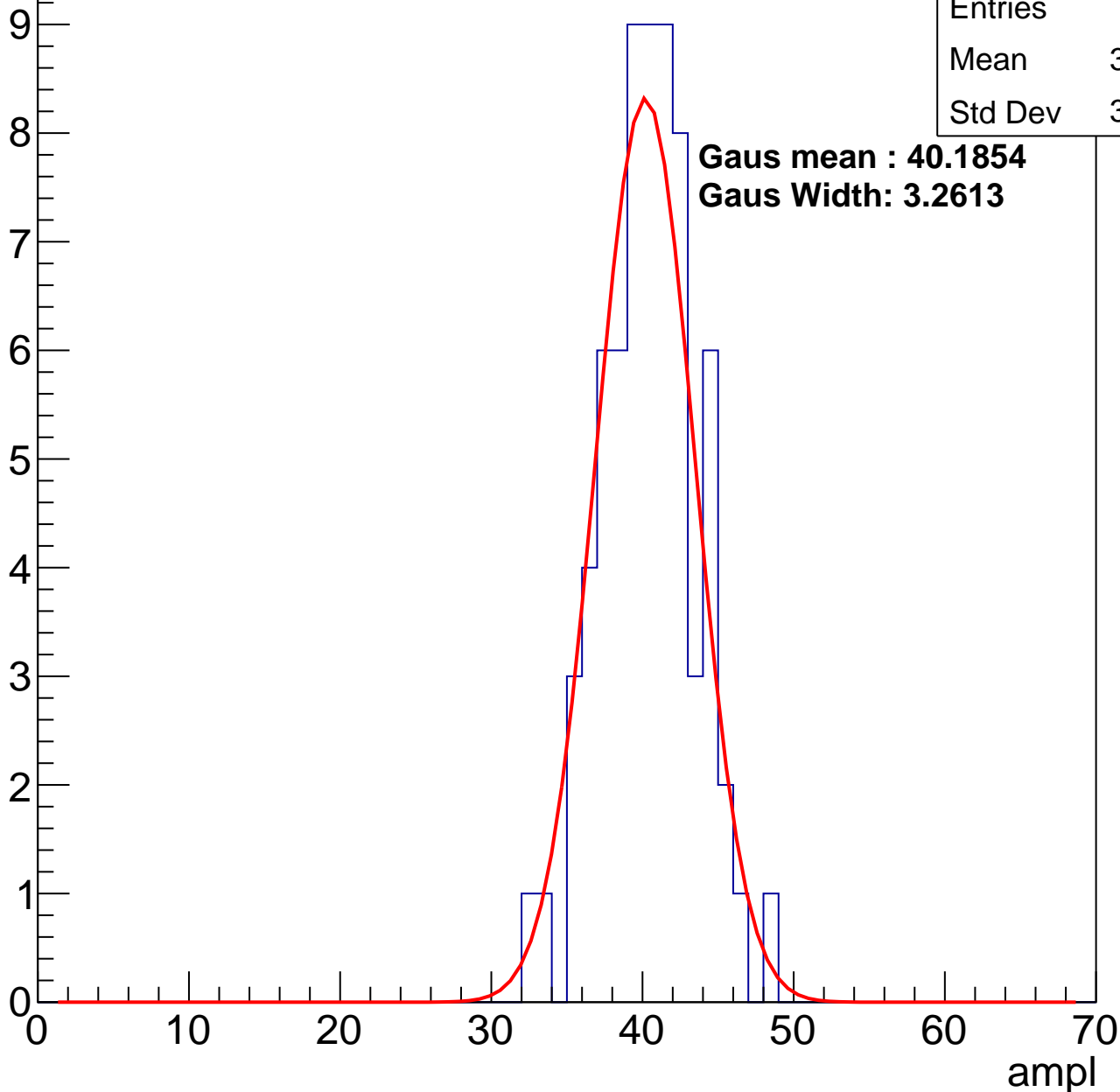
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	39.96
Std Dev	3.076

**Gaus mean : 40.1854**

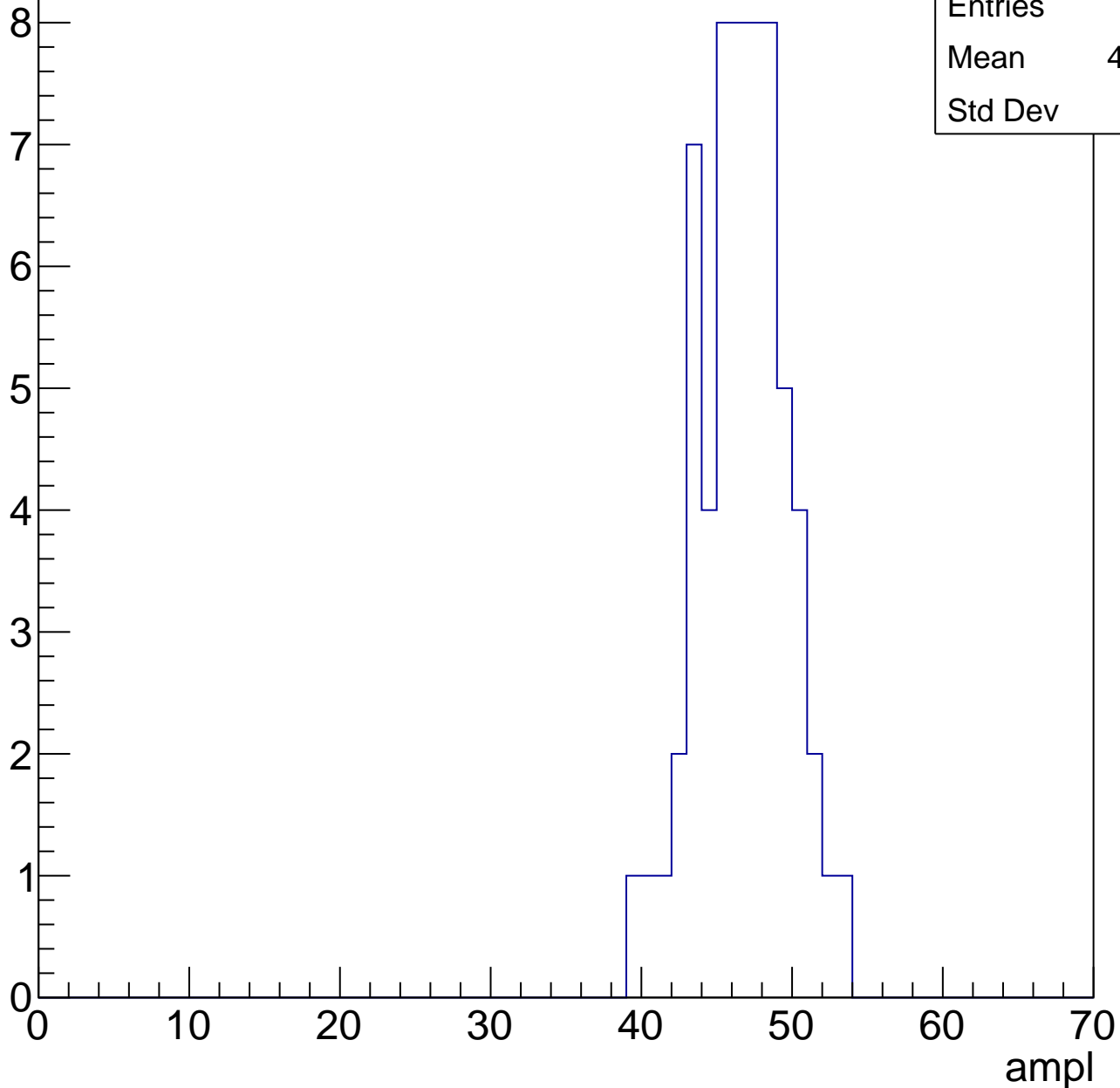
**Gaus Width: 3.2613**



# B1L000S, U25-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

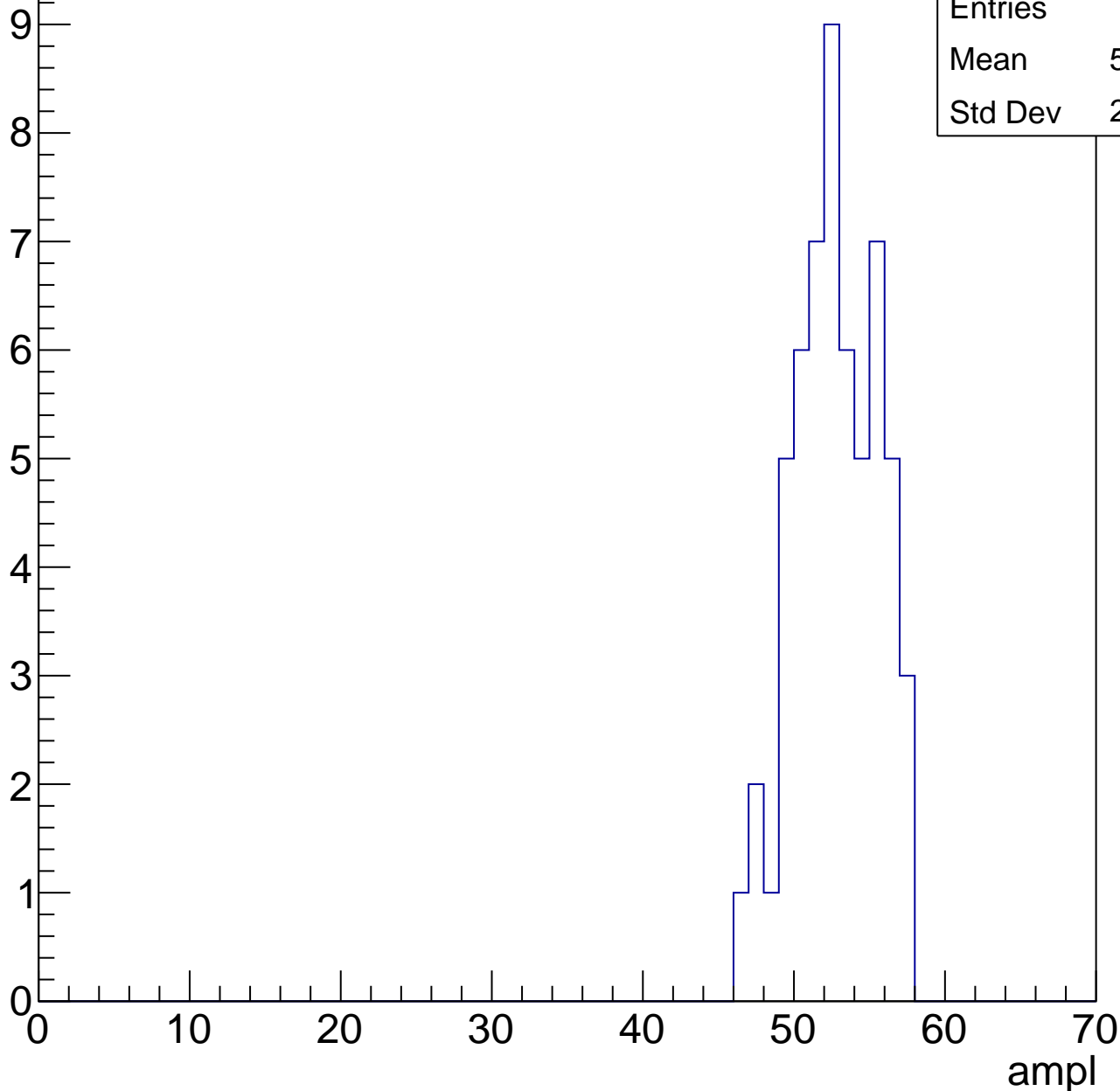


# B1L000S, U25-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

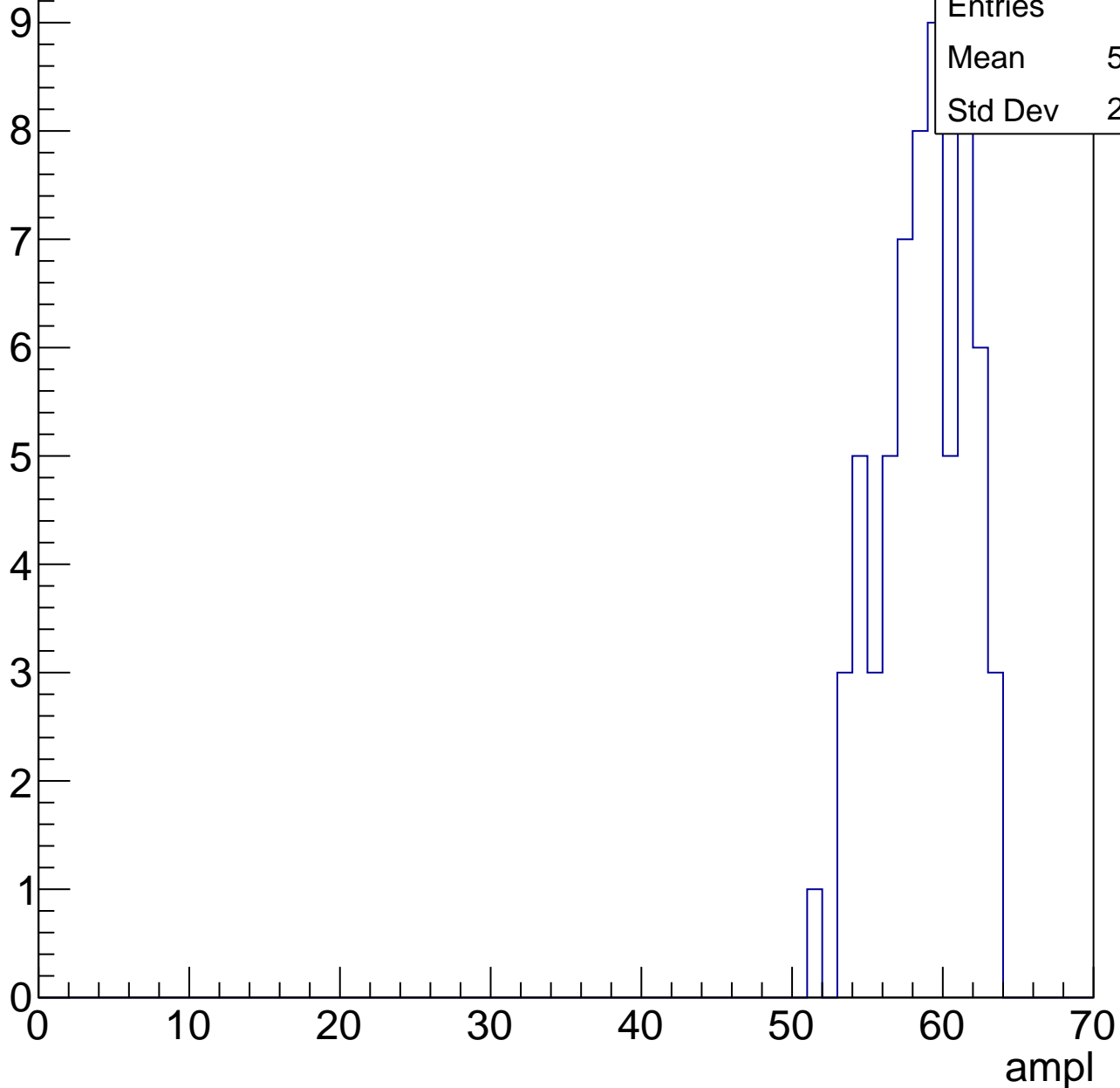
Entries	57
Mean	52.32
Std Dev	2.703



# B1L000S, U25-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

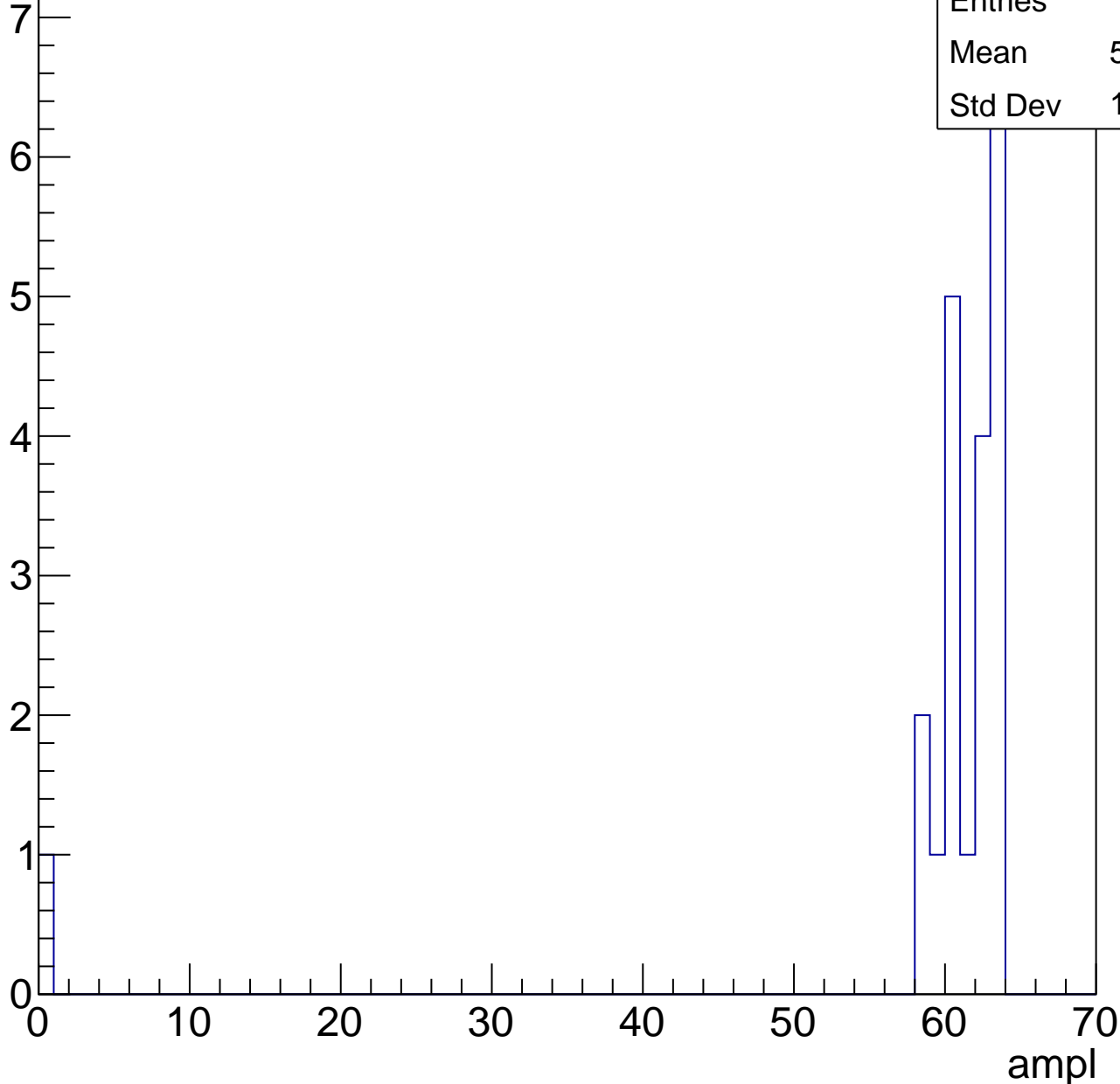


# B1L000S, U25-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	21
Mean	58.33
Std Dev	13.15

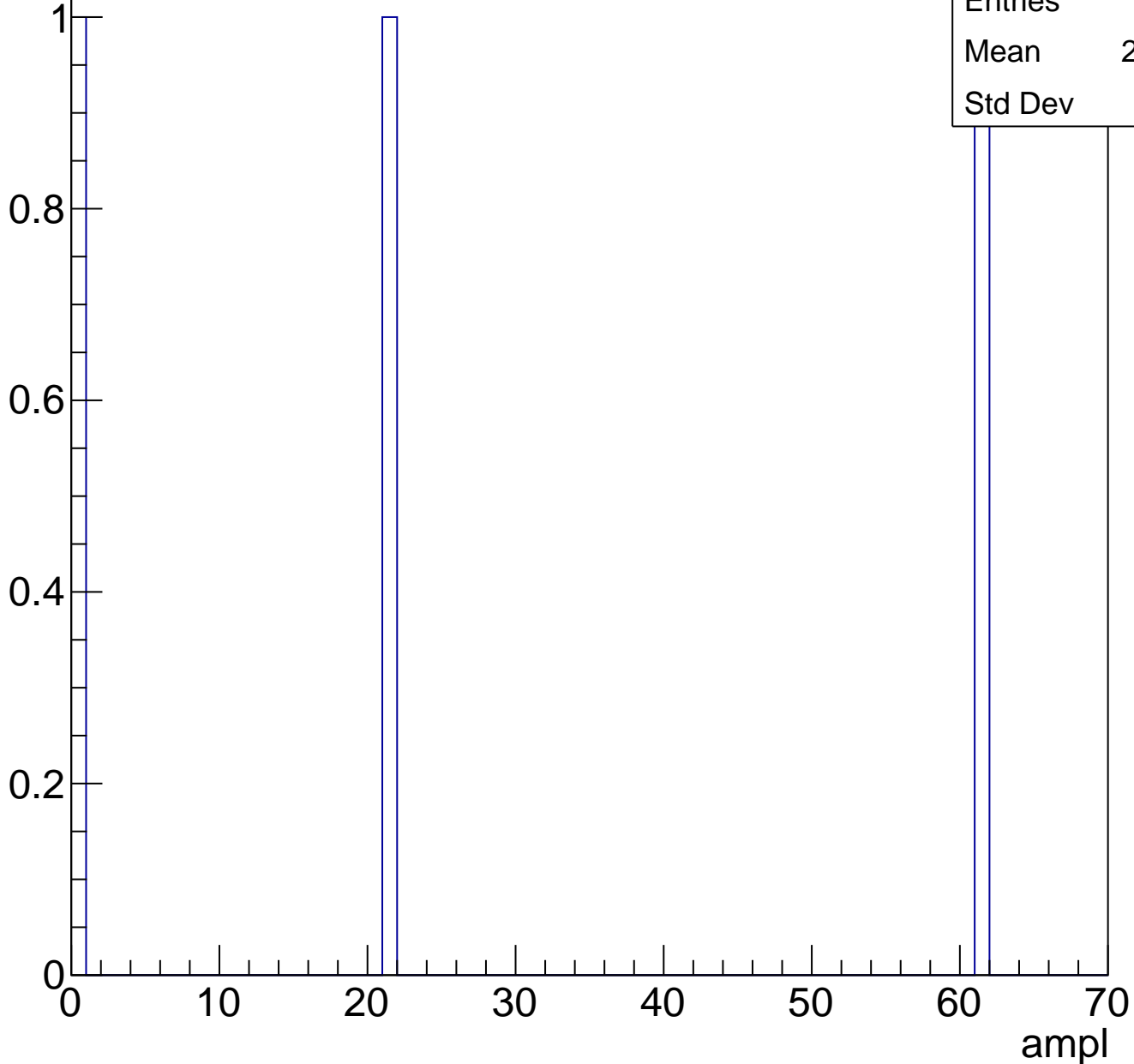




# B1L000S, U25-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	3
Mean	27.33
Std Dev	25.3

# B1L000S, U25-ch99, adc0

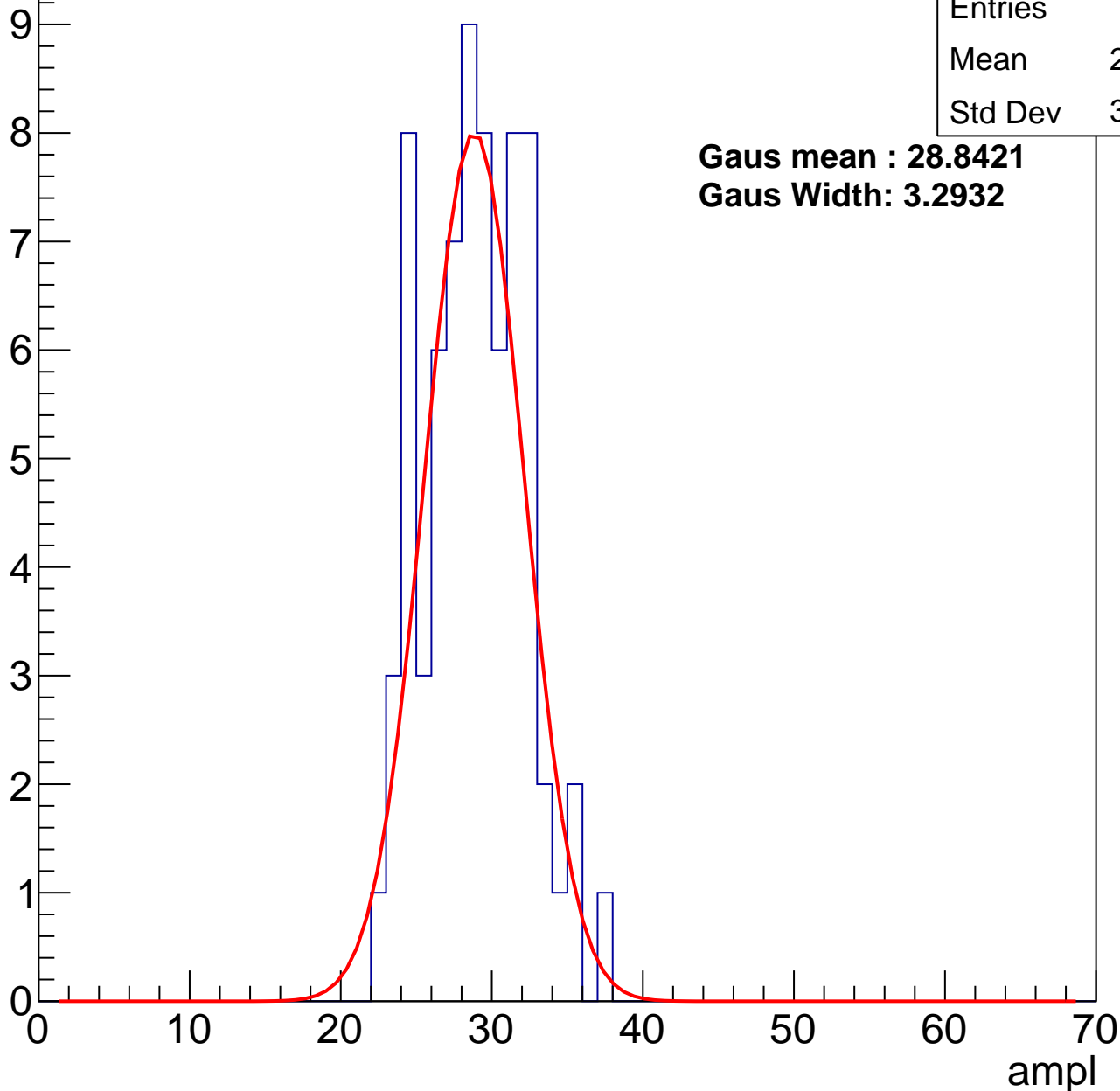
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	73
Mean	28.47
Std Dev	3.265

**Gaus mean : 28.8421**

**Gaus Width: 3.2932**



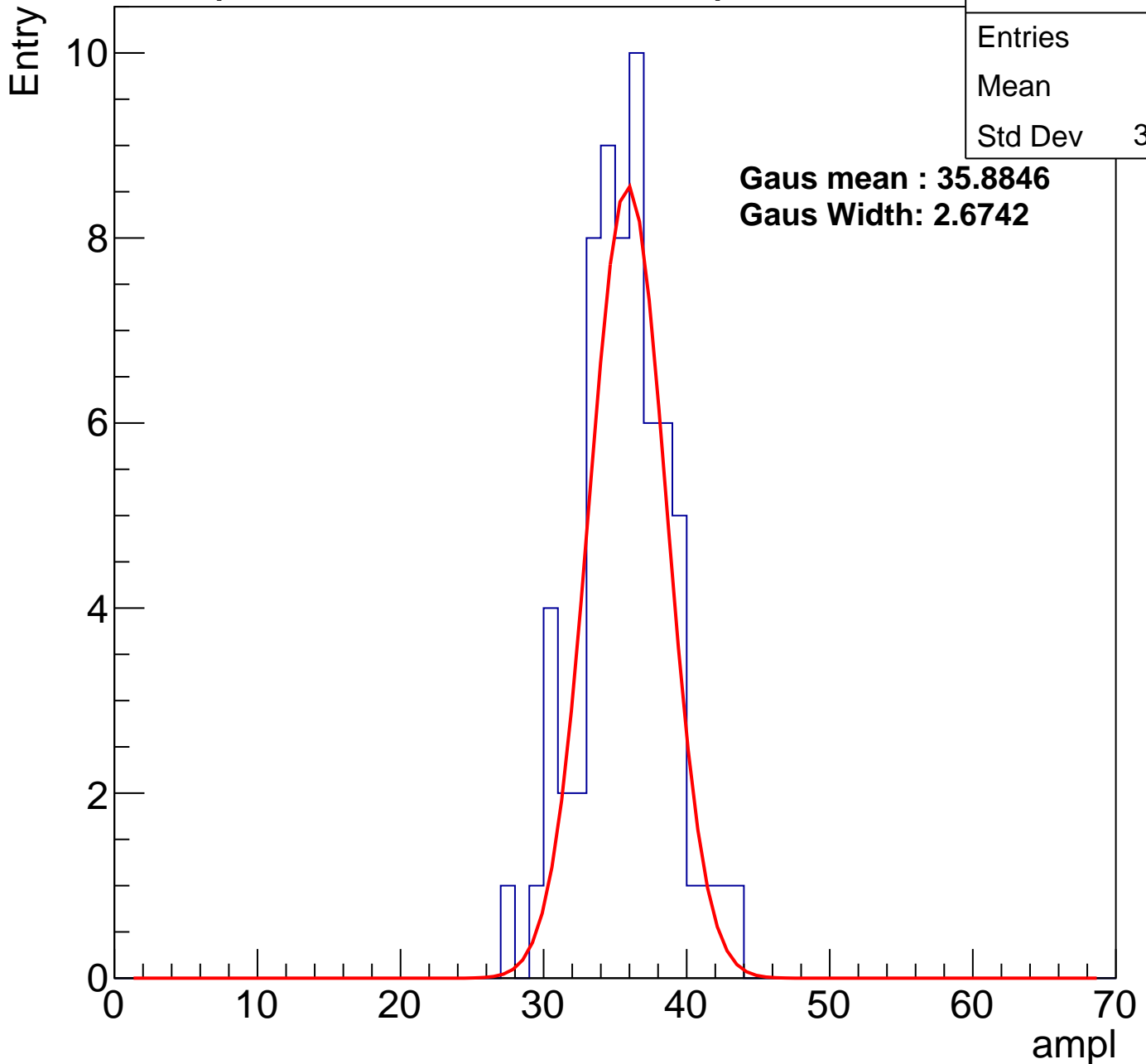
# B1L000S, U25-ch99, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	35.2
Std Dev	3.091

**Gaus mean : 35.8846**

**Gaus Width: 2.6742**



# B1L000S, U25-ch99, adc2

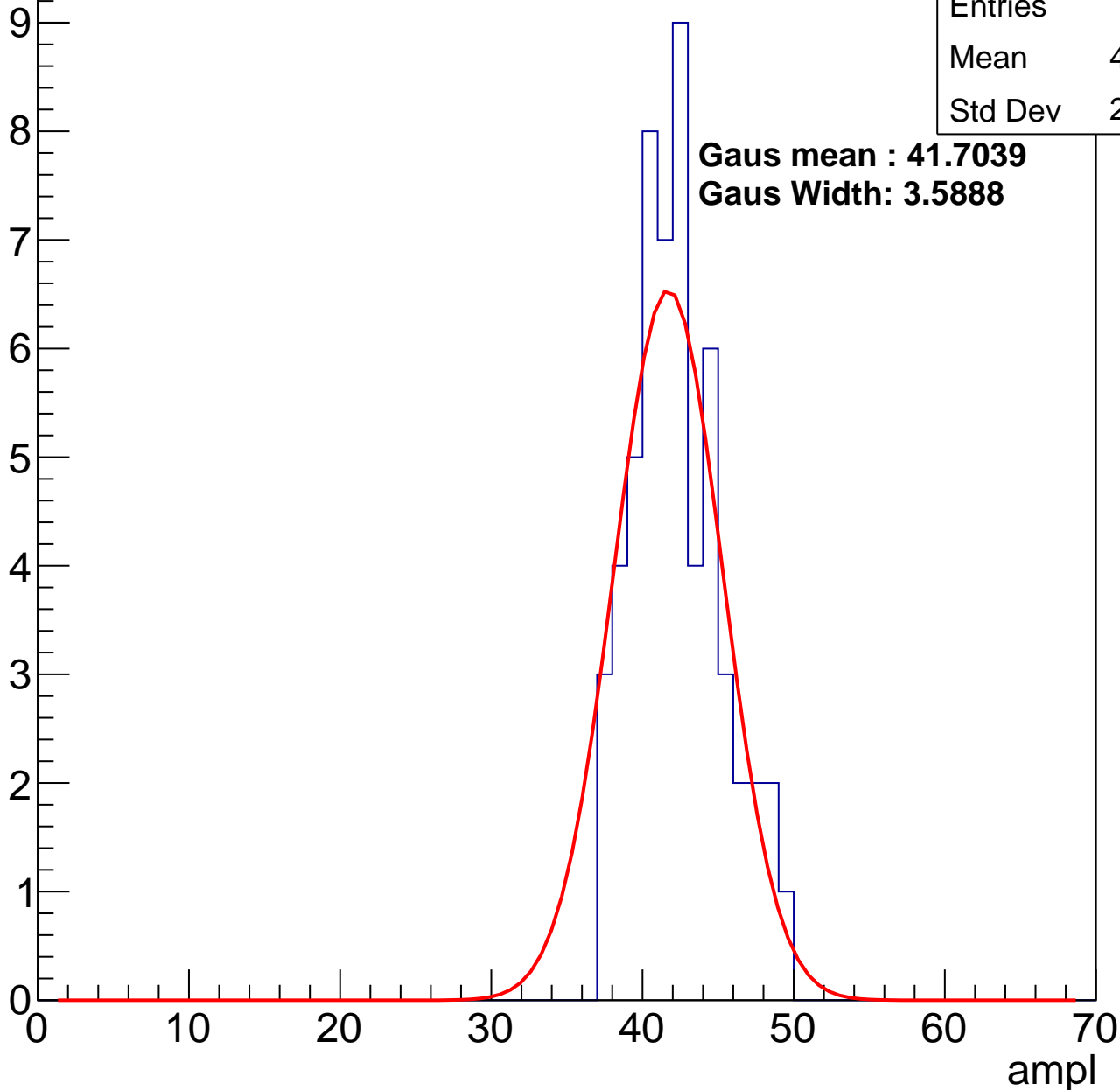
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	41.88
Std Dev	2.928

**Gaus mean : 41.7039**

**Gaus Width: 3.5888**

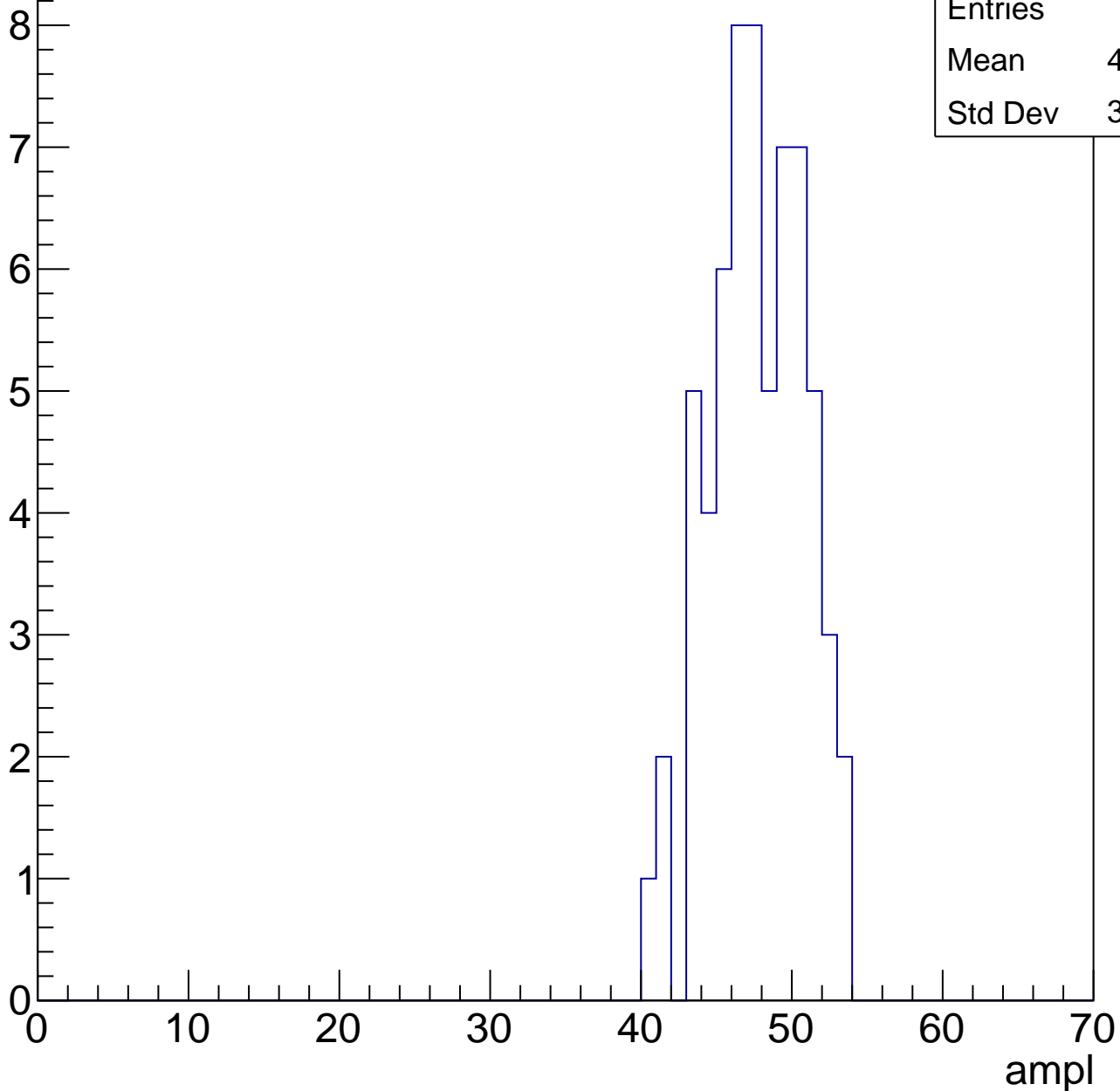


# B1L000S, U25-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	47.25
Std Dev	3.055

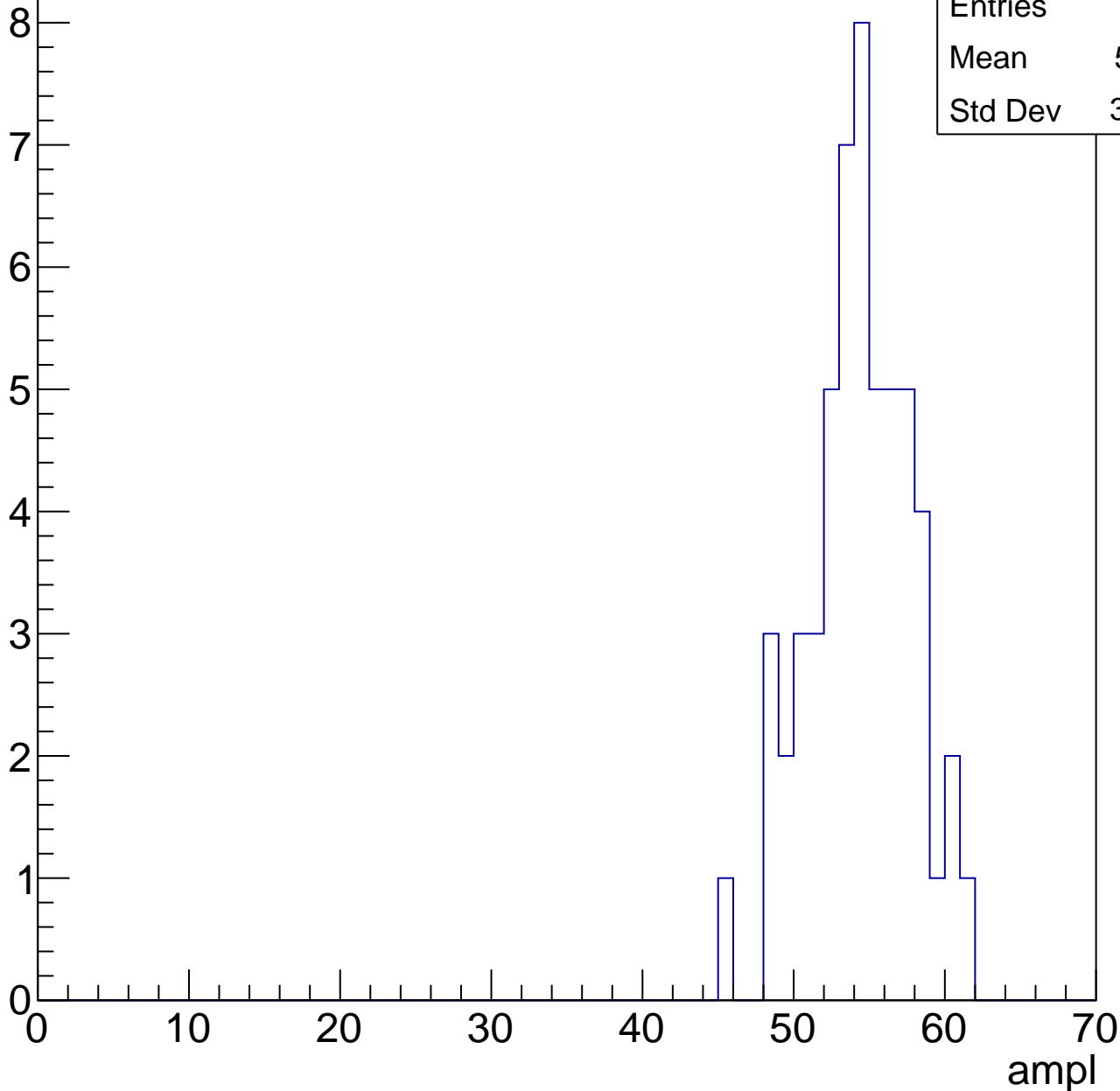


# B1L000S, U25-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

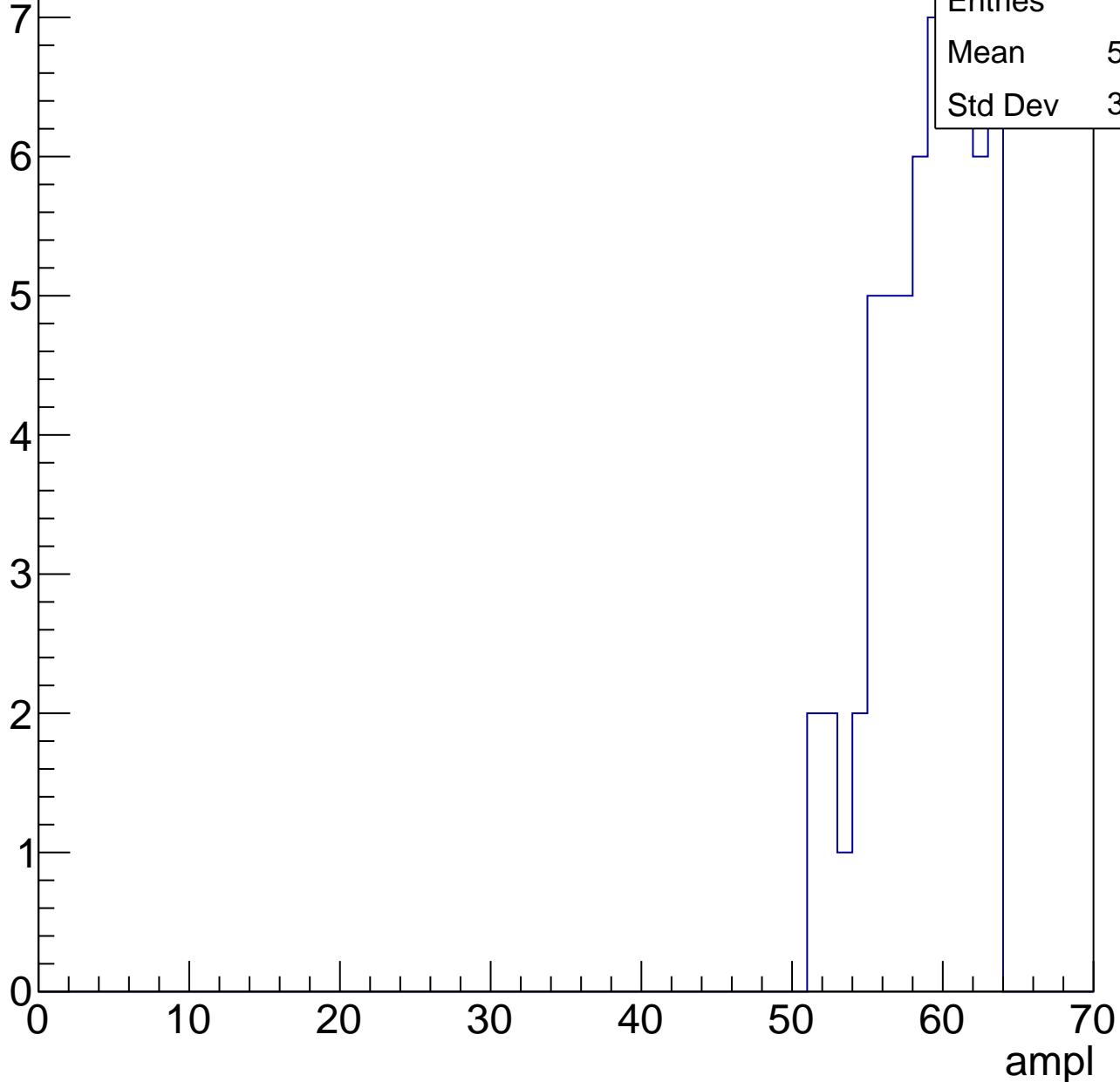
Entries	55
Mean	53.91
Std Dev	3.359



# B1L000S, U25-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	62
Mean	58.52
Std Dev	3.237

# B1L000S, U25-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

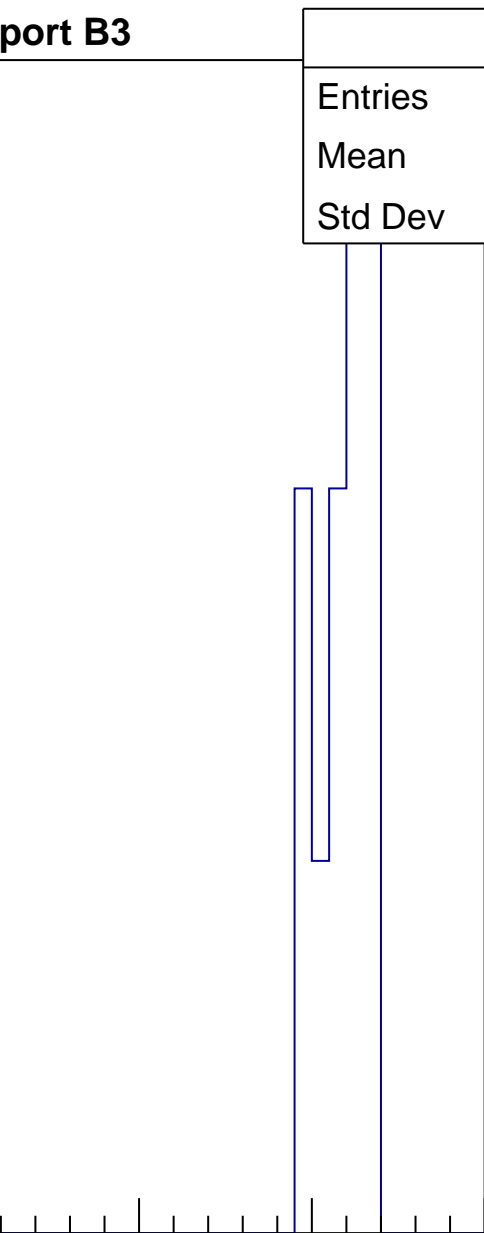
Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	11
Mean	61.36
Std Dev	1.432

ampl

0 10 20 30 40 50 60 70

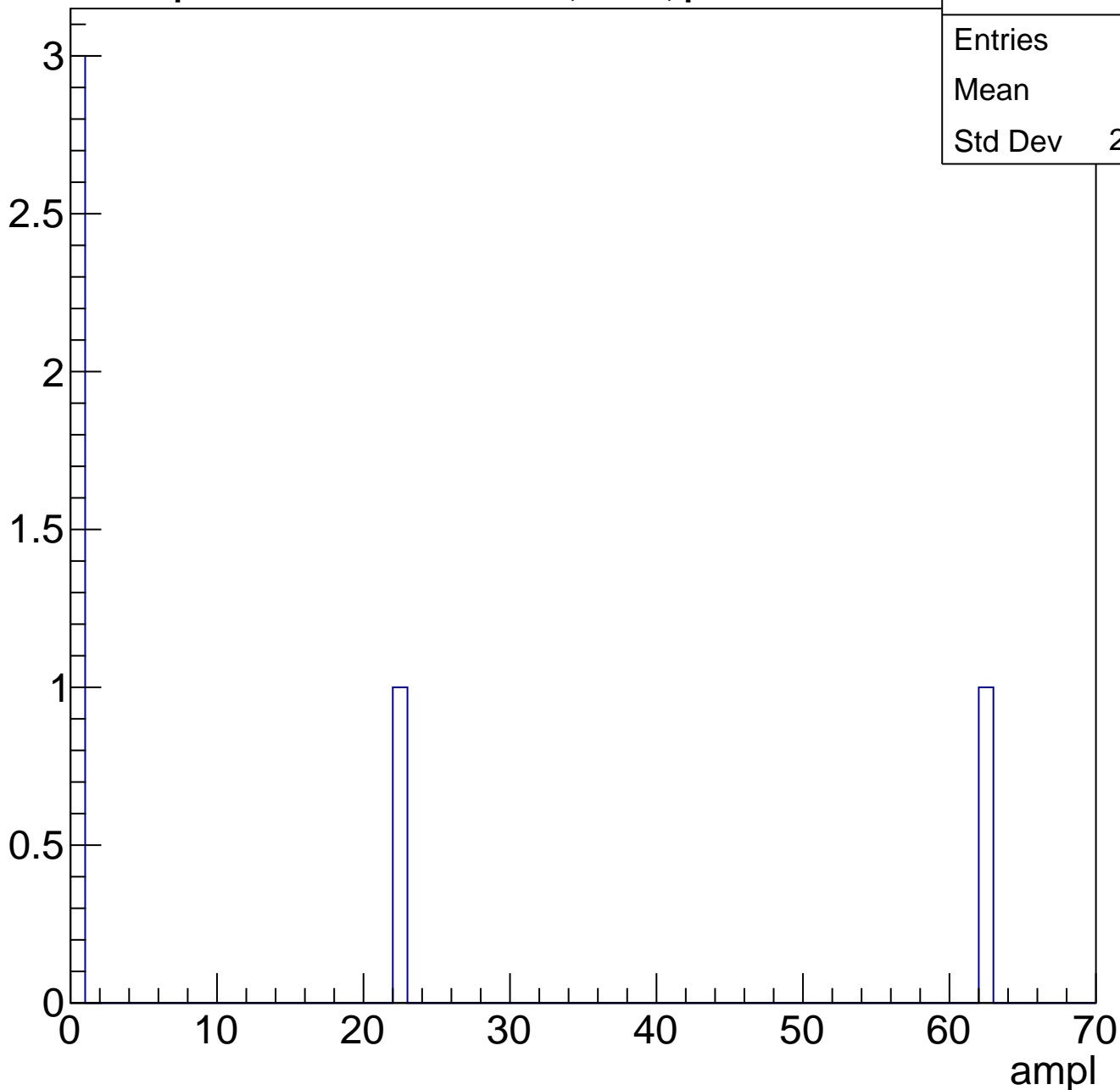




# B1L000S, U25-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	5
Mean	16.8
Std Dev	24.15

# B1L000S, U25-ch100, adc0

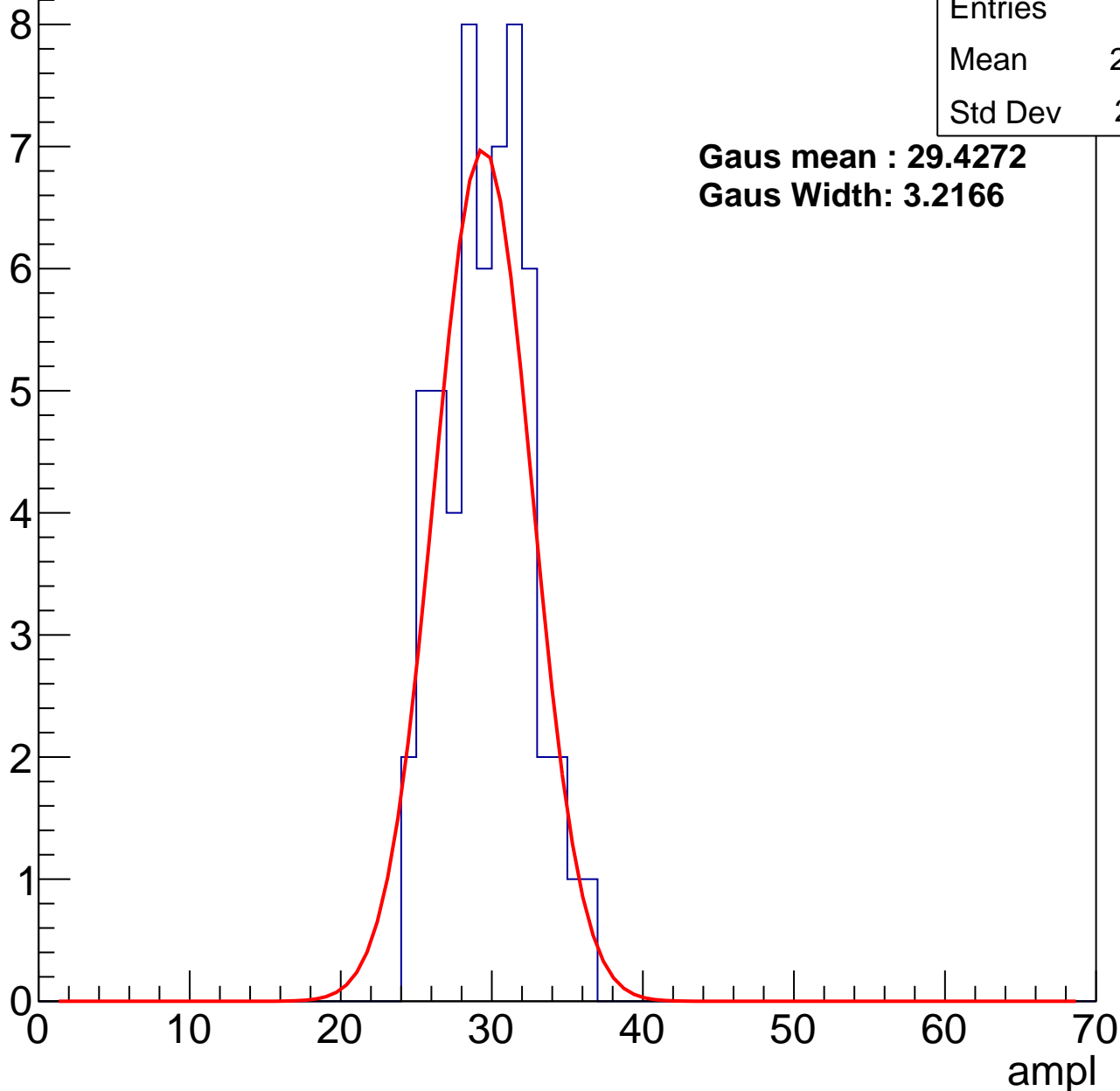
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	29.19
Std Dev	2.831

**Gaus mean : 29.4272**

**Gaus Width: 3.2166**



# B1L000S, U25-ch100, adc1

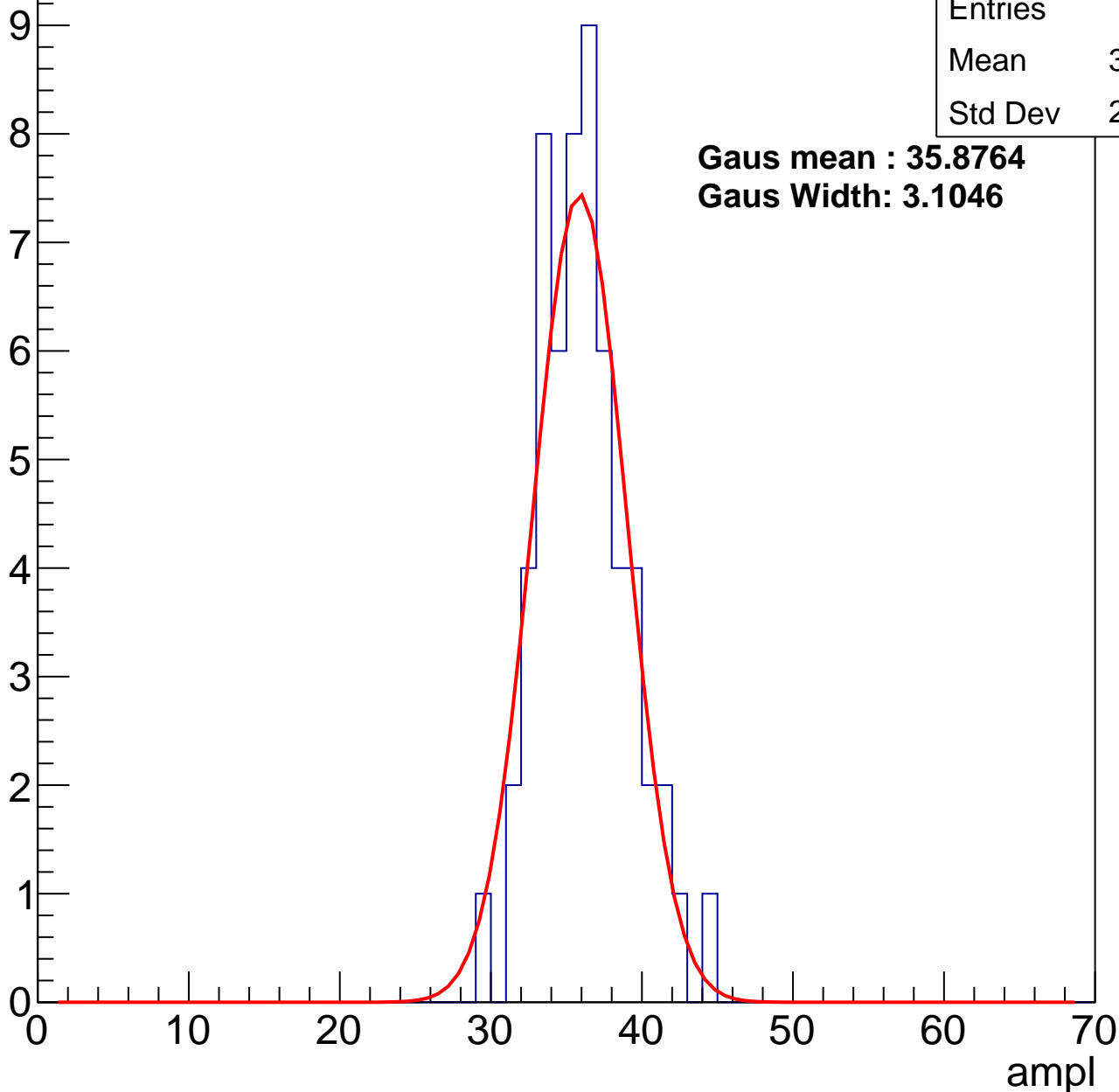
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	35.67
Std Dev	2.938

**Gaus mean : 35.8764**

**Gaus Width: 3.1046**



# B1L000S, U25-ch100, adc2

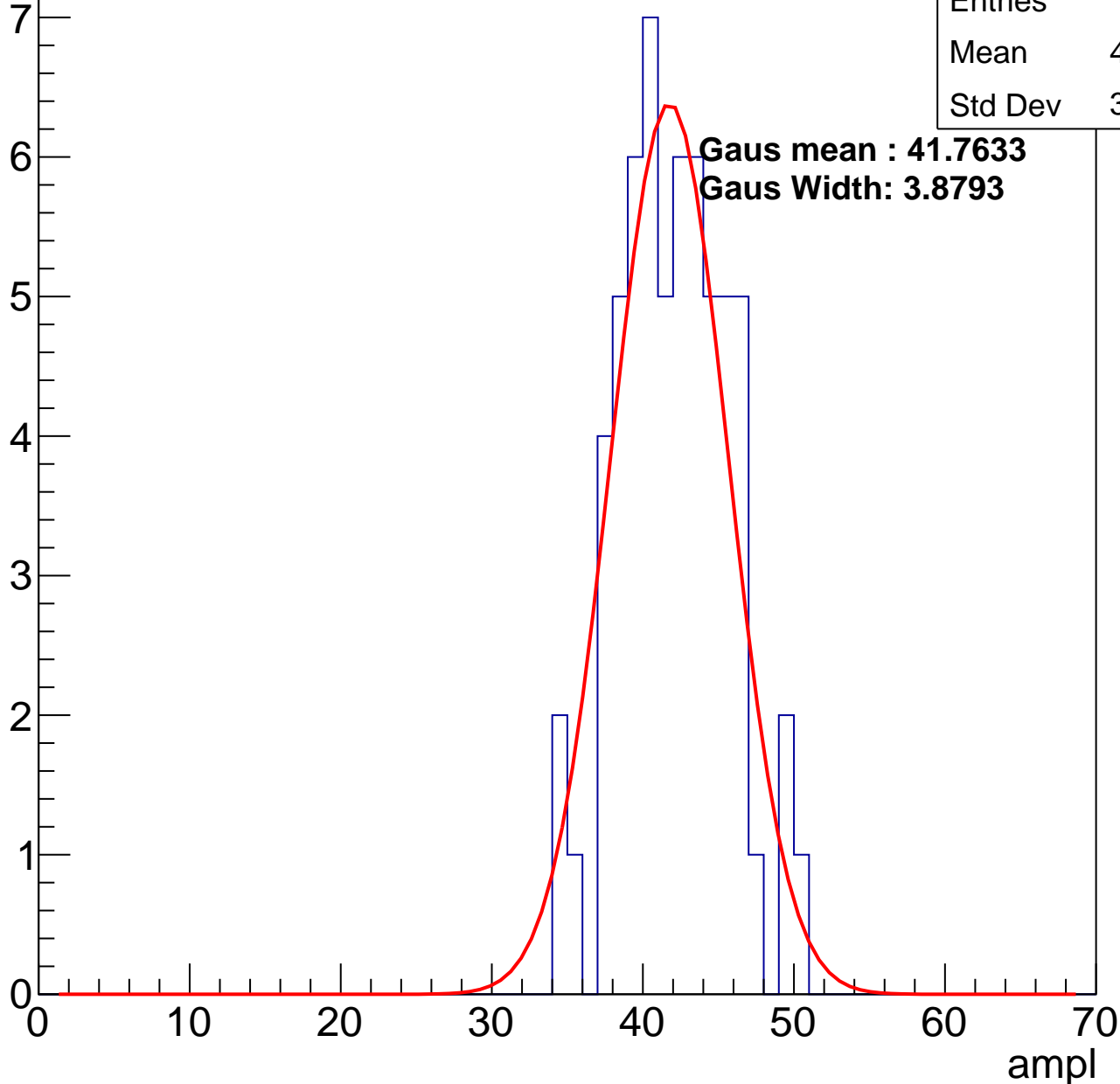
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	41.64
Std Dev	3.562

**Gaus mean : 41.7633**

**Gaus Width: 3.8793**

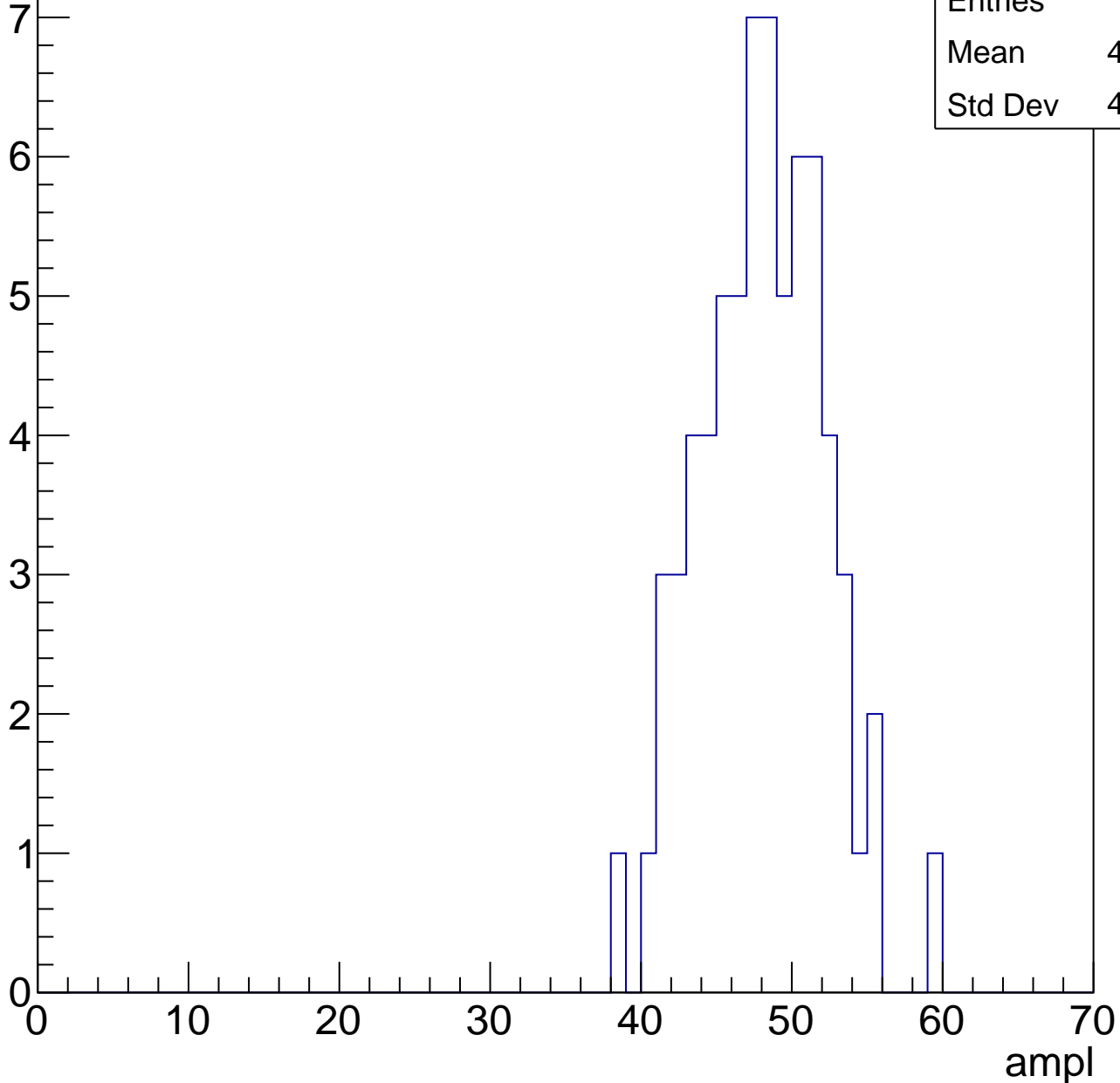


# B1L000S, U25-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	47.59
Std Dev	4.059



# B1L000S, U25-ch100, adc4

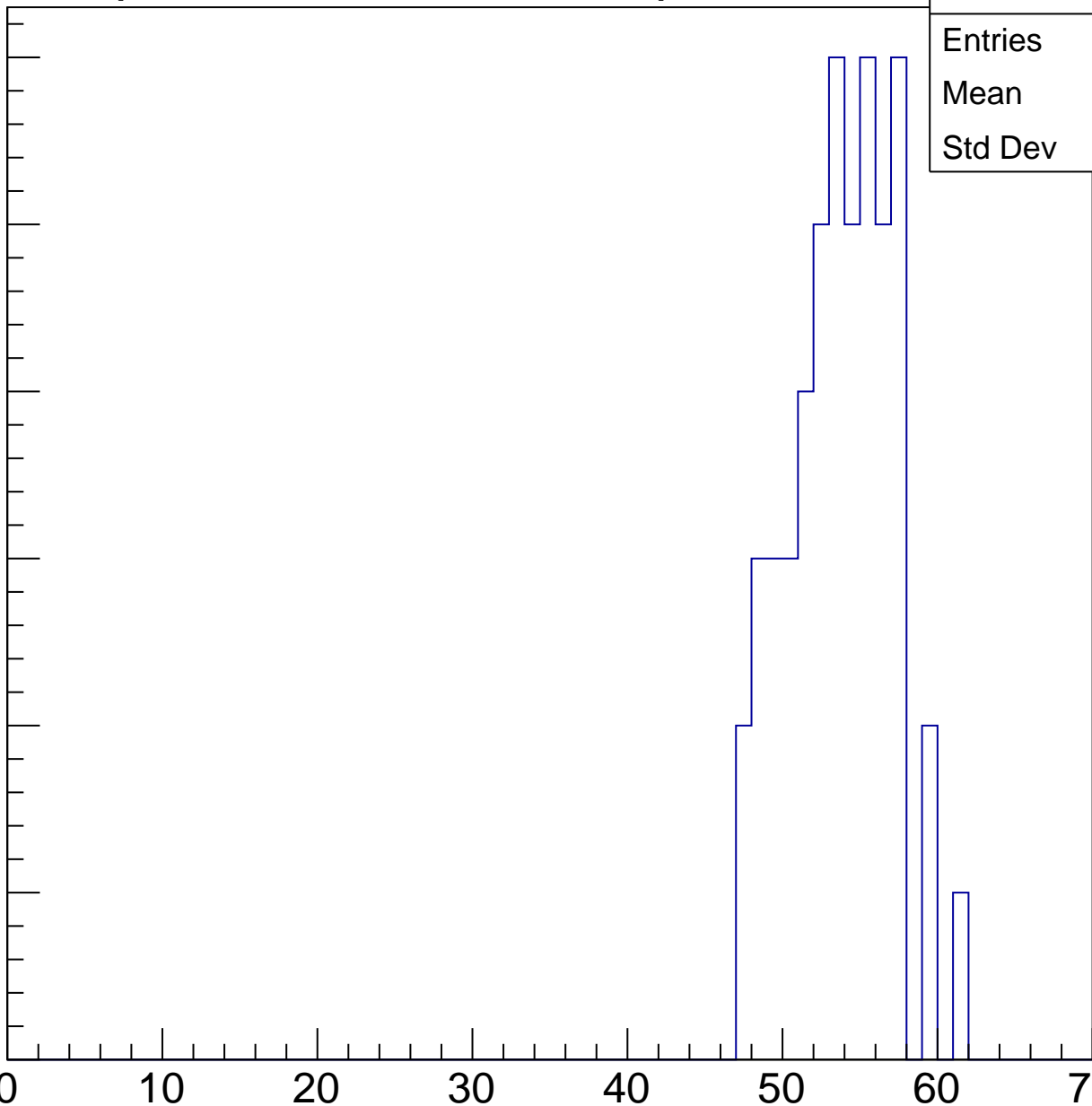
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	53.29
Std Dev	3.268

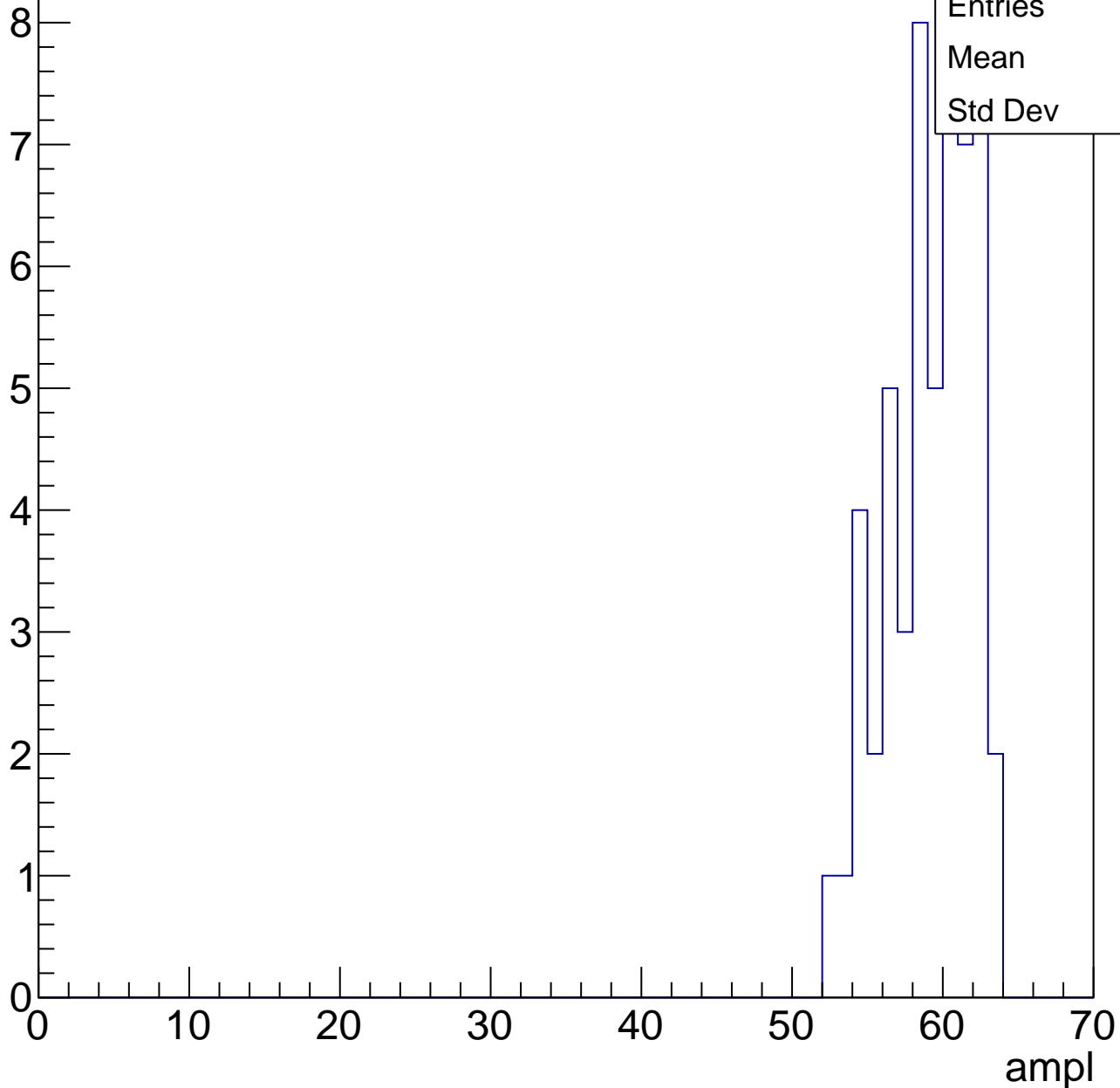
ampl



# B1L000S, U25-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

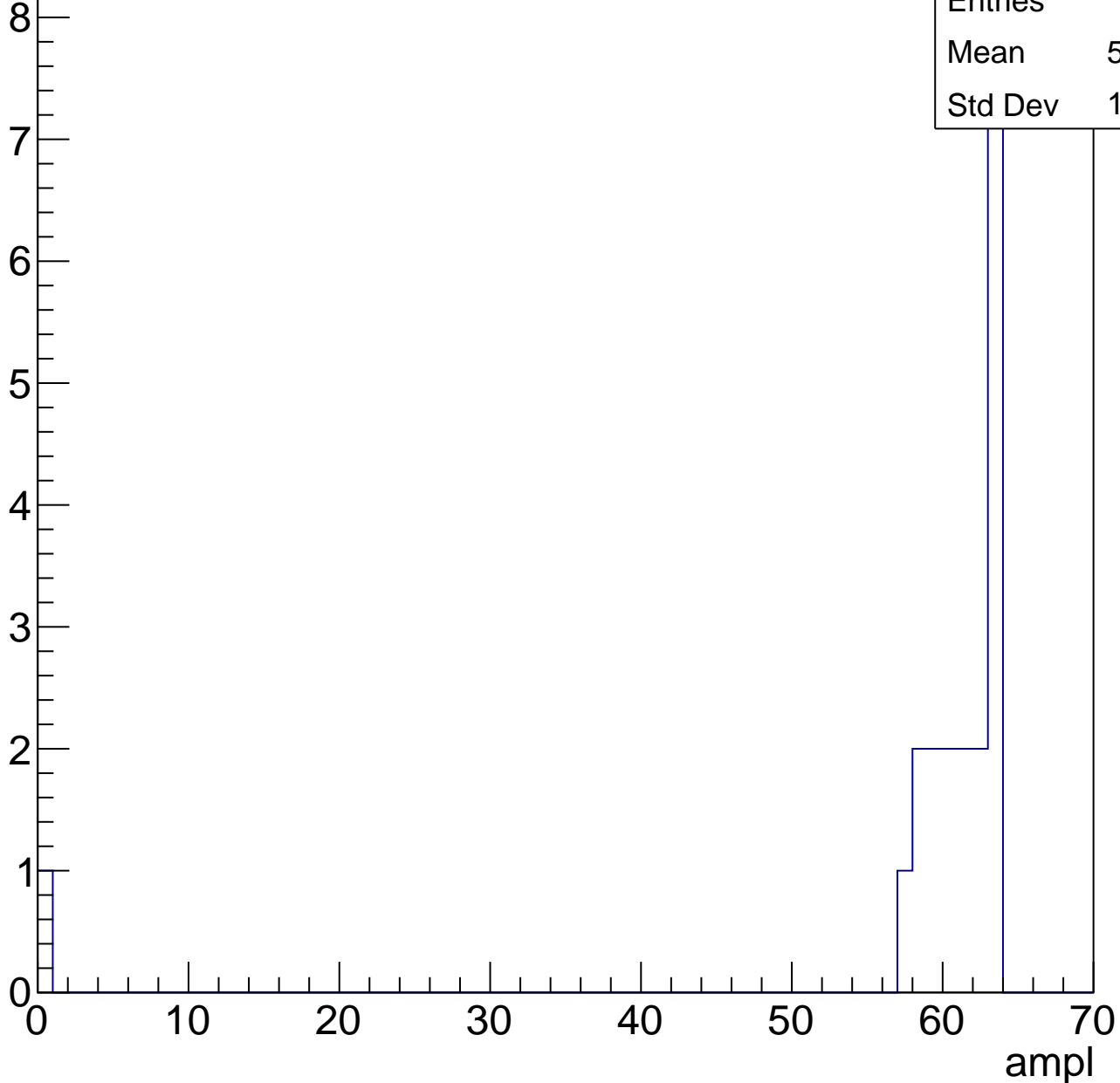


# B1L000S, U25-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	20
Mean	58.05
Std Dev	13.46





# B1L000S, U25-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch101, adc0

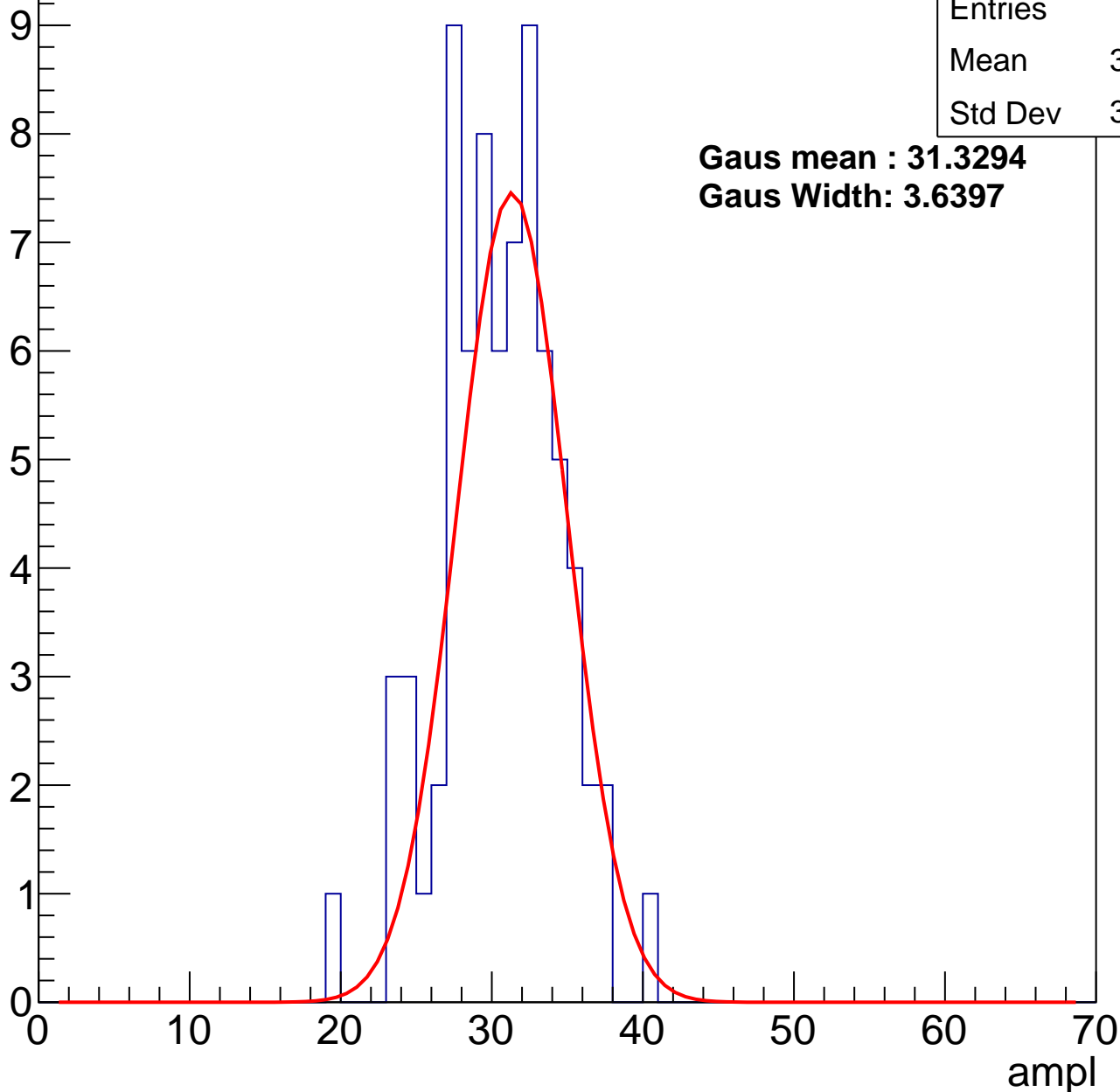
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	30.12
Std Dev	3.816

**Gaus mean : 31.3294**

**Gaus Width: 3.6397**



# B1L000S, U25-ch101, adc1

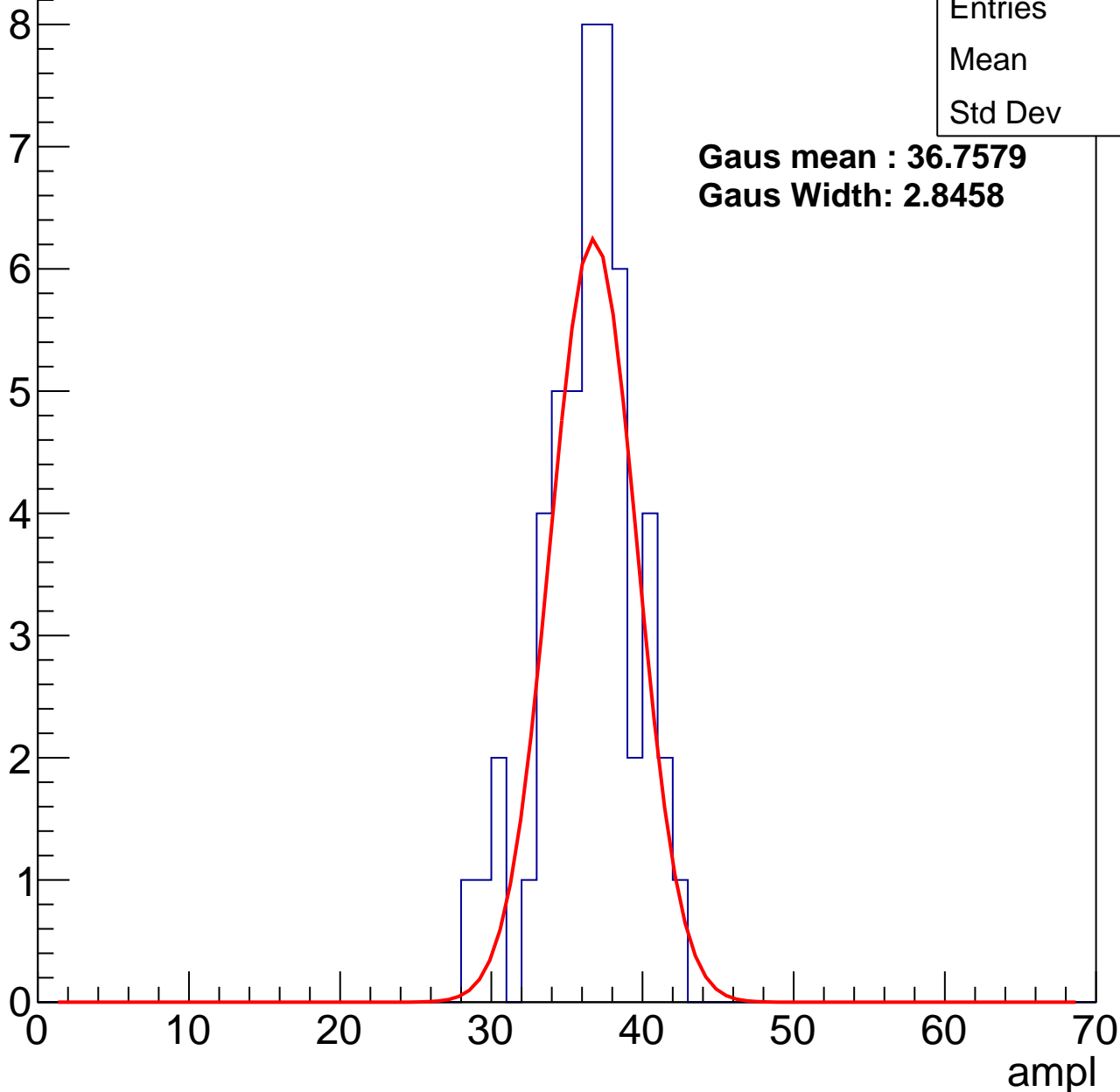
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	50
Mean	36
Std Dev	3.04

**Gaus mean : 36.7579**

**Gaus Width: 2.8458**



# B1L000S, U25-ch101, adc2

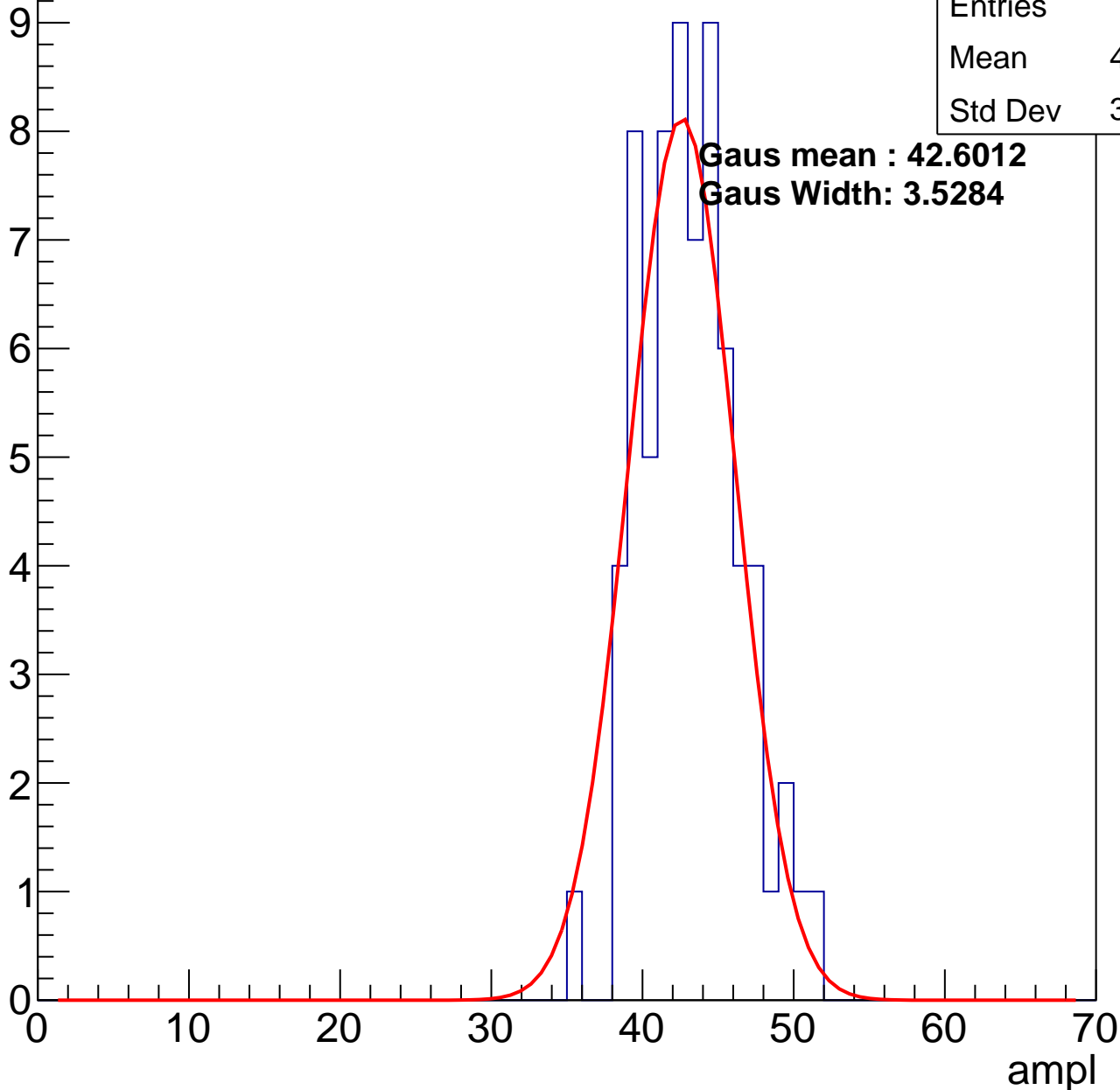
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	42.73
Std Dev	3.198

**Gaus mean : 42.6012**

**Gaus Width: 3.5284**

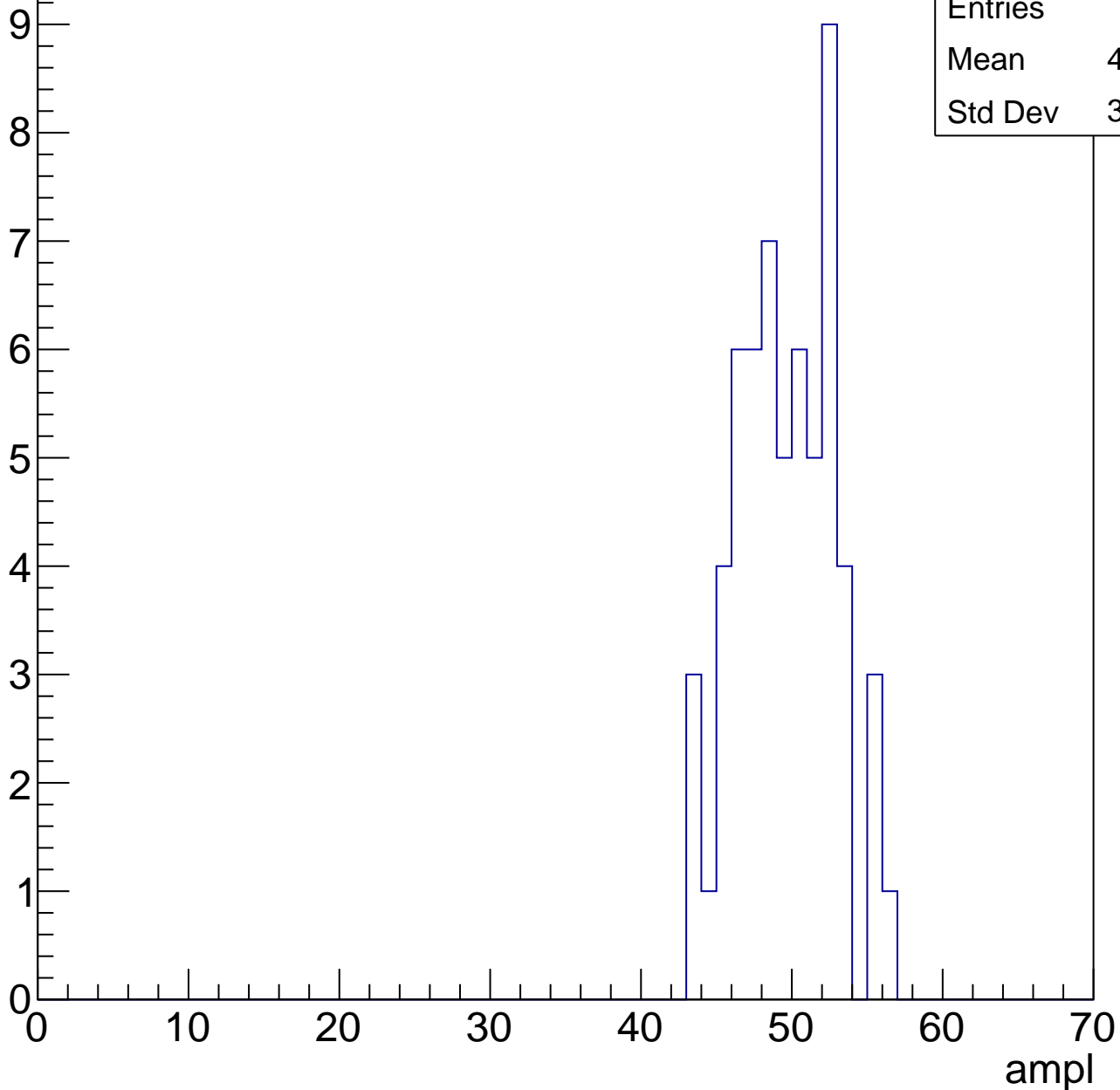


# B1L000S, U25-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	49.13
Std Dev	3.186

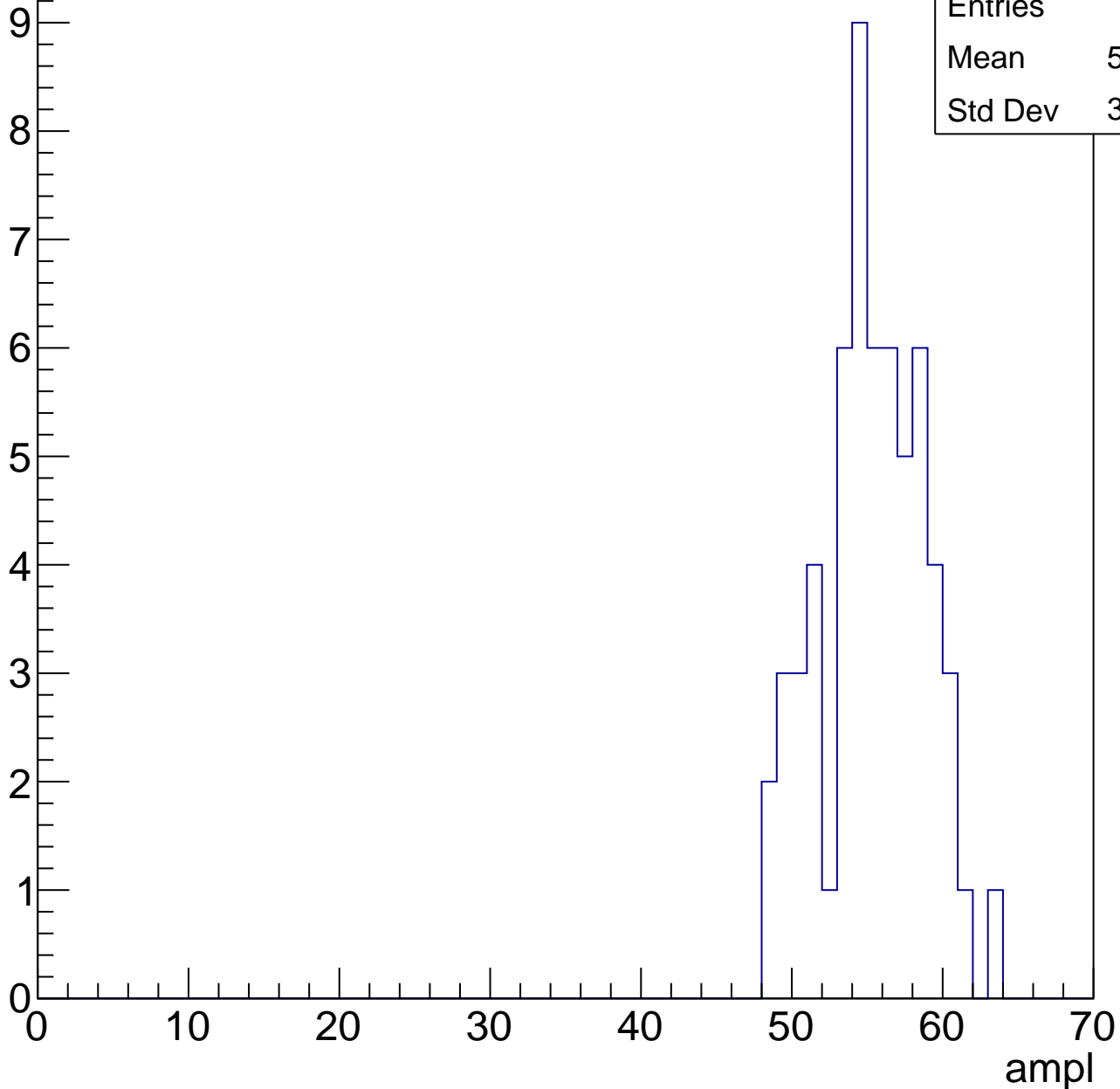


# B1L000S, U25-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	54.87
Std Dev	3.428

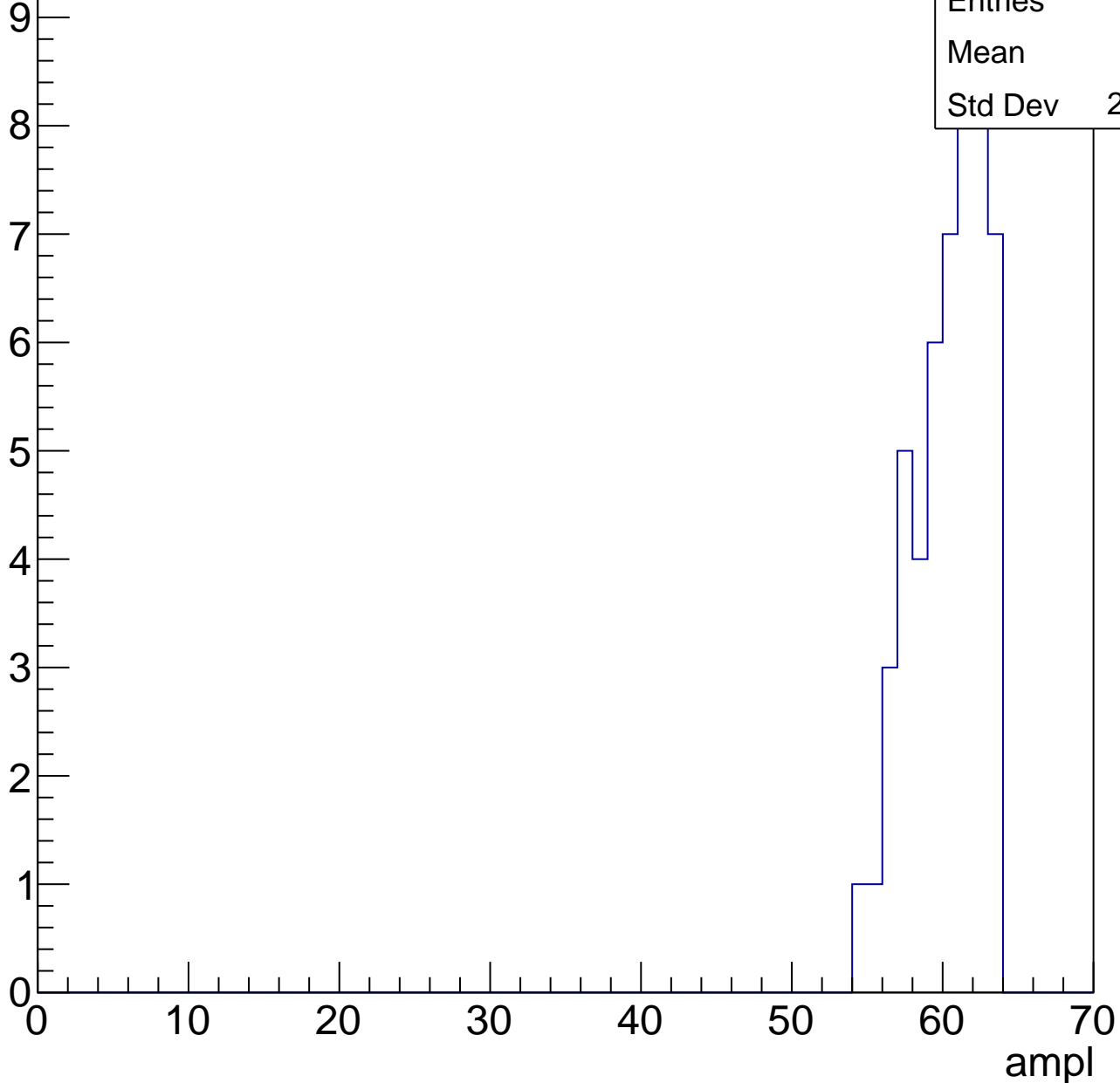


# B1L000S, U25-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	59.9
Std Dev	2.354



# B1L000S, U25-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

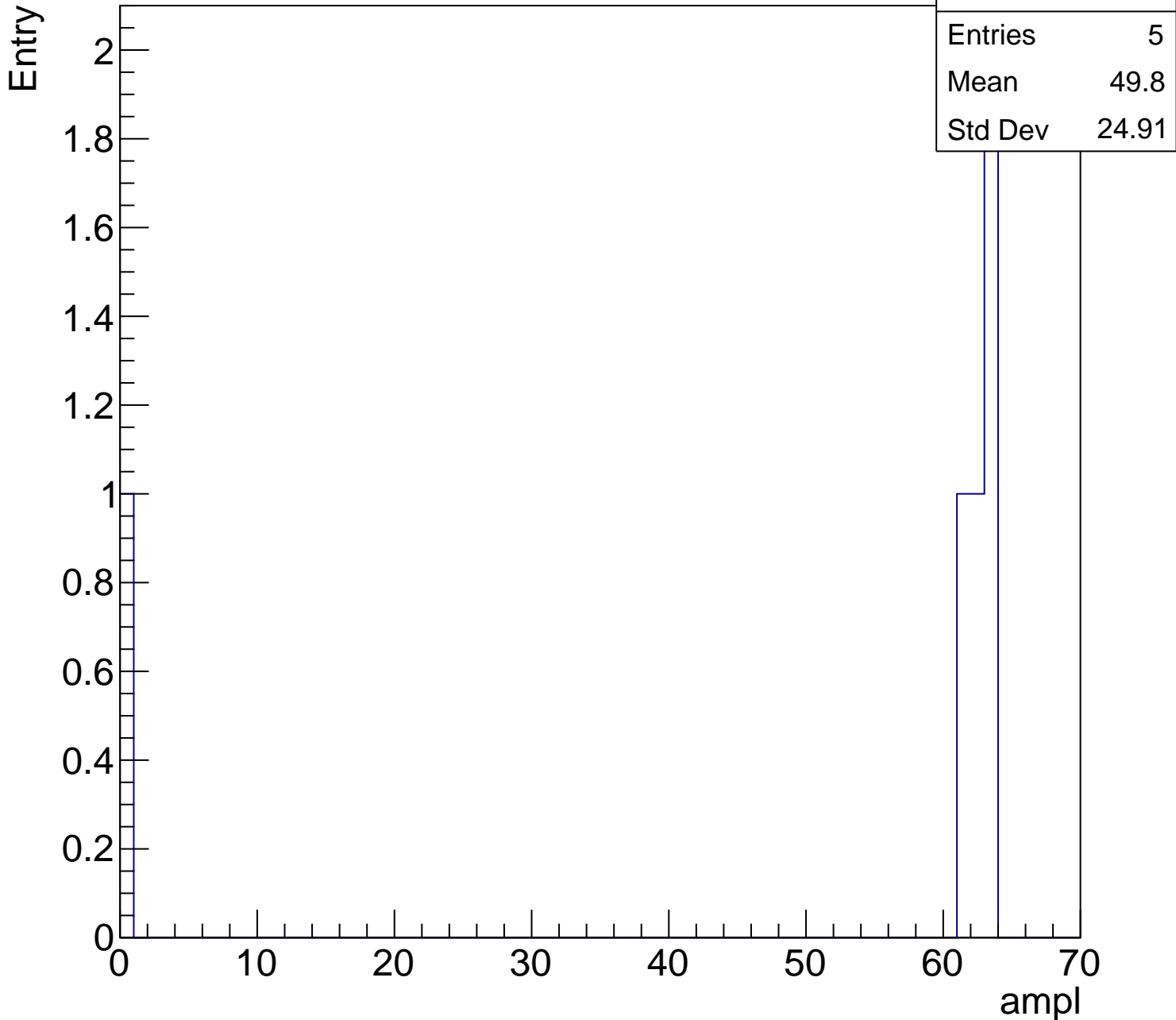
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.8
Std Dev	24.91

0 10 20 30 40 50 60 70

ampl





# B1L000S, U25-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

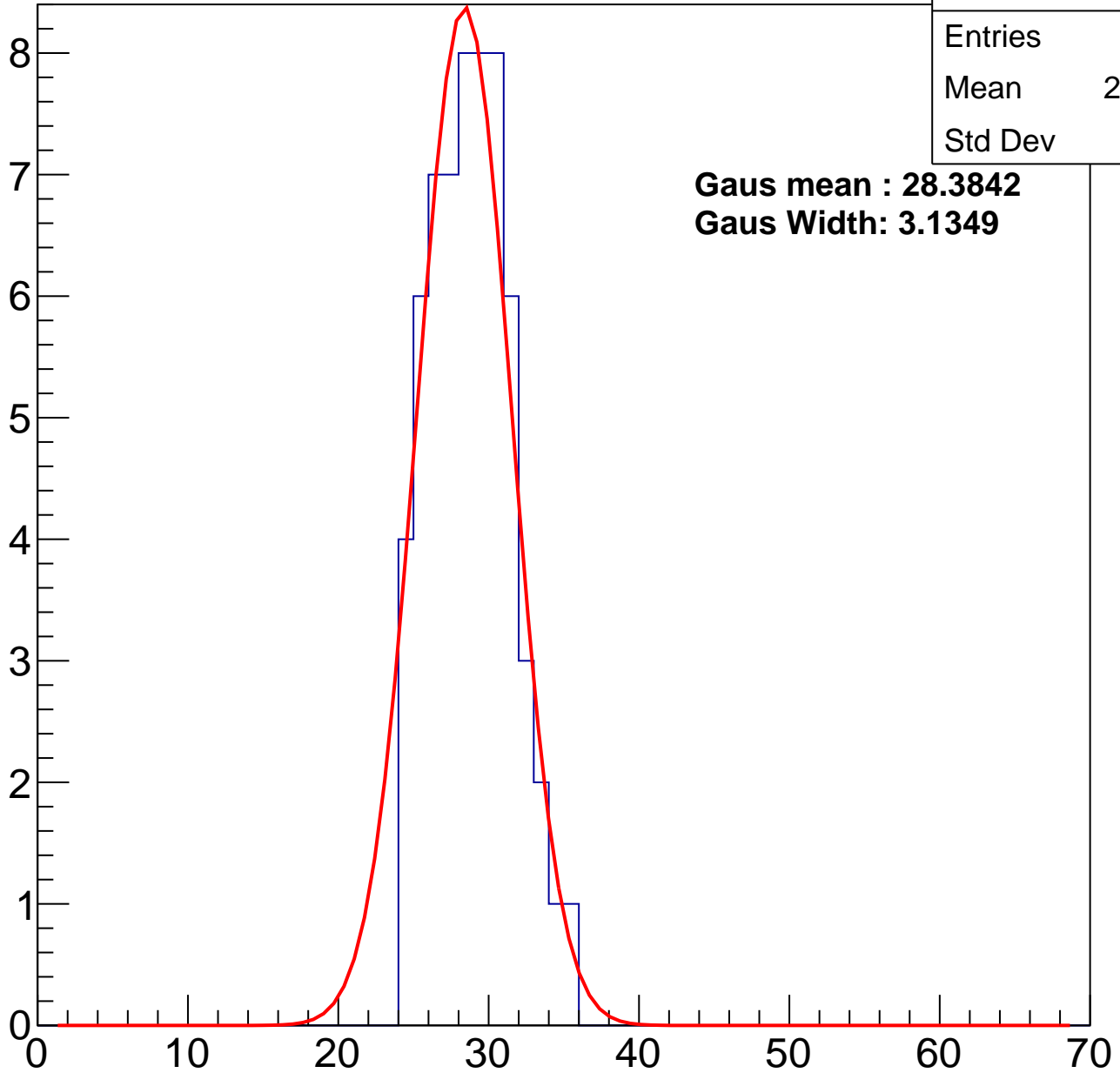
Entries	61
Mean	28.36
Std Dev	2.63

**Gaus mean : 28.3842**

**Gaus Width: 3.1349**

8  
7  
6  
5  
4  
3  
2  
1  
0

ampl



# B1L000S, U25-ch102, adc1

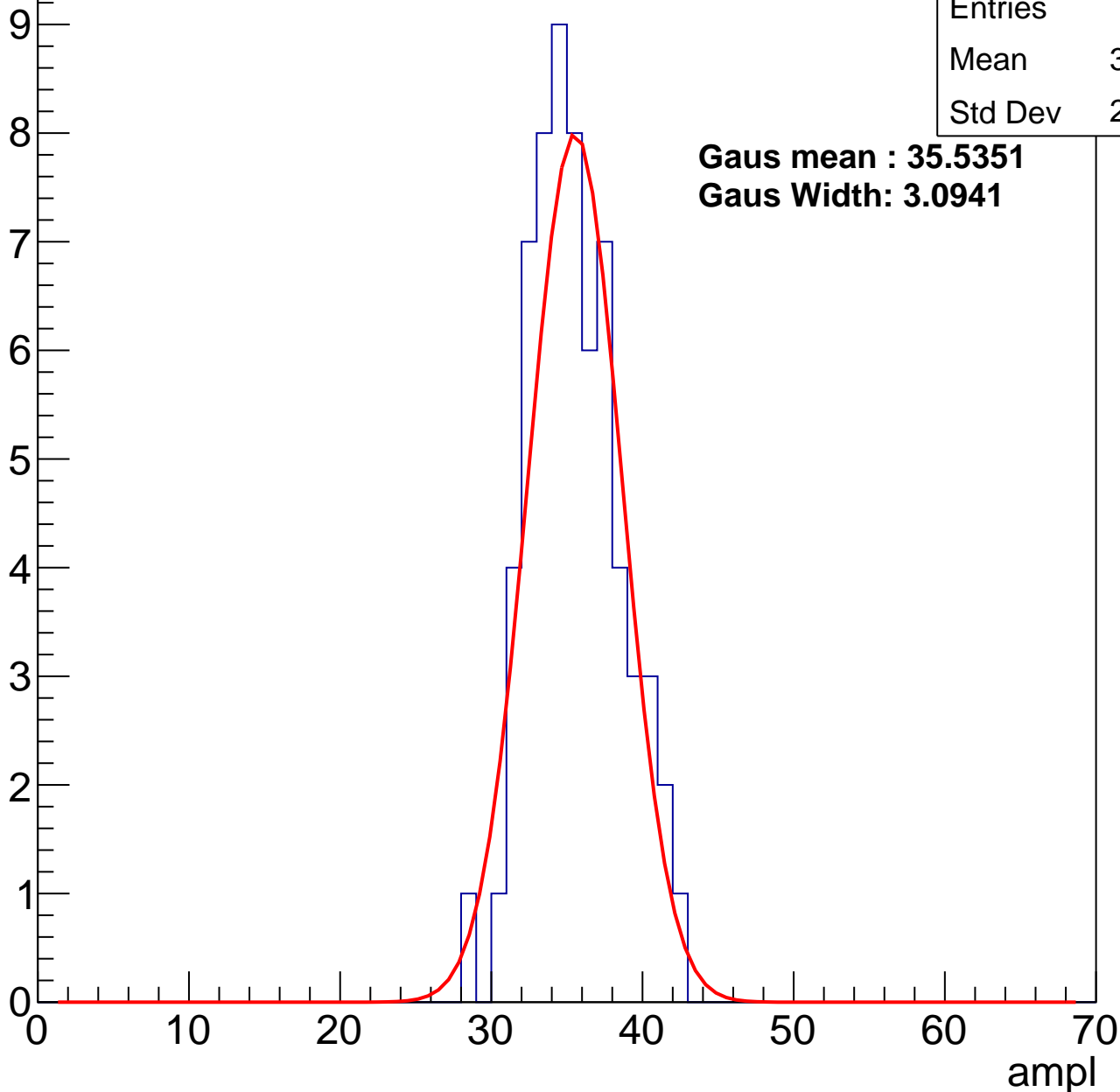
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	35.06
Std Dev	2.947

**Gaus mean : 35.5351**

**Gaus Width: 3.0941**



# B1L000S, U25-ch102, adc2

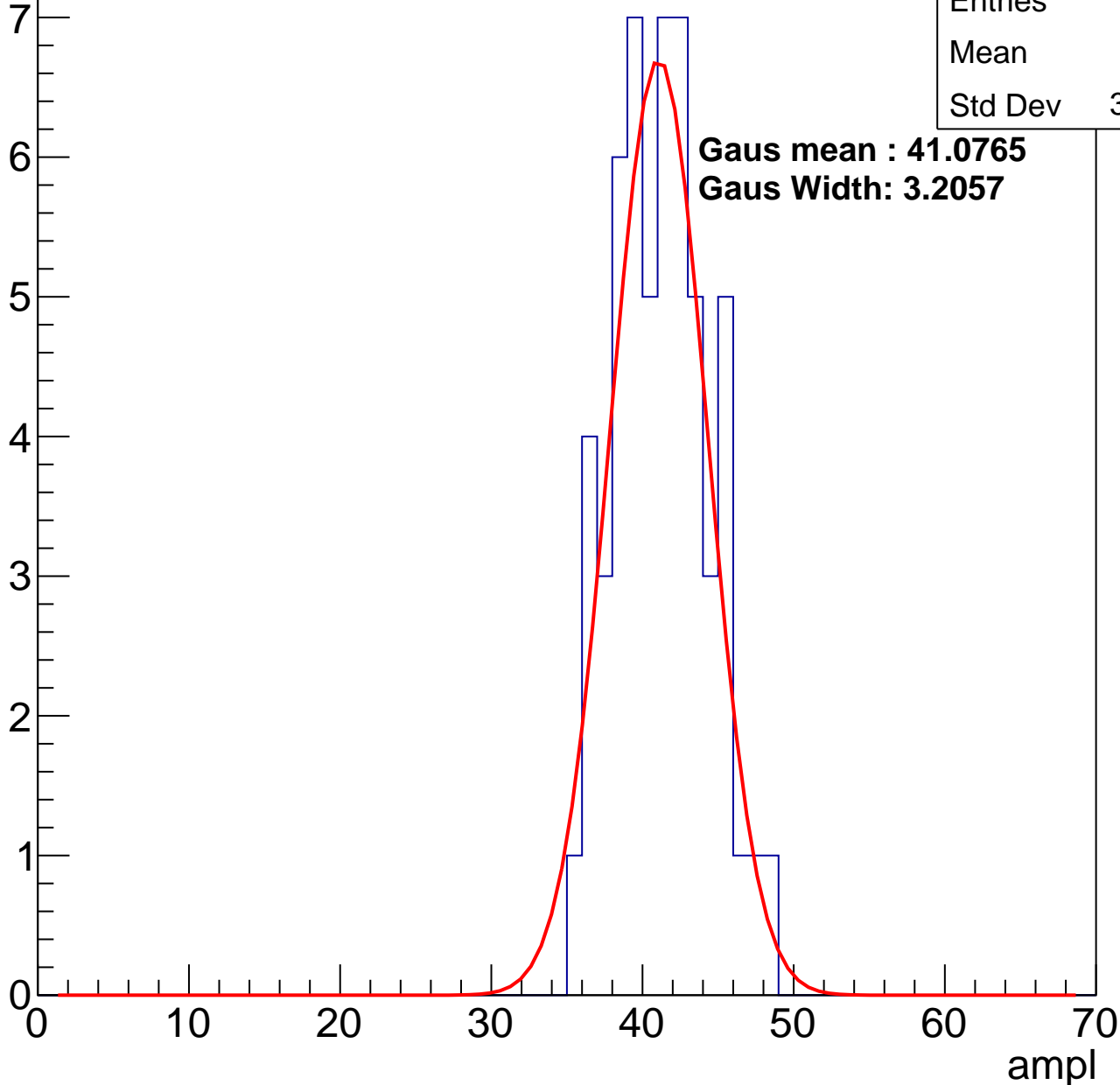
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	40.8
Std Dev	3.026

**Gaus mean : 41.0765**

**Gaus Width: 3.2057**

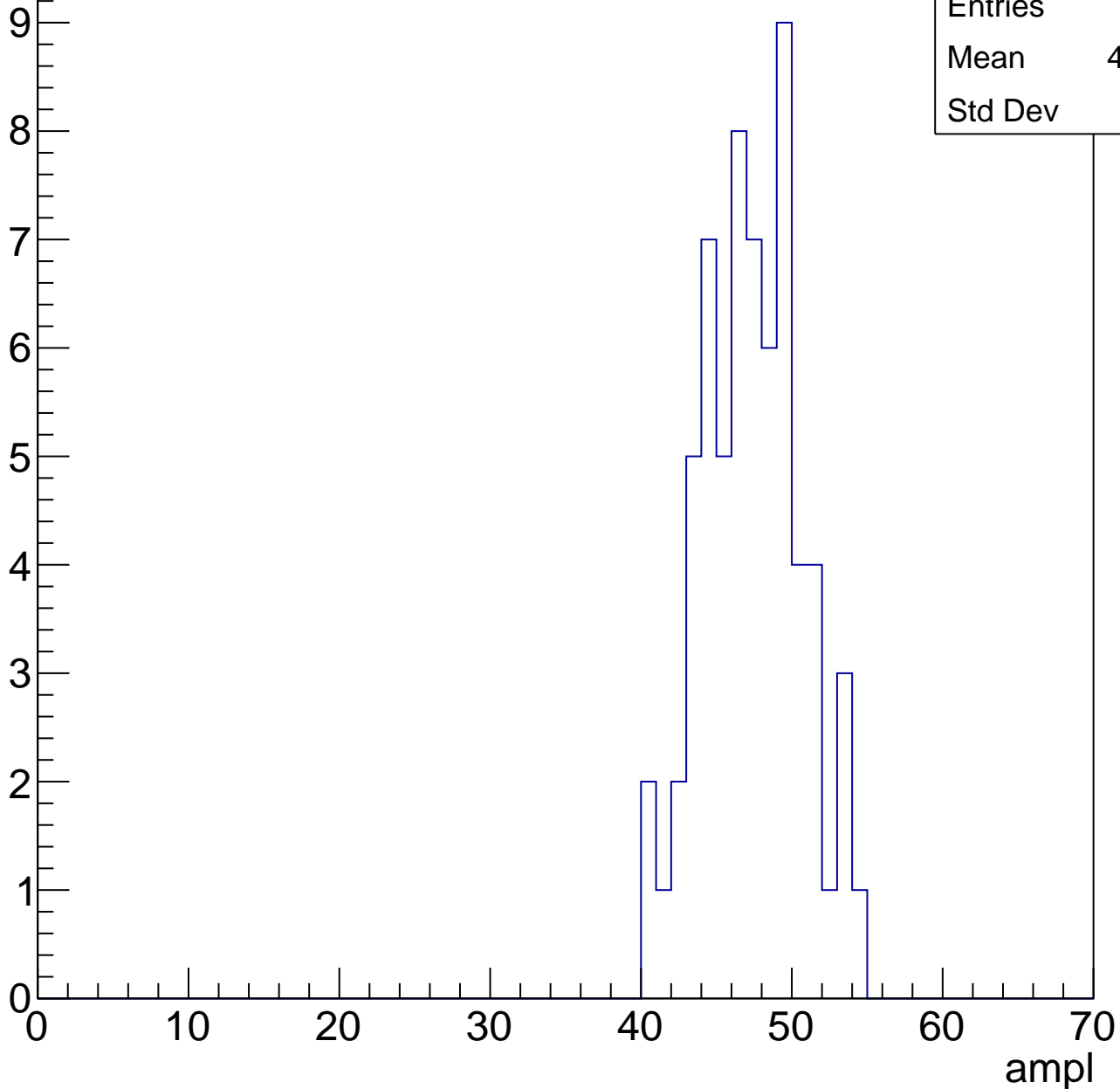


# B1L000S, U25-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	46.89
Std Dev	3.23

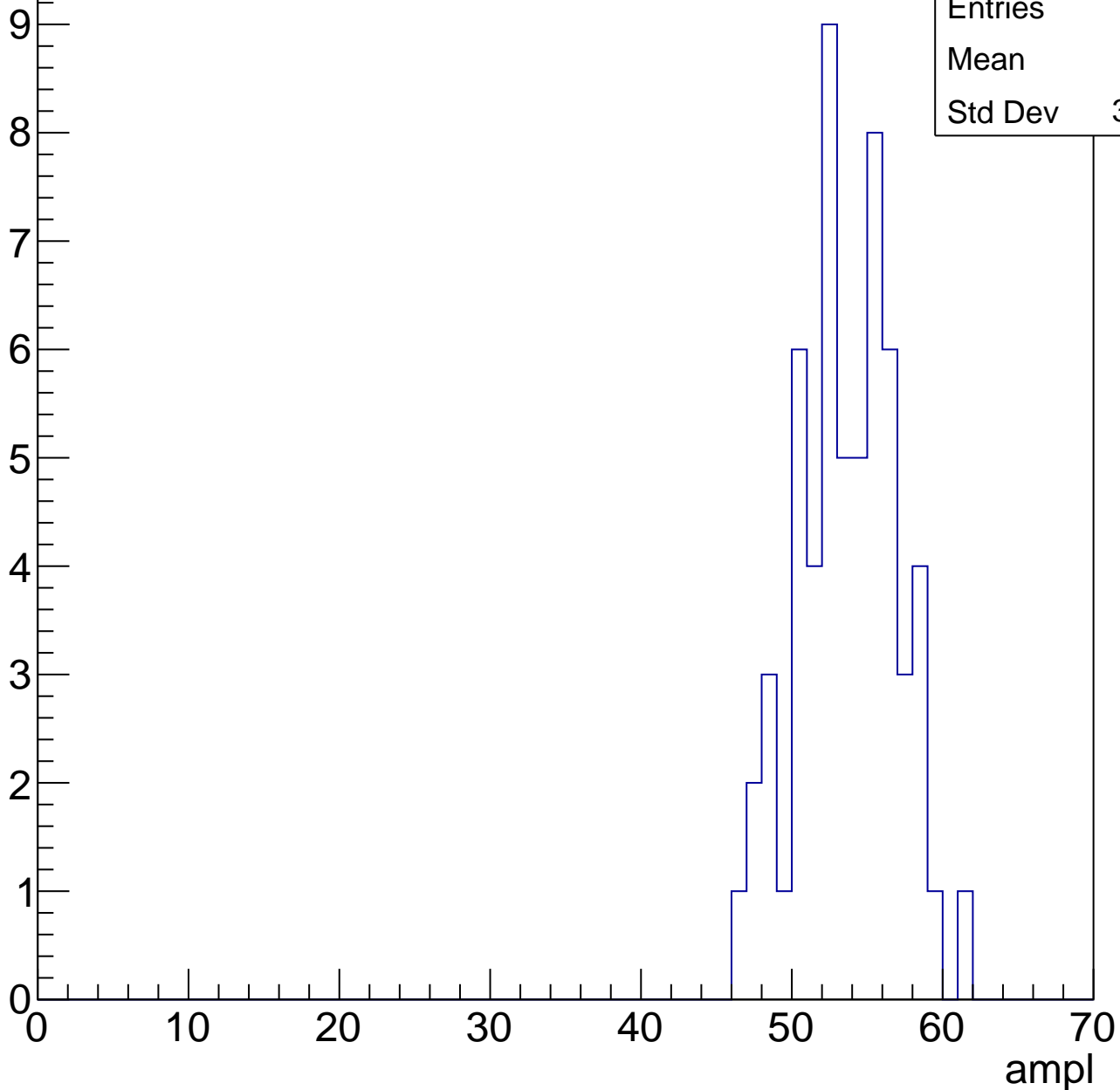


# B1L000S, U25-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.2
Std Dev	3.261

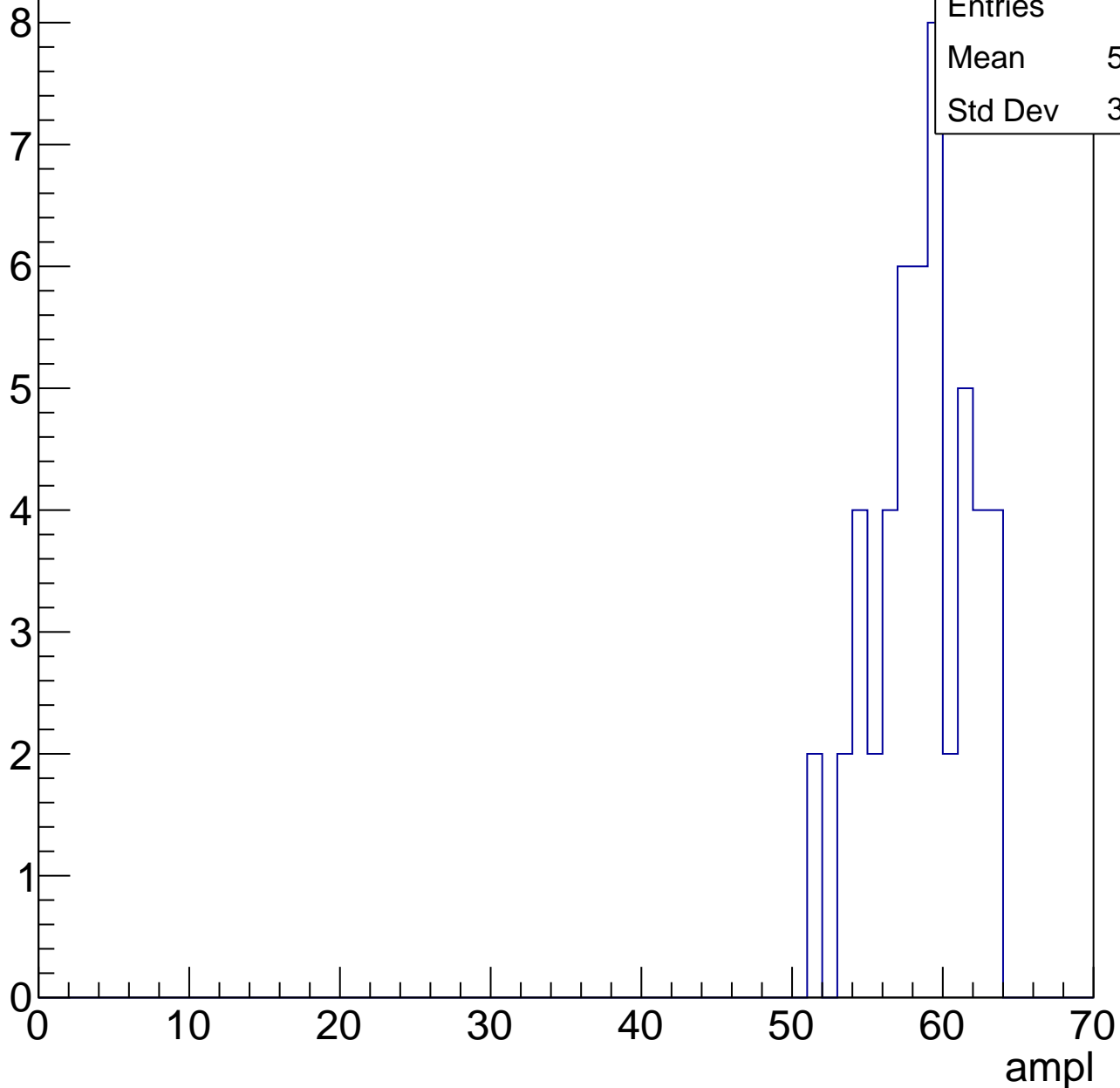


# B1L000S, U25-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	58.06
Std Dev	3.119

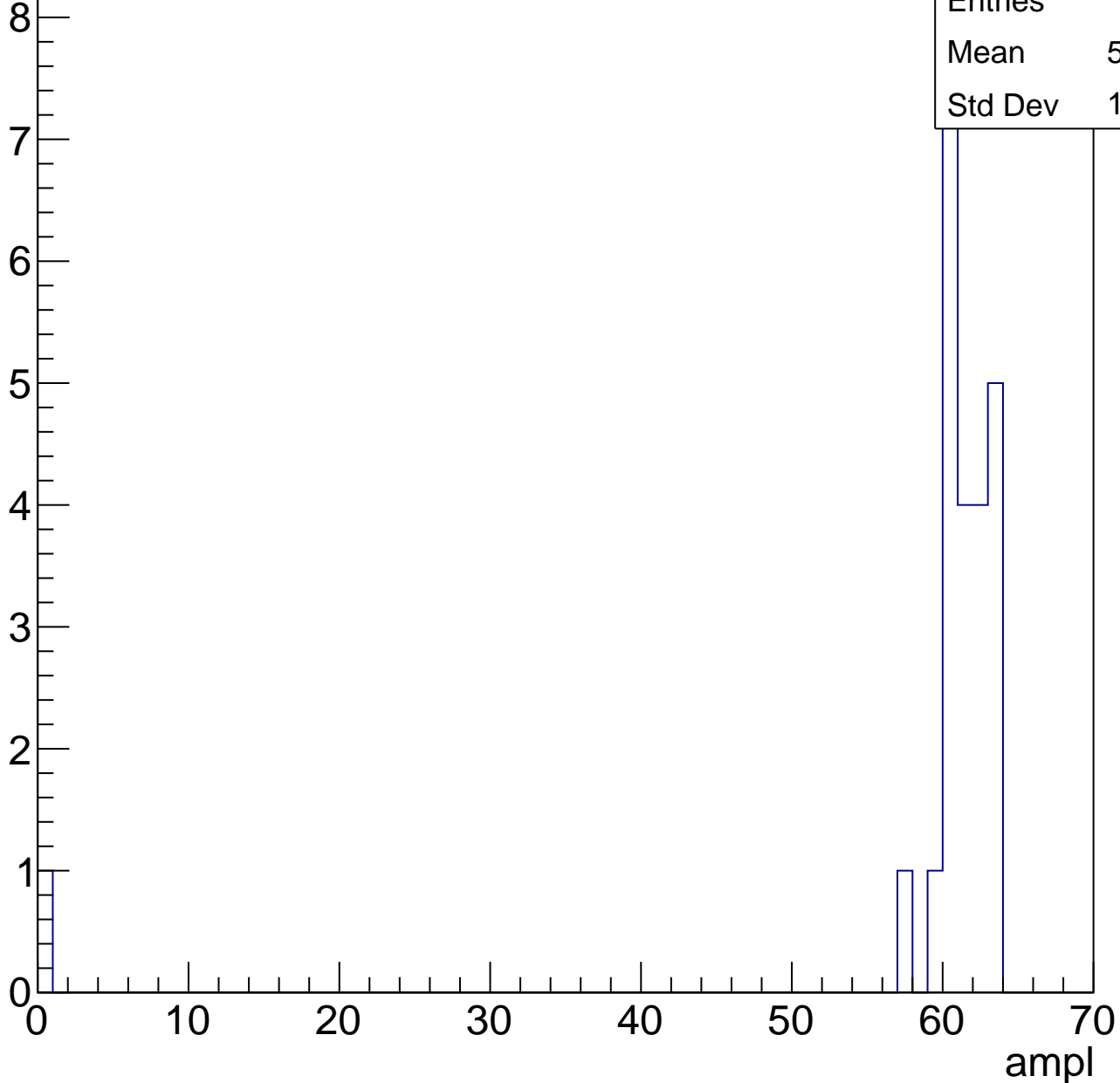


# B1L000S, U25-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.46
Std Dev	12.28

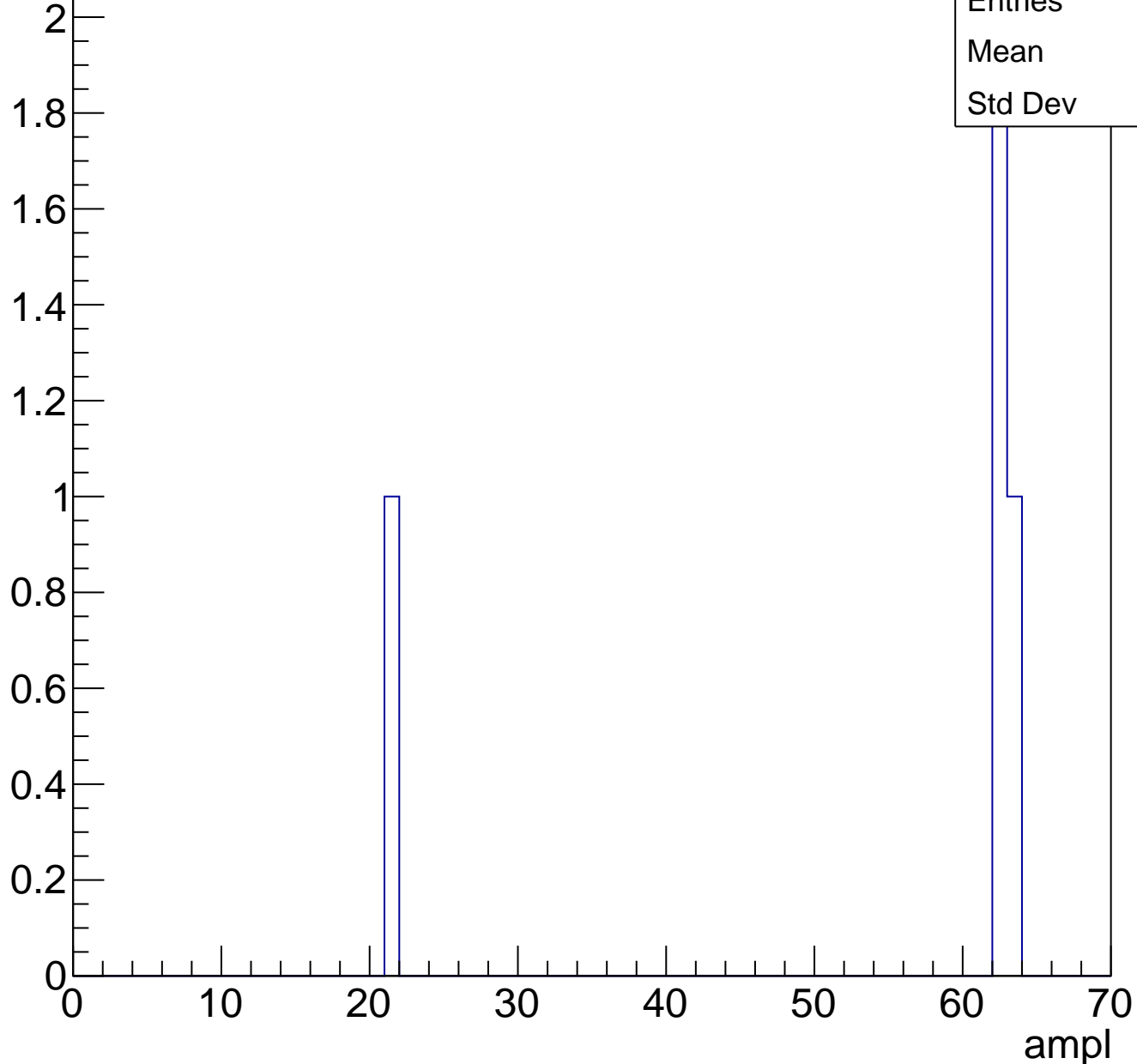




# B1L000S, U25-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch103, adc0

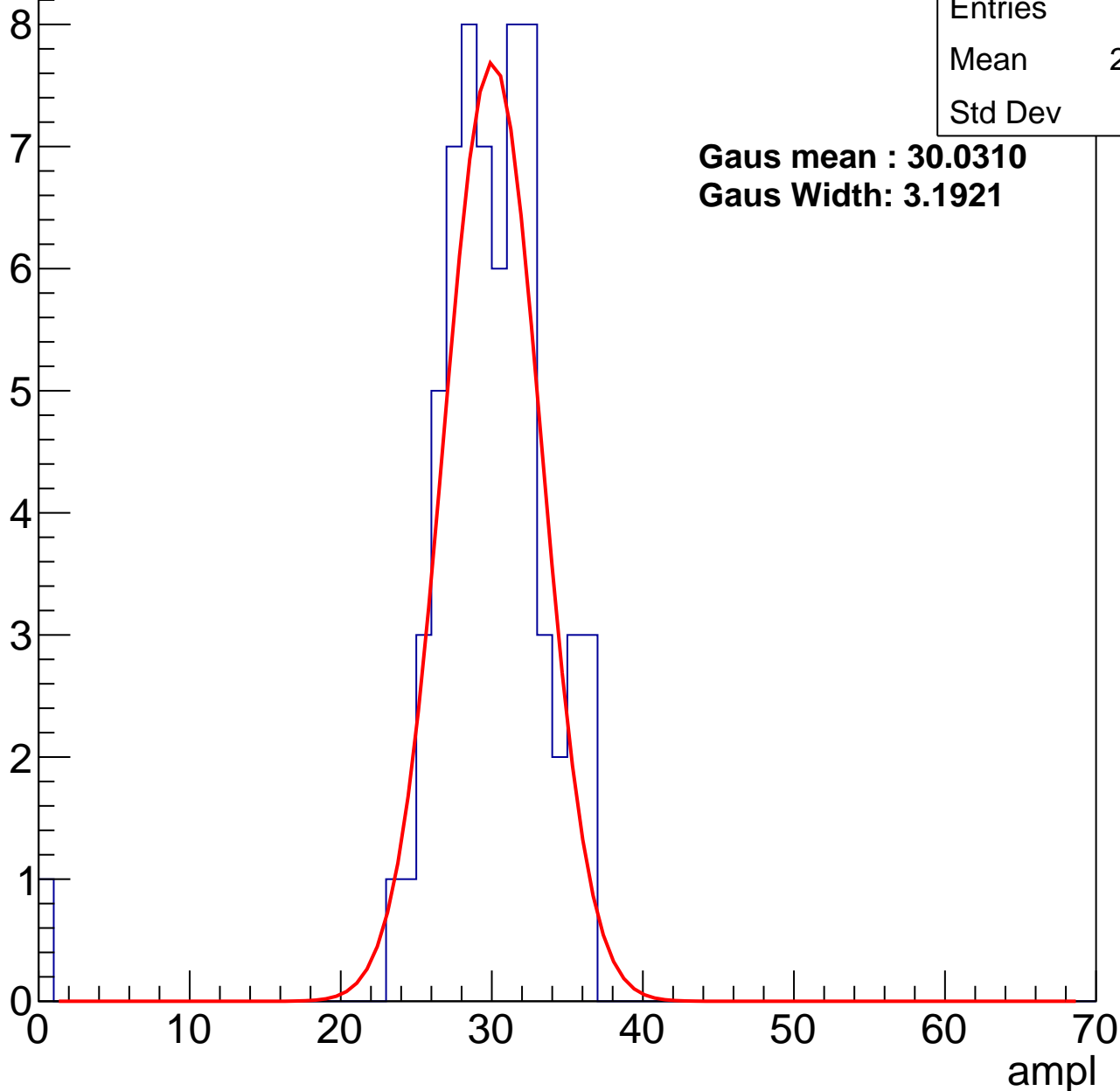
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	29.27
Std Dev	4.75

**Gaus mean : 30.0310**

**Gaus Width: 3.1921**



# B1L000S, U25-ch103, adc1

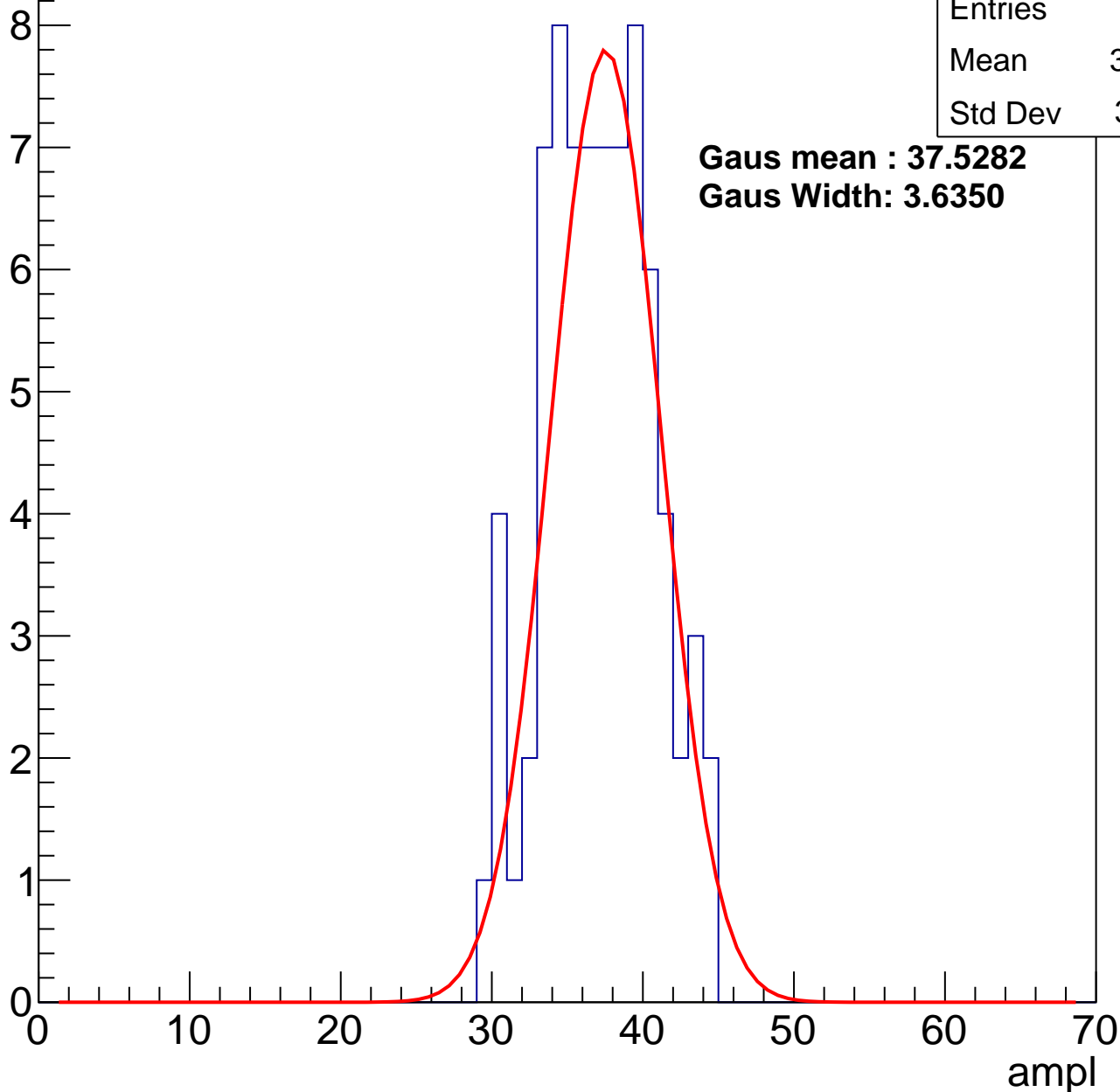
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	76
Mean	36.66
Std Dev	3.571

**Gaus mean : 37.5282**

**Gaus Width: 3.6350**



# B1L000S, U25-ch103, adc2

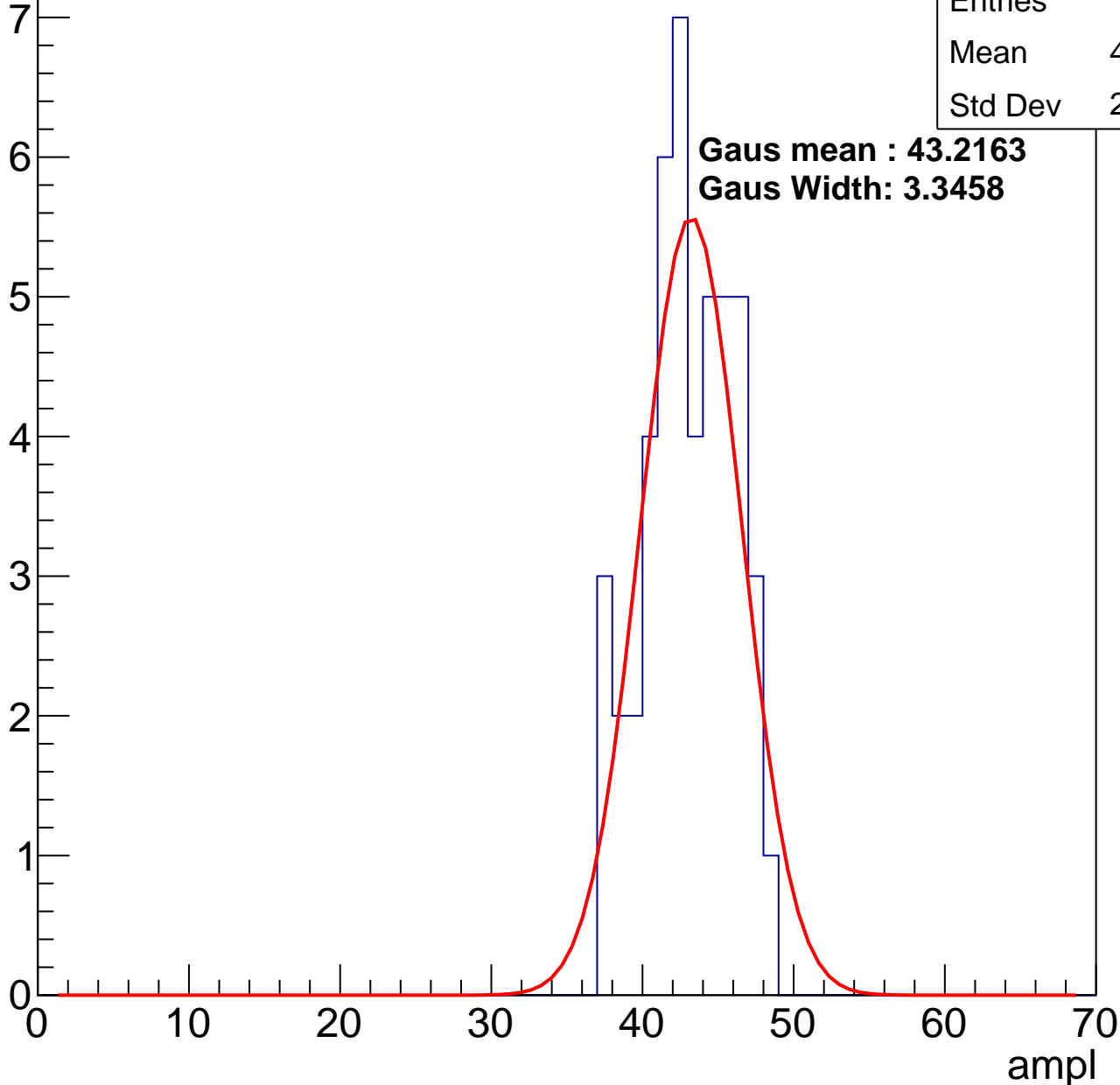
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	42.57
Std Dev	2.886

**Gaus mean : 43.2163**

**Gaus Width: 3.3458**

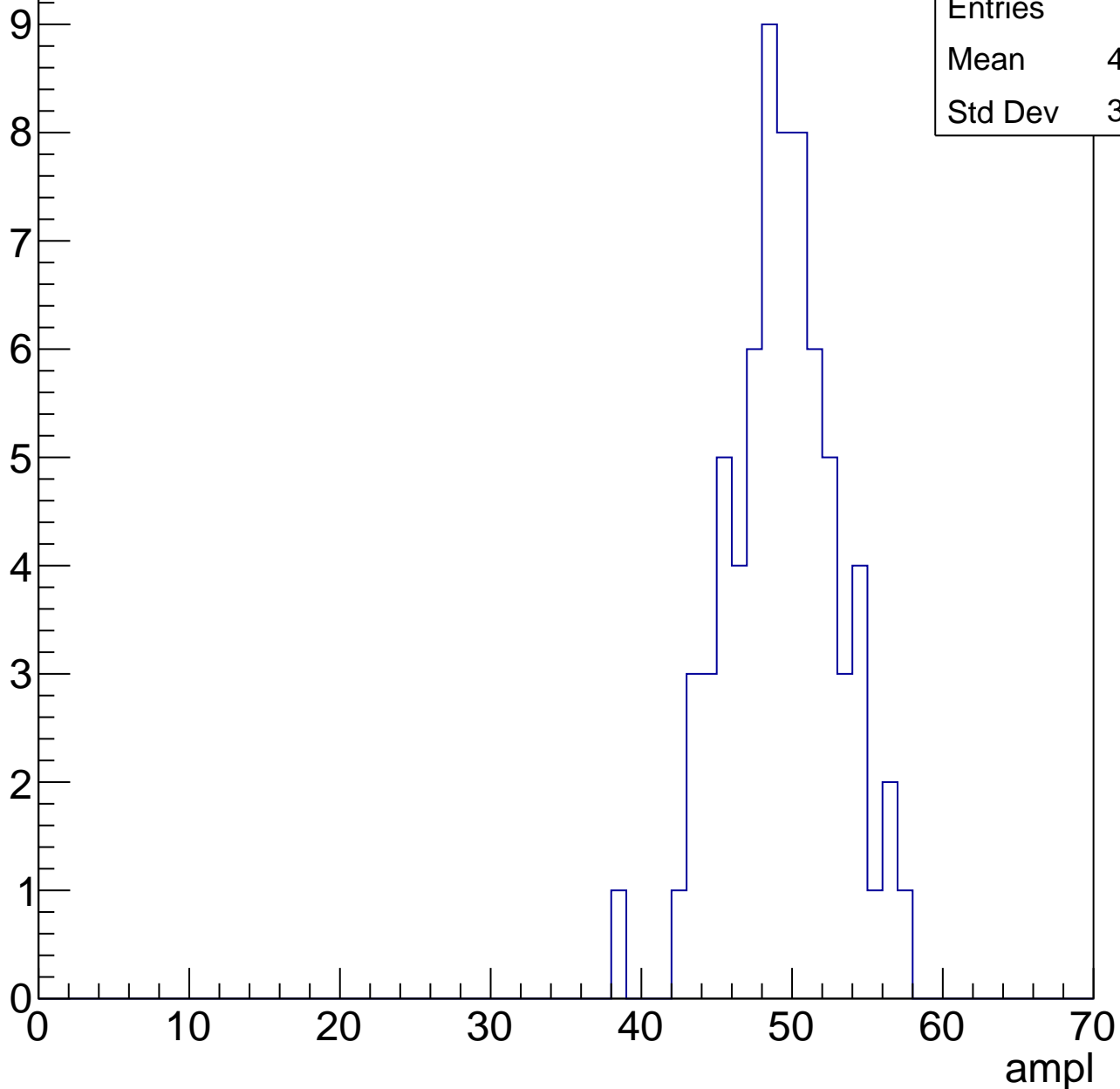


# B1L000S, U25-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

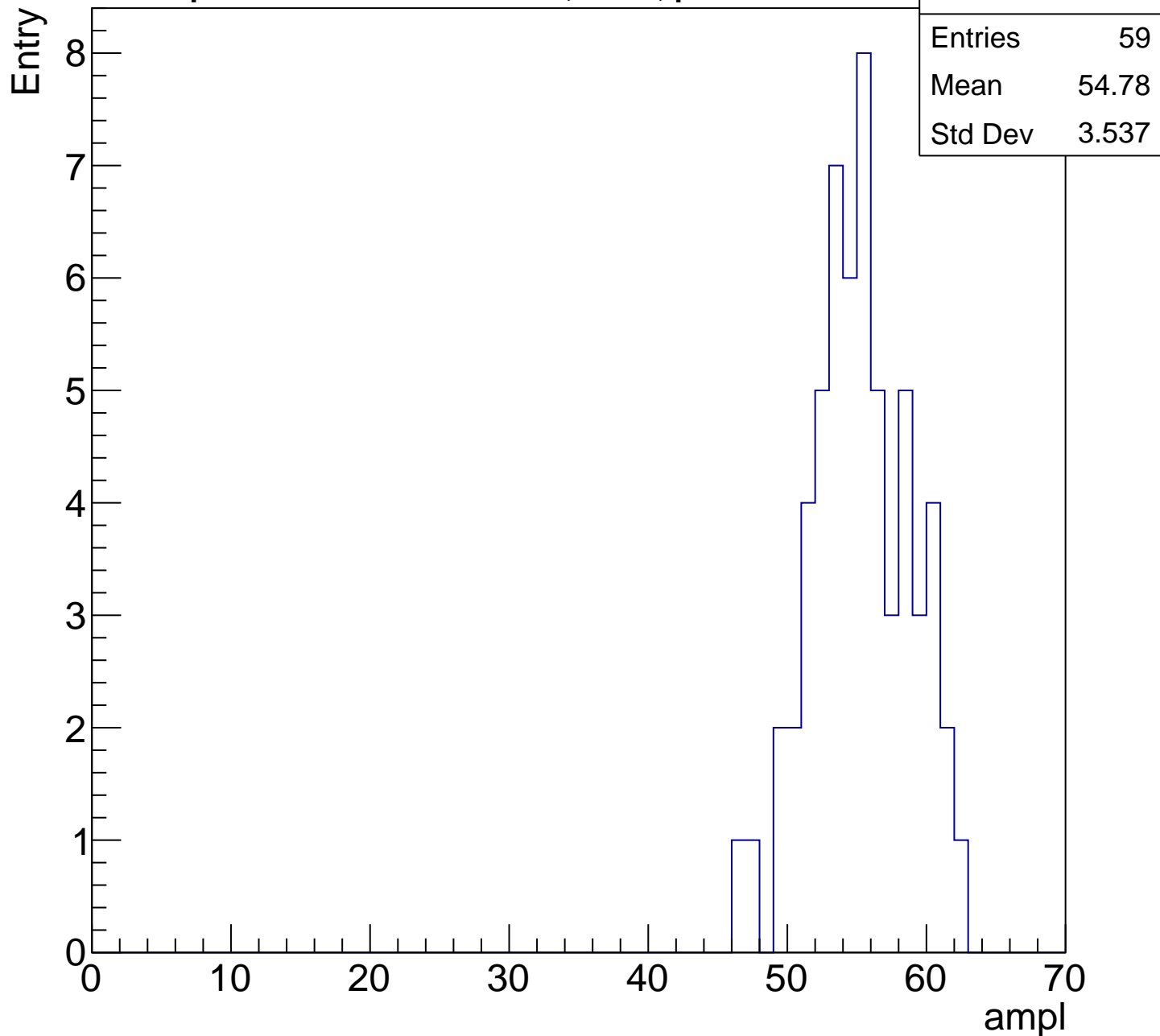
Entry

Entries	70
Mean	48.87
Std Dev	3.633



# B1L000S, U25-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

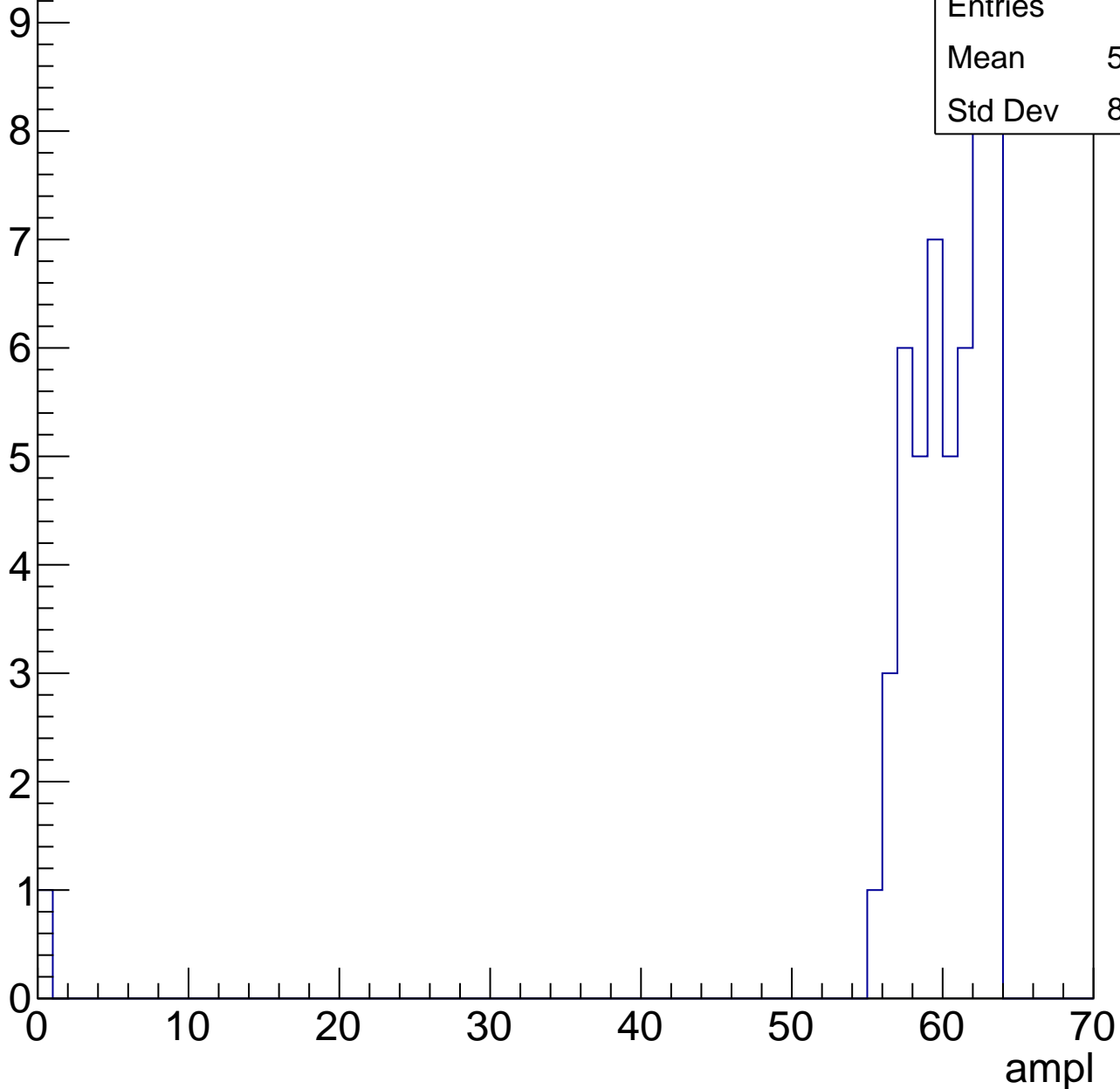


# B1L000S, U25-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

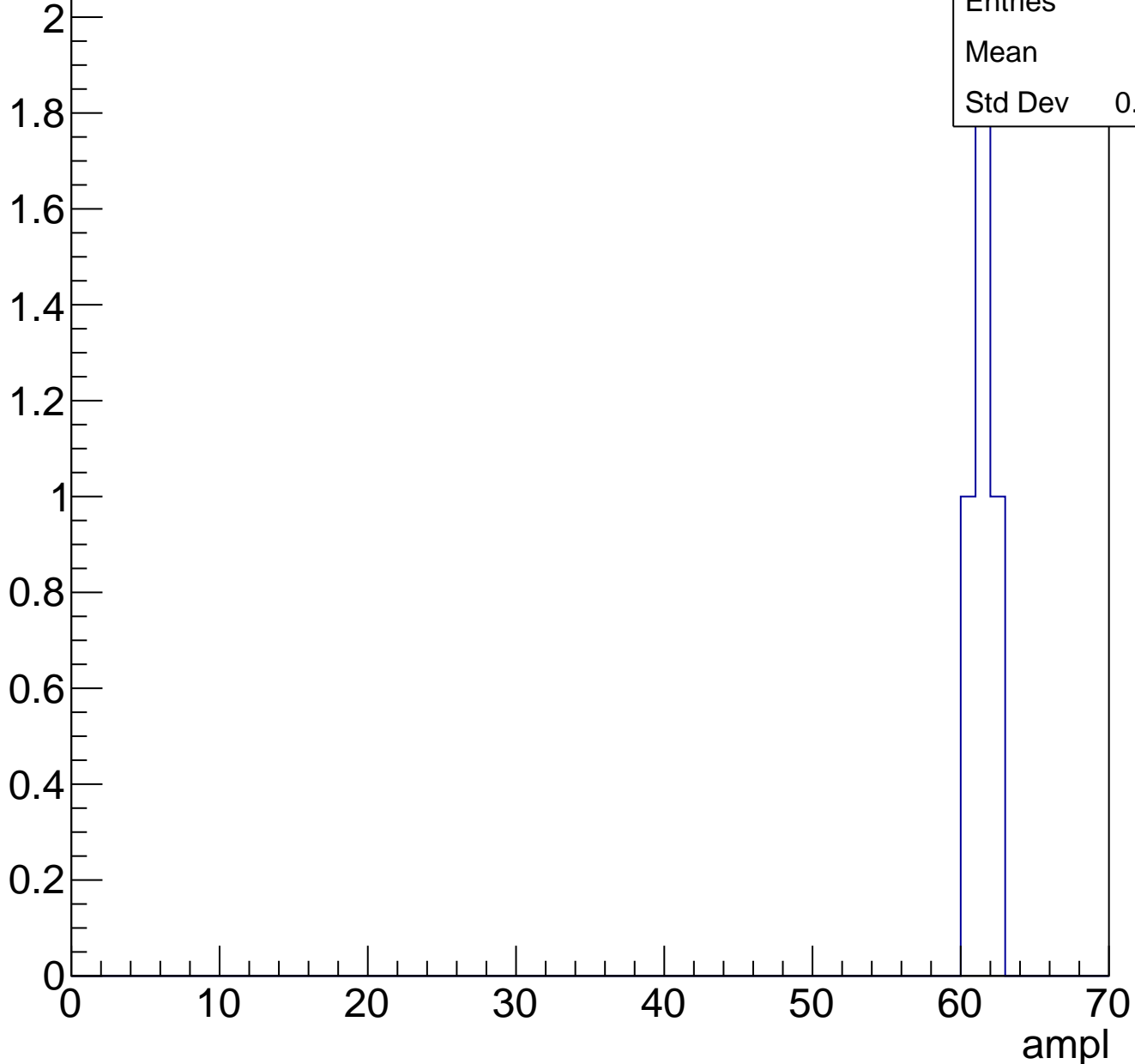
Entries	51
Mean	58.76
Std Dev	8.626



# B1L000S, U25-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



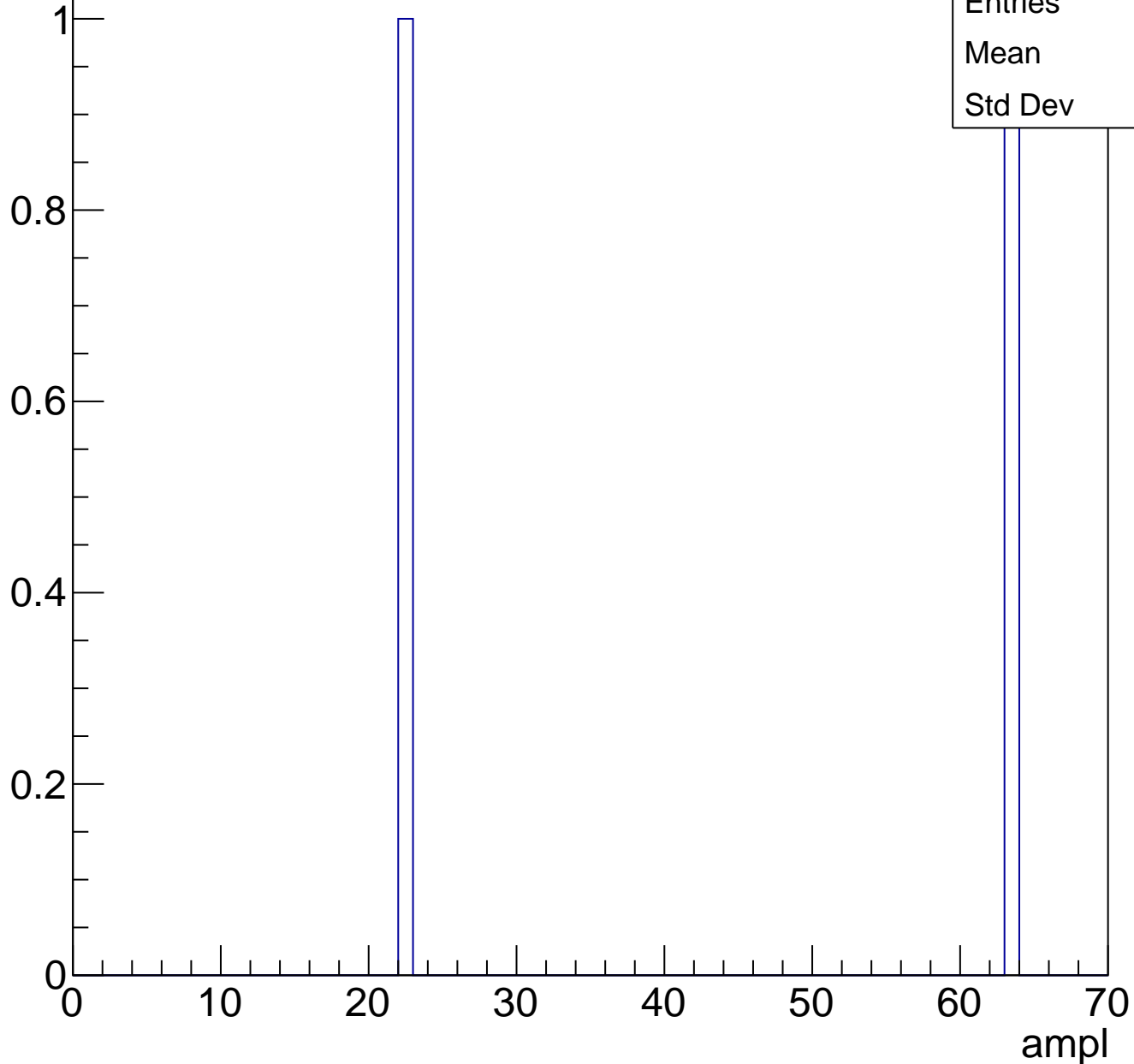
Entries	4
Mean	61
Std Dev	0.7071



# B1L000S, U25-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	2
Mean	42.5
Std Dev	20.5

# B1L000S, U25-ch104, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	64
Mean	30.53
Std Dev	3.419

**Gaus mean : 32.1902**

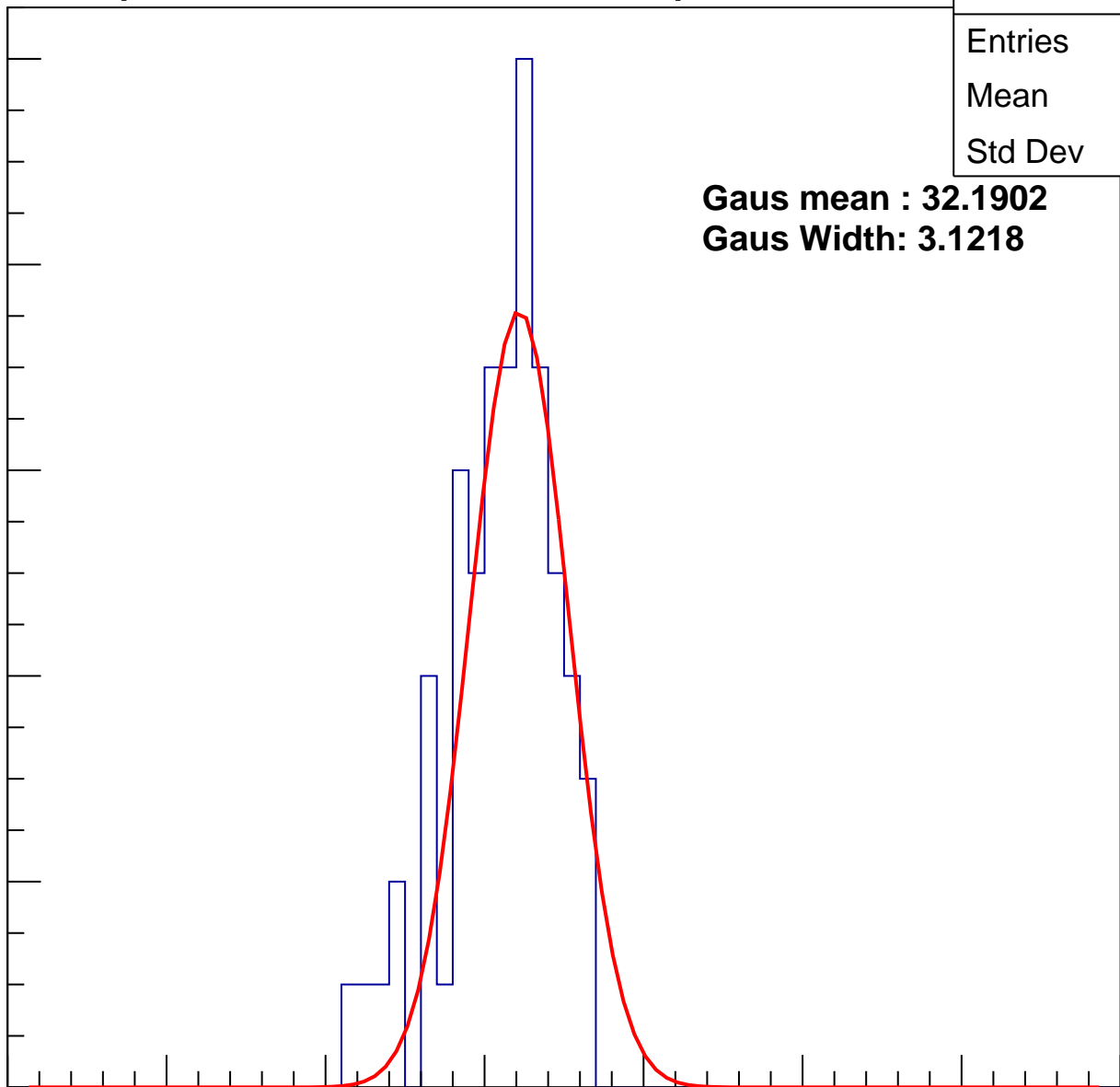
**Gaus Width: 3.1218**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	66
Mean	36.91
Std Dev	3.441

**Gaus mean : 37.7051**

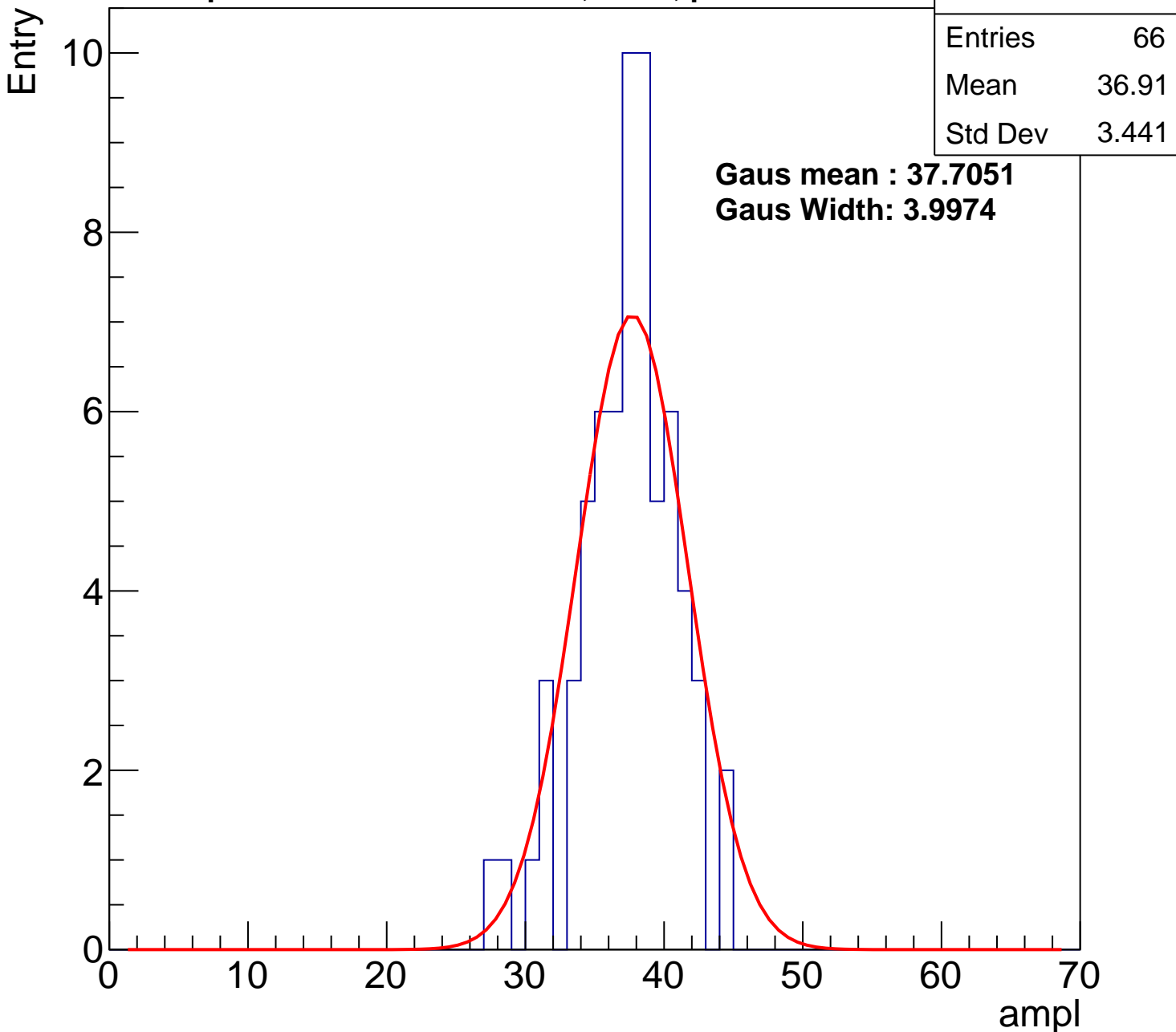
**Gaus Width: 3.9974**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch104, adc2

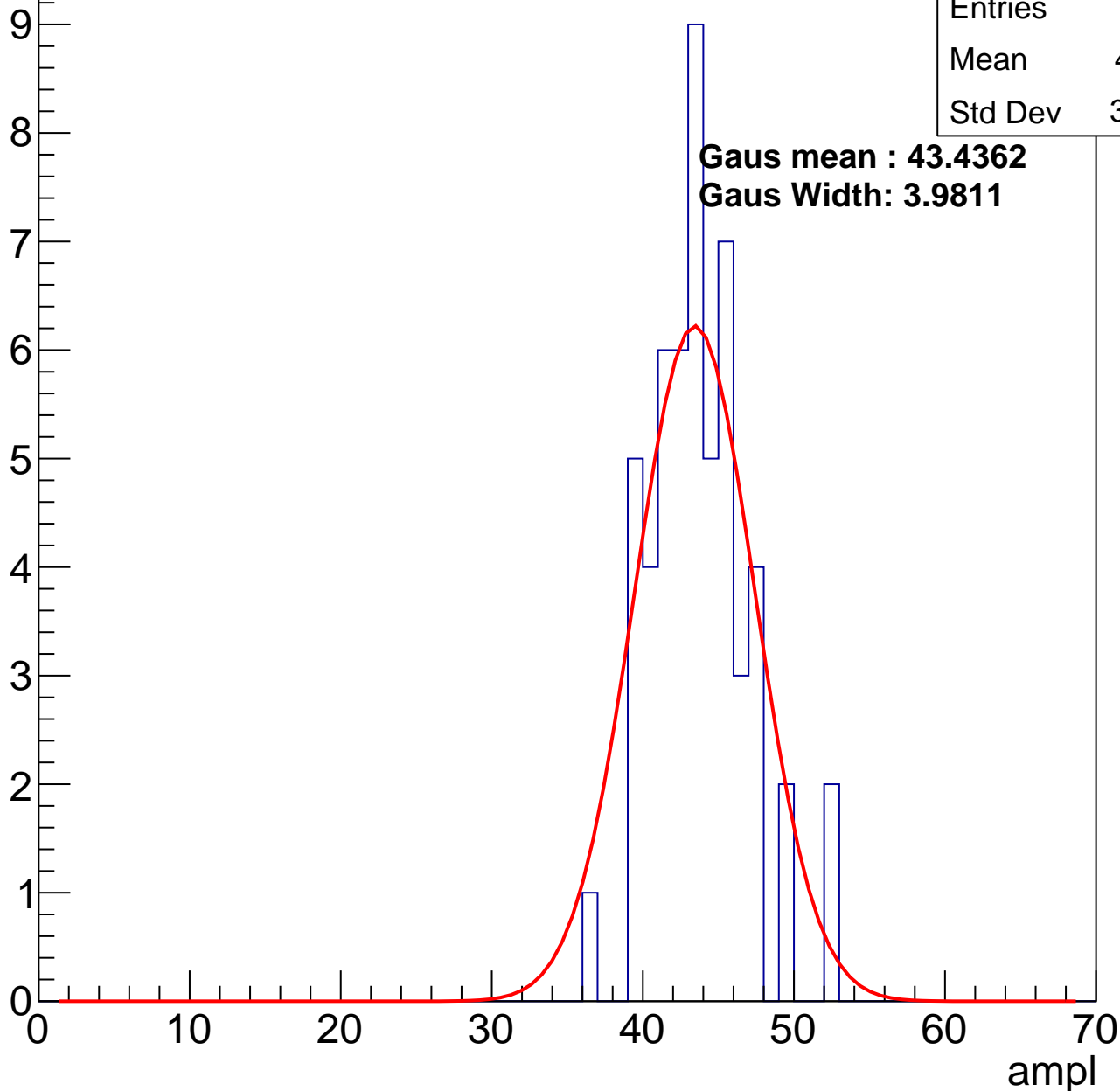
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	43.31
Std Dev	3.185

**Gaus mean : 43.4362**

**Gaus Width: 3.9811**

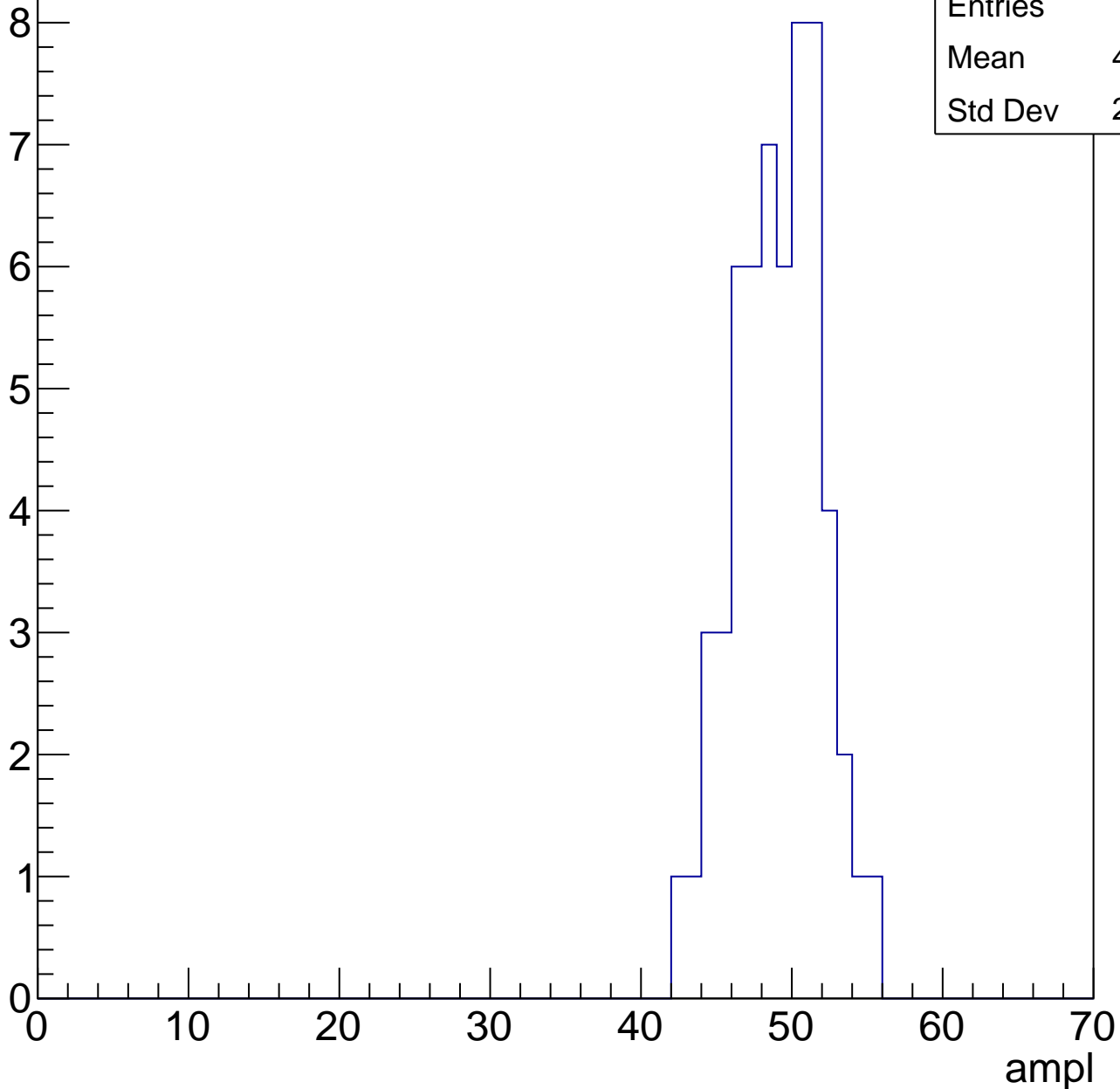


# B1L000S, U25-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	48.61
Std Dev	2.821

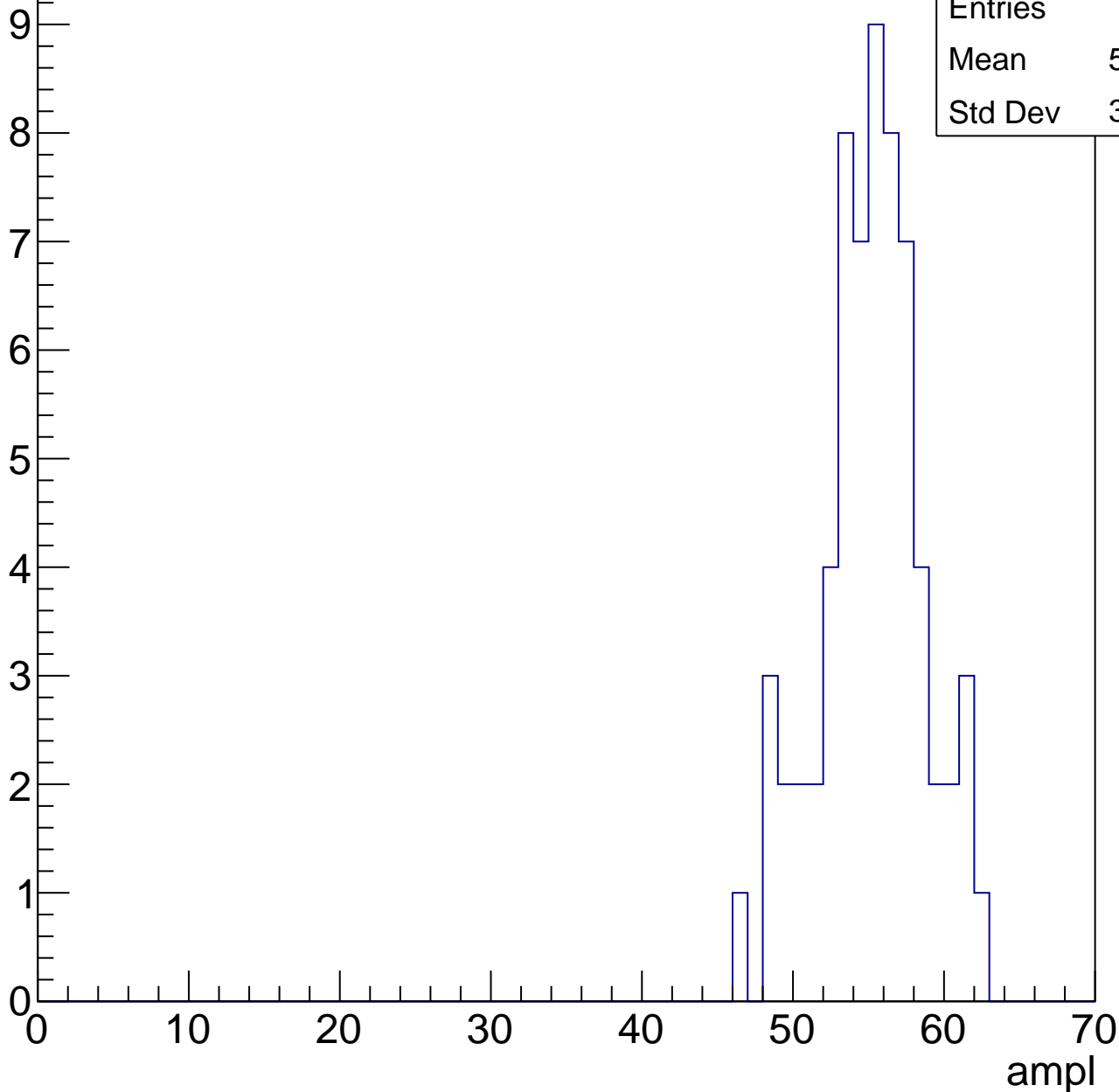


# B1L000S, U25-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.72
Std Dev	3.426

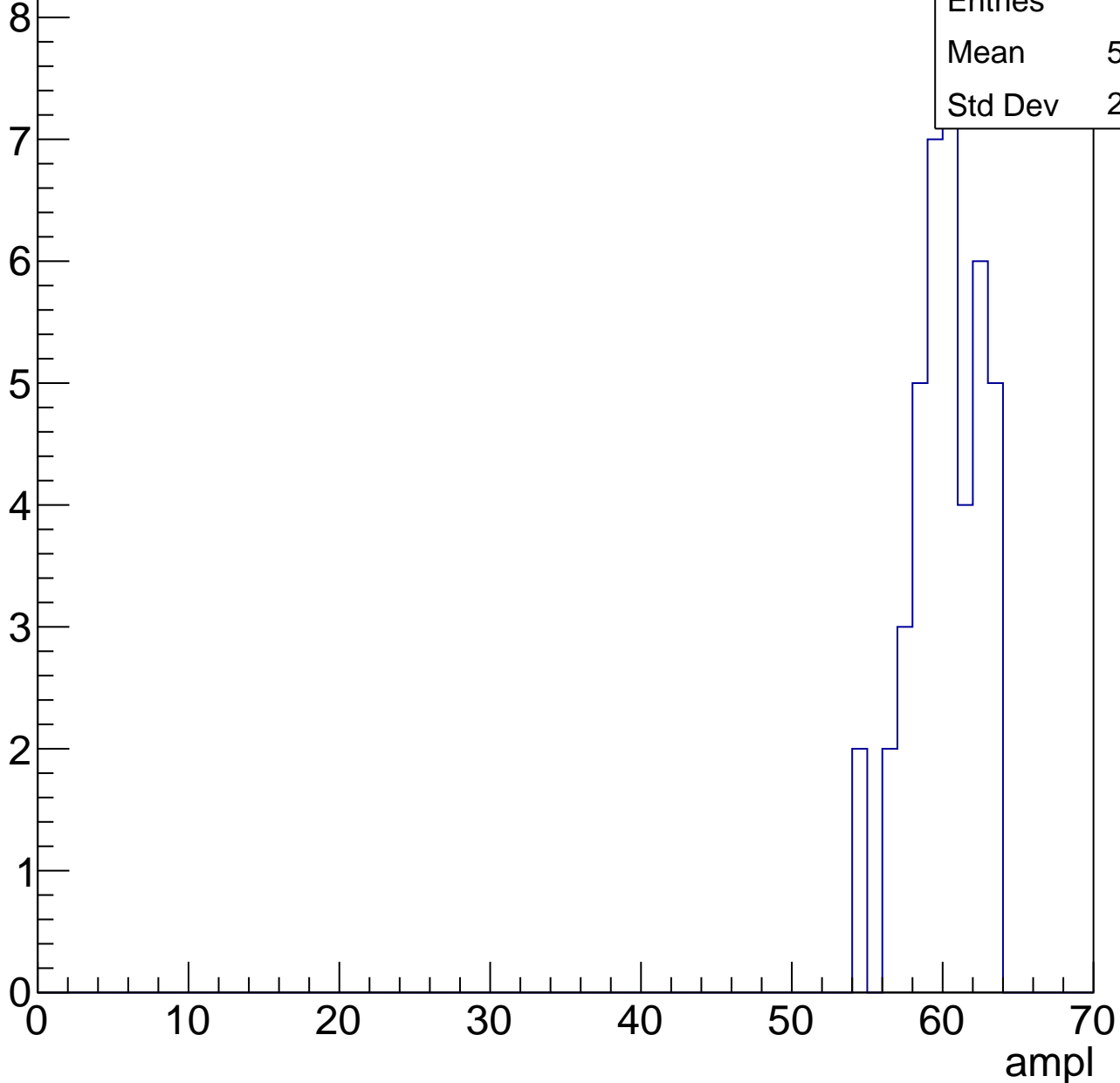


# B1L000S, U25-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	42
Mean	59.64
Std Dev	2.318

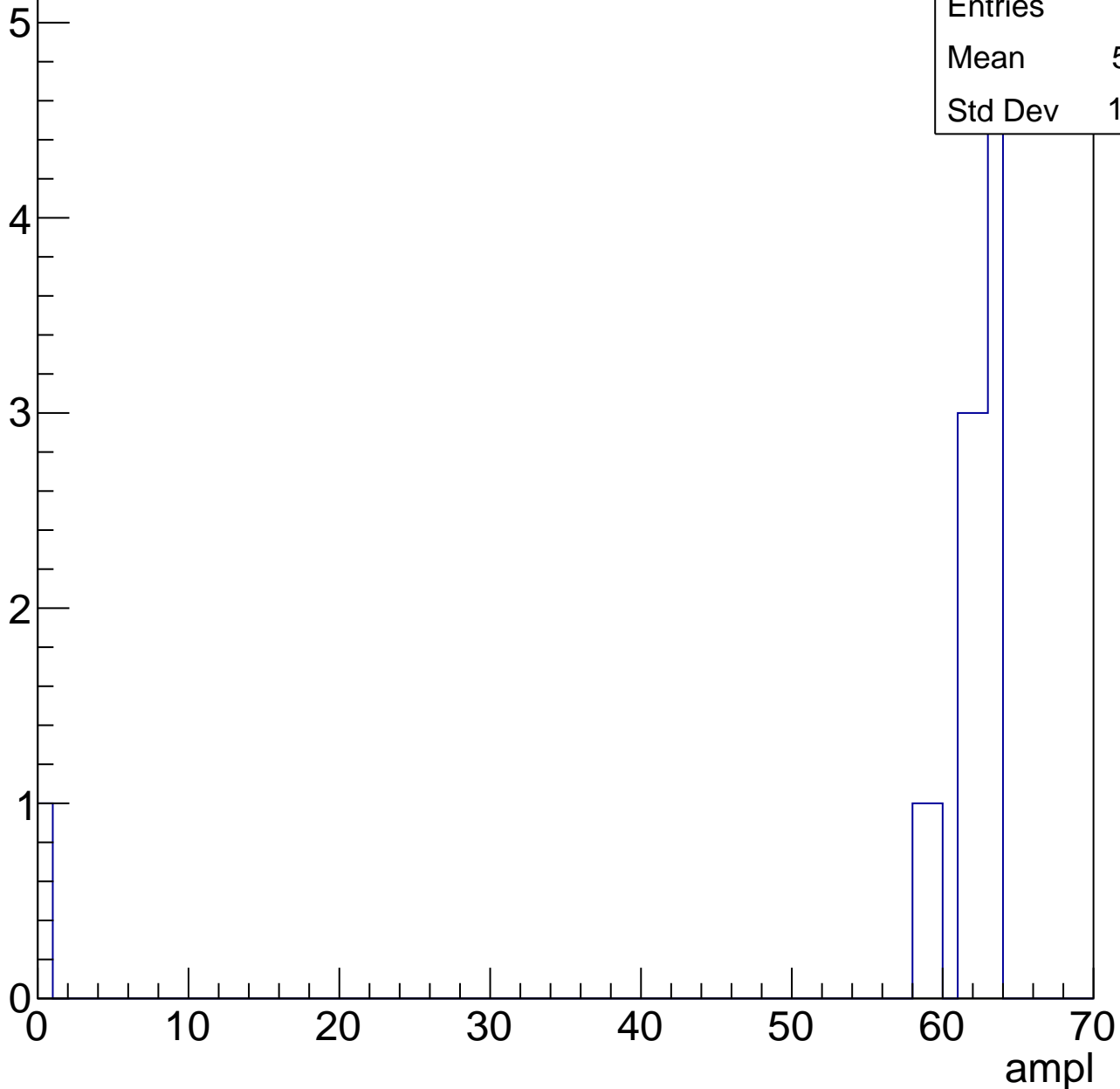


# B1L000S, U25-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.21
Std Dev	15.94





# B1L000S, U25-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch105, adc0

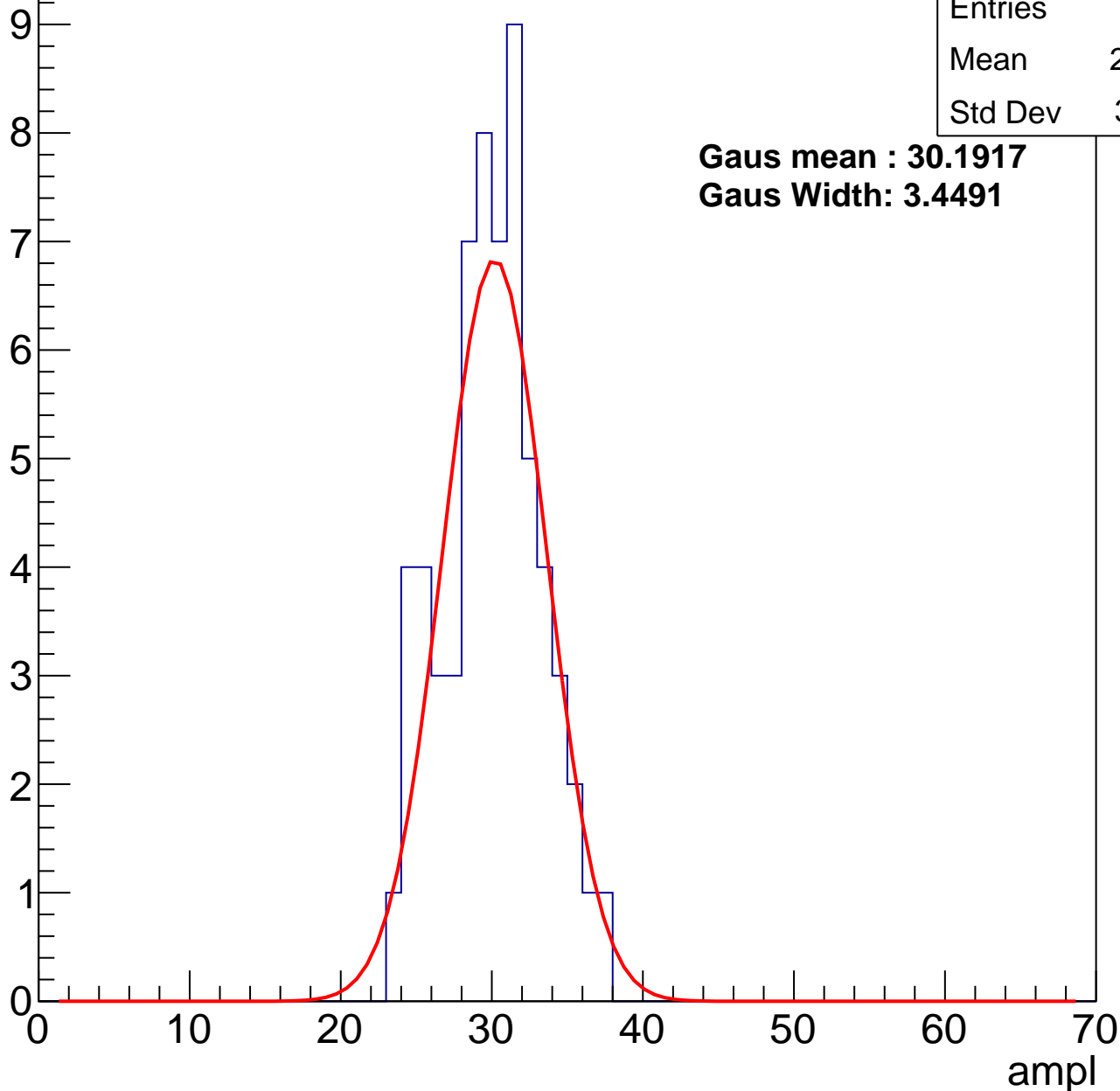
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	29.55
Std Dev	3.211

**Gaus mean : 30.1917**

**Gaus Width: 3.4491**



# B1L000S, U25-ch105, adc1

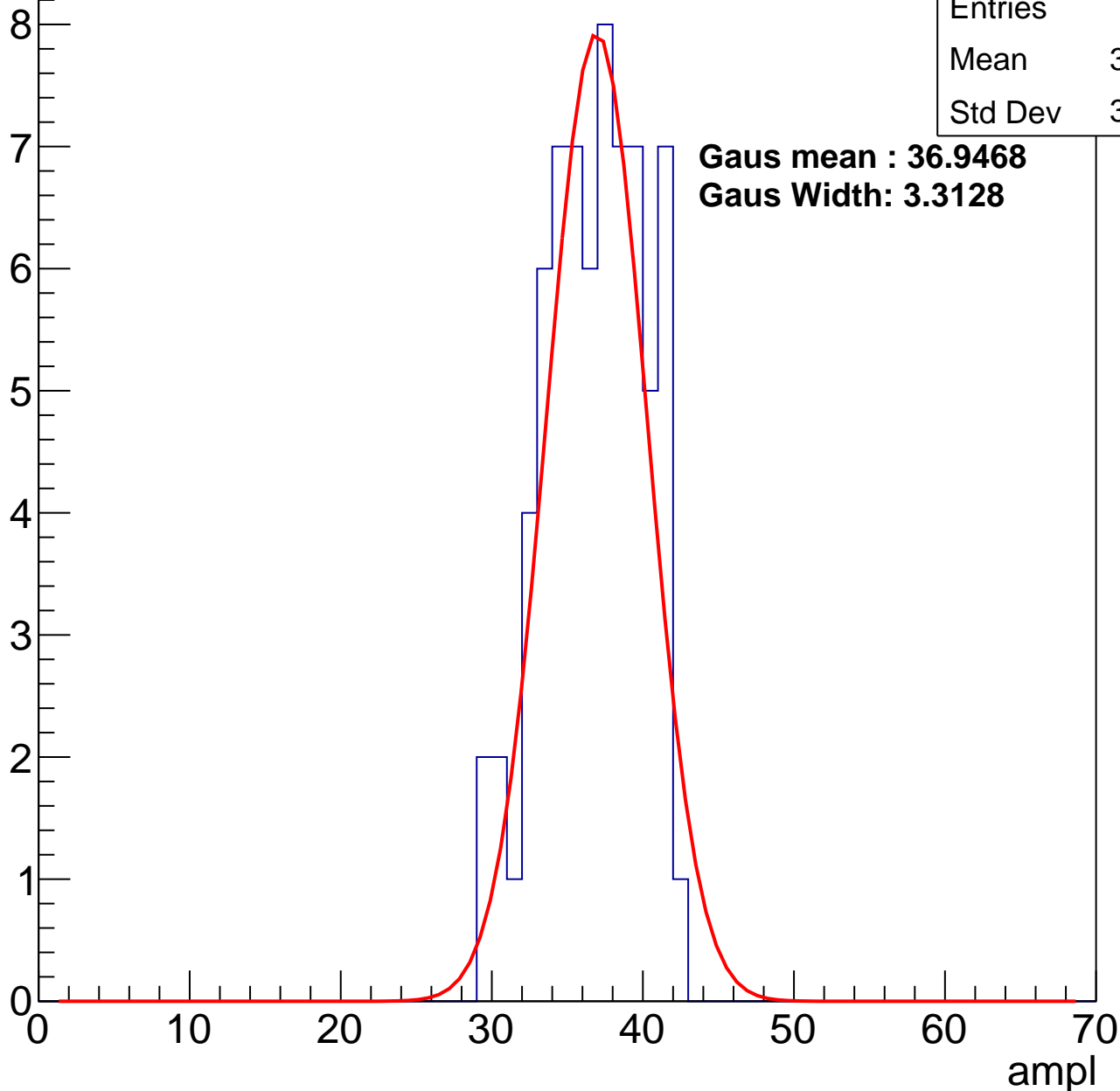
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	36.26
Std Dev	3.237

**Gaus mean : 36.9468**

**Gaus Width: 3.3128**



# B1L000S, U25-ch105, adc2

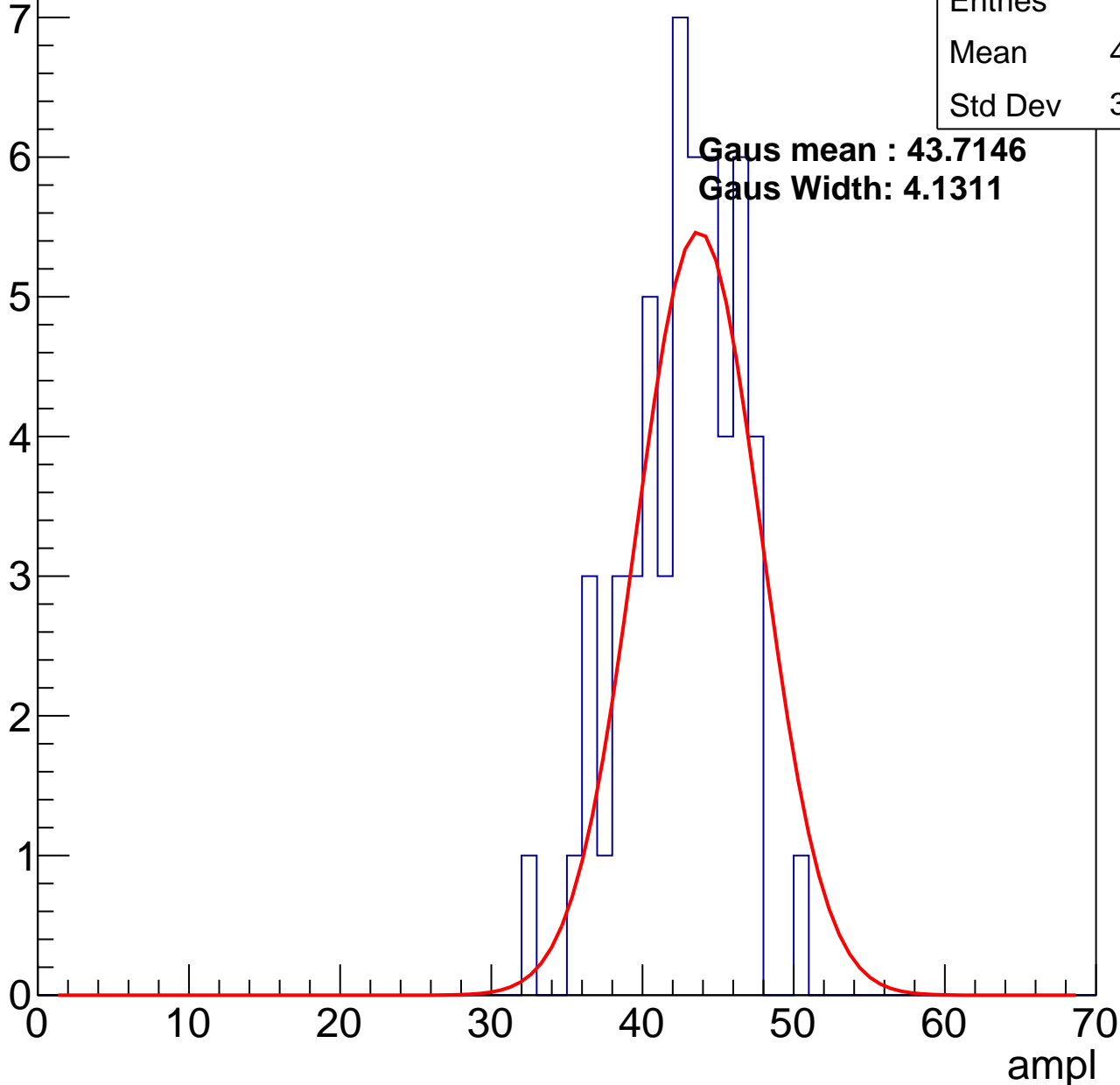
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	42.15
Std Dev	3.613

**Gaus mean : 43.7146**

**Gaus Width: 4.1311**

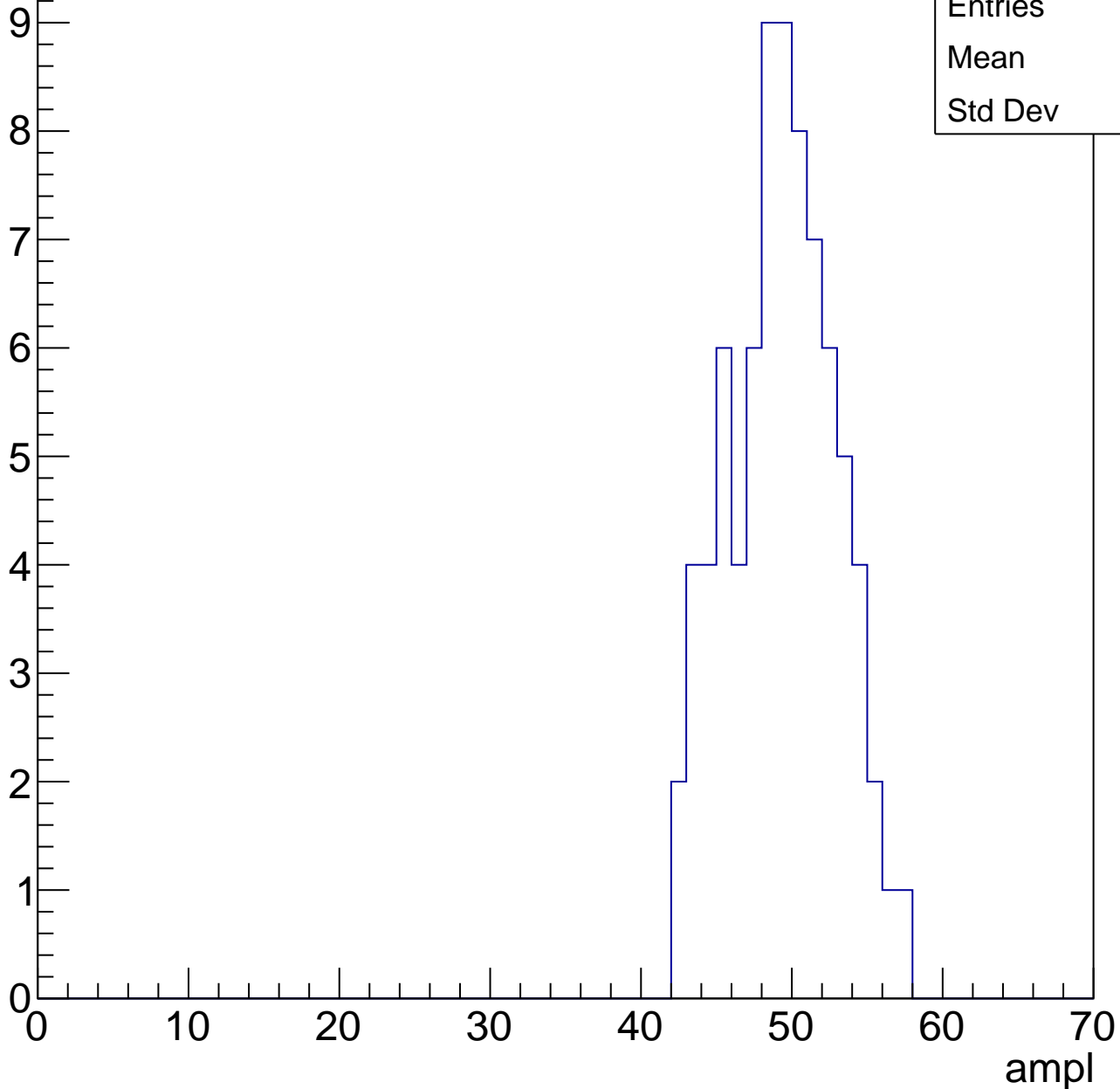


# B1L000S, U25-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	48.9
Std Dev	3.51

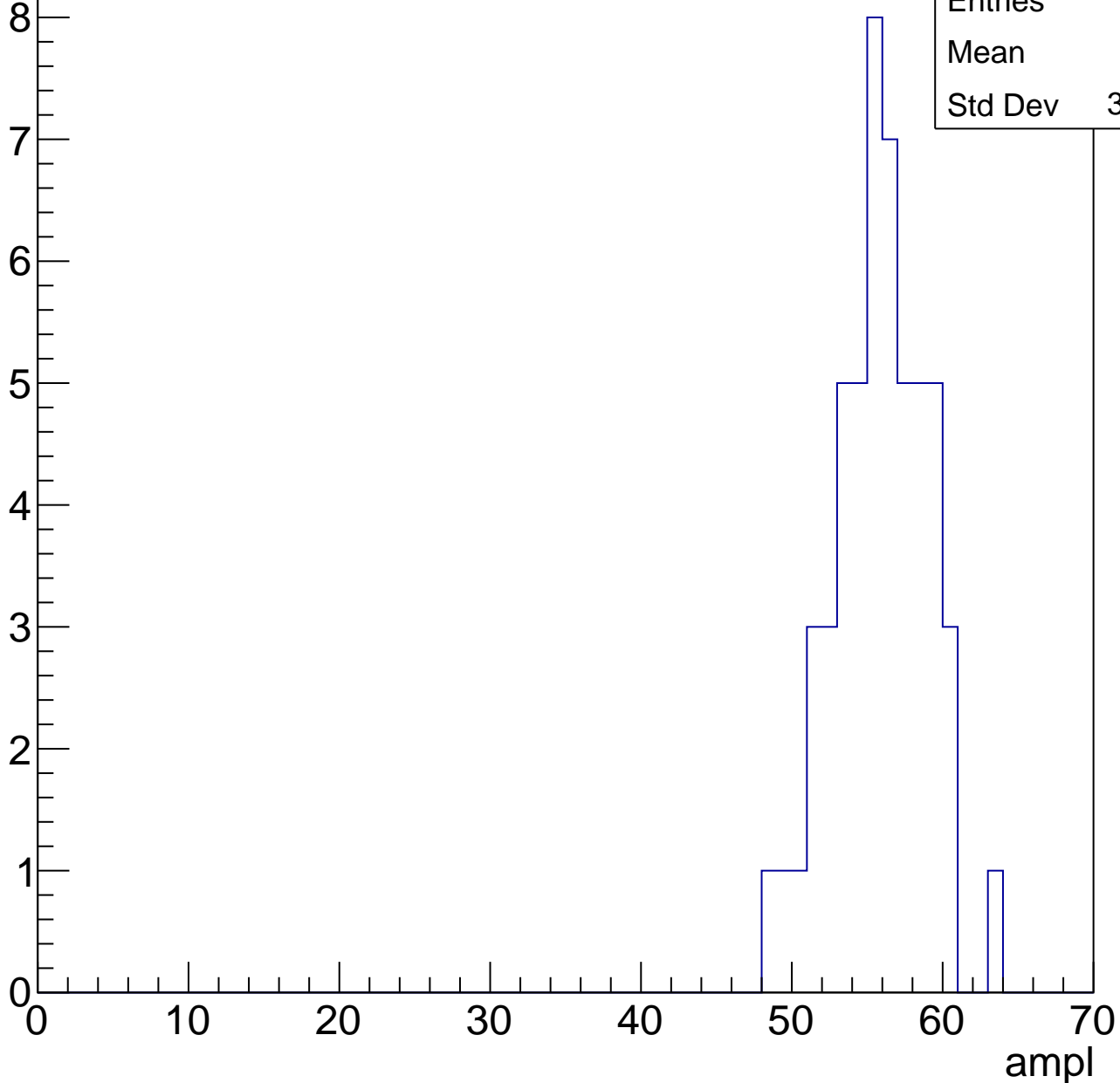


# B1L000S, U25-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	55.4
Std Dev	3.049



# B1L000S, U25-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	41
Mean	59.49
Std Dev	2.39

Entry

10

8

6

4

2

0

0

10

20

30

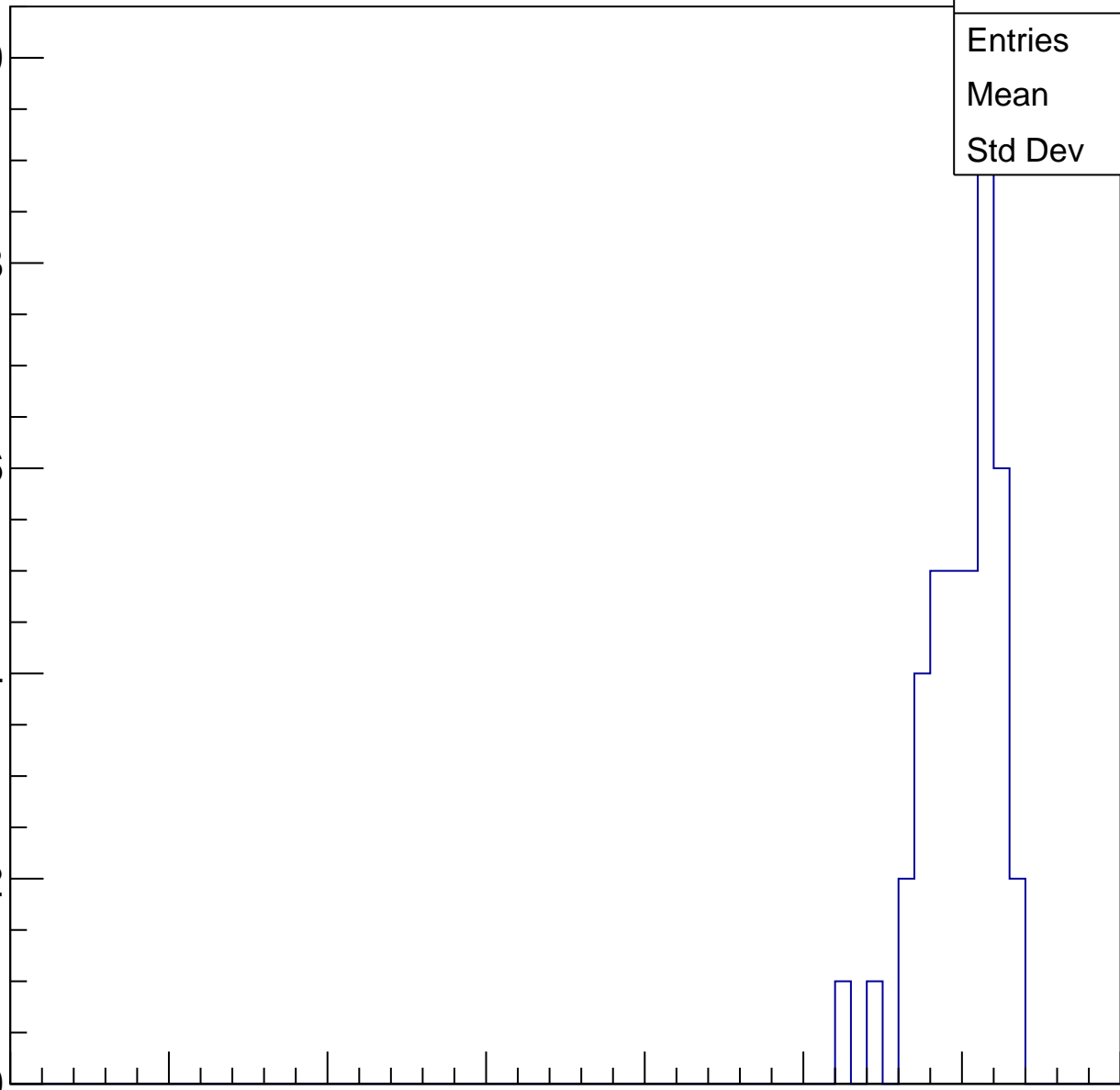
40

50

60

70

ampl

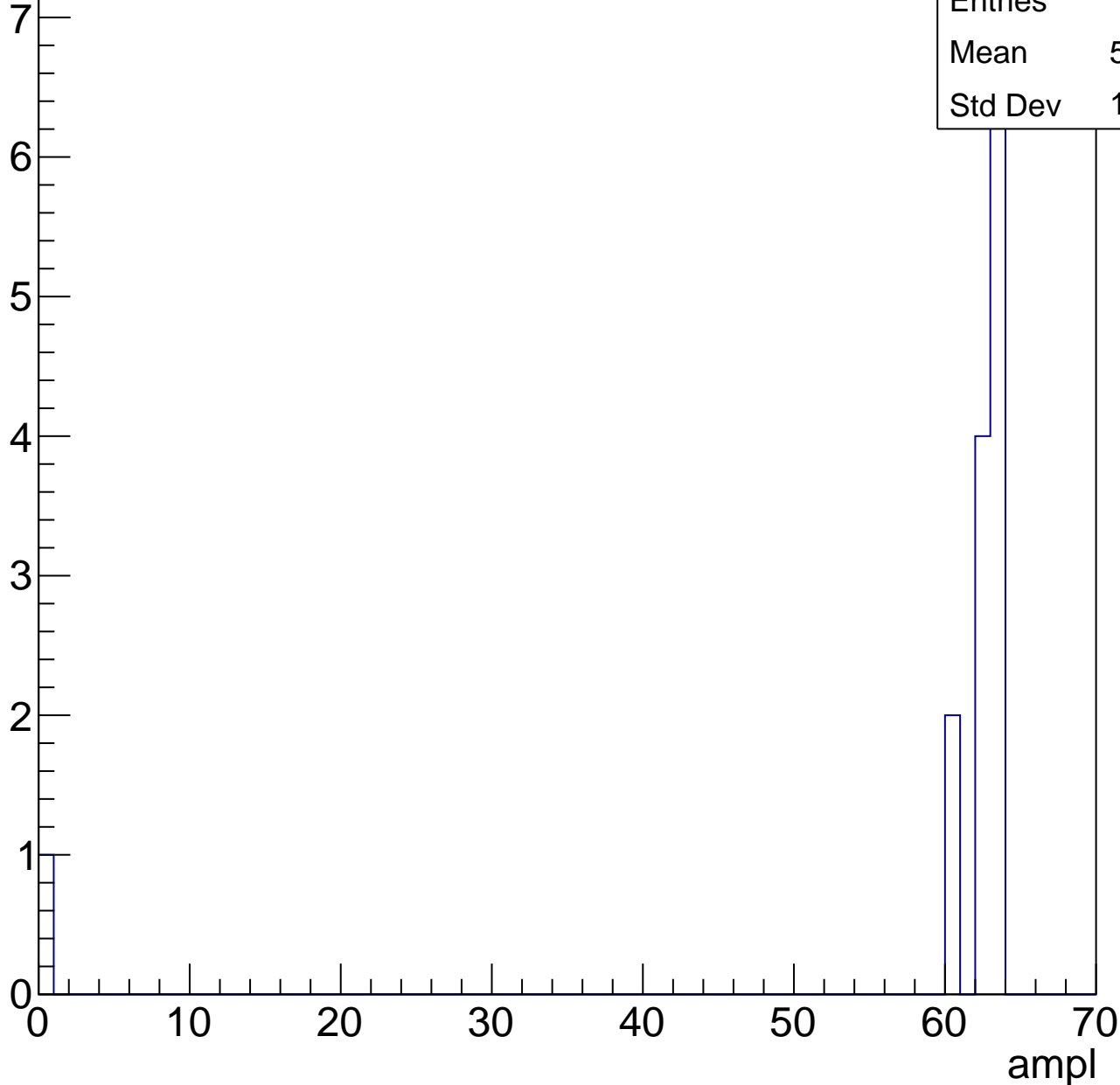


# B1L000S, U25-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	57.79
Std Dev	16.06





# B1L000S, U25-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L000S, U25-ch106, adc0

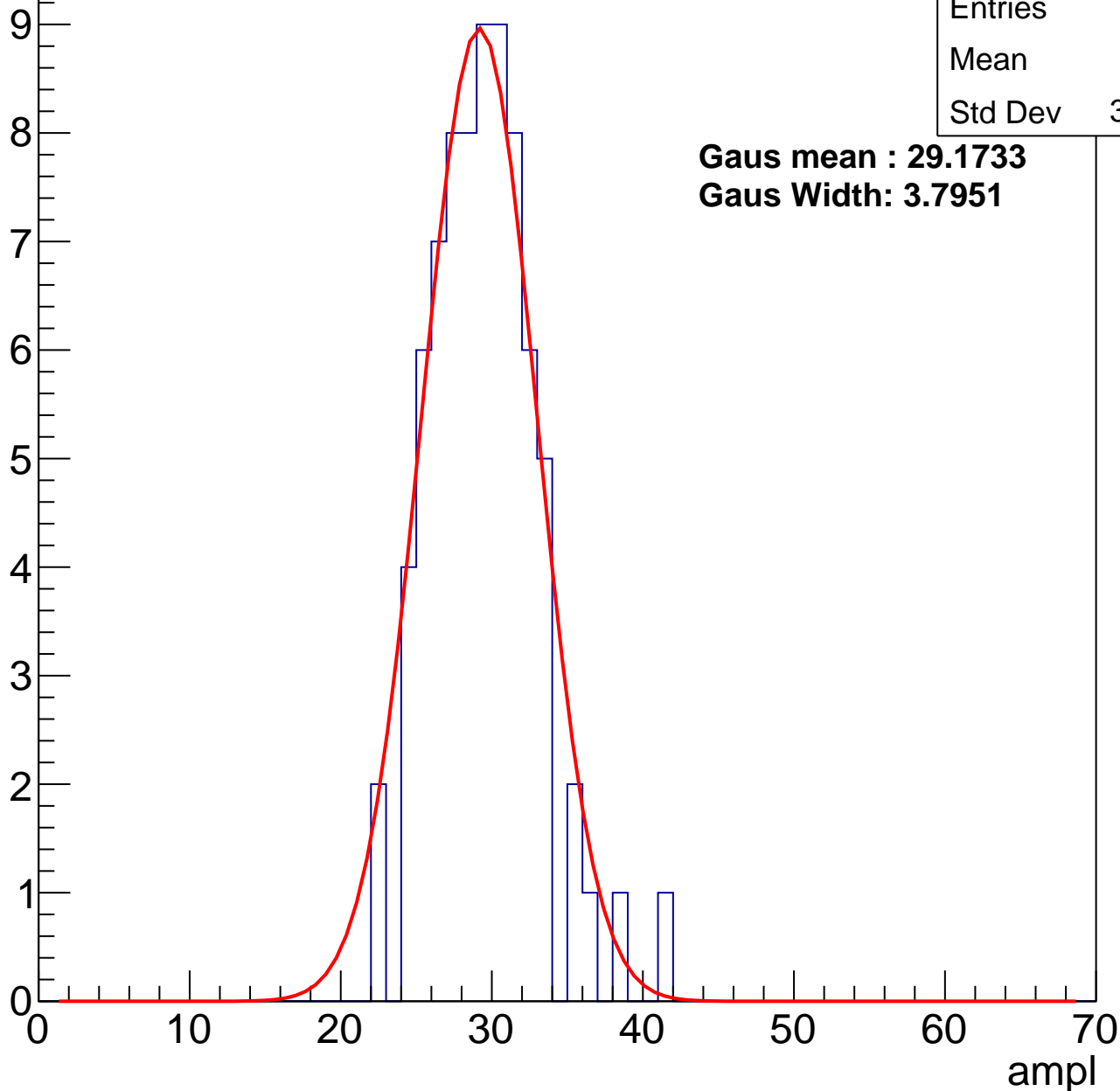
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	29
Std Dev	3.453

**Gaus mean : 29.1733**

**Gaus Width: 3.7951**



# B1L000S, U25-ch106, adc1

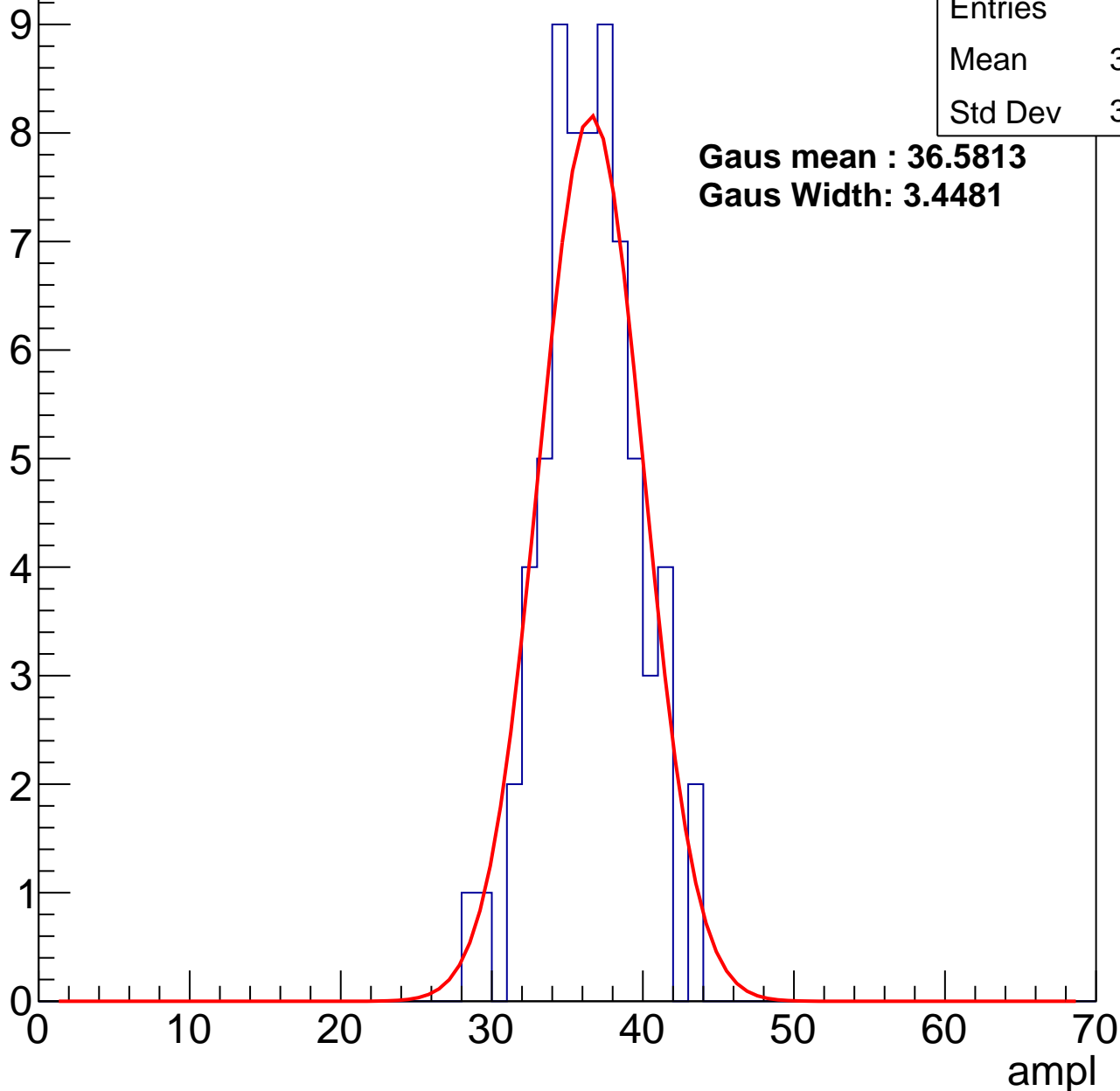
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	36.03
Std Dev	3.077

**Gaus mean : 36.5813**

**Gaus Width: 3.4481**



# B1L000S, U25-ch106, adc2

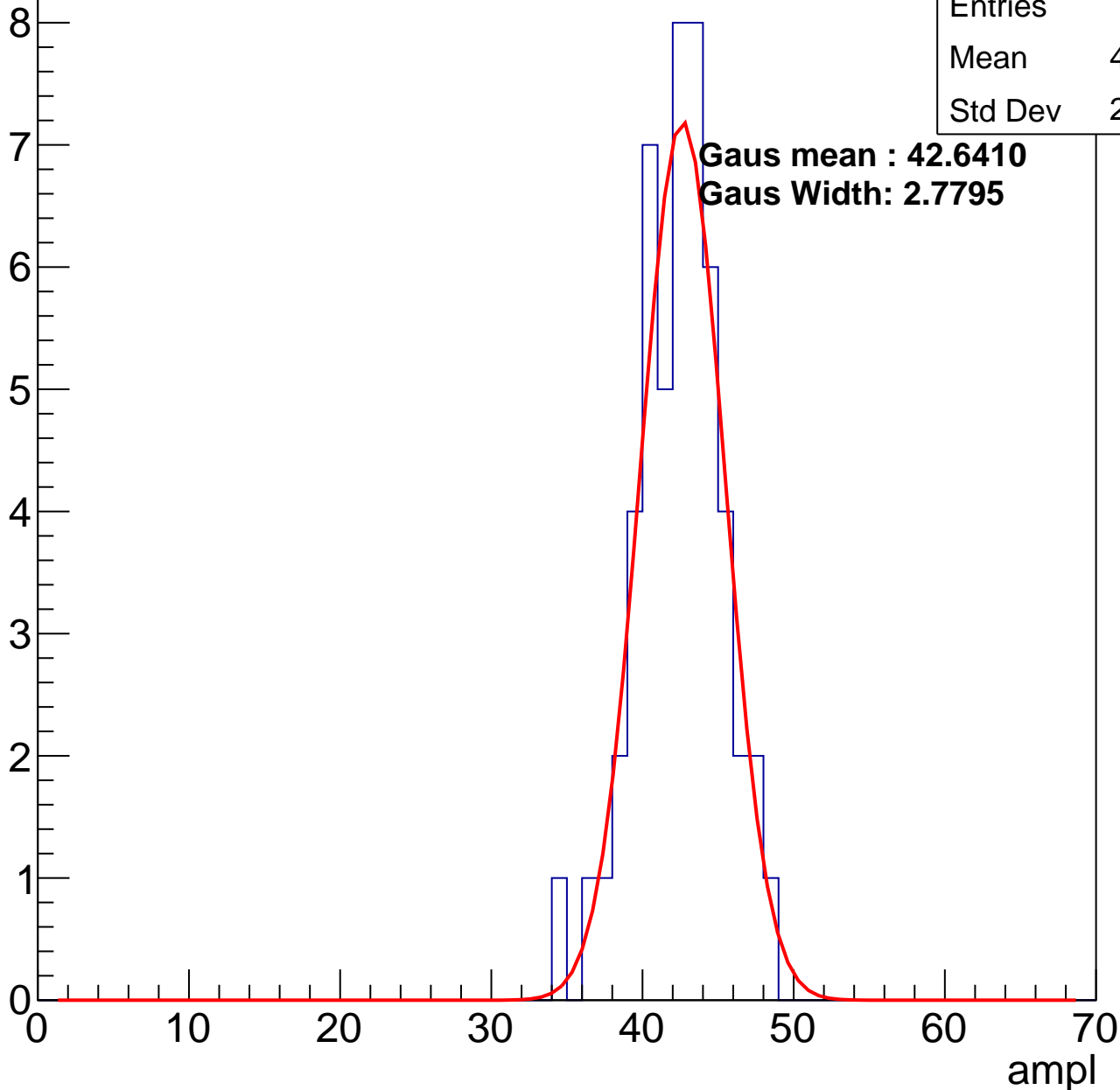
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	41.96
Std Dev	2.815

**Gaus mean : 42.6410**

**Gaus Width: 2.7795**

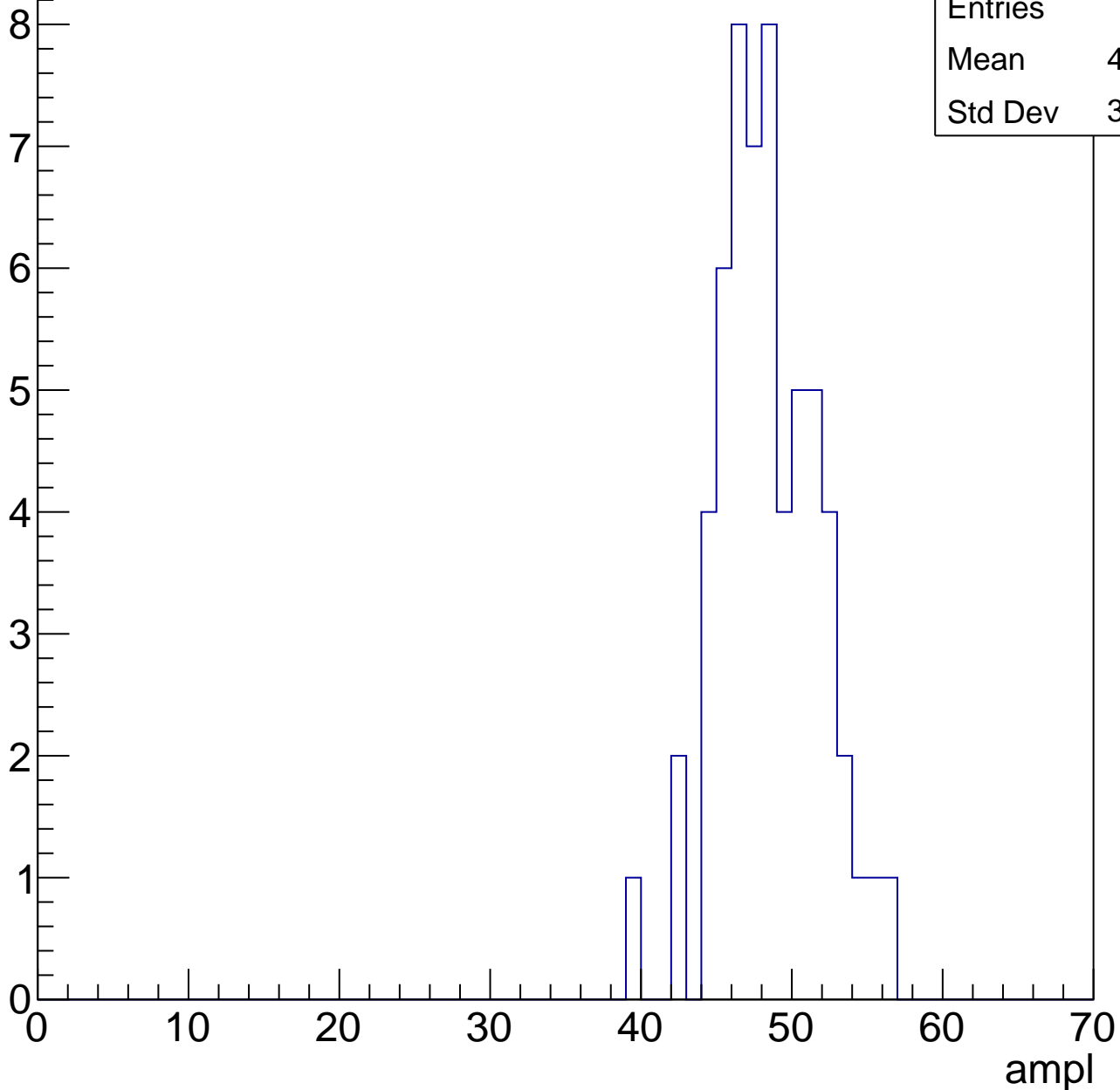


# B1L000S, U25-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.97
Std Dev	3.299

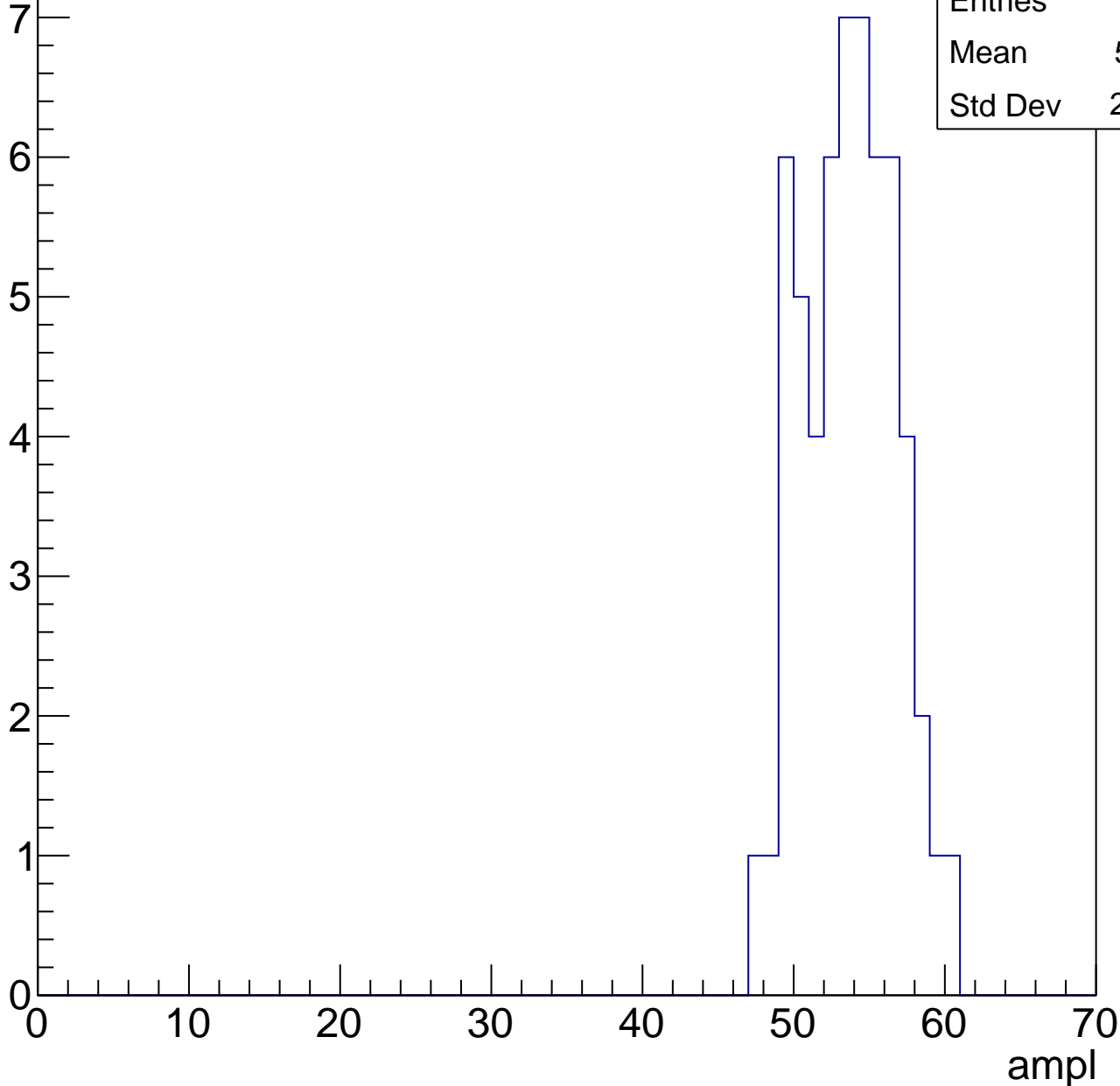


# B1L000S, U25-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	53.21
Std Dev	2.978

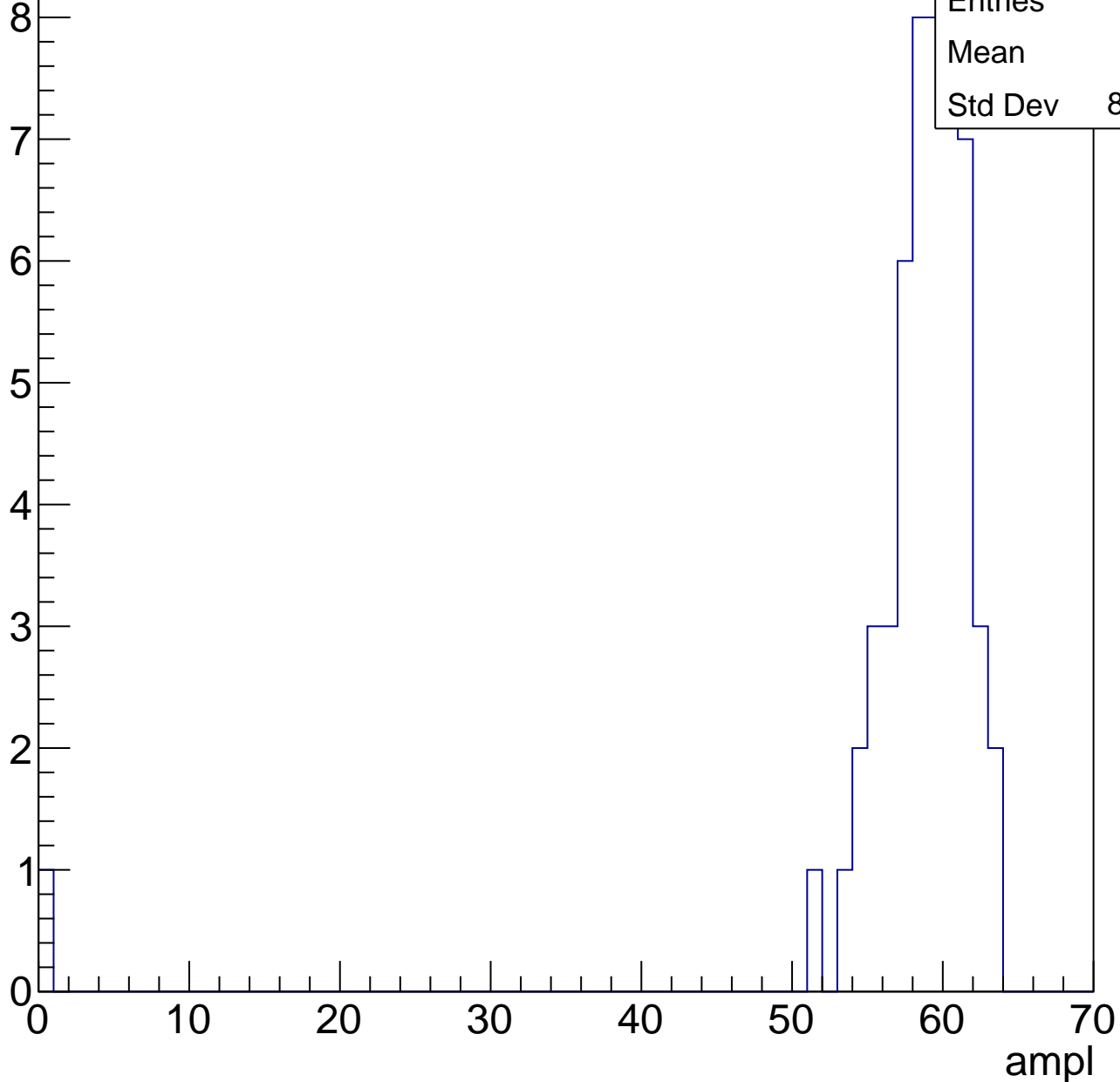


# B1L000S, U25-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	57.4
Std Dev	8.352

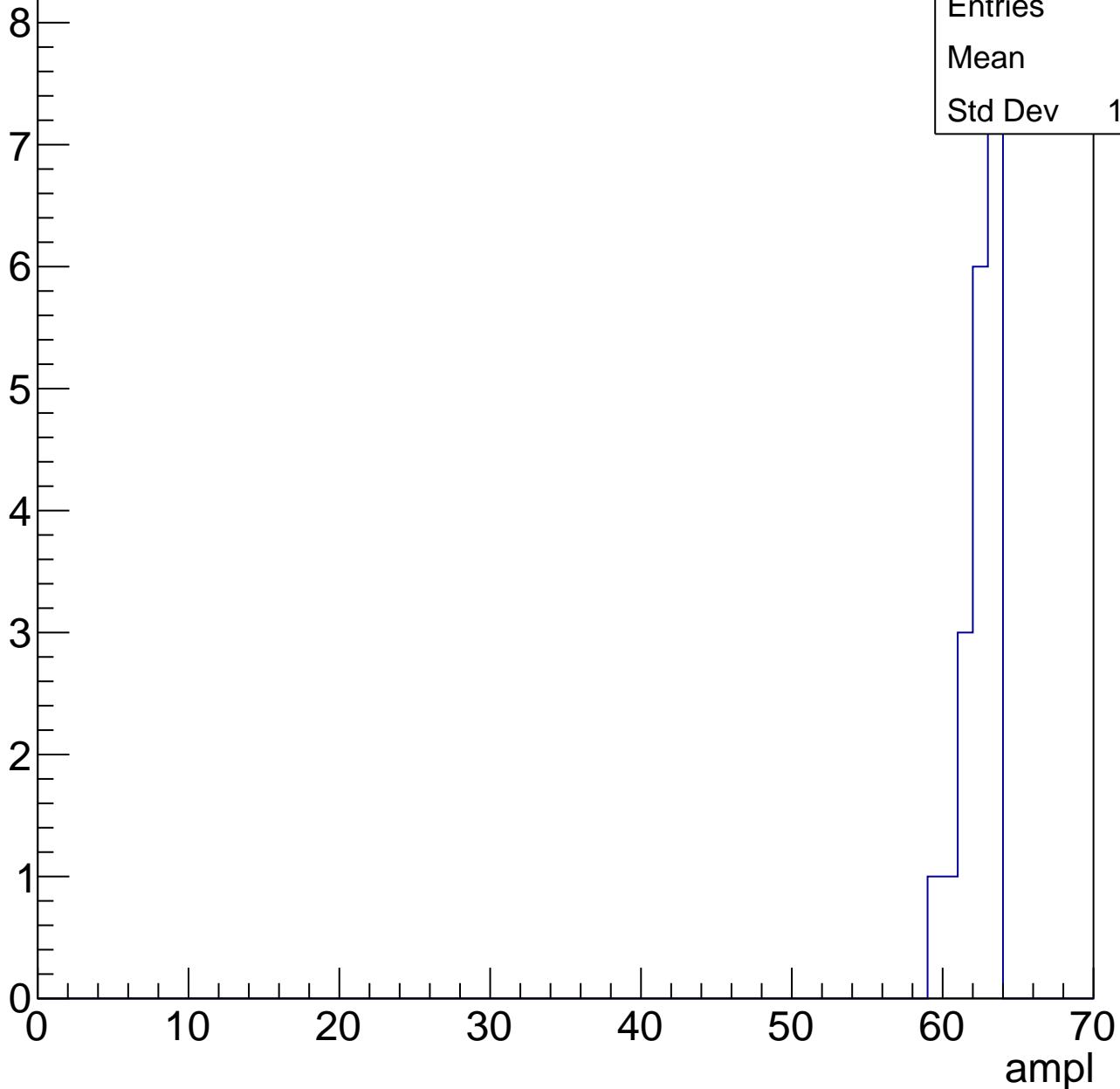


# B1L000S, U25-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	19
Mean	62
Std Dev	1.124

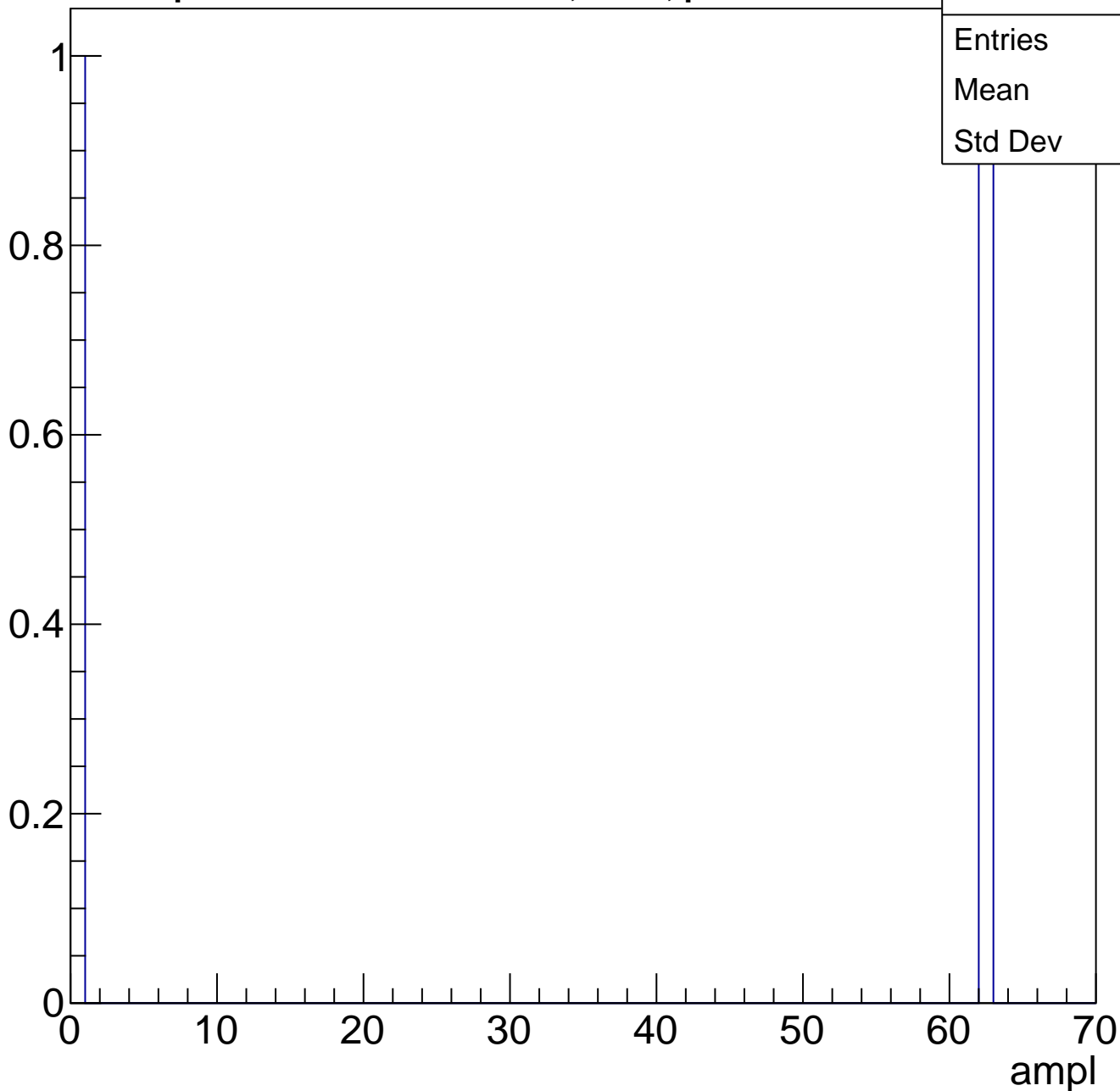




# B1L000S, U25-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch107, adc0

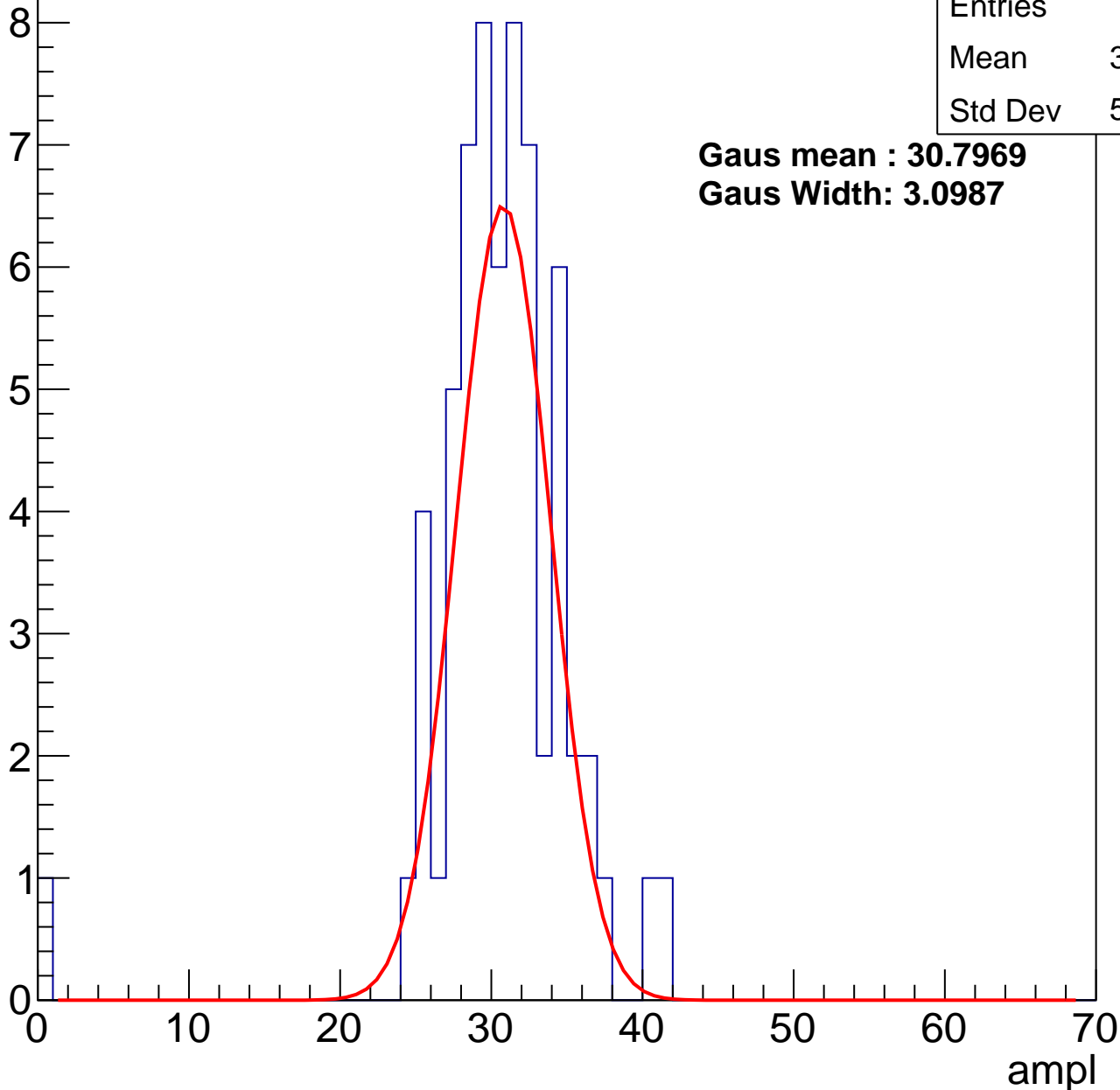
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	30.08
Std Dev	5.156

**Gaus mean : 30.7969**

**Gaus Width: 3.0987**



# B1L000S, U25-ch107, adc1

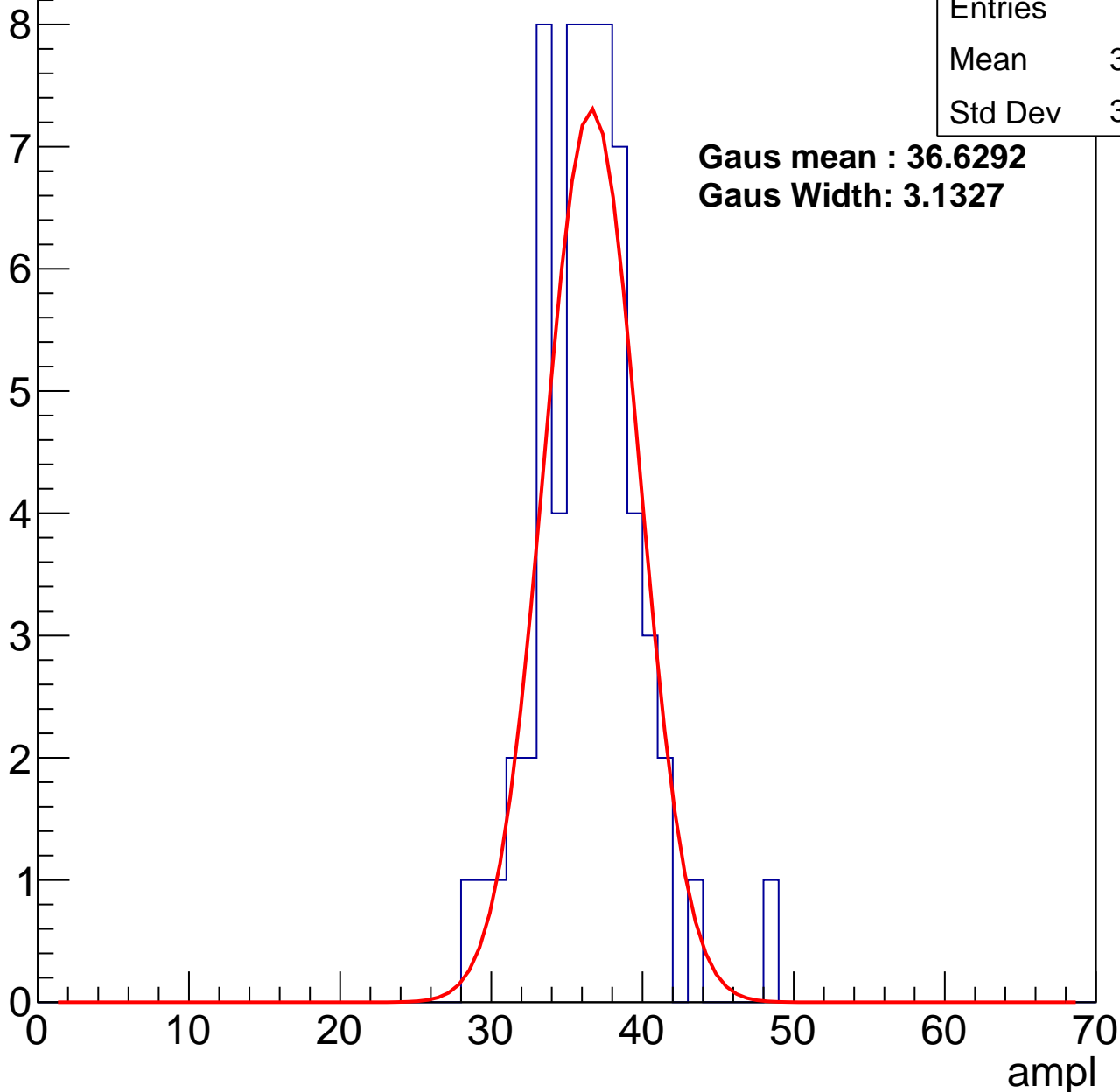
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	35.93
Std Dev	3.363

**Gaus mean : 36.6292**

**Gaus Width: 3.1327**



# B1L000S, U25-ch107, adc2

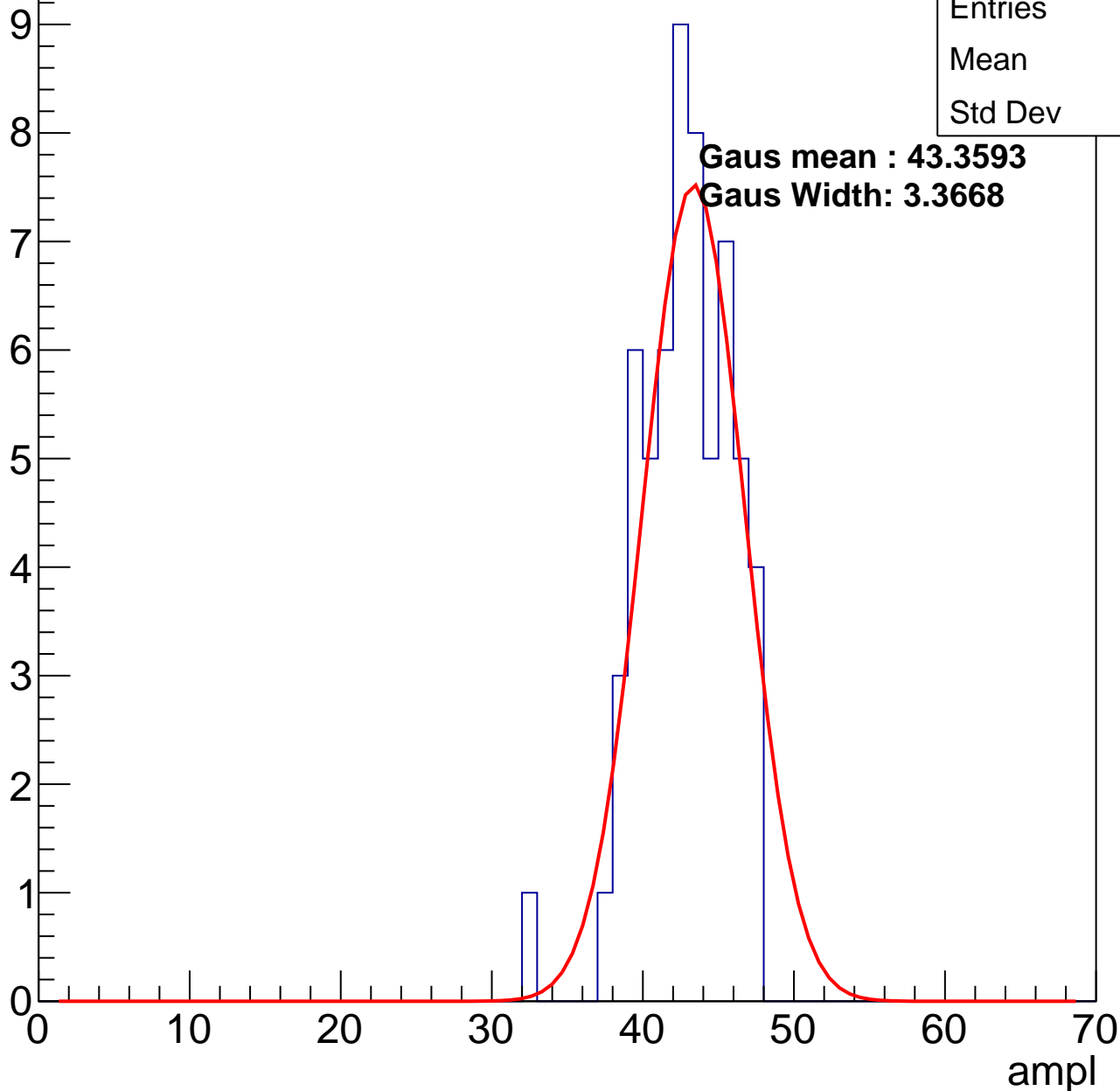
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	42.3
Std Dev	2.94

**Gaus mean : 43.3593**

**Gaus Width: 3.3668**



# B1L000S, U25-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

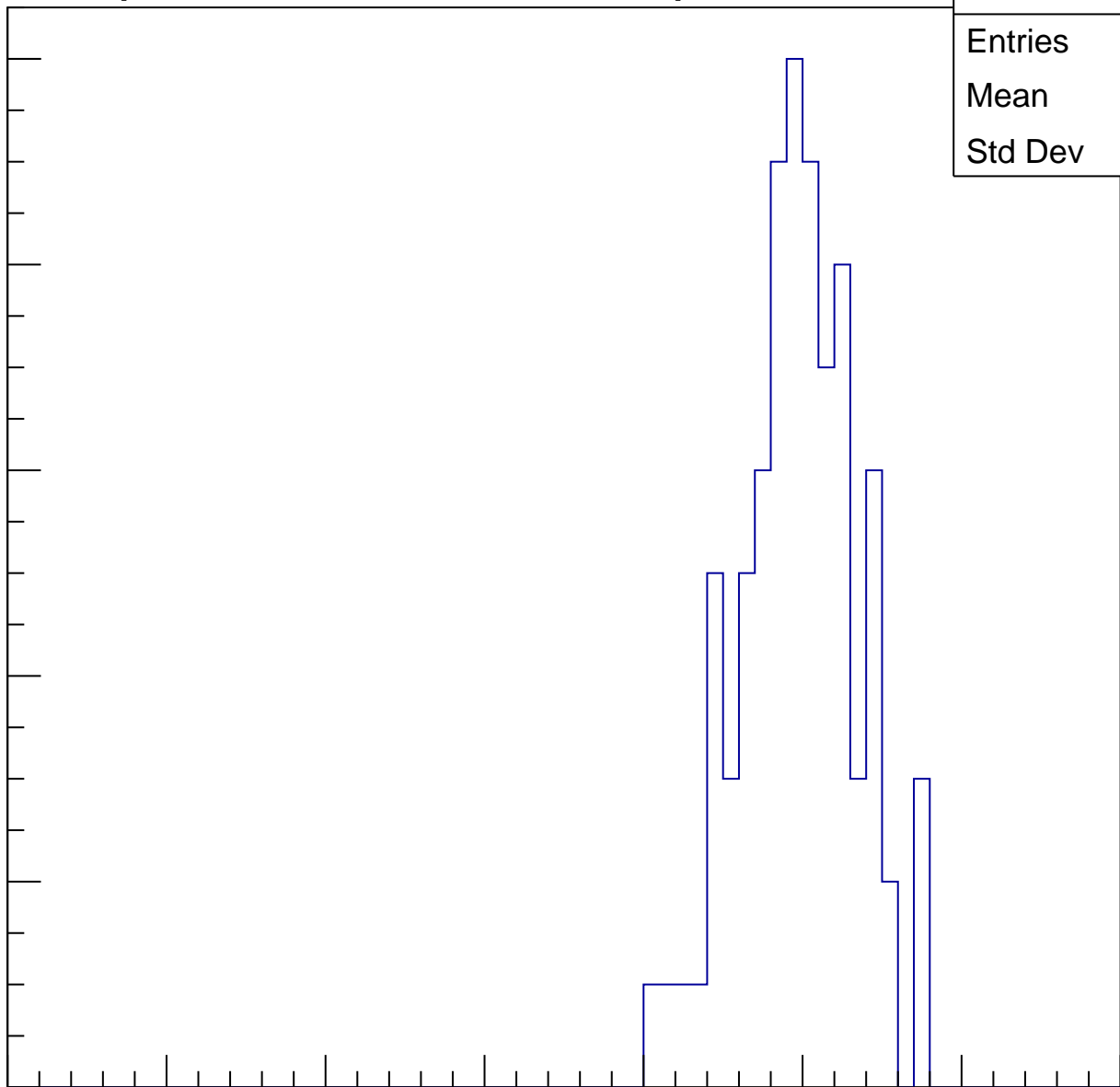
Entries	80
Mean	49.27
Std Dev	3.616

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

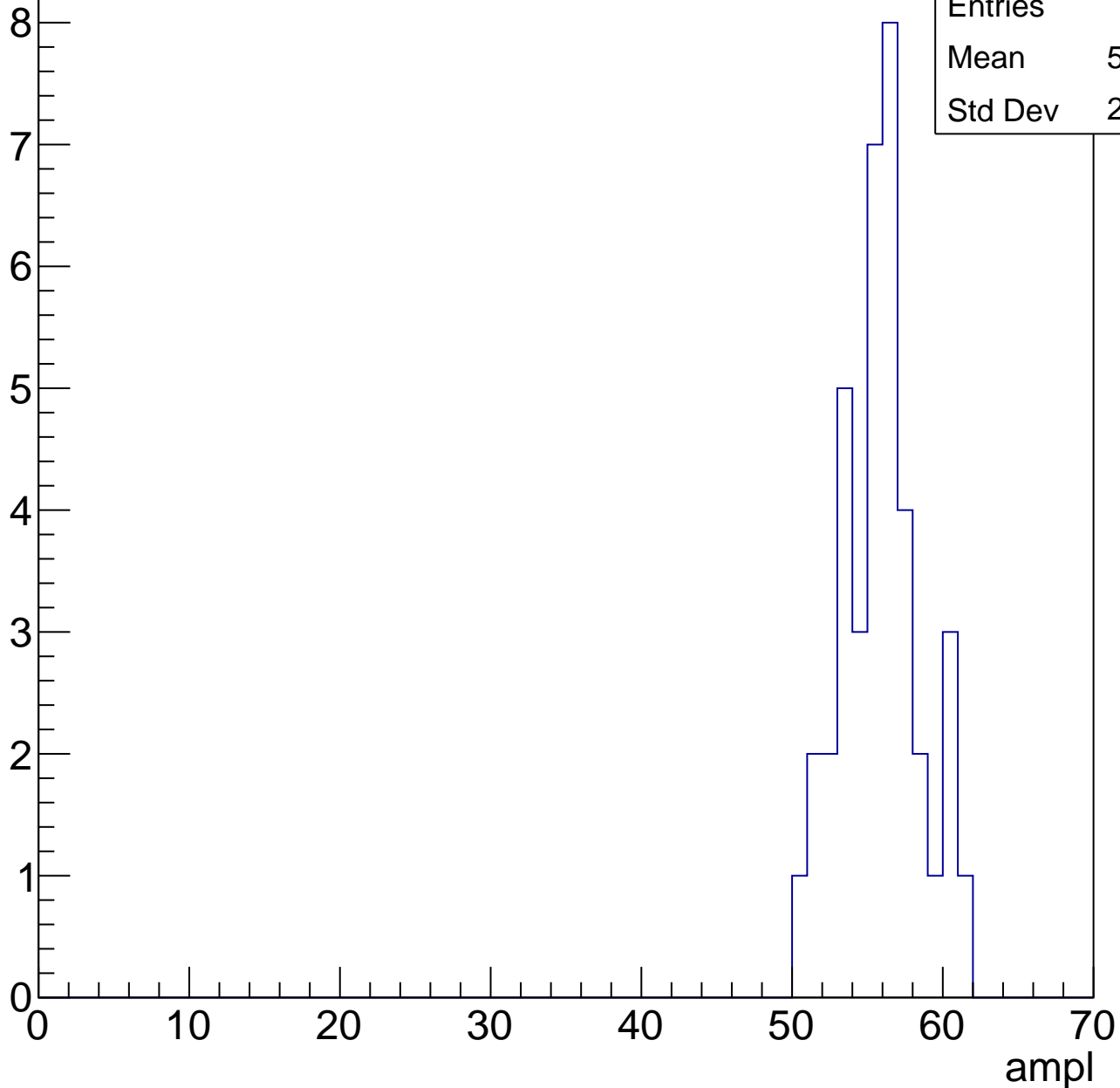
ampl



# B1L000S, U25-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

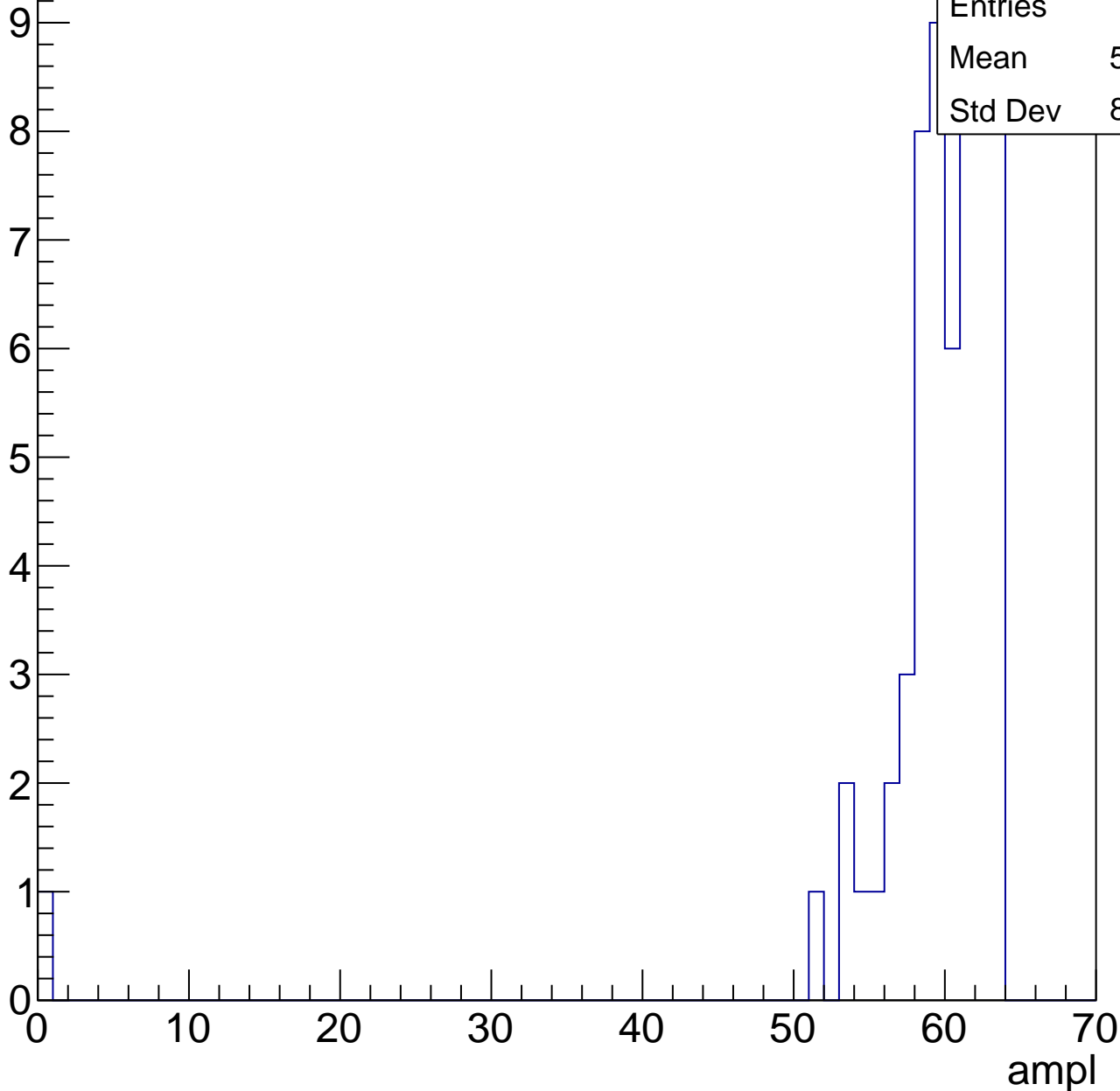


# B1L000S, U25-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	58.62
Std Dev	8.114



# B1L000S, U25-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.5
Std Dev	1.118

ampl



# B1L000S, U25-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch108, adc0

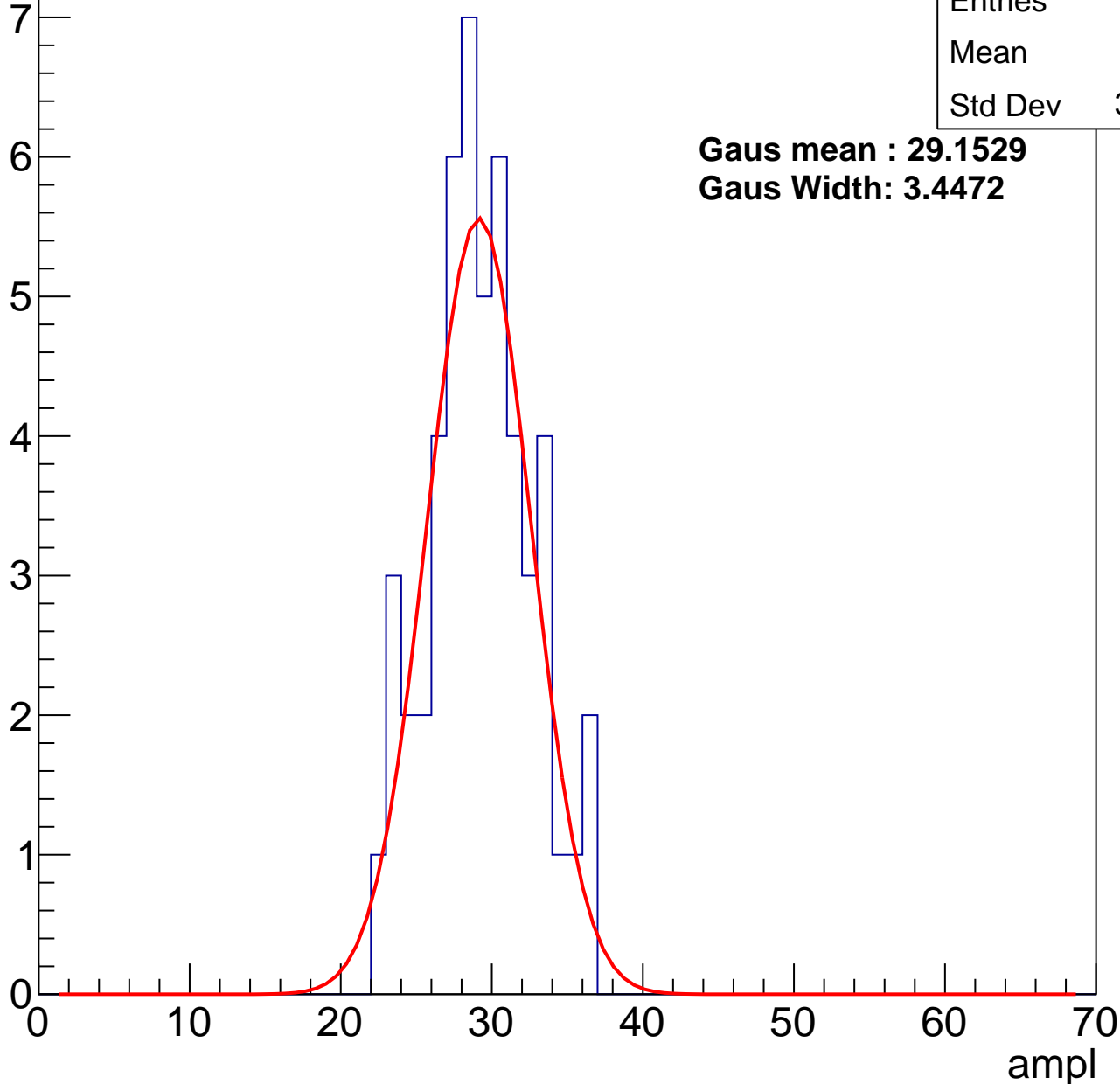
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	28.8
Std Dev	3.361

**Gaus mean : 29.1529**

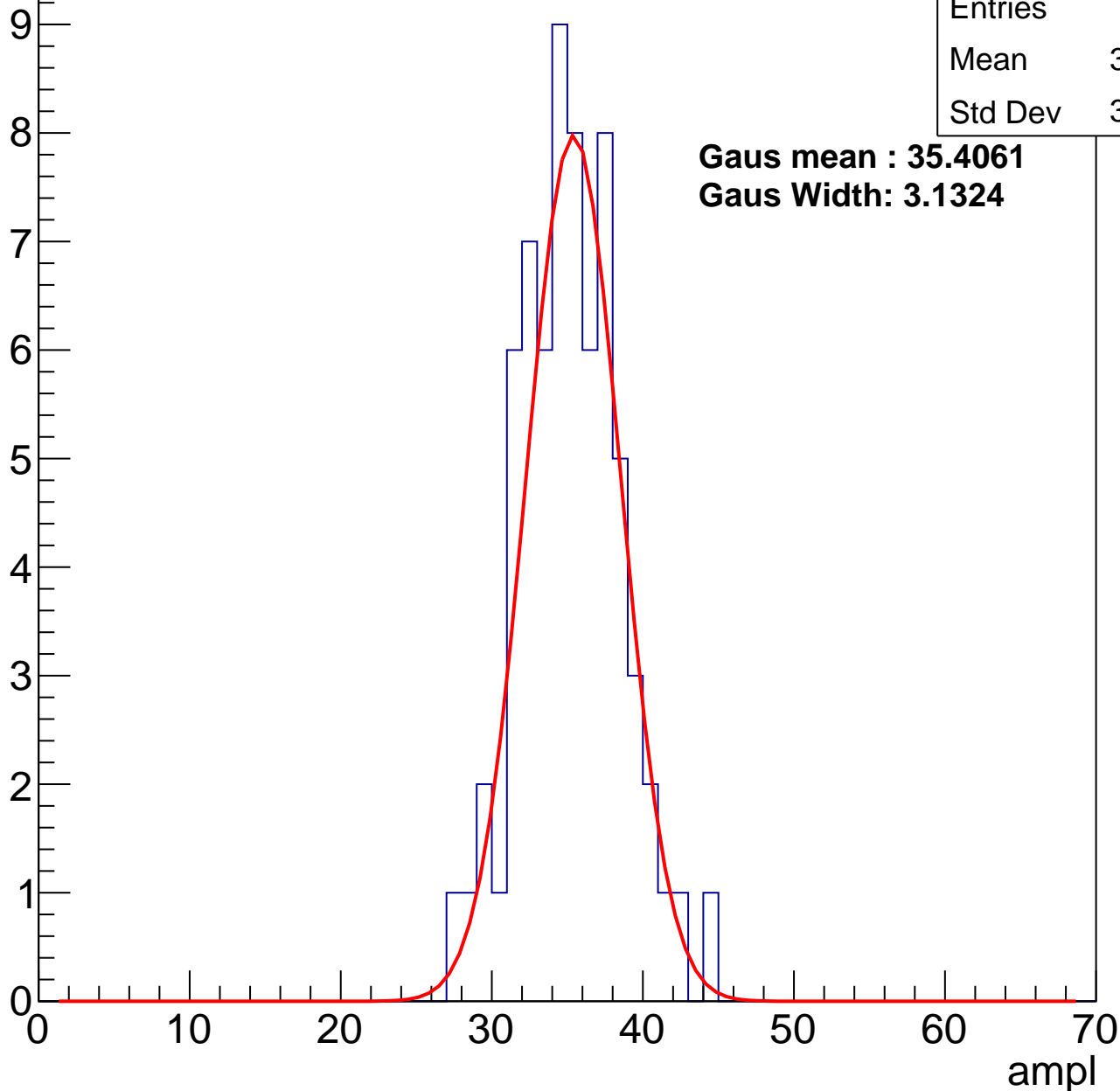
**Gaus Width: 3.4472**



# B1L000S, U25-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch108, adc2

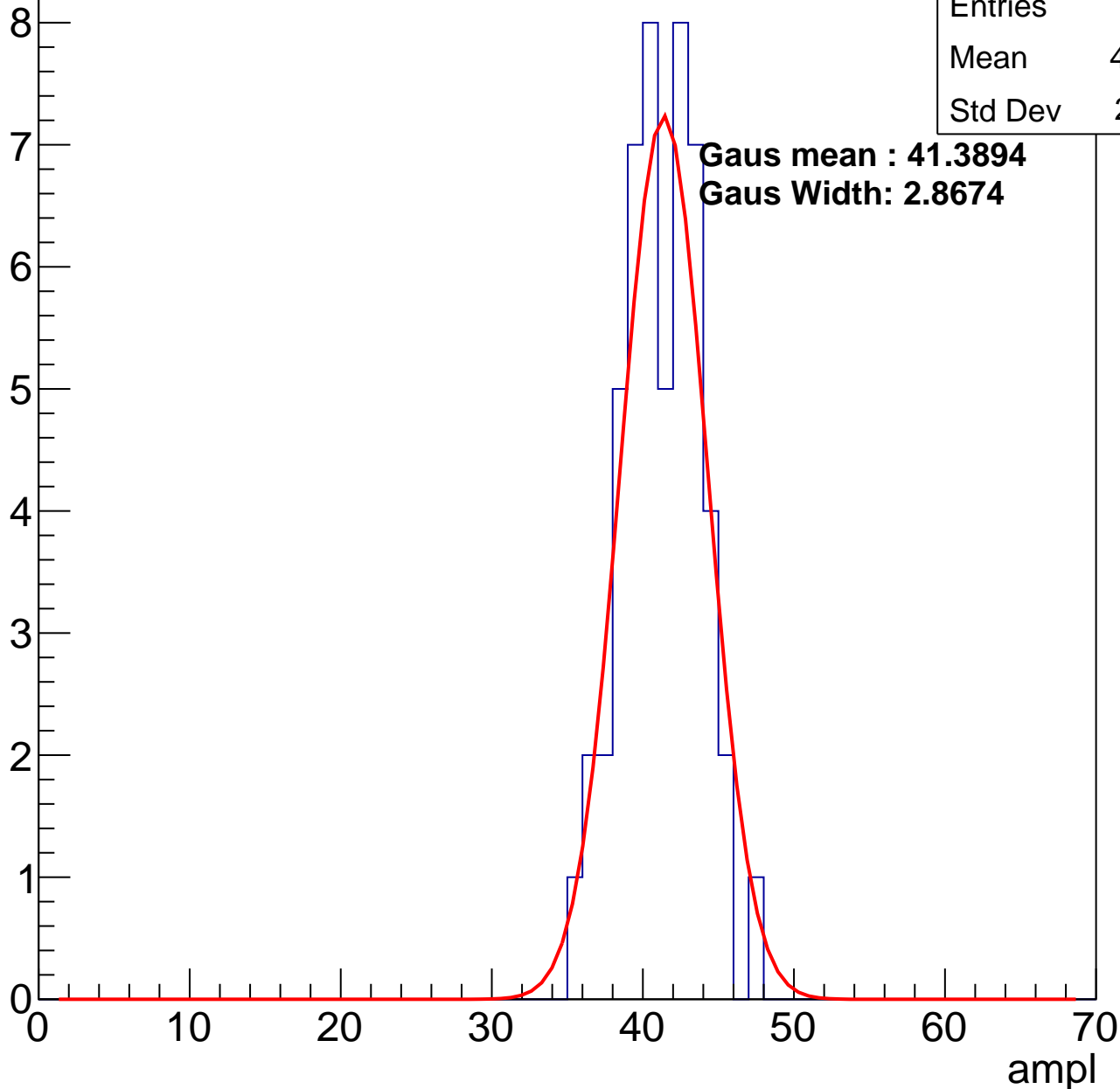
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	40.75
Std Dev	2.541

**Gaus mean : 41.3894**

**Gaus Width: 2.8674**



# B1L000S, U25-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

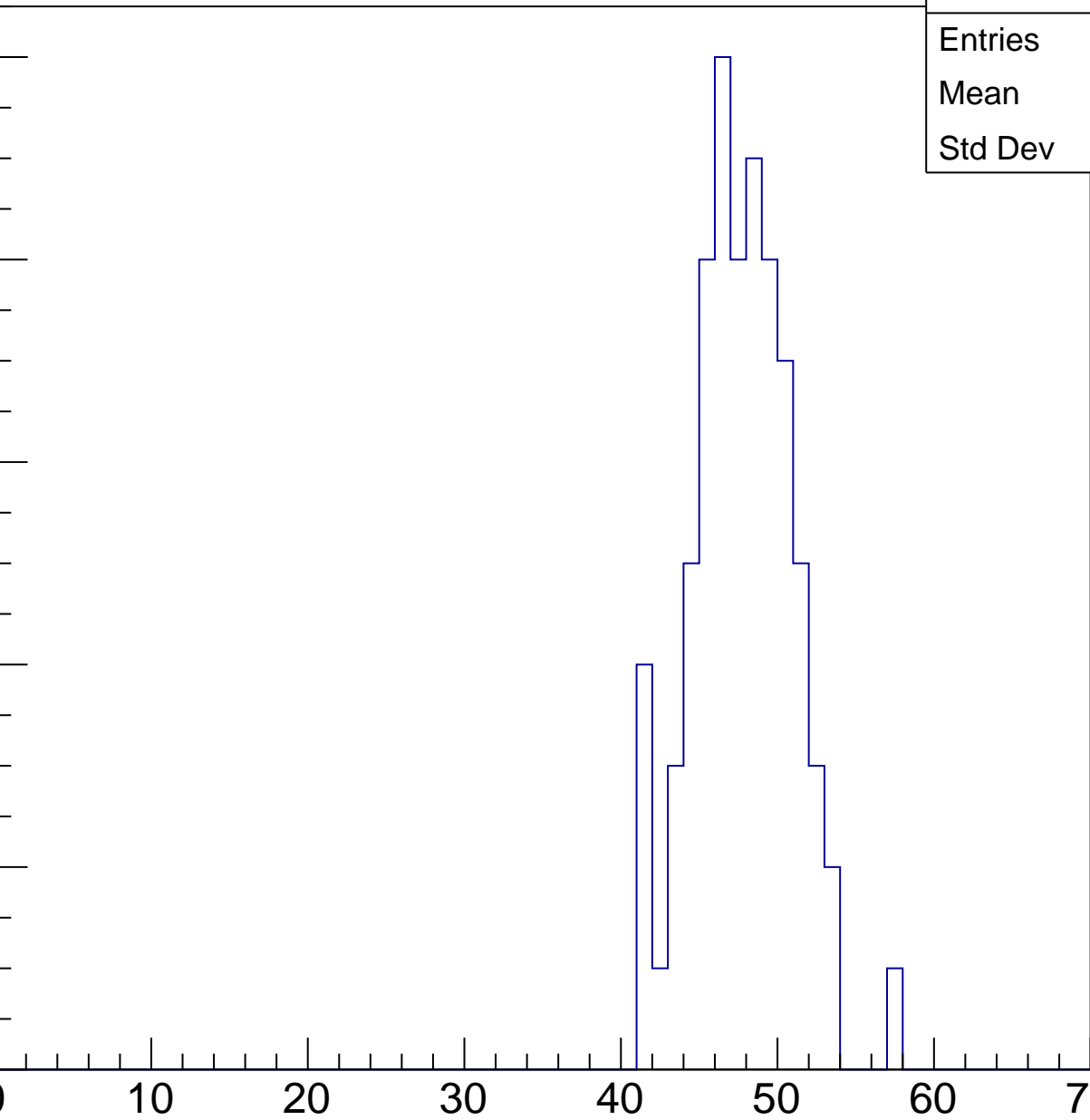
Entries	74
Mean	47.28
Std Dev	3.139

Entry

10  
8  
6  
4  
2  
0

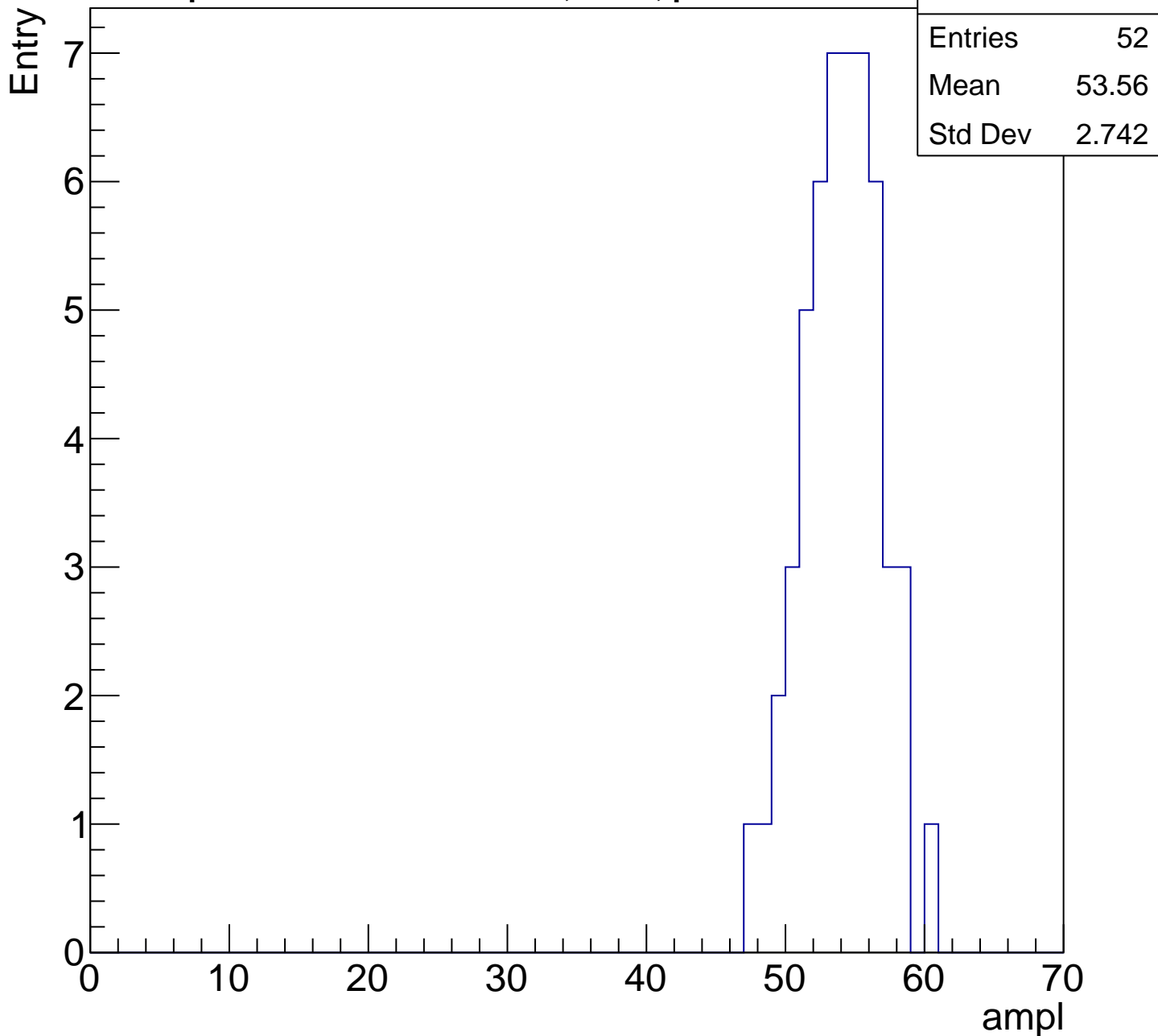
0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch108, adc4

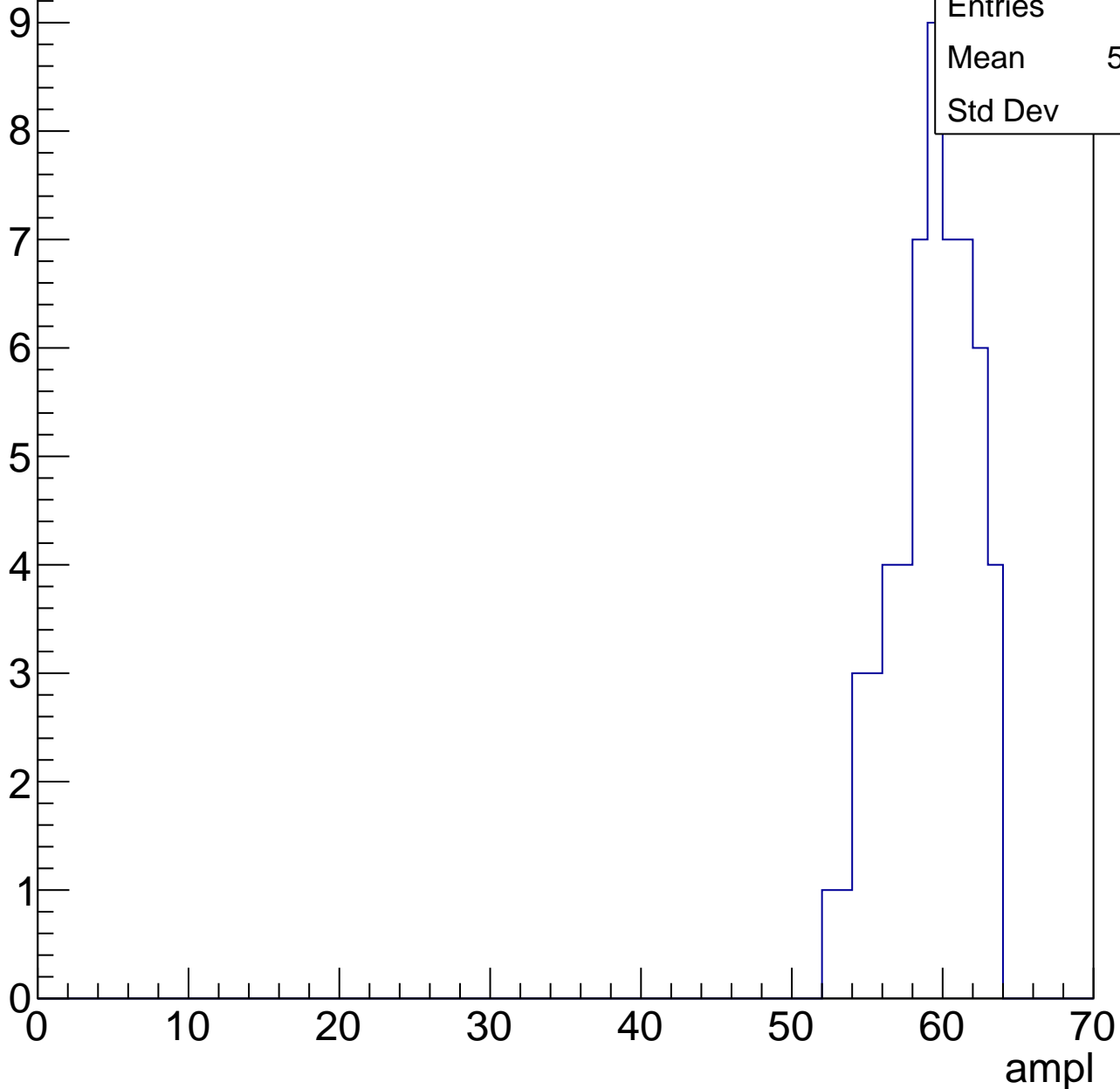
calib\_packv5\_042523\_0143.root, FC#3, port B3



# B1L000S, U25-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



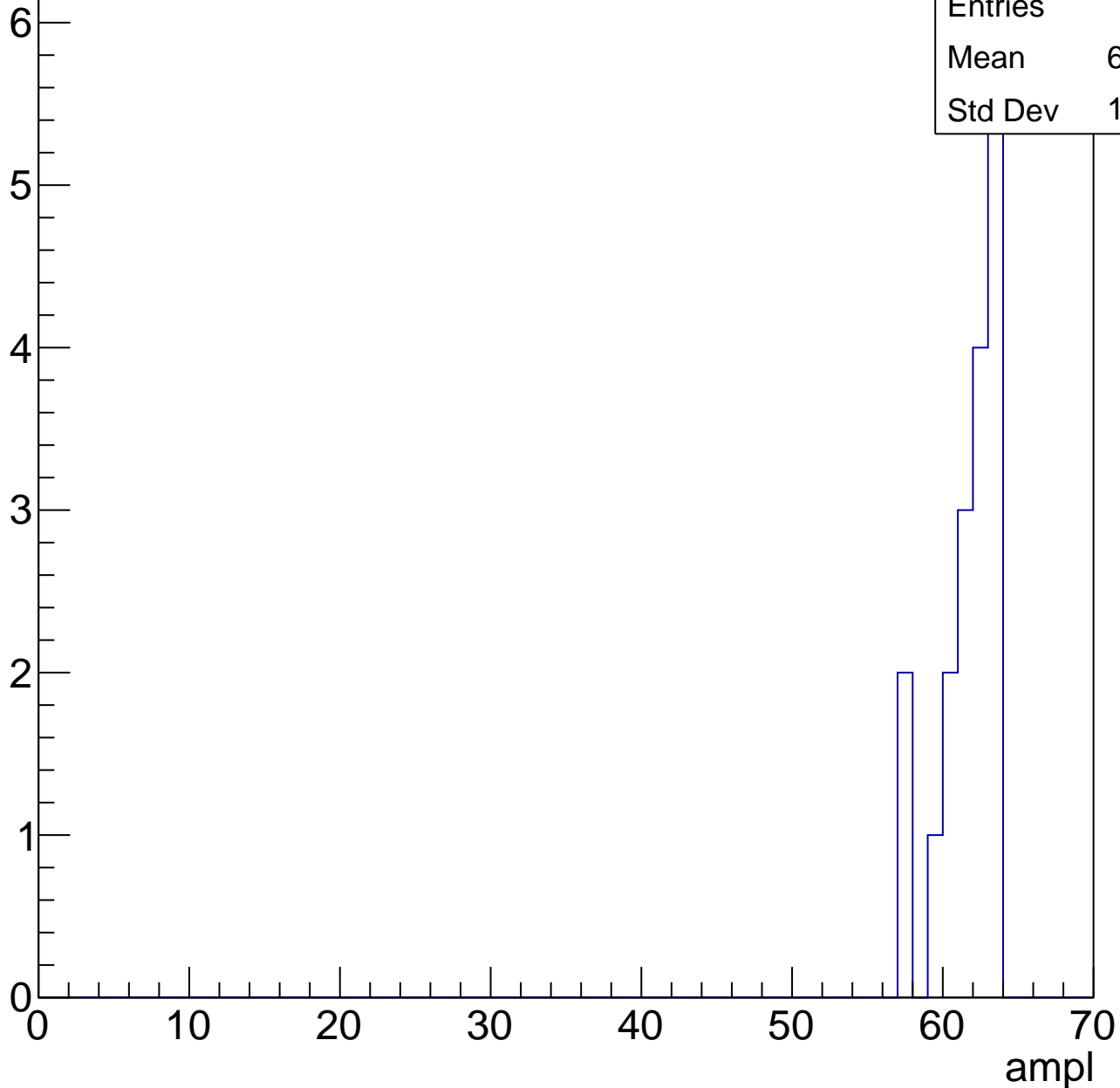
Entries	56
Mean	58.79
Std Dev	2.73

# B1L000S, U25-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.22
Std Dev	1.902





# B1L000S, U25-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch109, adc0

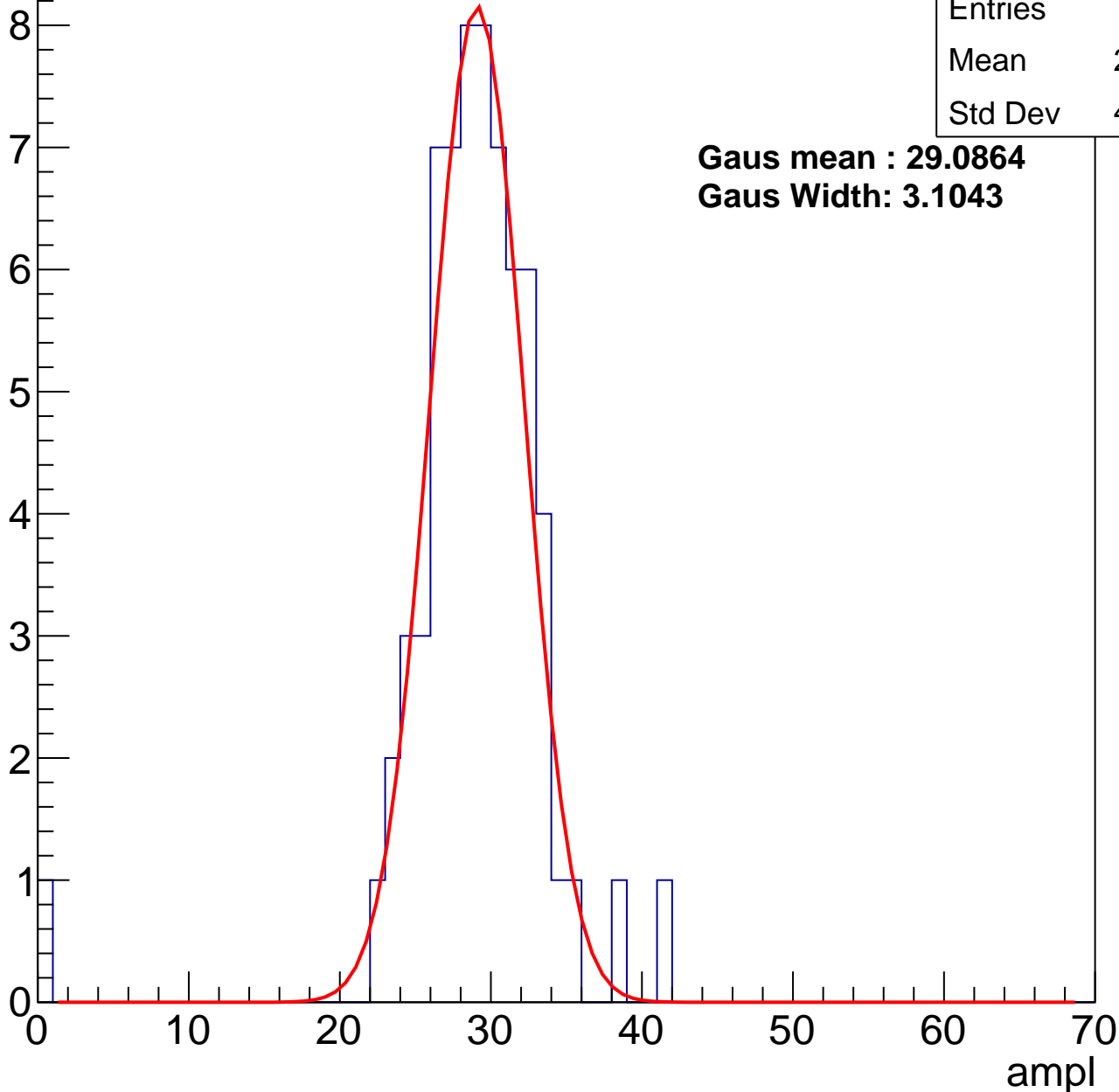
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.51
Std Dev	4.891

**Gaus mean : 29.0864**

**Gaus Width: 3.1043**



# B1L000S, U25-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	36.51
Std Dev	3.683

**Gaus mean : 36.2825**

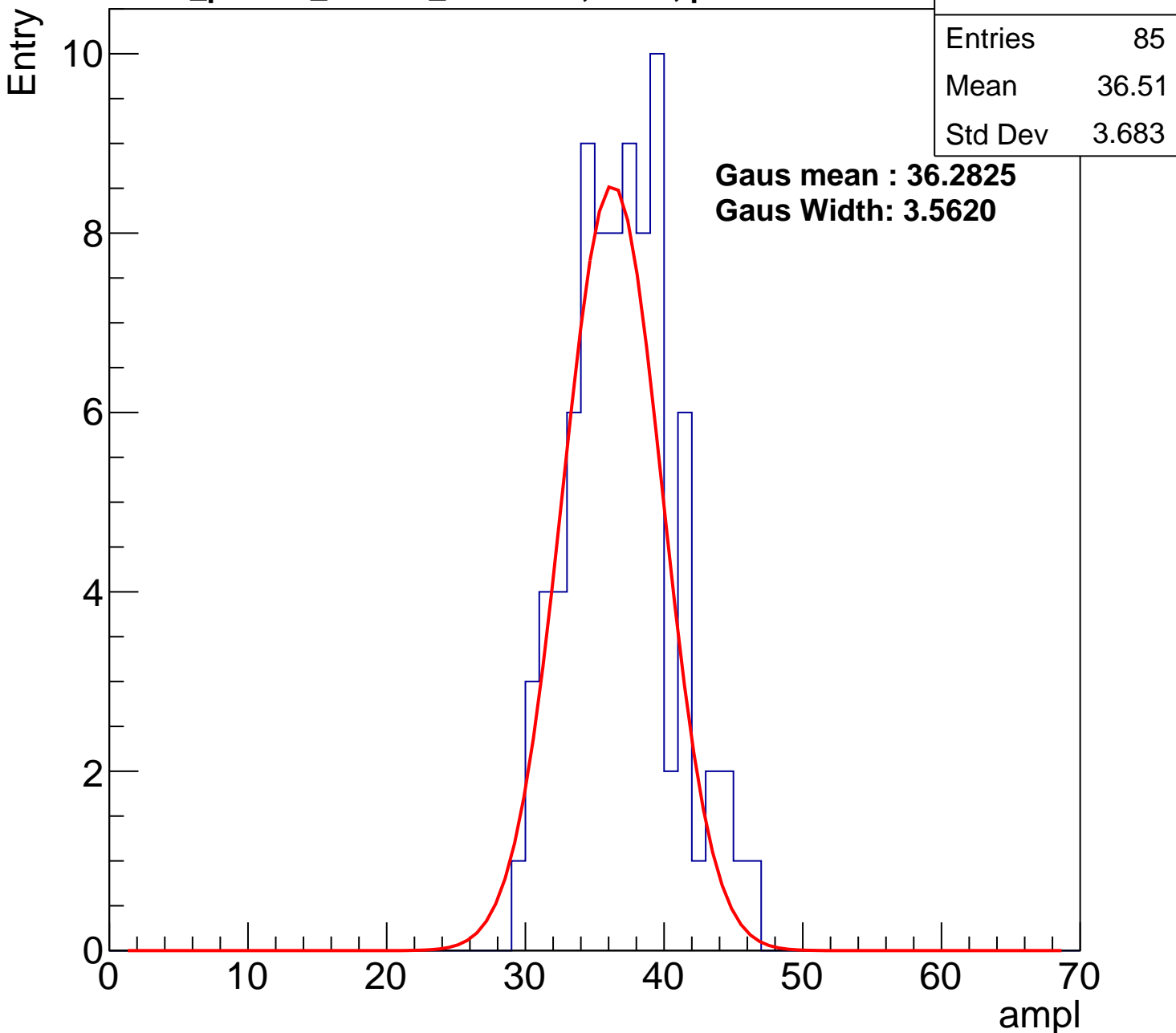
**Gaus Width: 3.5620**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch109, adc2

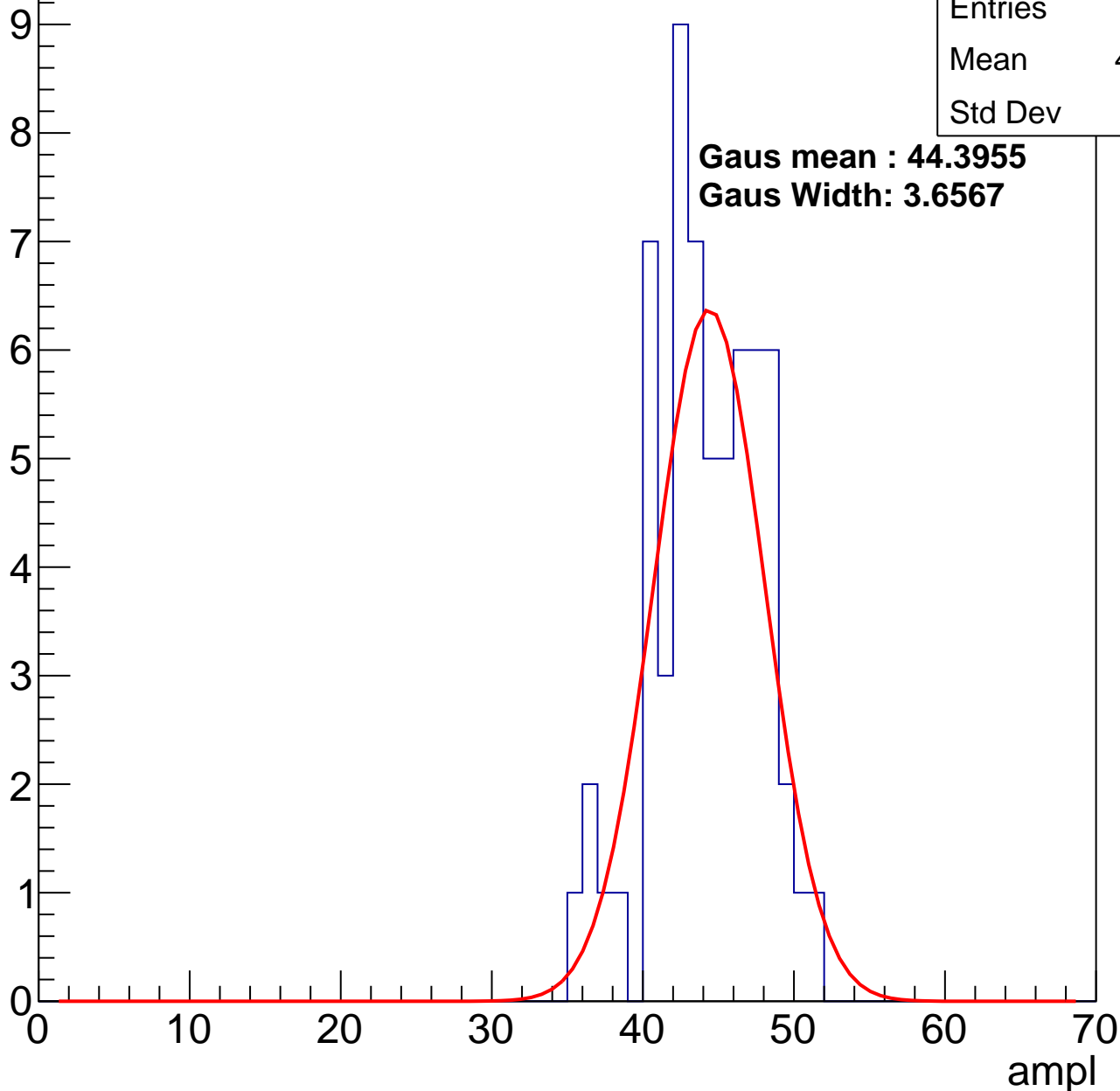
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	43.71
Std Dev	3.53

**Gaus mean : 44.3955**

**Gaus Width: 3.6567**

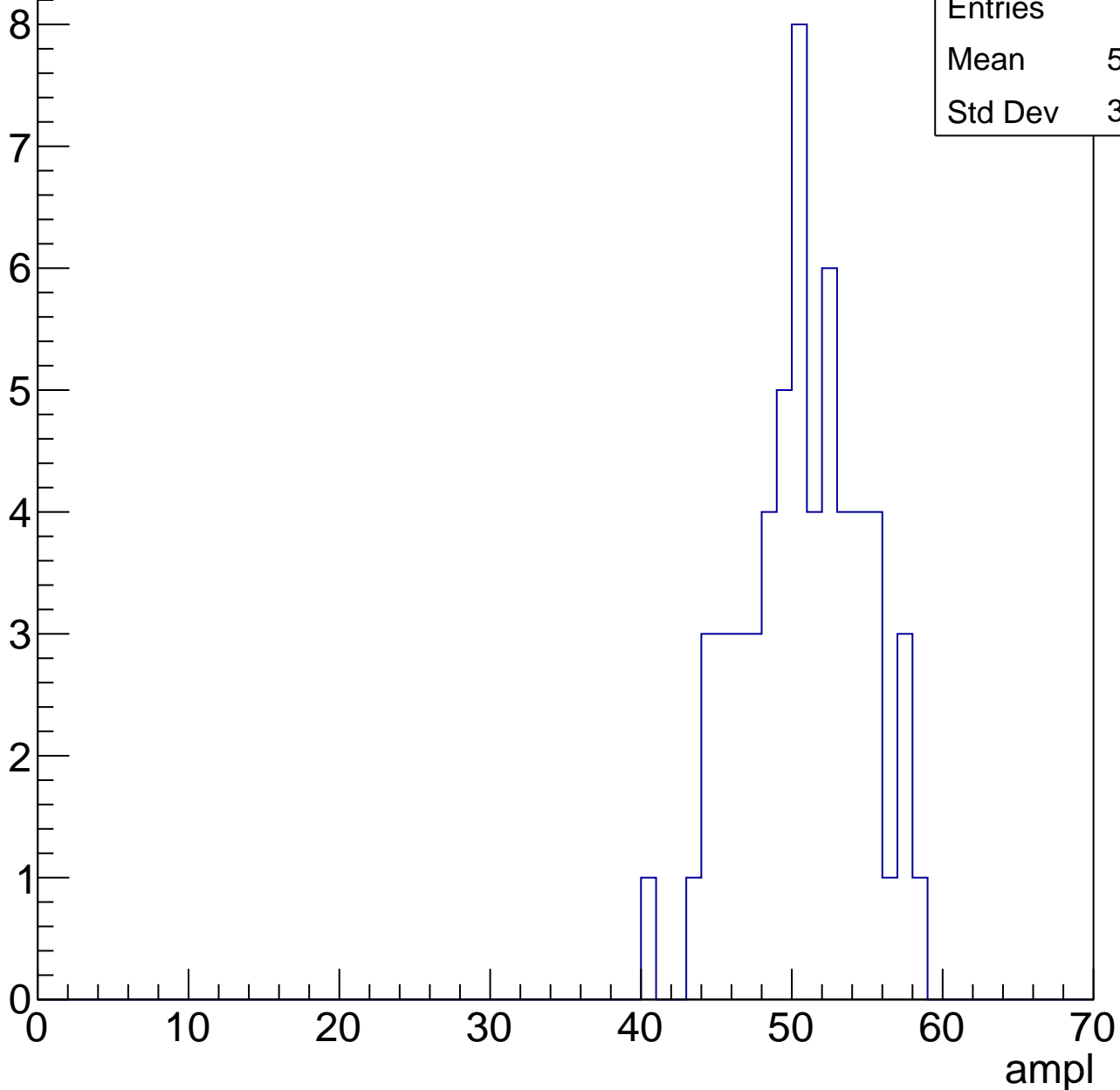


# B1L000S, U25-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	50.26
Std Dev	3.937

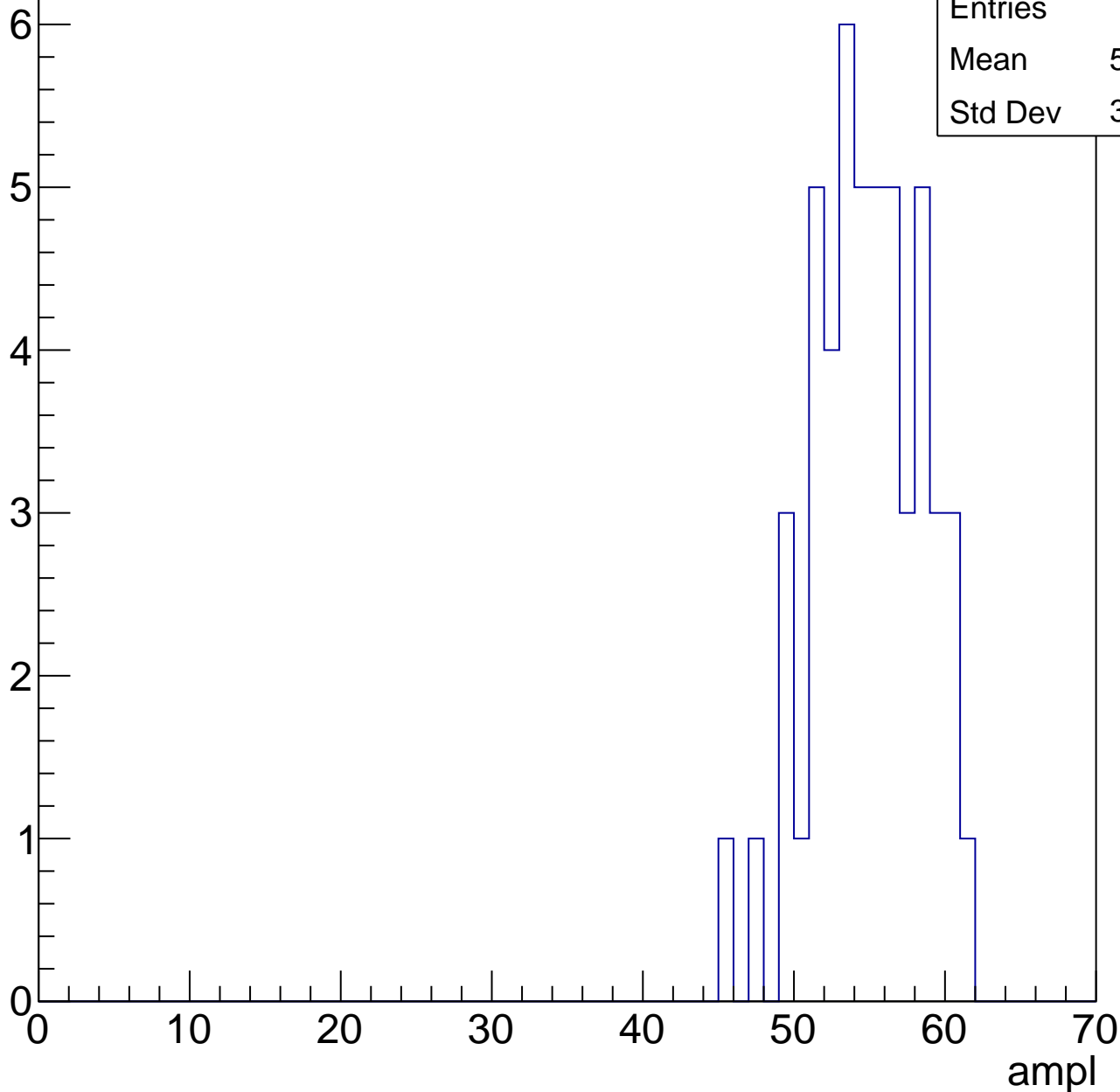


# B1L000S, U25-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	54.39
Std Dev	3.554

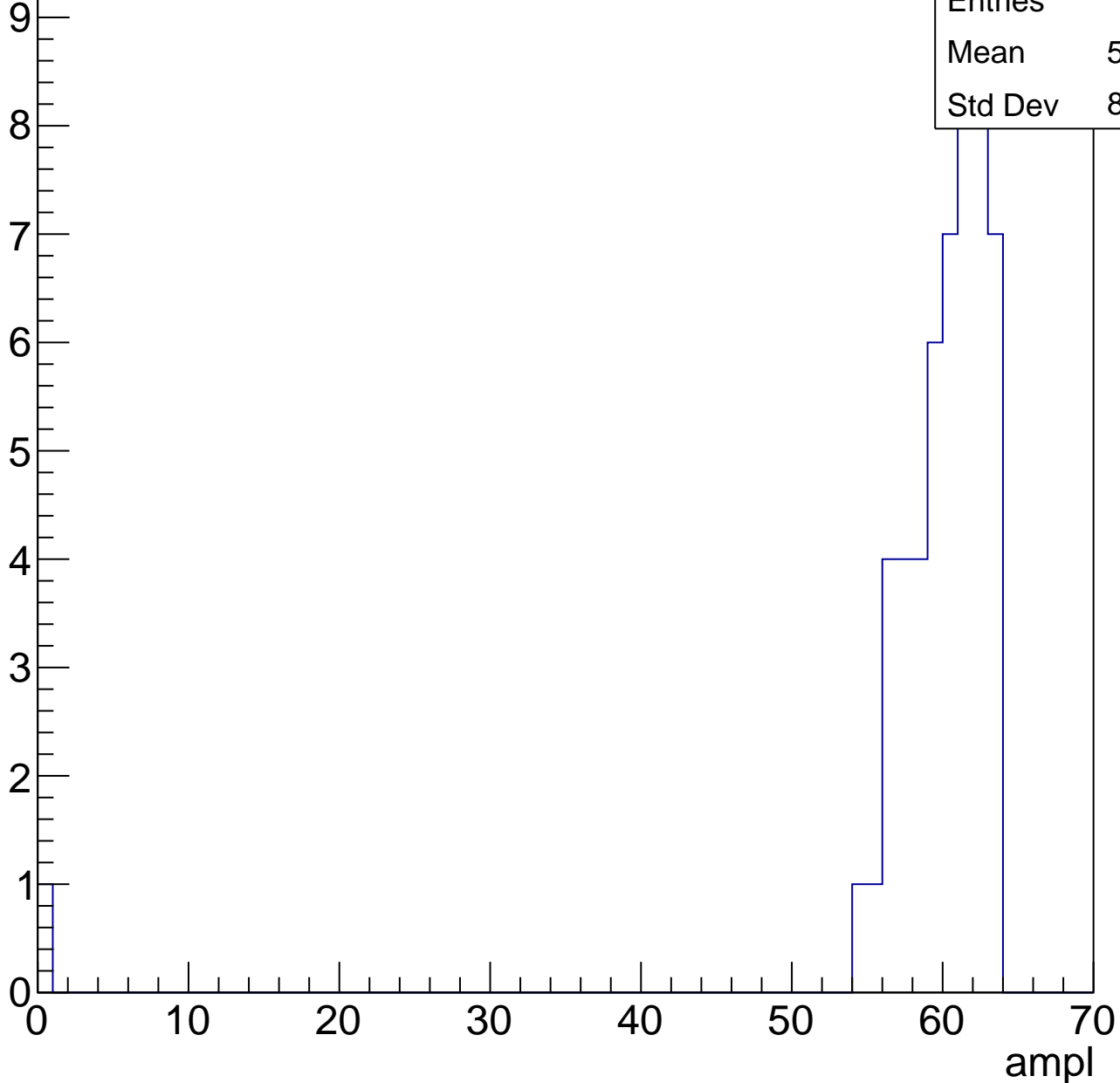


# B1L000S, U25-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

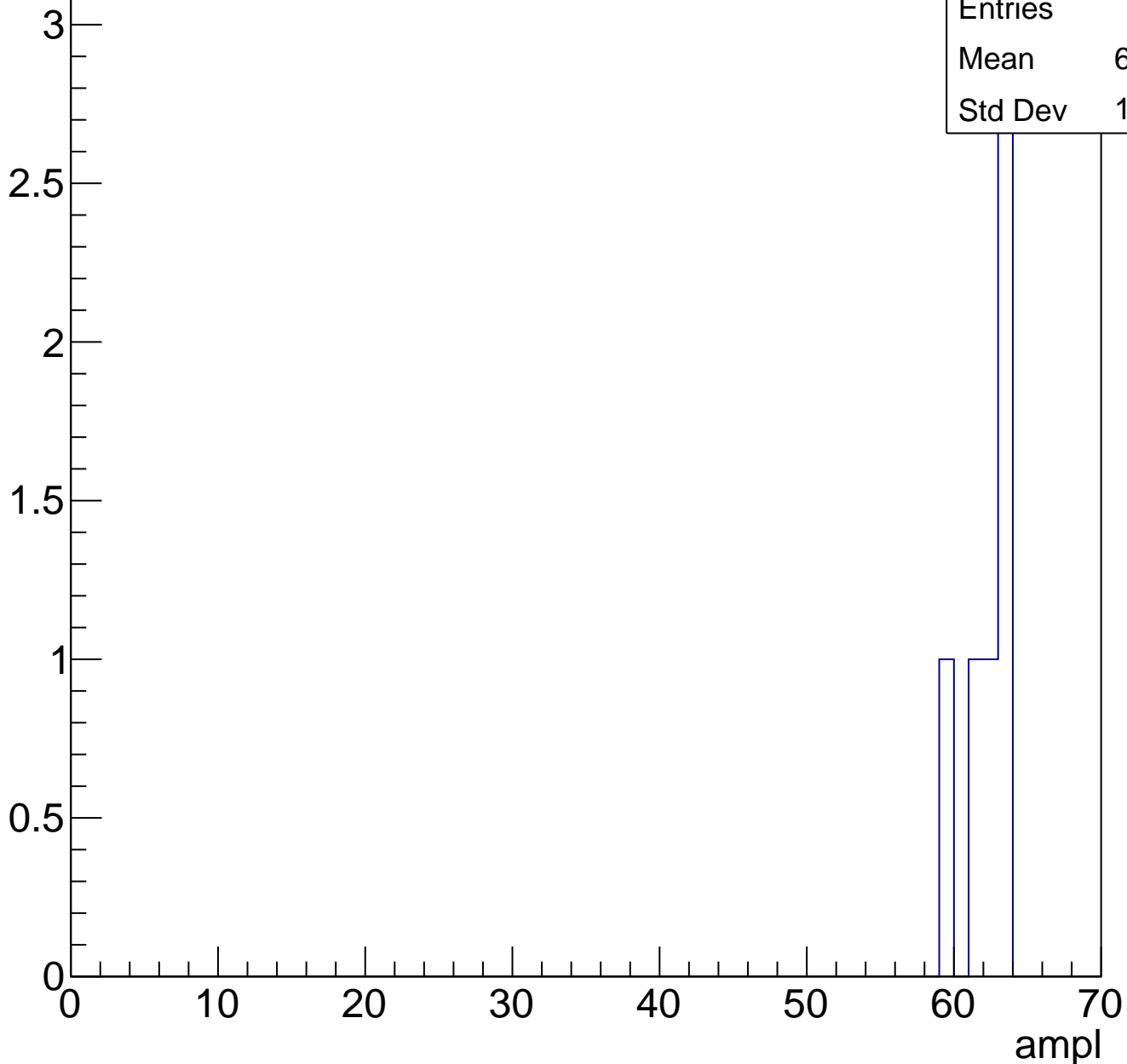
Entries	52
Mean	58.73
Std Dev	8.556



# B1L000S, U25-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch110, adc0

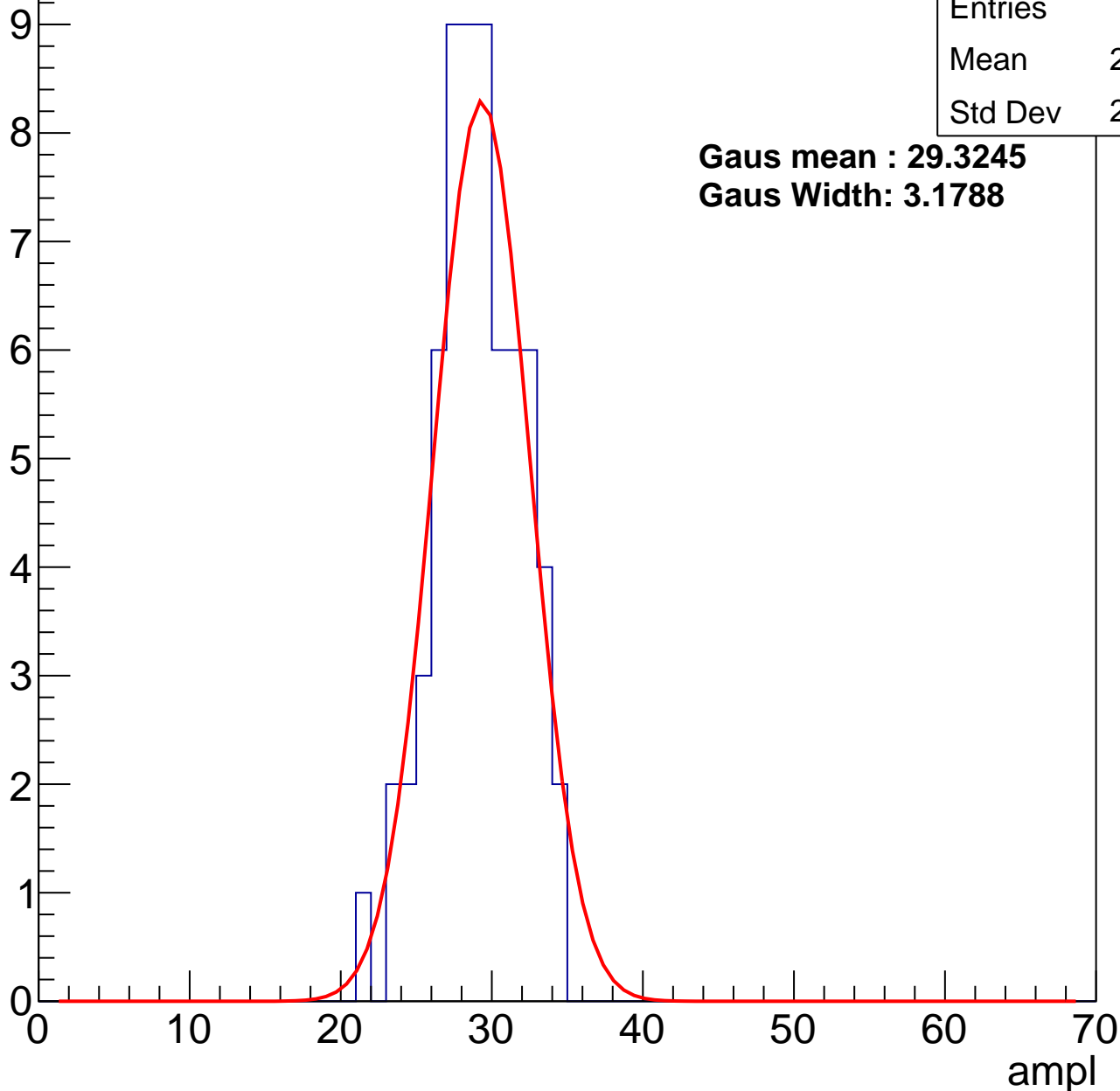
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.62
Std Dev	2.832

**Gaus mean : 29.3245**

**Gaus Width: 3.1788**



# B1L000S, U25-ch110, adc1

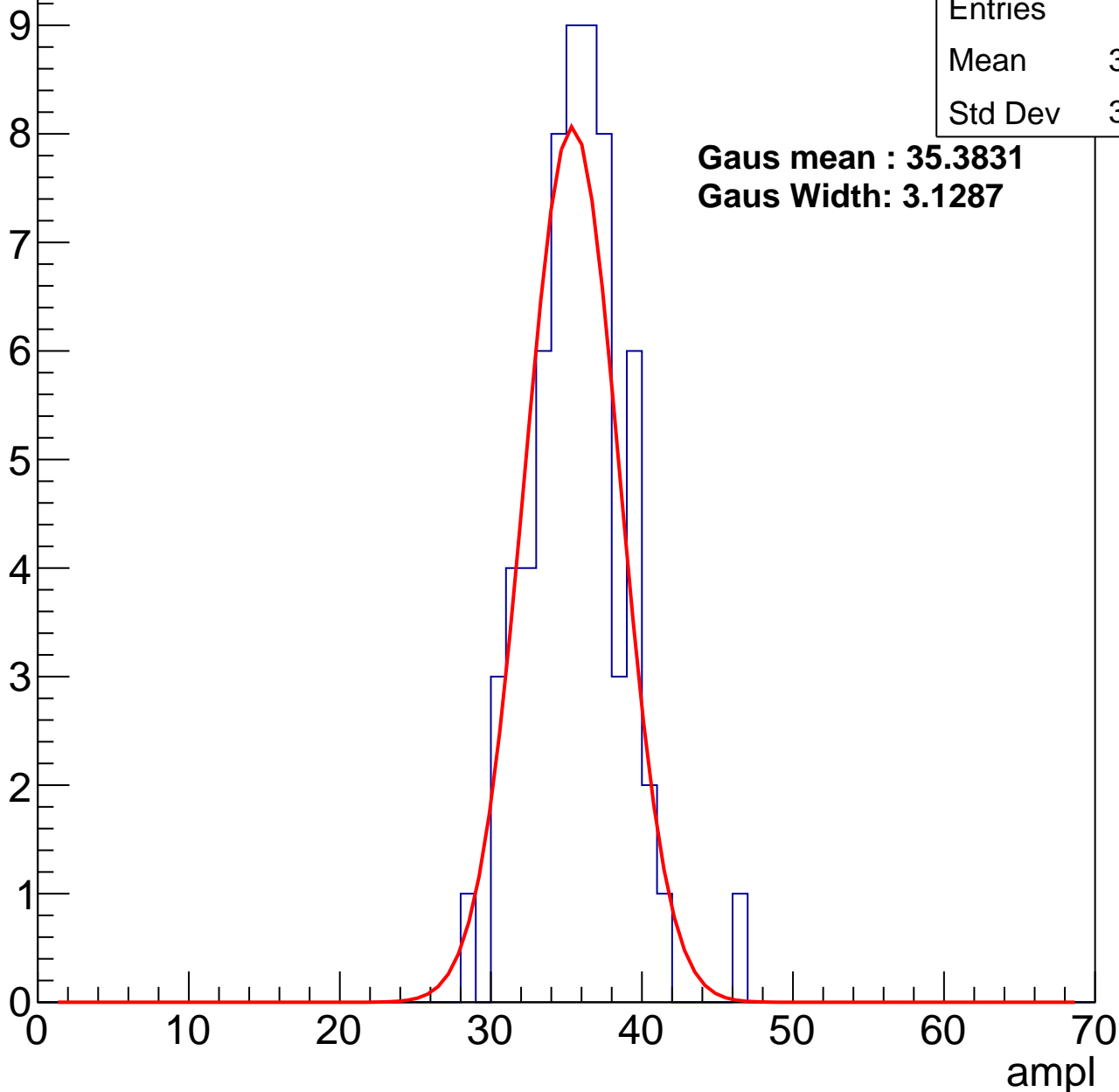
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	35.23
Std Dev	3.097

**Gaus mean : 35.3831**

**Gaus Width: 3.1287**



# B1L000S, U25-ch110, adc2

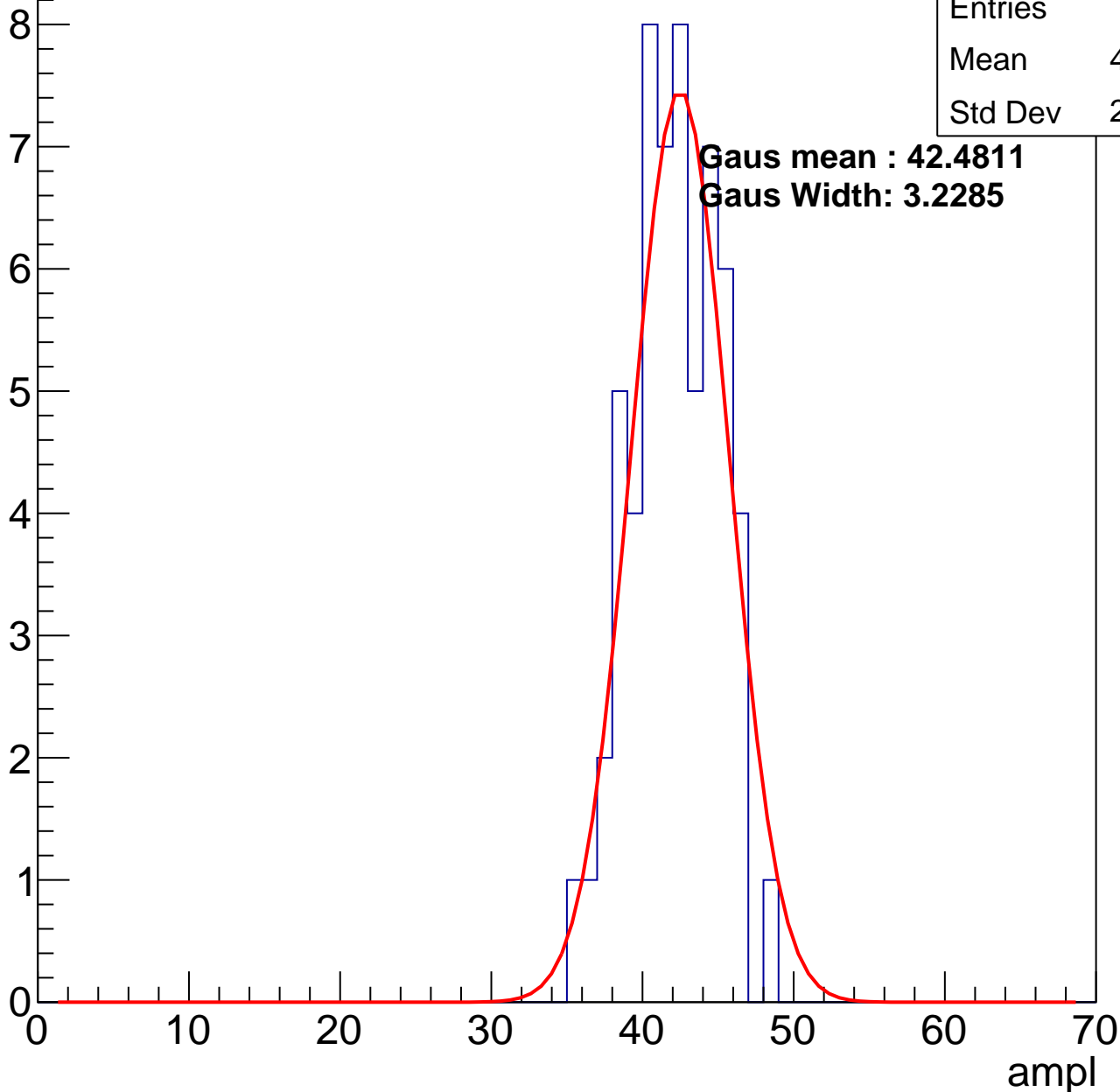
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.68
Std Dev	2.825

**Gaus mean : 42.4811**

**Gaus Width: 3.2285**

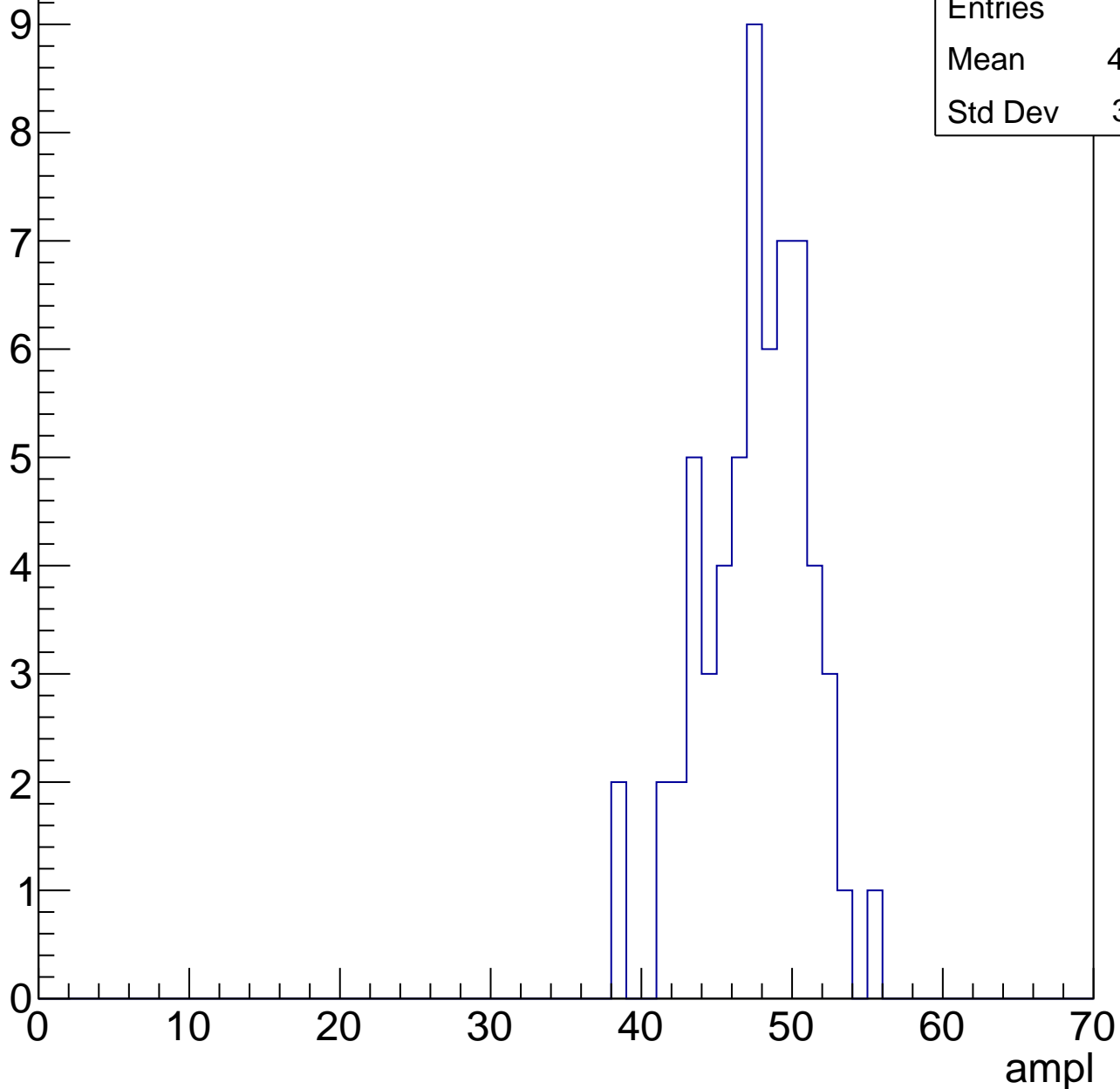


# B1L000S, U25-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

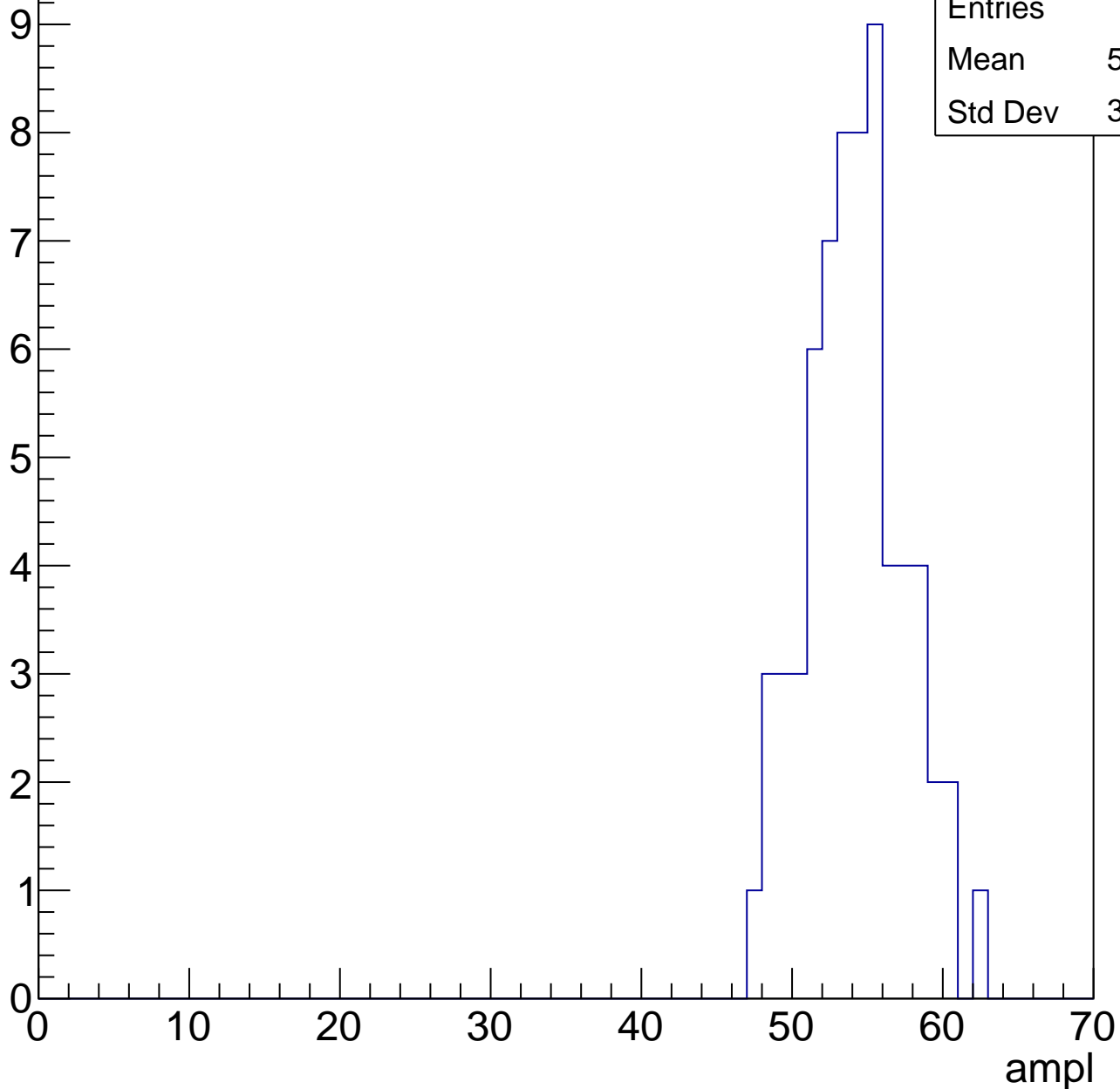
Entries	61
Mean	47.07
Std Dev	3.501



# B1L000S, U25-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



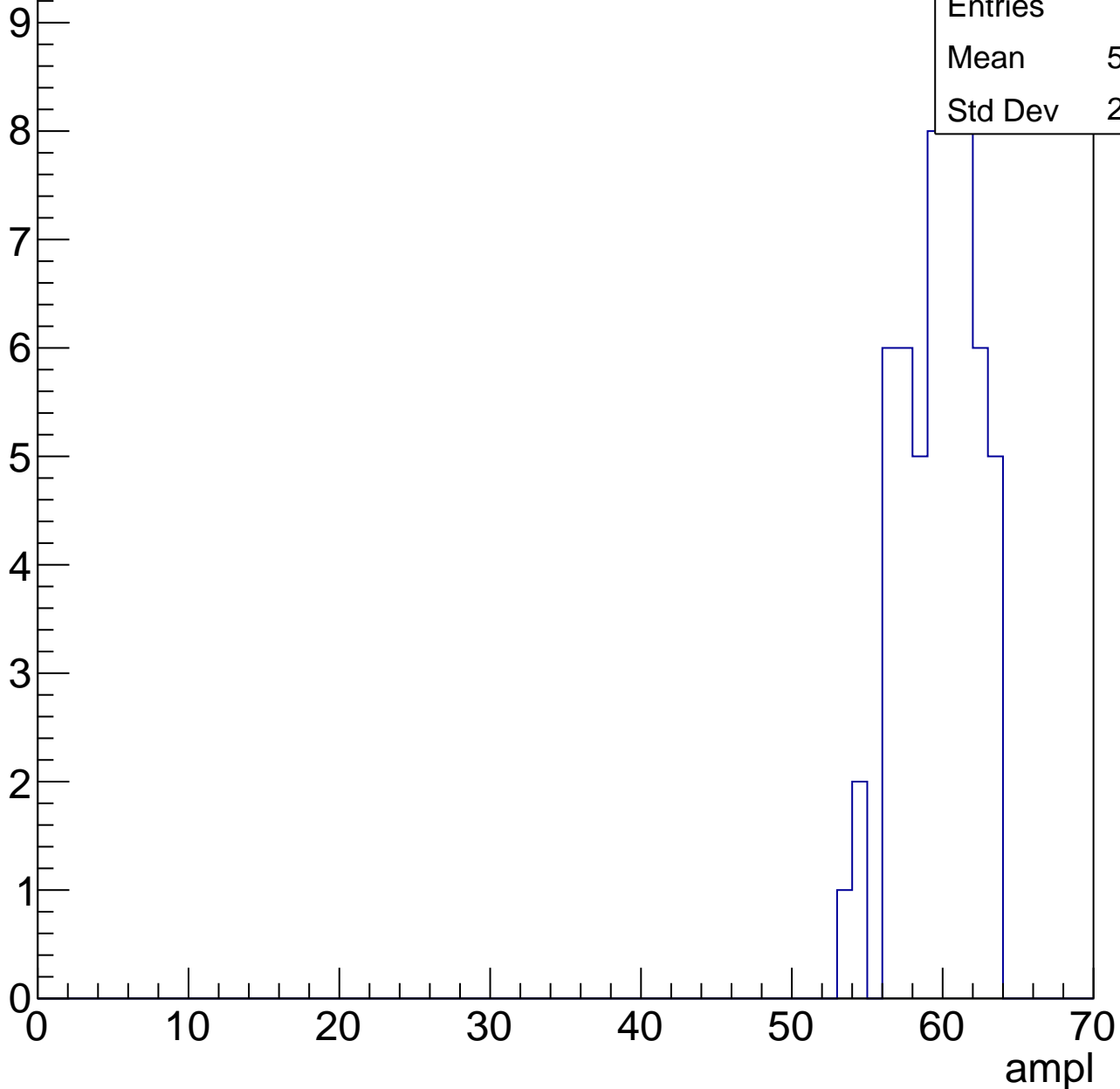
Entries	65
Mean	53.74
Std Dev	3.226

# B1L000S, U25-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

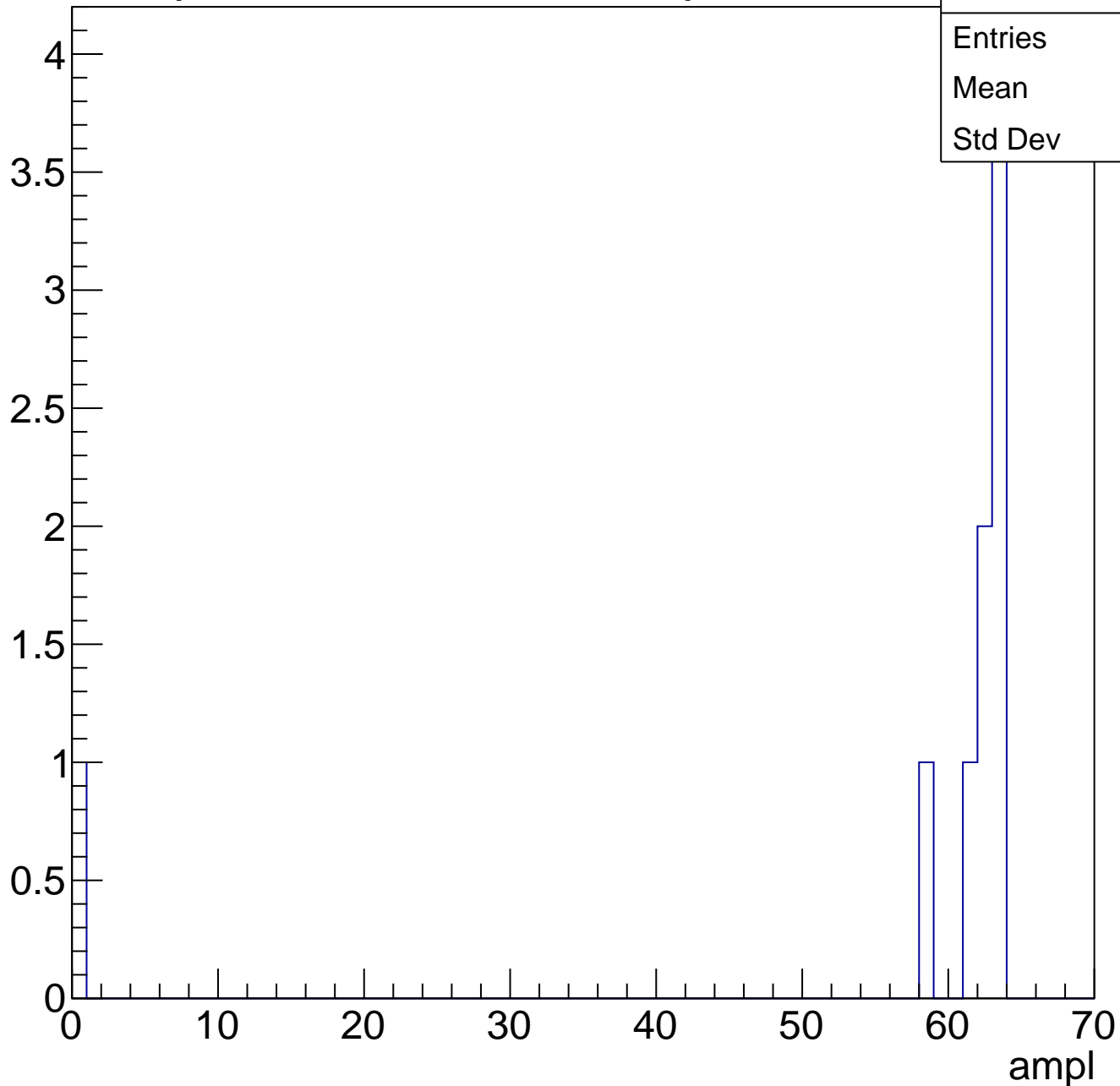
Entries	56
Mean	59.23
Std Dev	2.478



# B1L000S, U25-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

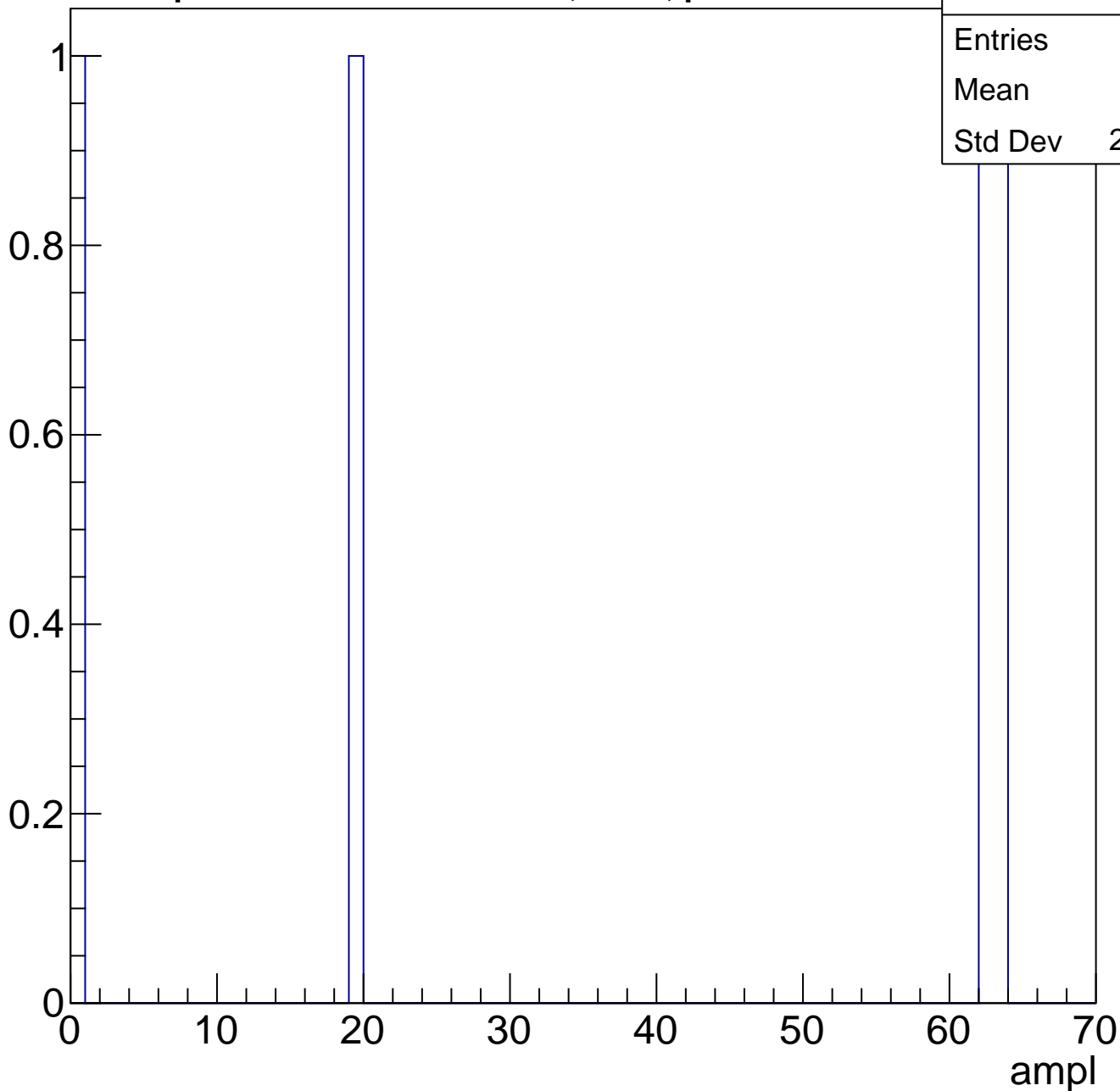




# B1L000S, U25-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch111, adc0

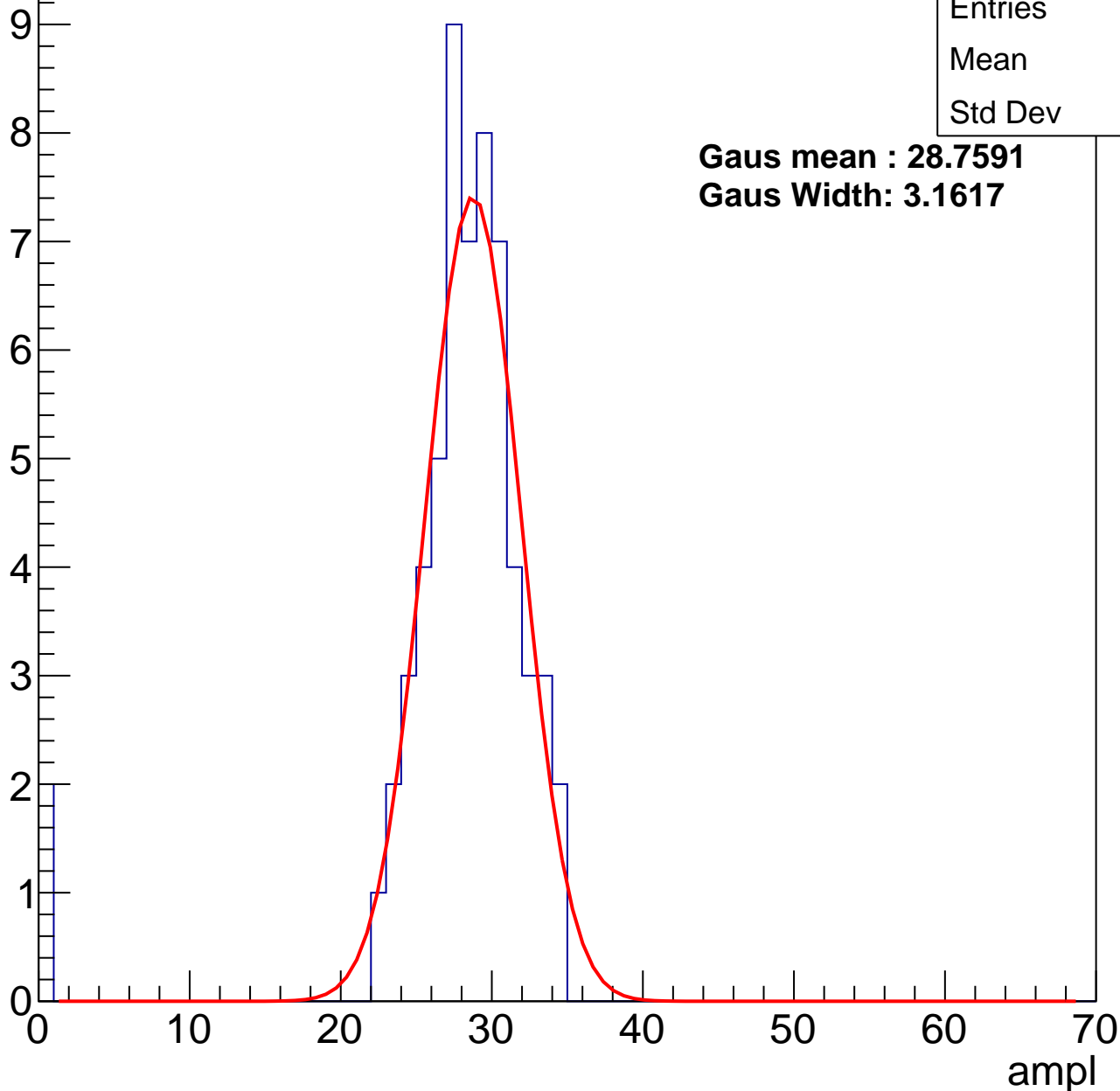
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	27.3
Std Dev	5.78

**Gaus mean : 28.7591**

**Gaus Width: 3.1617**



# B1L000S, U25-ch111, adc1

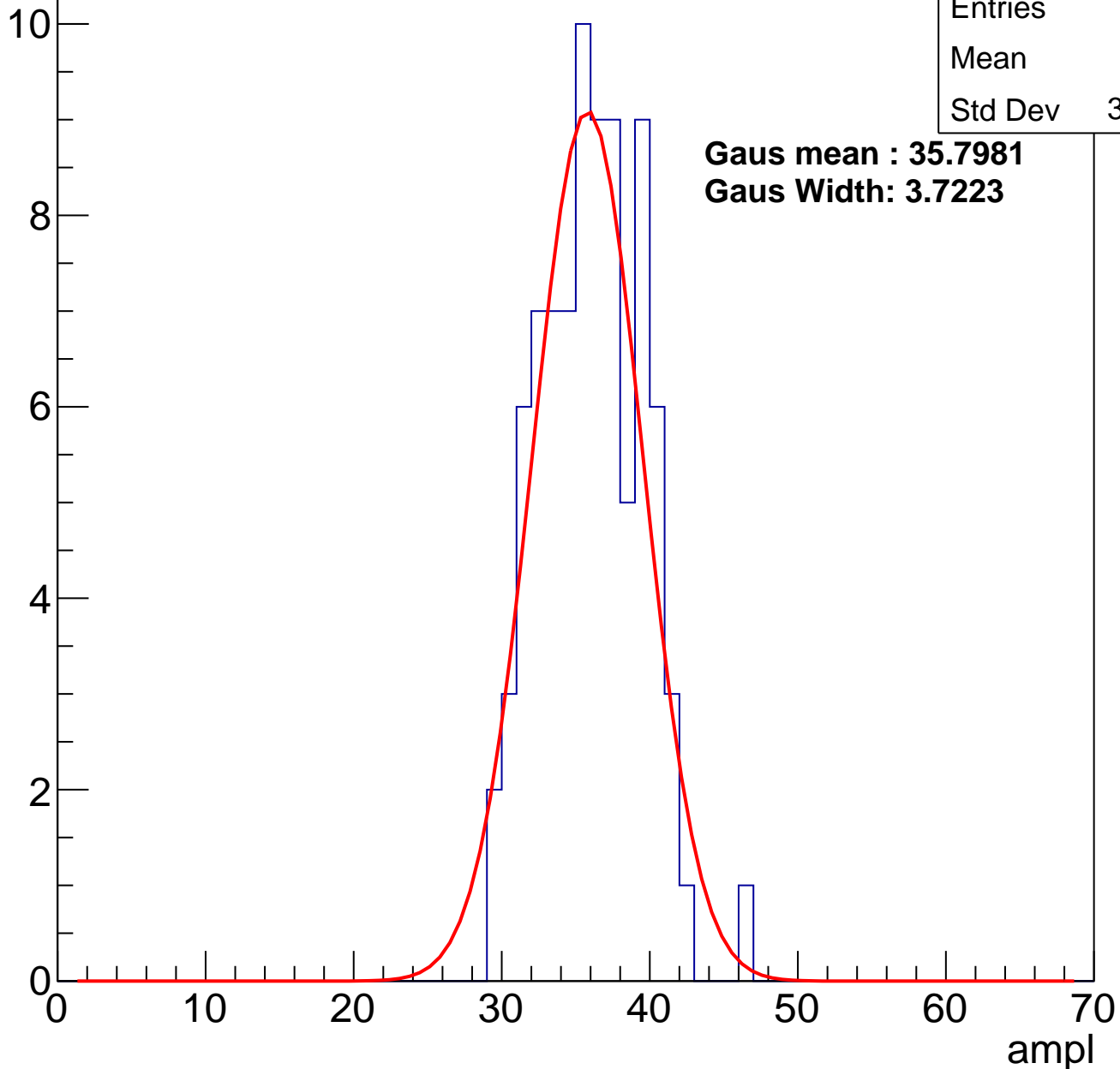
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	85
Mean	35.6
Std Dev	3.382

**Gaus mean : 35.7981**

**Gaus Width: 3.7223**

Entry



# B1L000S, U25-ch111, adc2

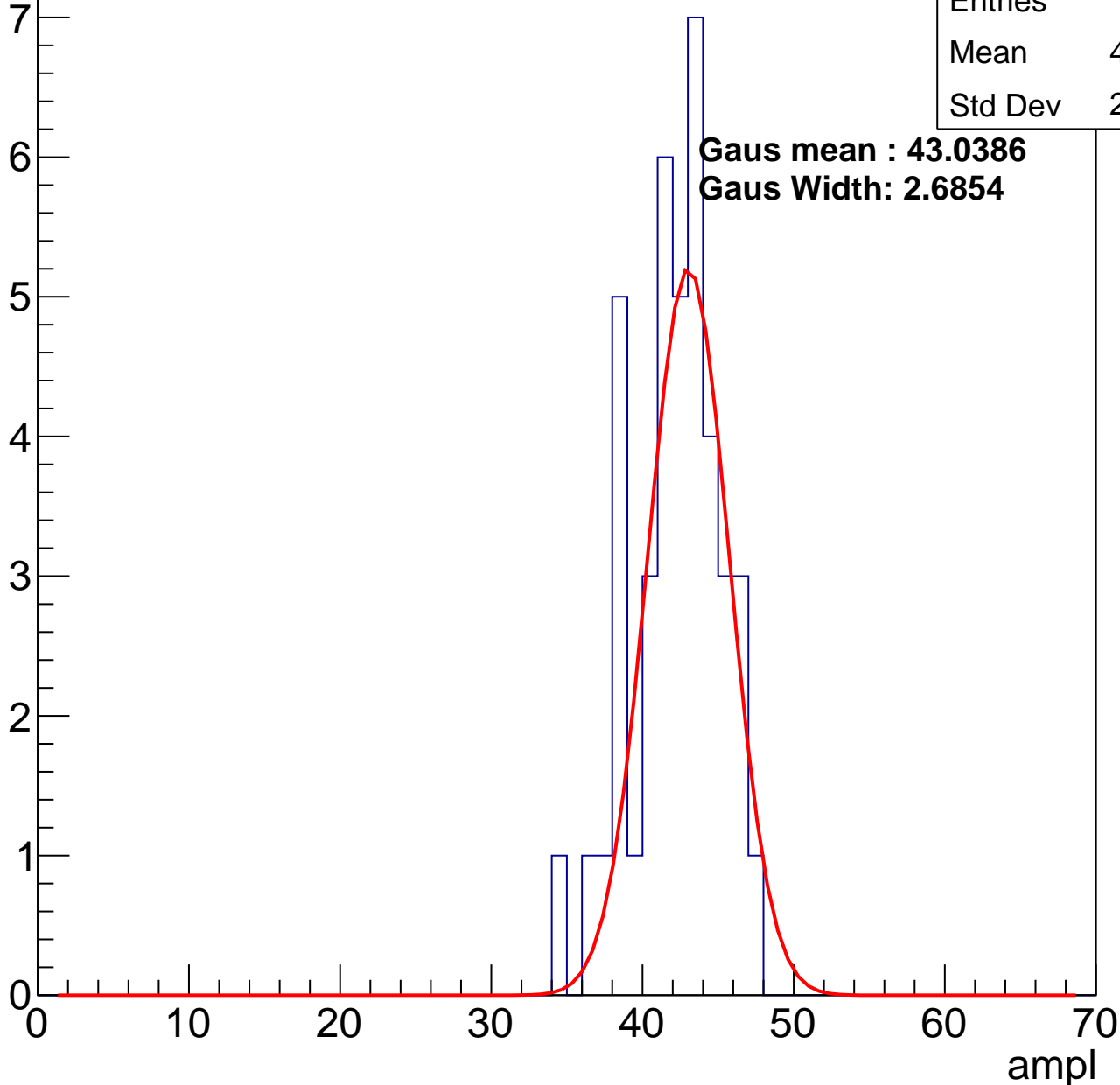
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	41
Mean	41.68
Std Dev	2.925

**Gaus mean : 43.0386**

**Gaus Width: 2.6854**



# B1L000S, U25-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

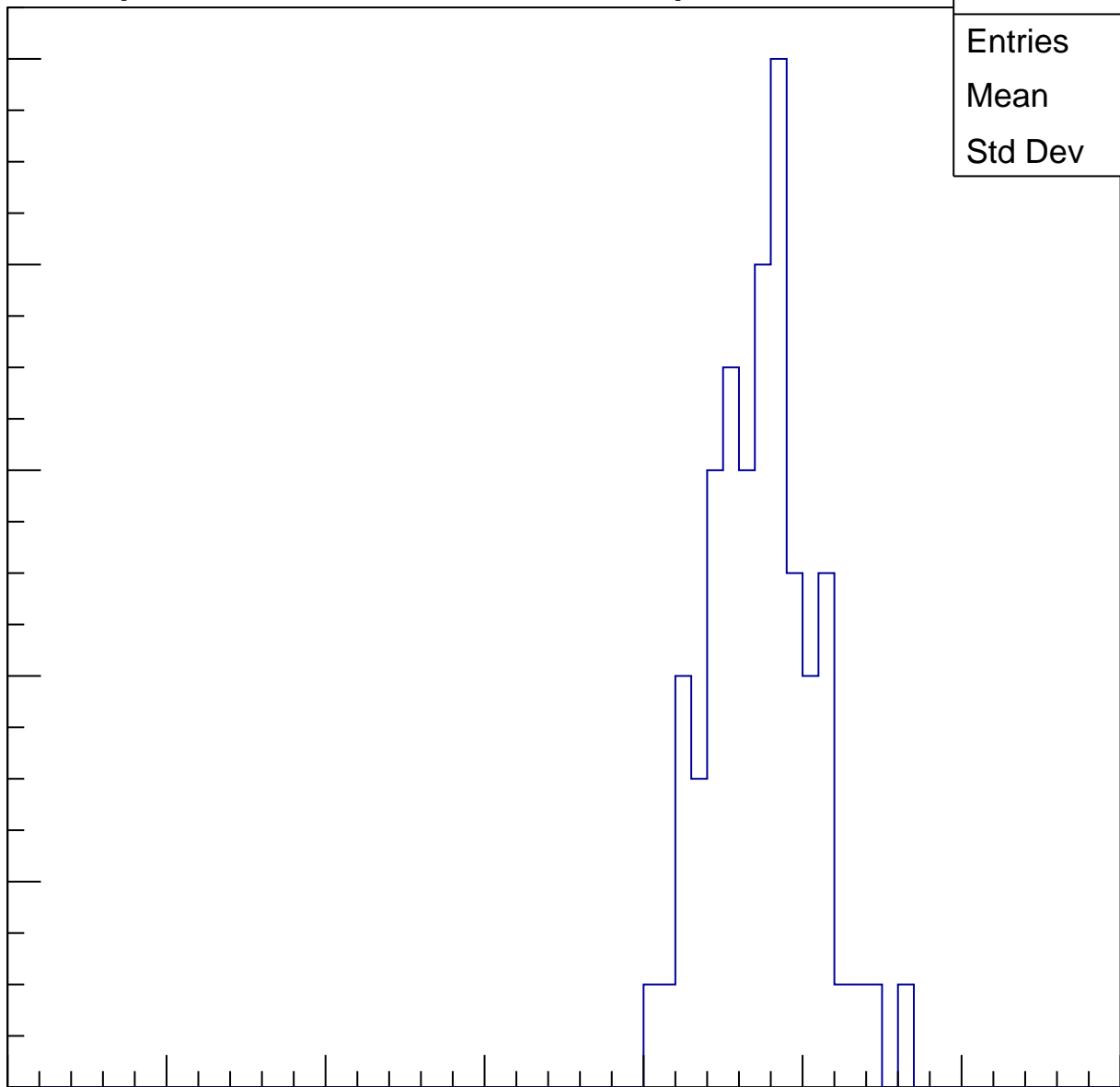
Entries	64
Mean	46.94
Std Dev	3.206

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

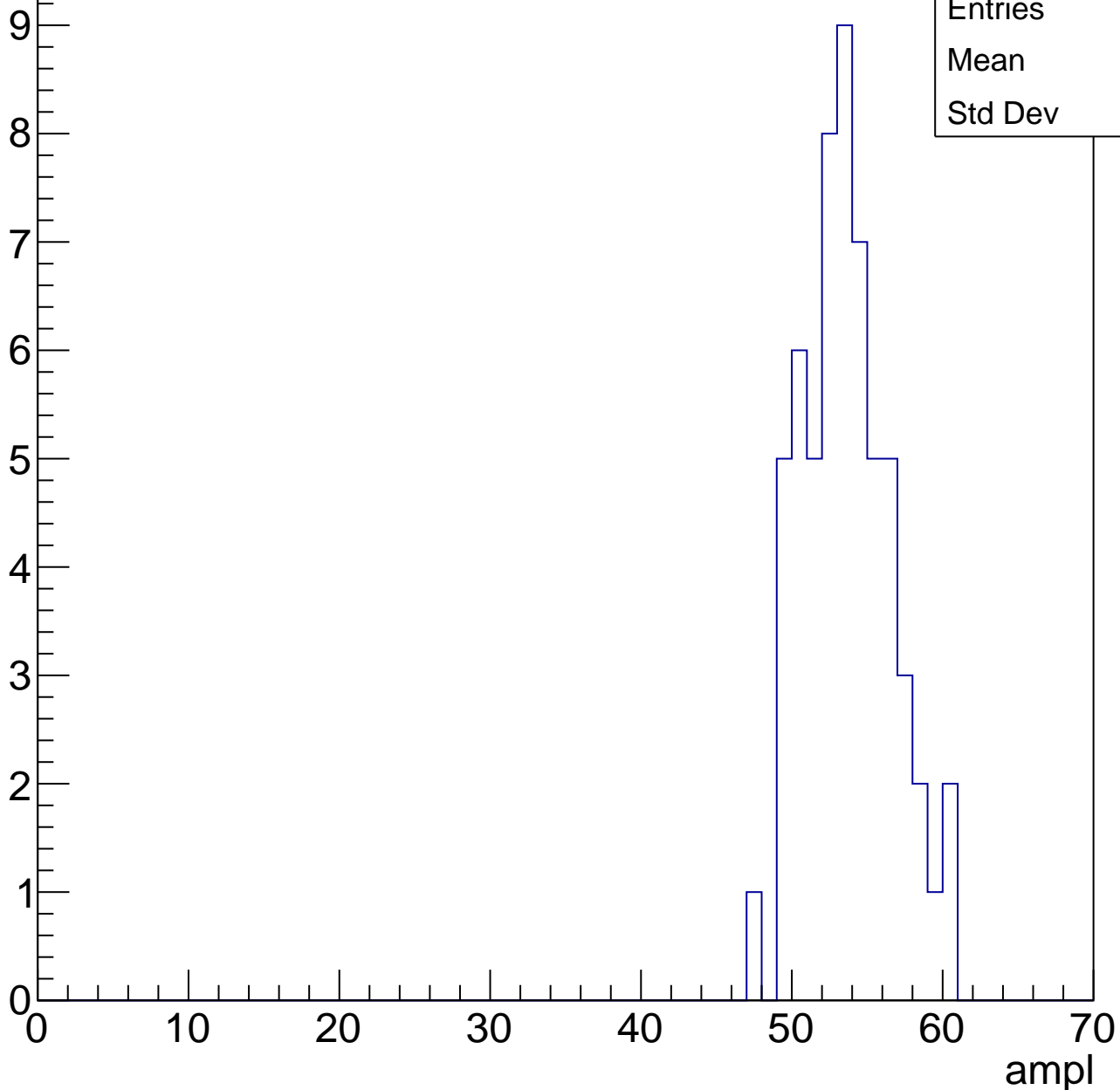


# B1L000S, U25-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	53.2
Std Dev	2.91

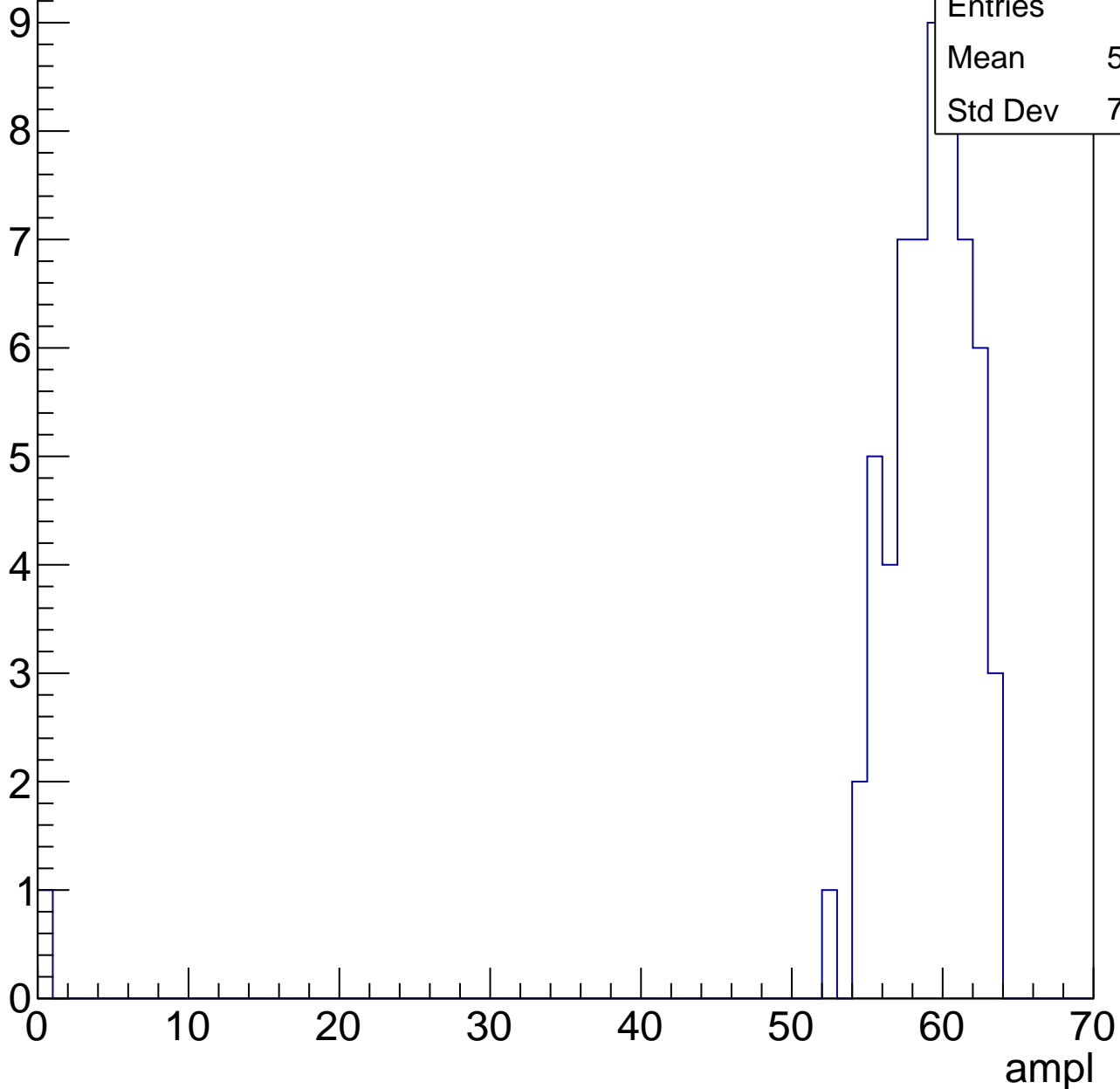


# B1L000S, U25-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	57.72
Std Dev	7.927

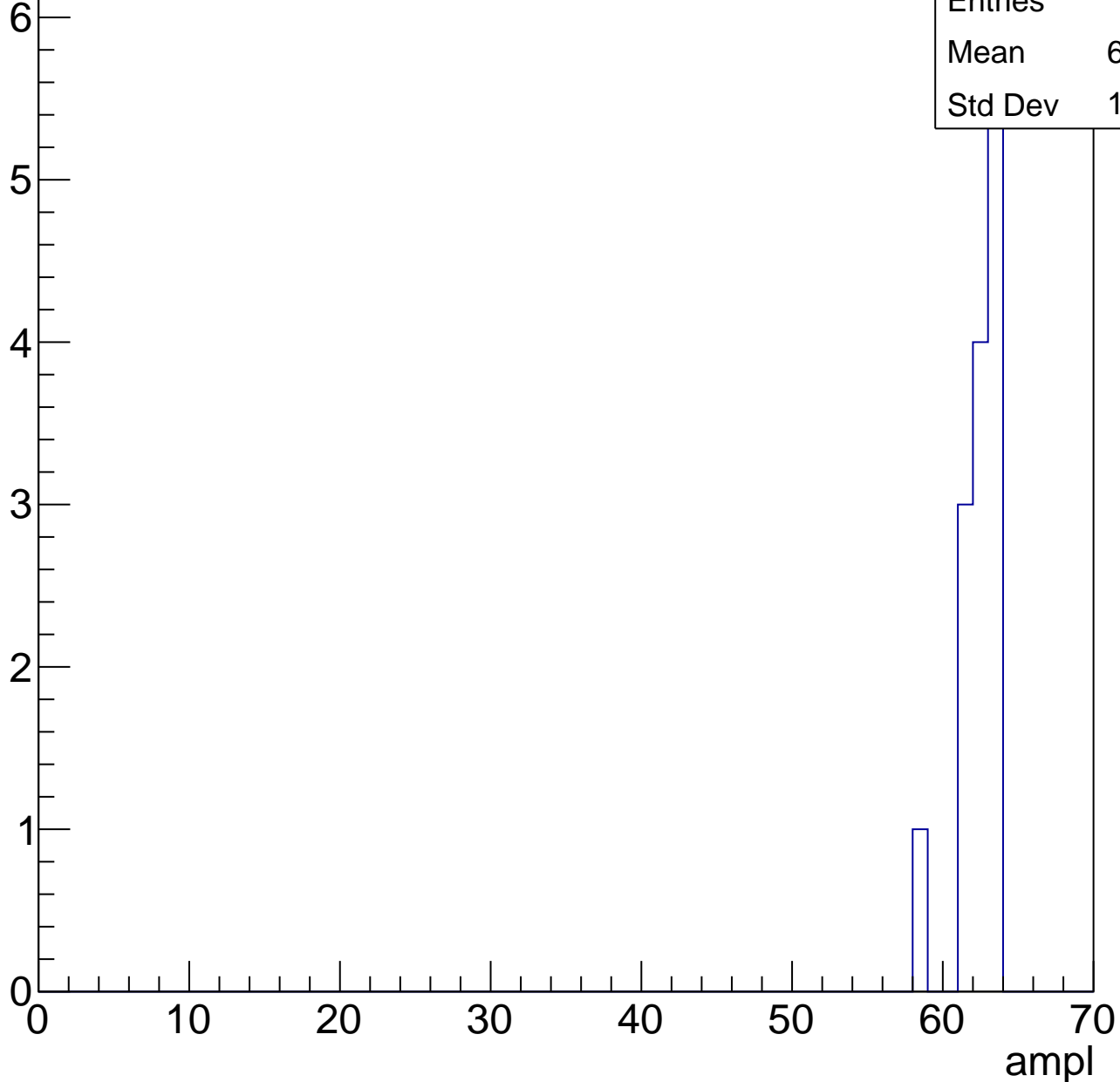


# B1L000S, U25-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.93
Std Dev	1.334





# B1L000S, U25-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	1
Mean	63
Std Dev	0

# B1L000S, U25-ch112, adc0

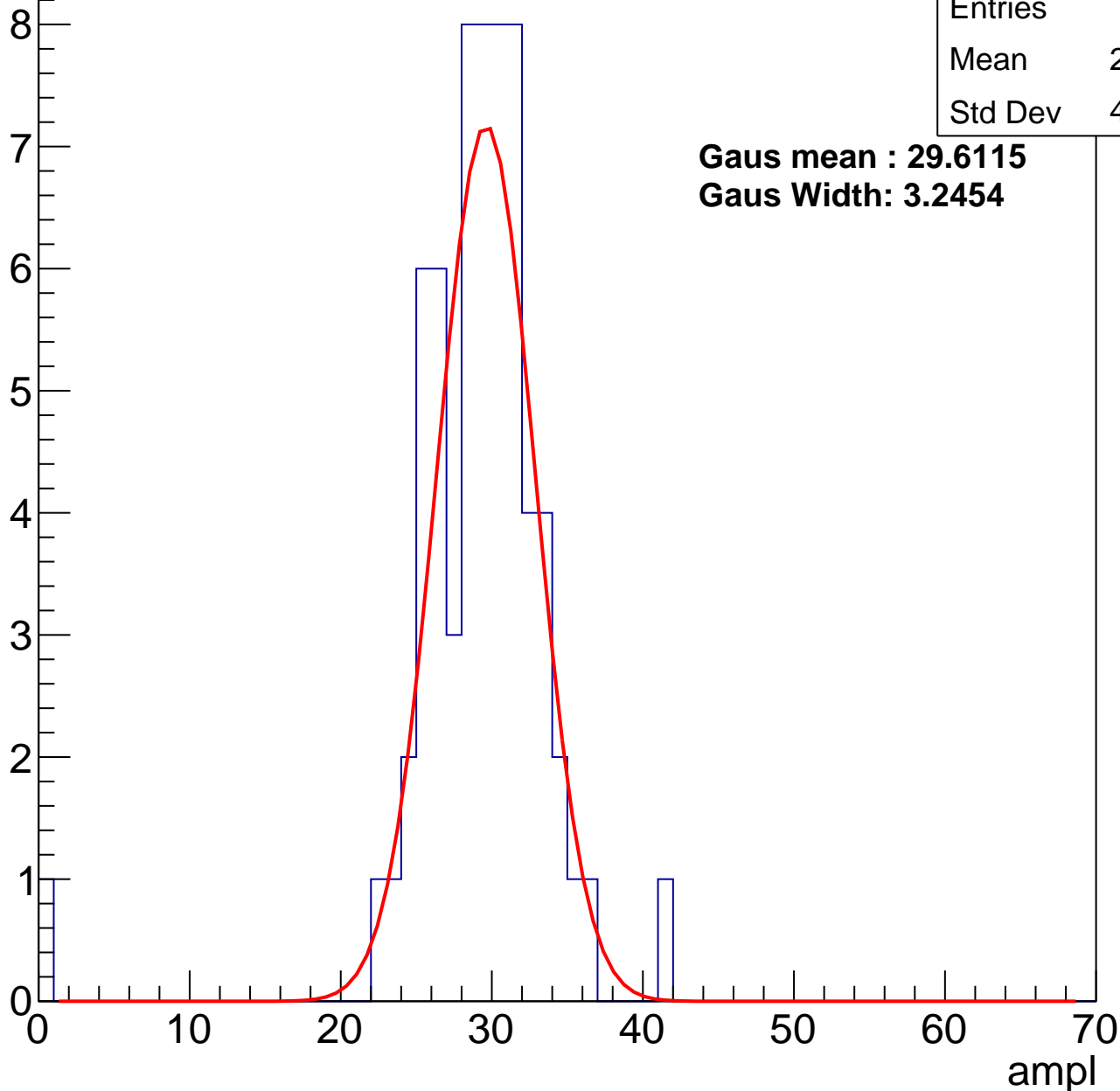
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	28.68
Std Dev	4.896

**Gaus mean : 29.6115**

**Gaus Width: 3.2454**



# B1L000S, U25-ch112, adc1

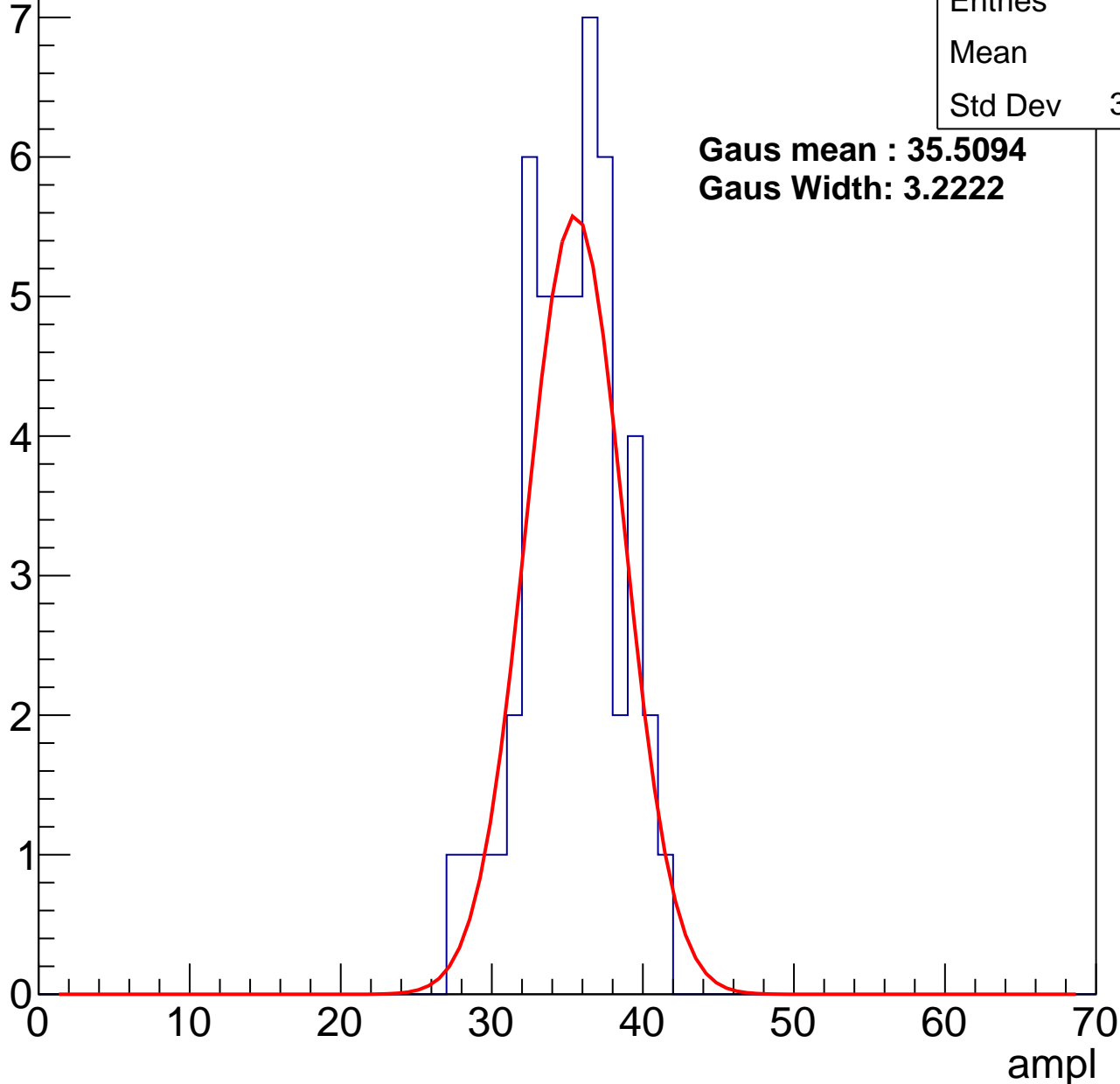
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	34.8
Std Dev	3.136

**Gaus mean : 35.5094**

**Gaus Width: 3.2222**



# B1L000S, U25-ch112, adc2

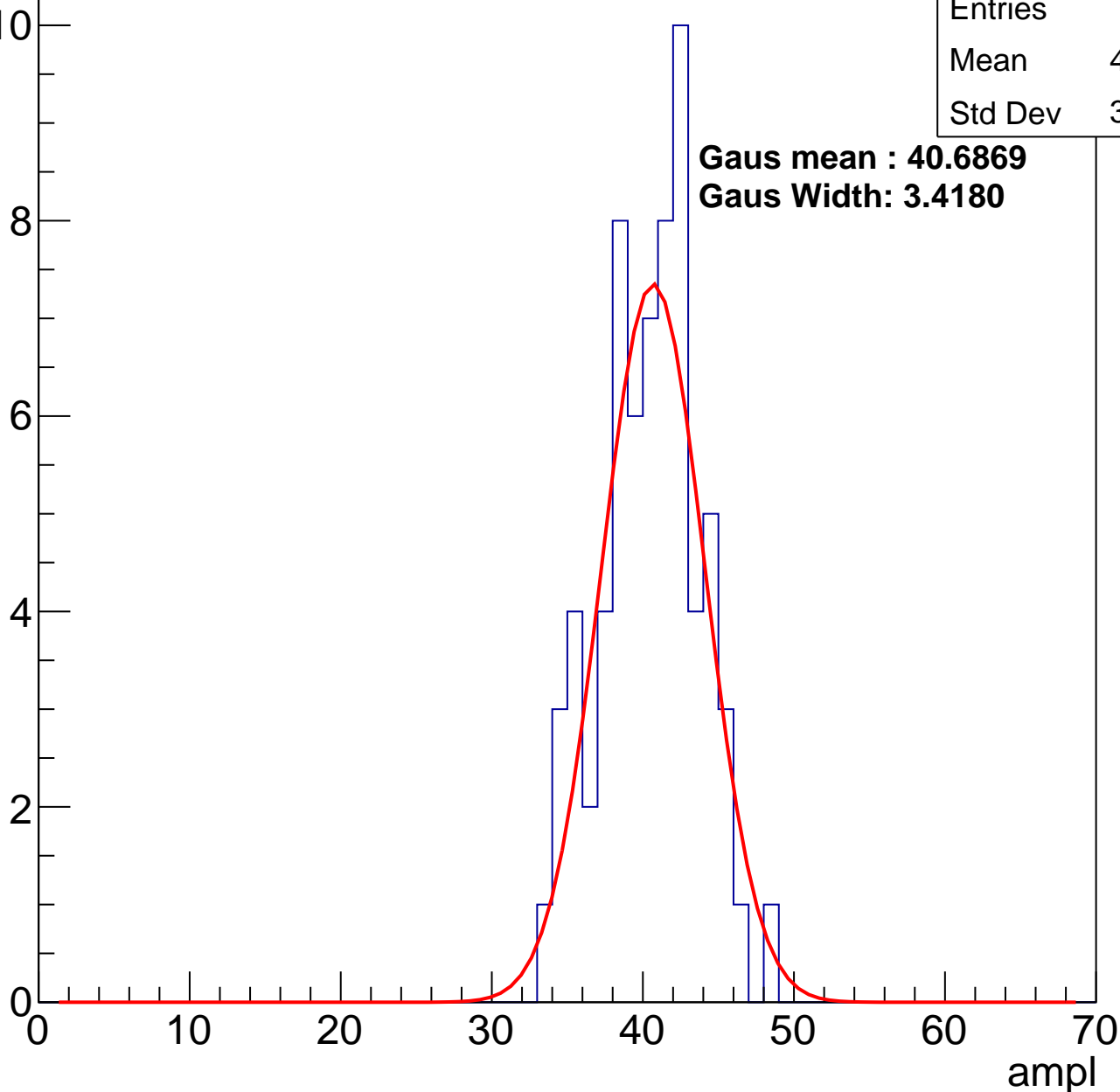
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	40.03
Std Dev	3.237

**Gaus mean : 40.6869**

**Gaus Width: 3.4180**

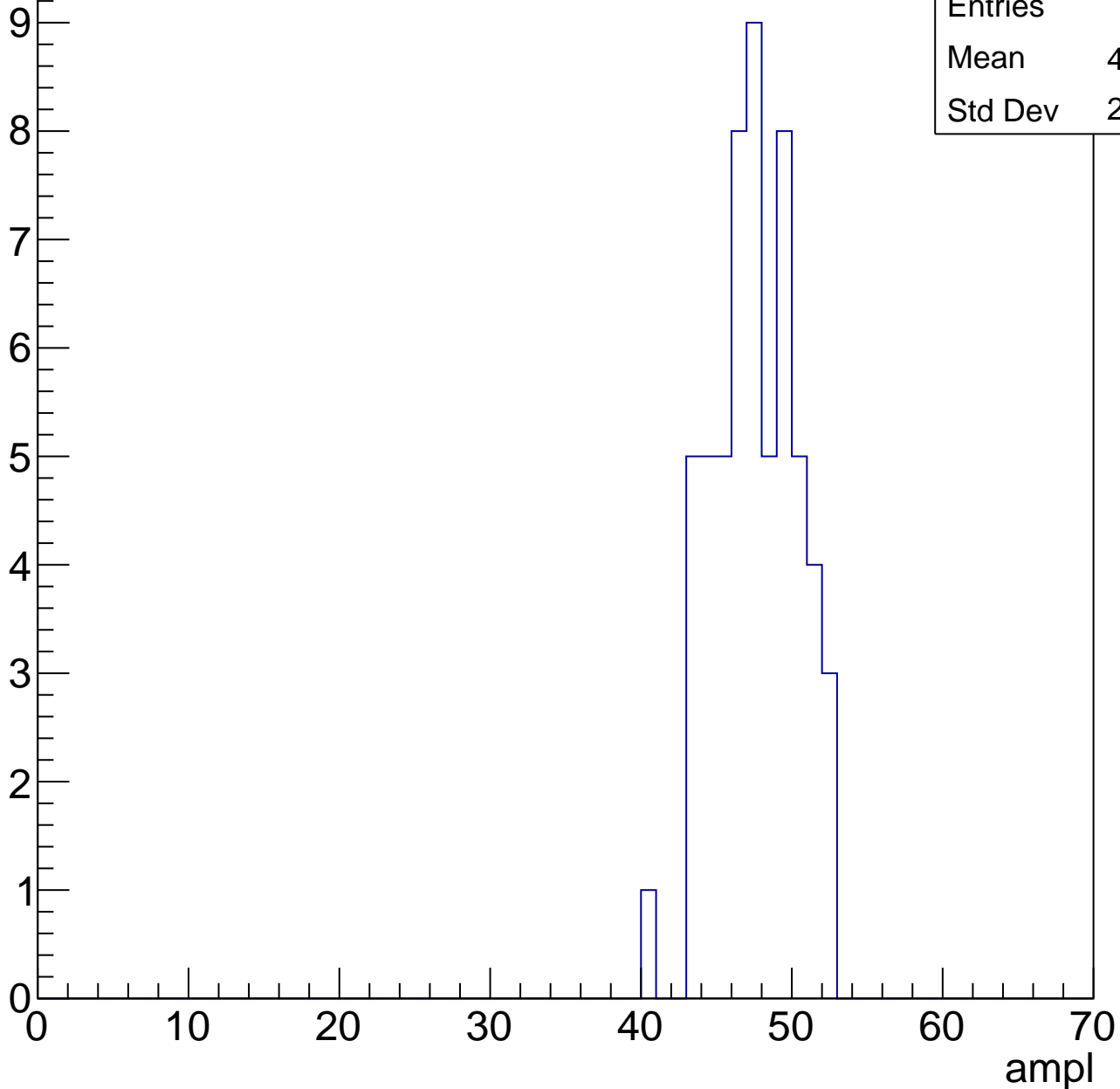


# B1L000S, U25-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

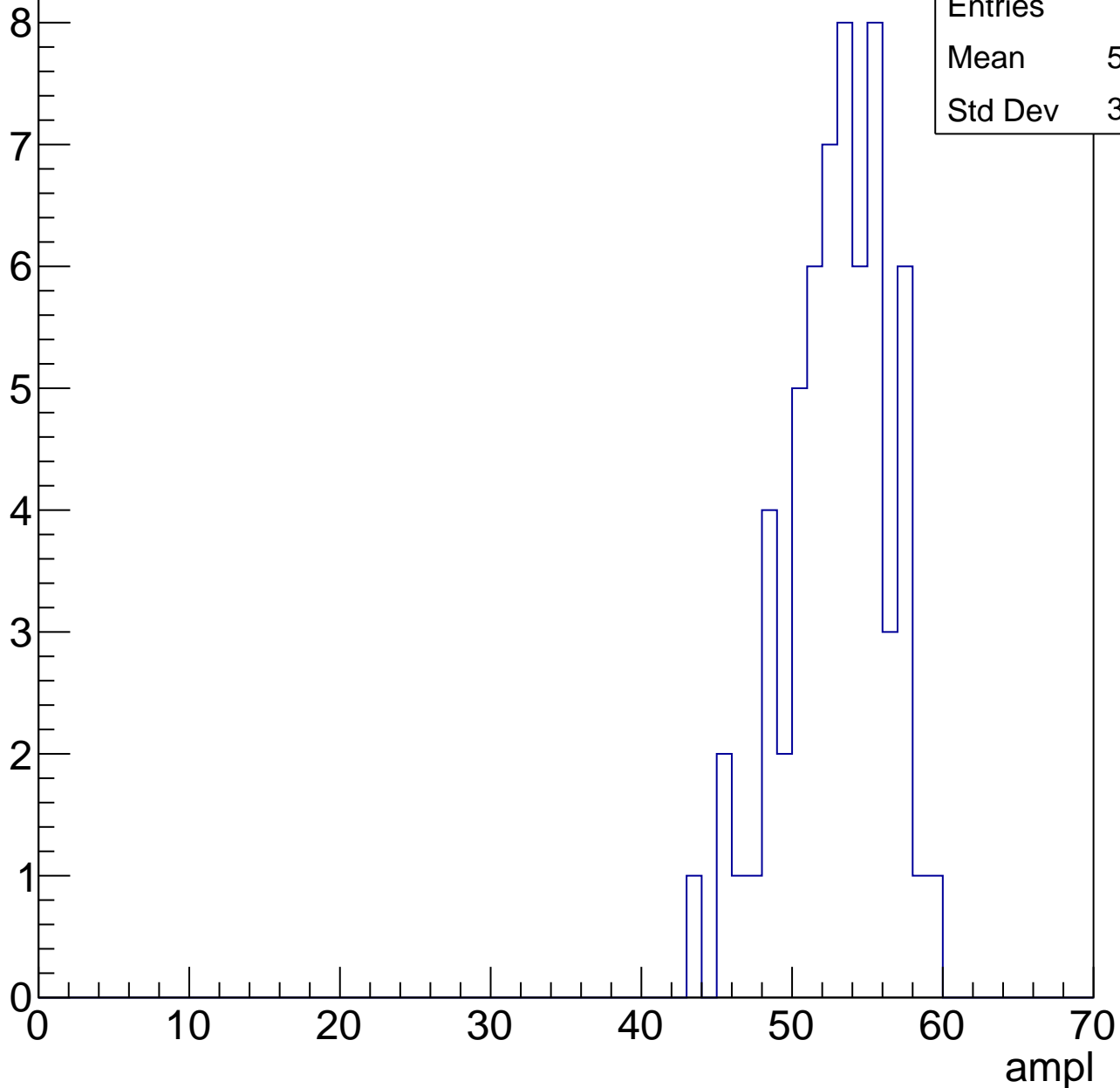
Entries	58
Mean	47.12
Std Dev	2.698



# B1L000S, U25-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

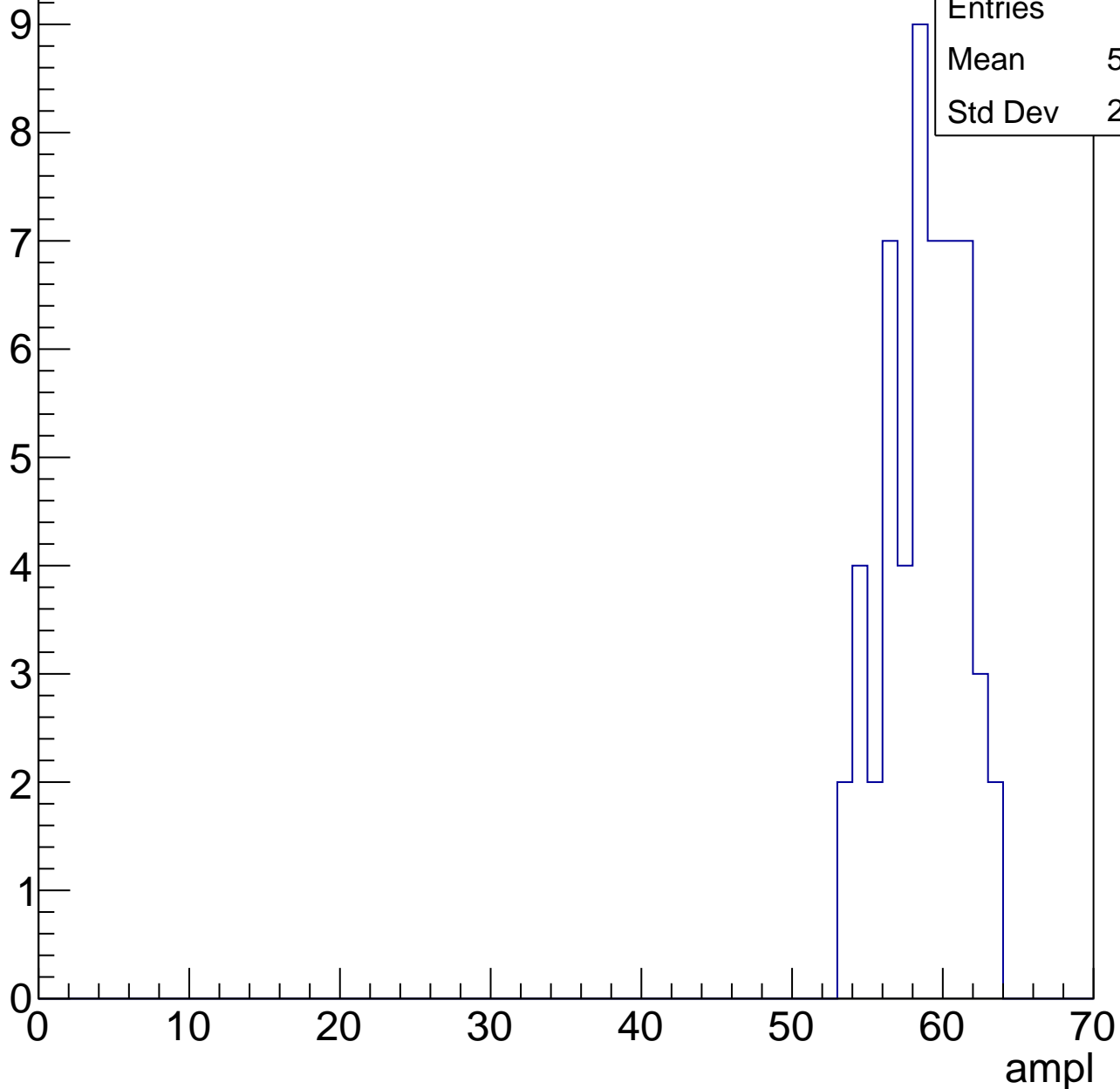


# B1L000S, U25-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	54
Mean	58.26
Std Dev	2.569

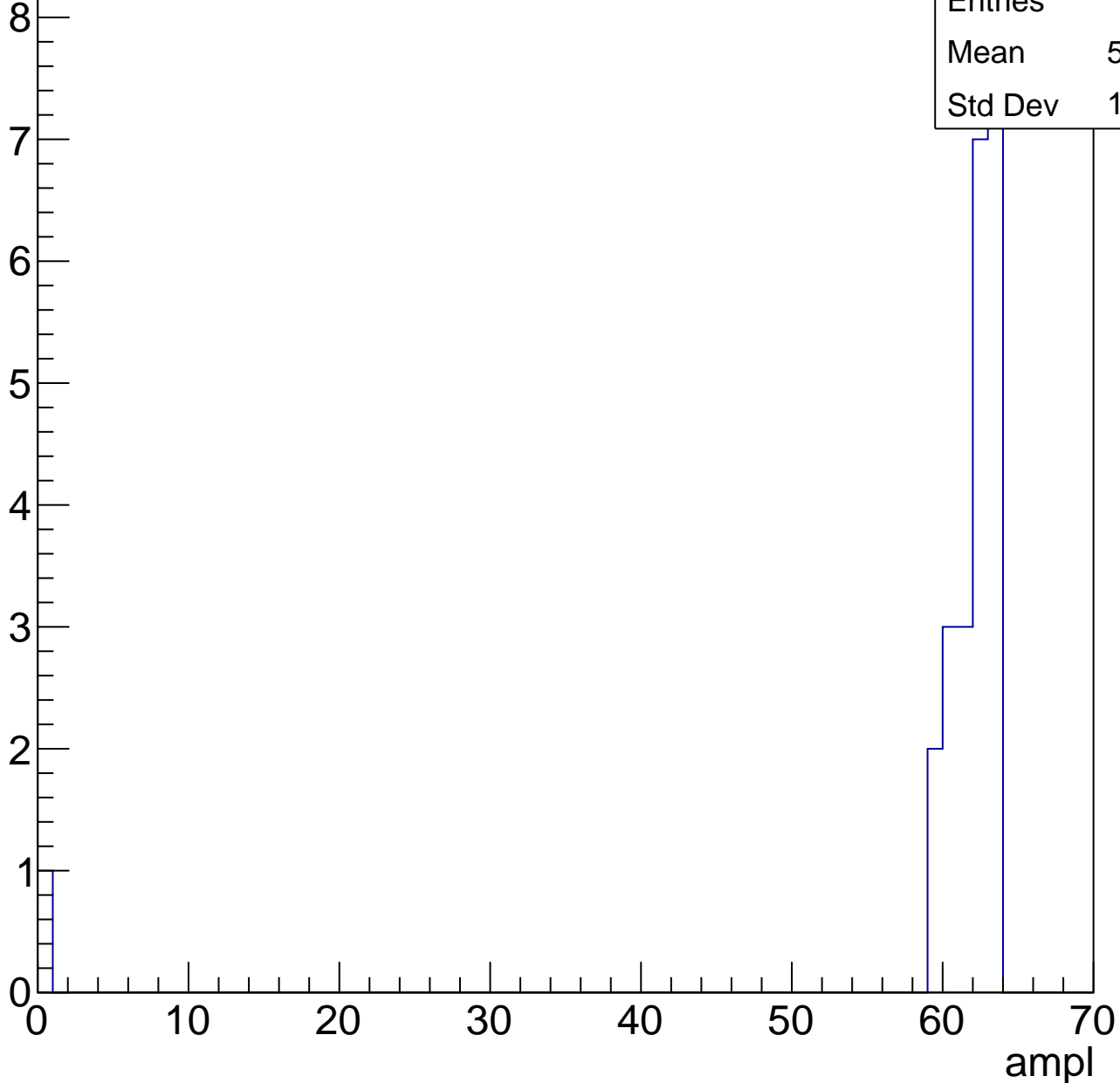


# B1L000S, U25-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	59.12
Std Dev	12.39





# B1L000S, U25-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch113, adc0

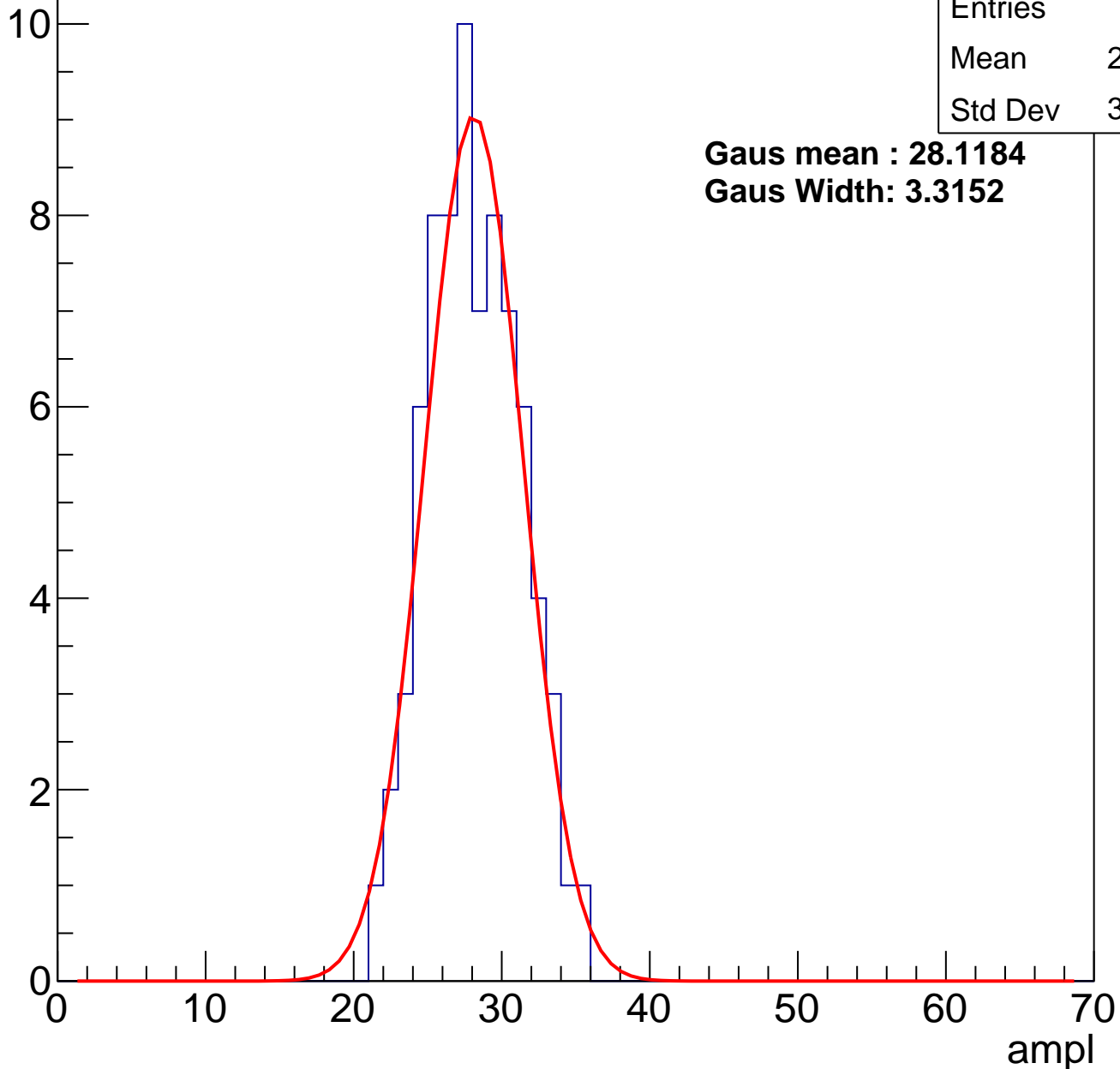
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	27.68
Std Dev	3.082

**Gaus mean : 28.1184**

**Gaus Width: 3.3152**

Entry



# B1L000S, U25-ch113, adc1

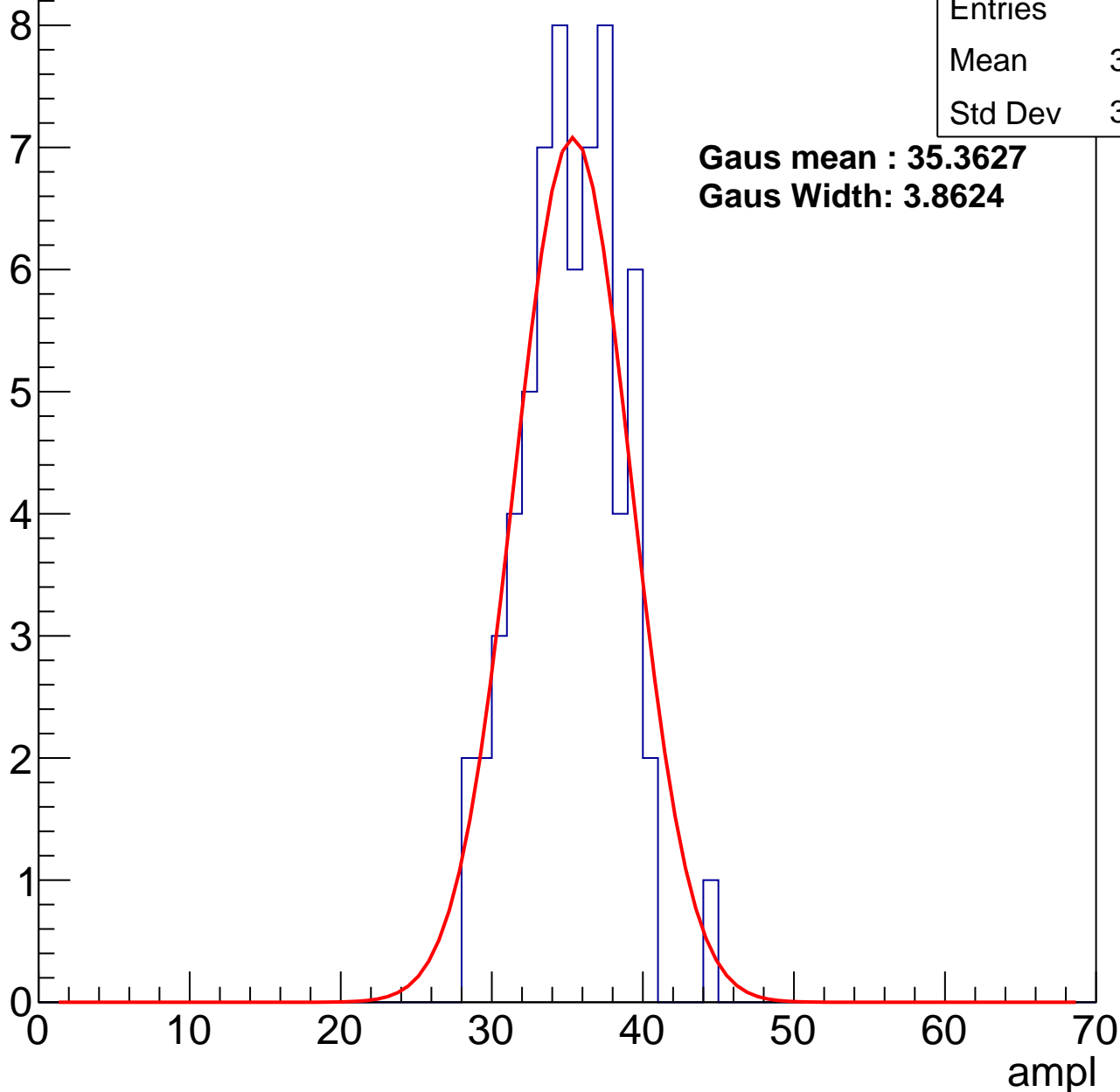
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	34.75
Std Dev	3.254

**Gaus mean : 35.3627**

**Gaus Width: 3.8624**



# B1L000S, U25-ch113, adc2

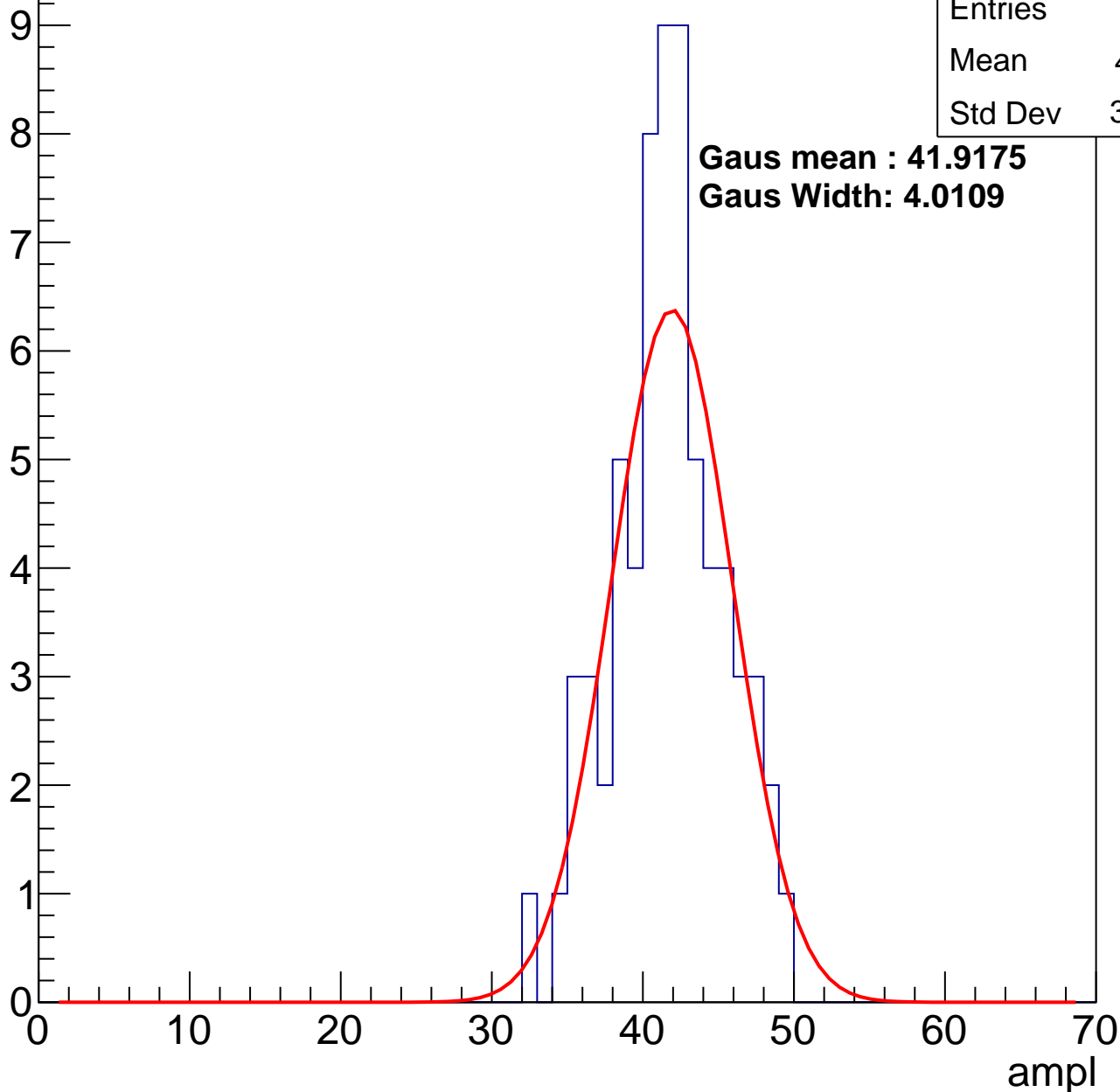
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	41.21
Std Dev	3.635

**Gaus mean : 41.9175**

**Gaus Width: 4.0109**

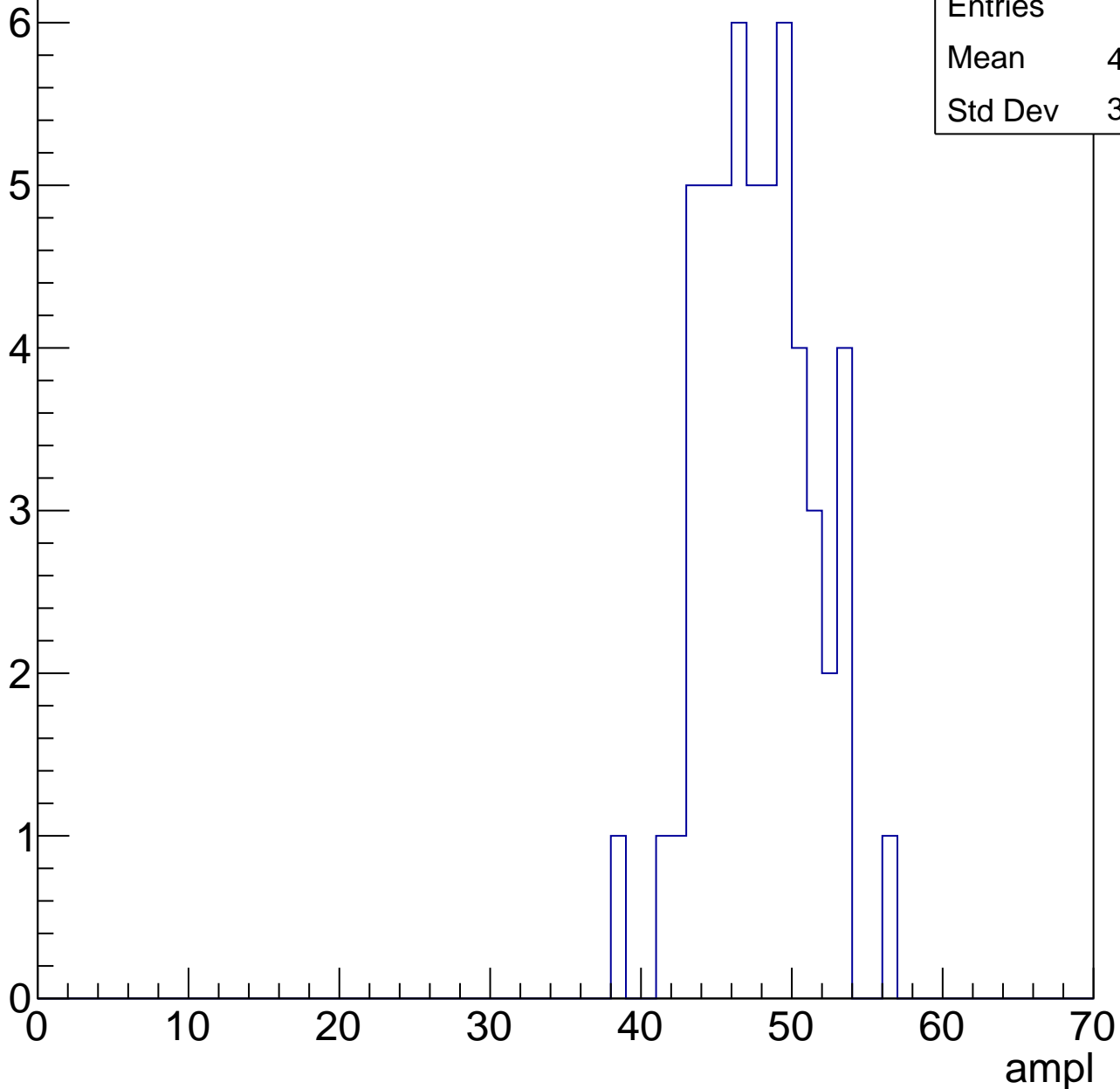


# B1L000S, U25-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

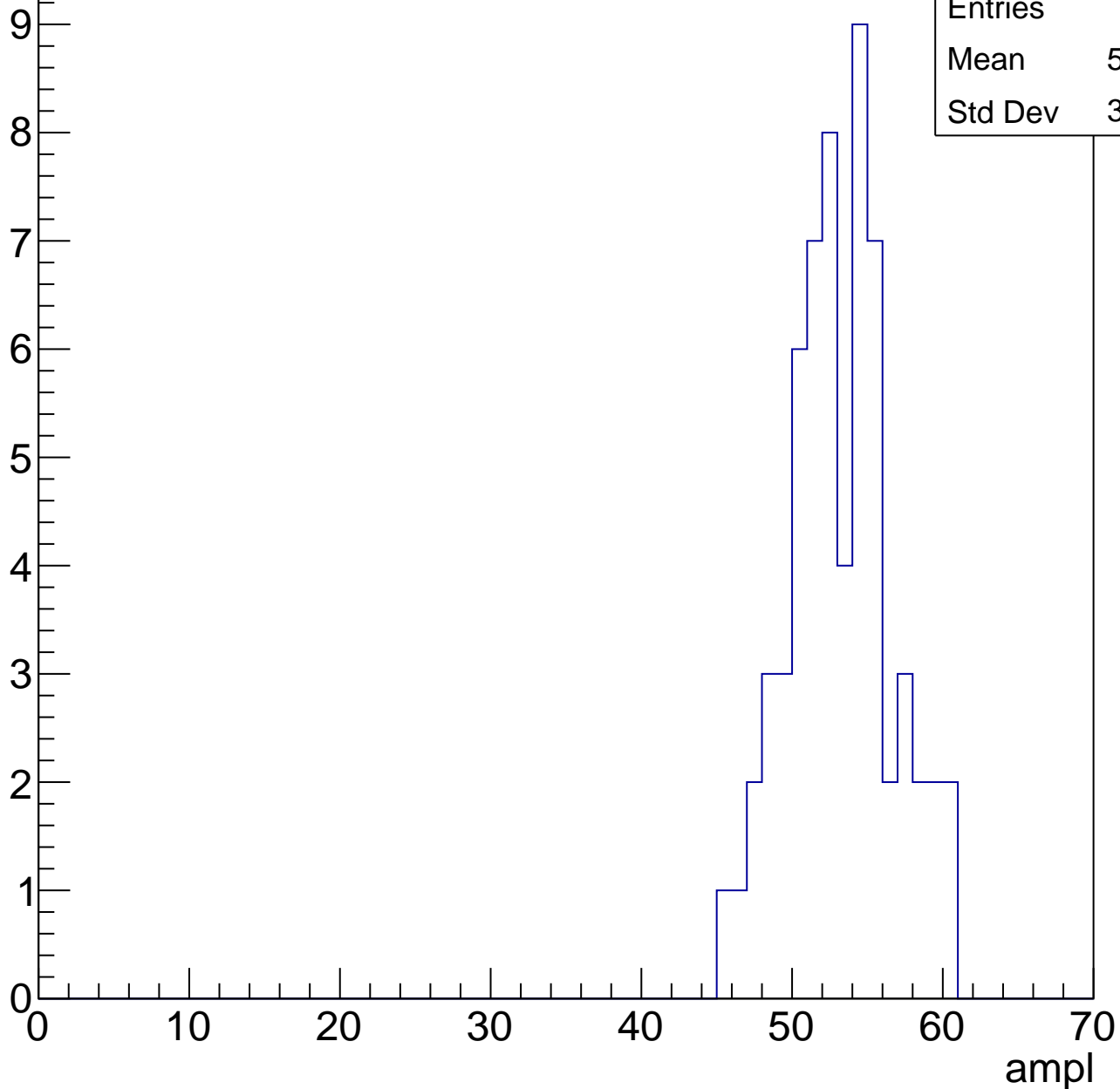
Entries	54
Mean	47.24
Std Dev	3.543



# B1L000S, U25-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



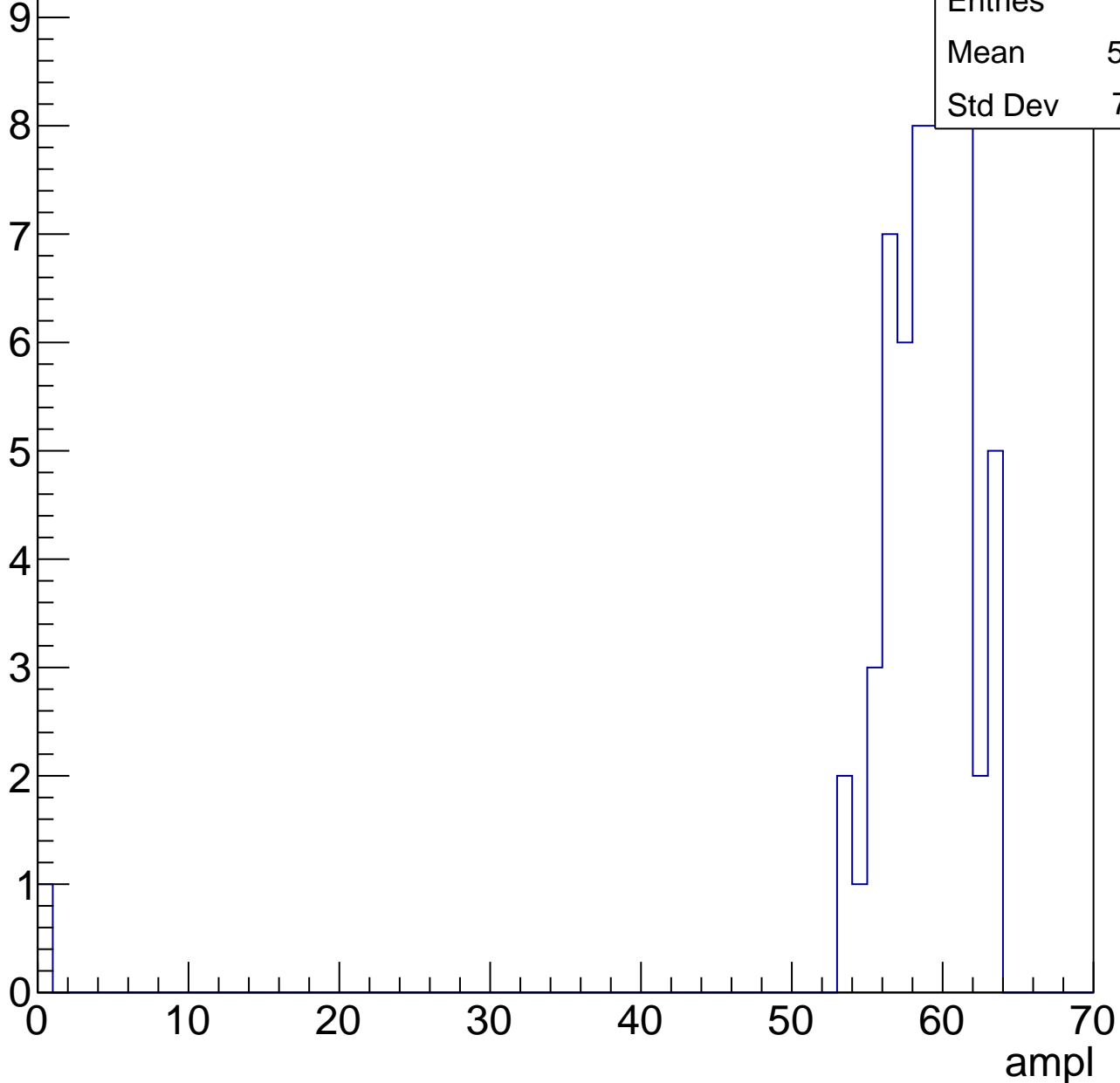
Entries	62
Mean	52.73
Std Dev	3.399

# B1L000S, U25-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	57.72
Std Dev	7.921

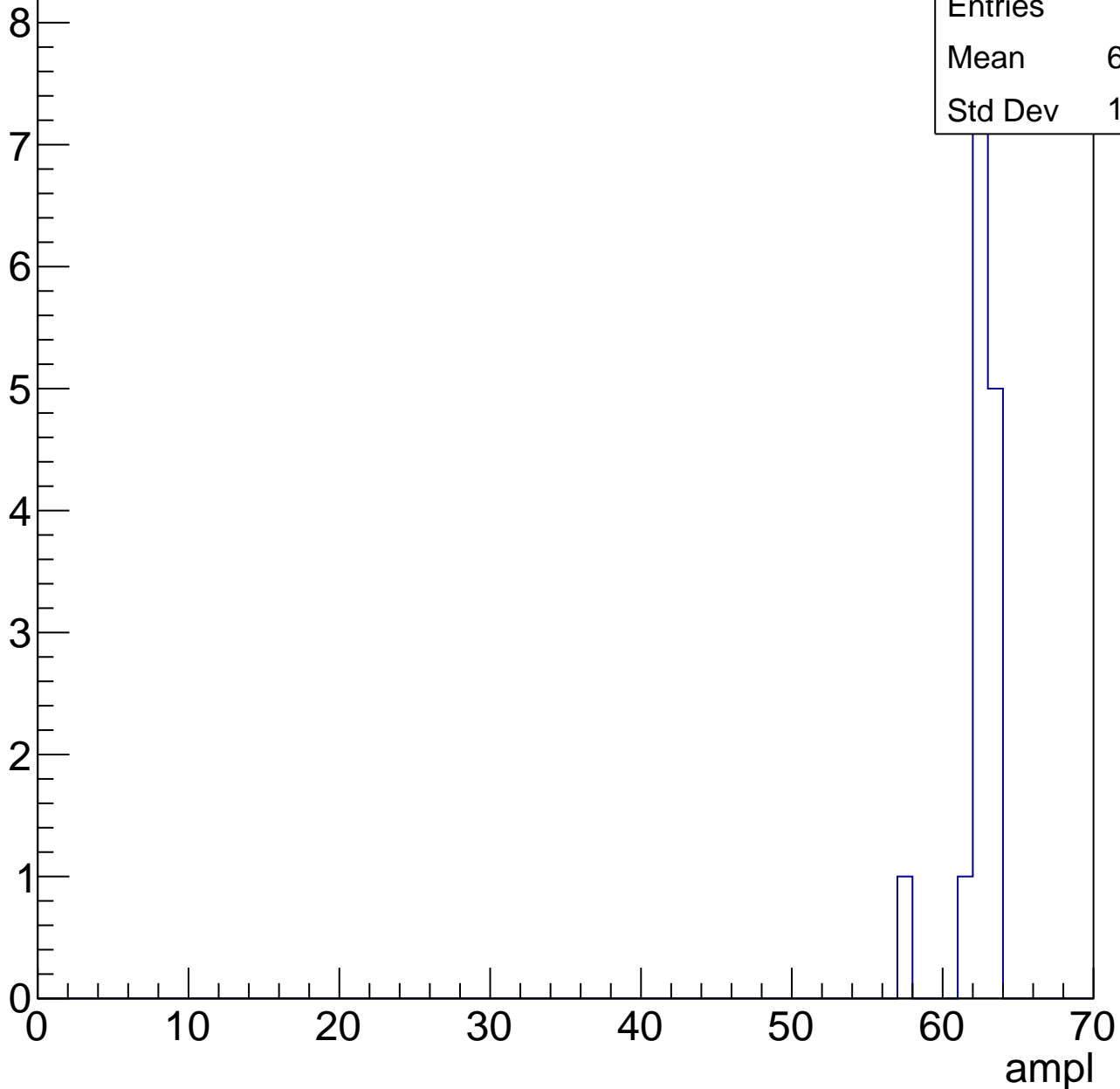


# B1L000S, U25-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	61.93
Std Dev	1.436





# B1L000S, U25-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch114, adc0

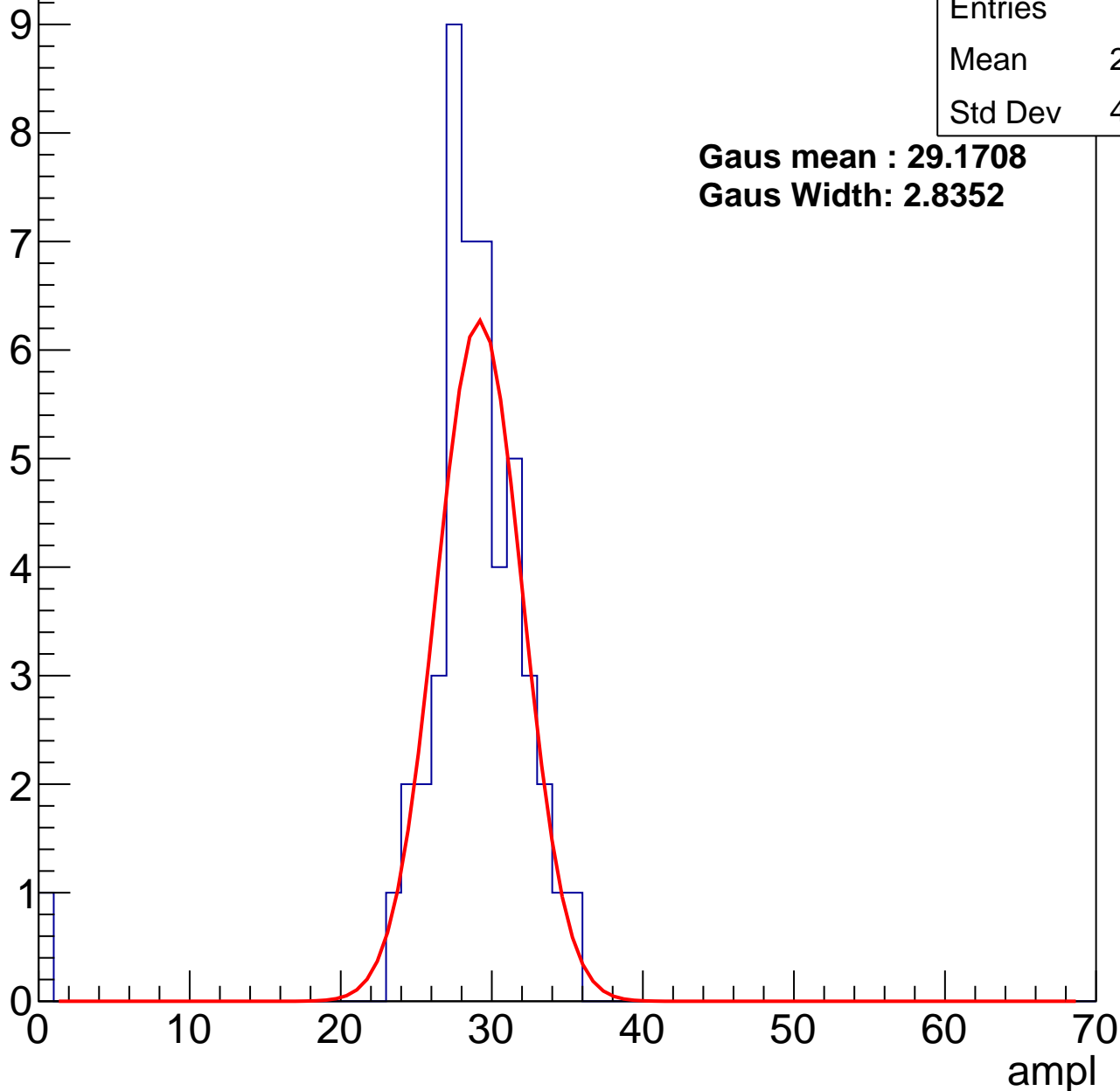
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	28.06
Std Dev	4.854

**Gaus mean : 29.1708**

**Gaus Width: 2.8352**



# B1L000S, U25-ch114, adc1

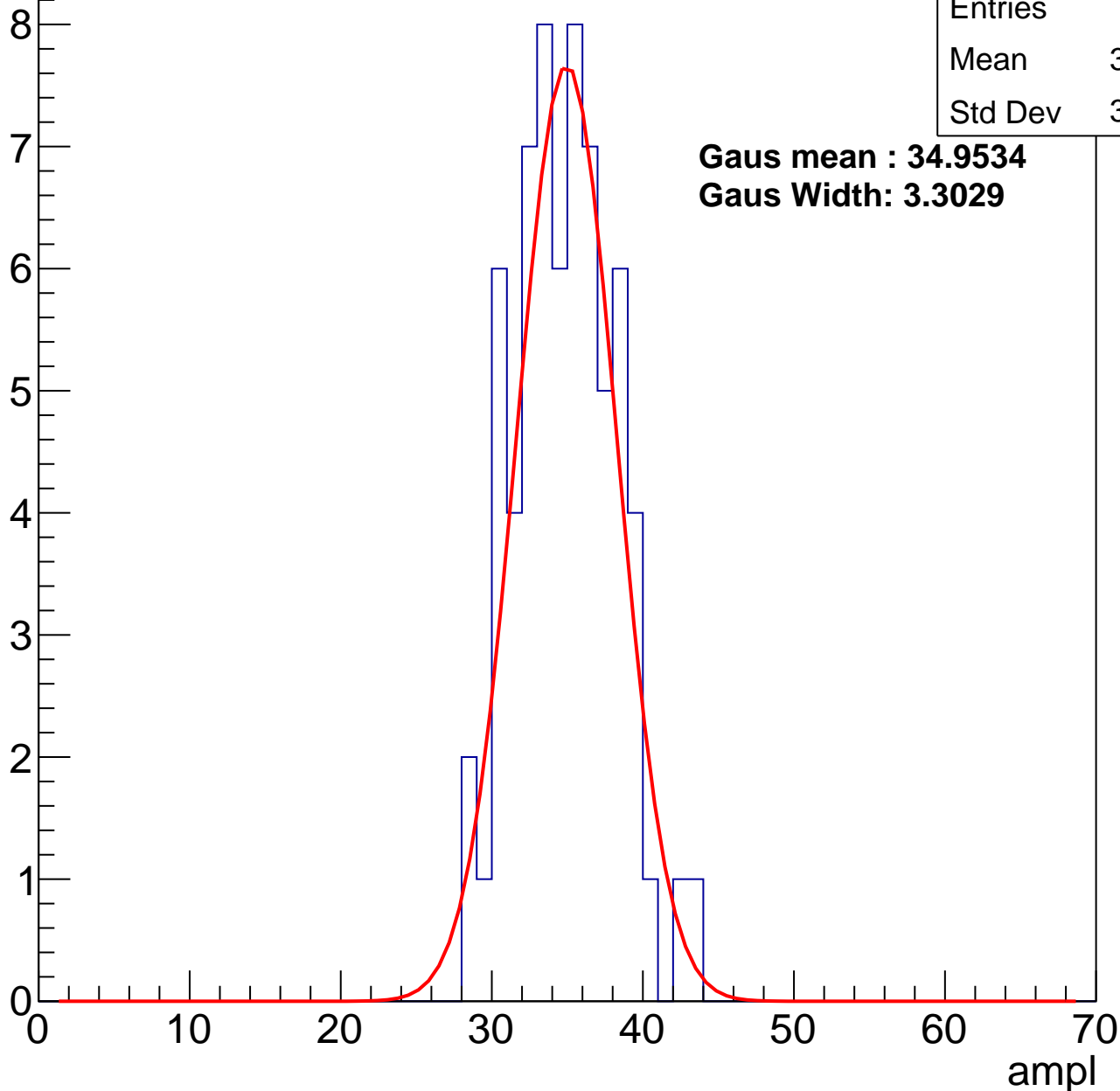
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	34.43
Std Dev	3.256

**Gaus mean : 34.9534**

**Gaus Width: 3.3029**



# B1L000S, U25-ch114, adc2

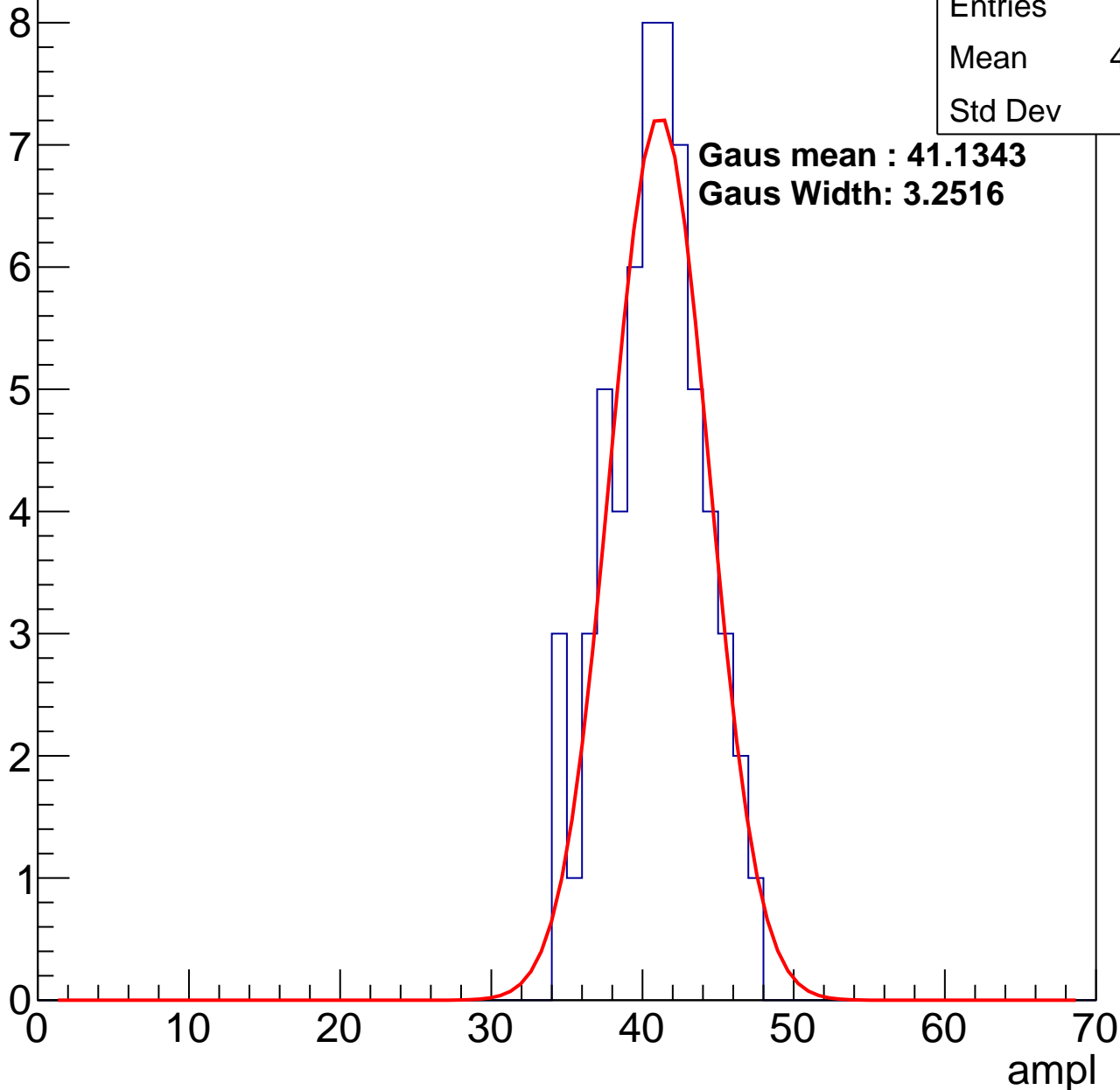
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	40.38
Std Dev	3.11

**Gaus mean : 41.1343**

**Gaus Width: 3.2516**

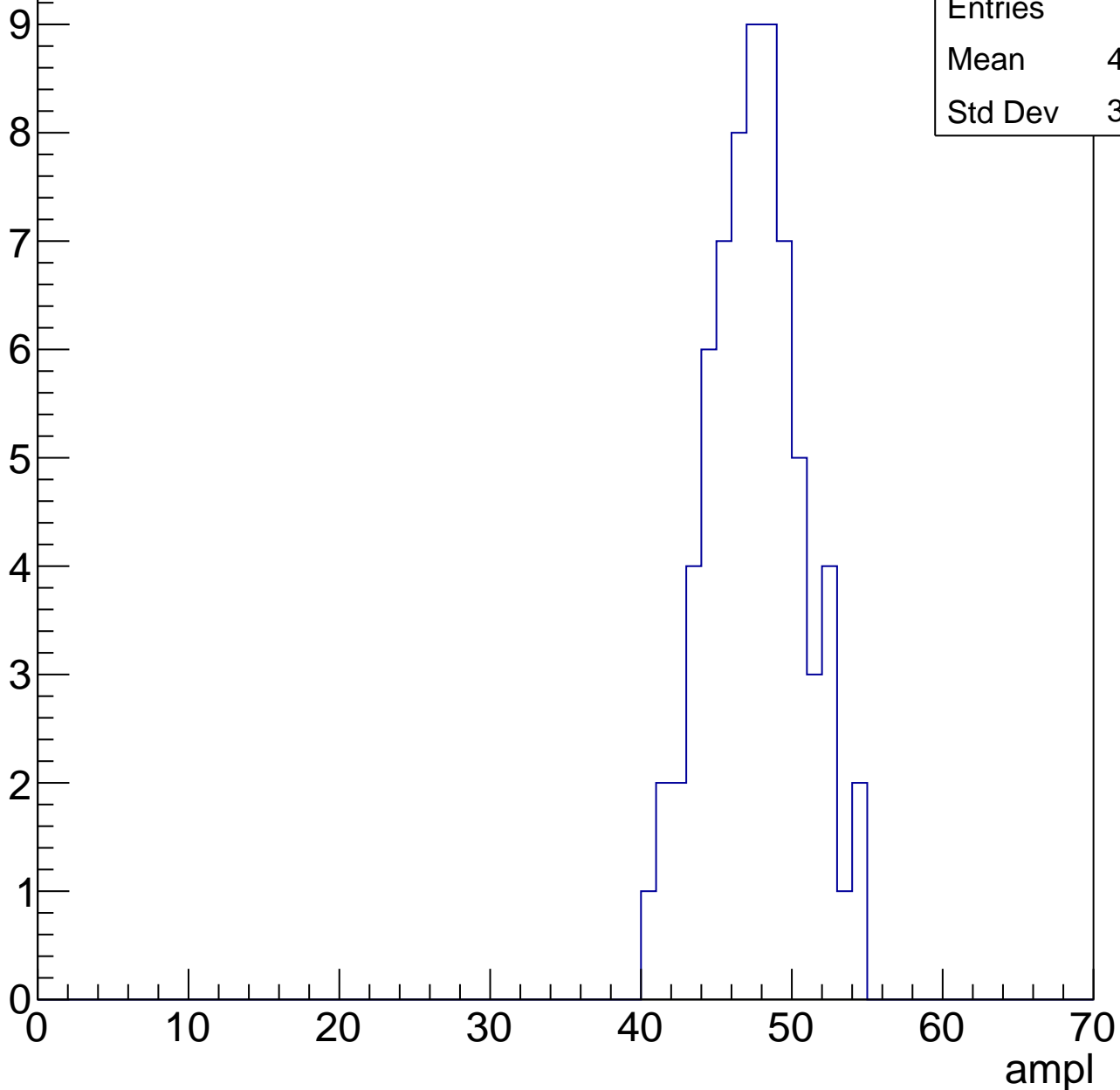


# B1L000S, U25-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	70
Mean	47.07
Std Dev	3.137

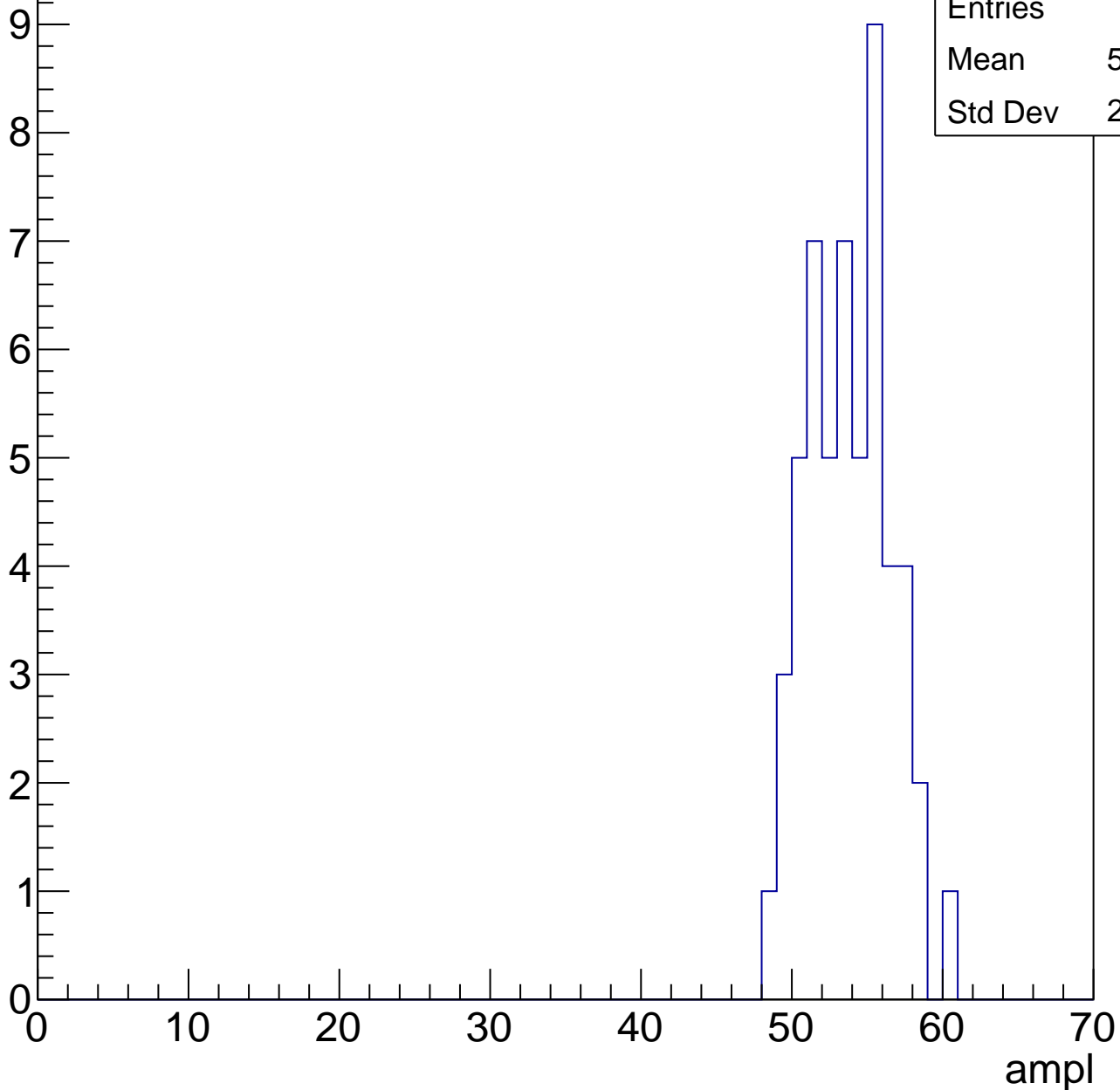


# B1L000S, U25-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	53.32
Std Dev	2.697



# B1L000S, U25-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

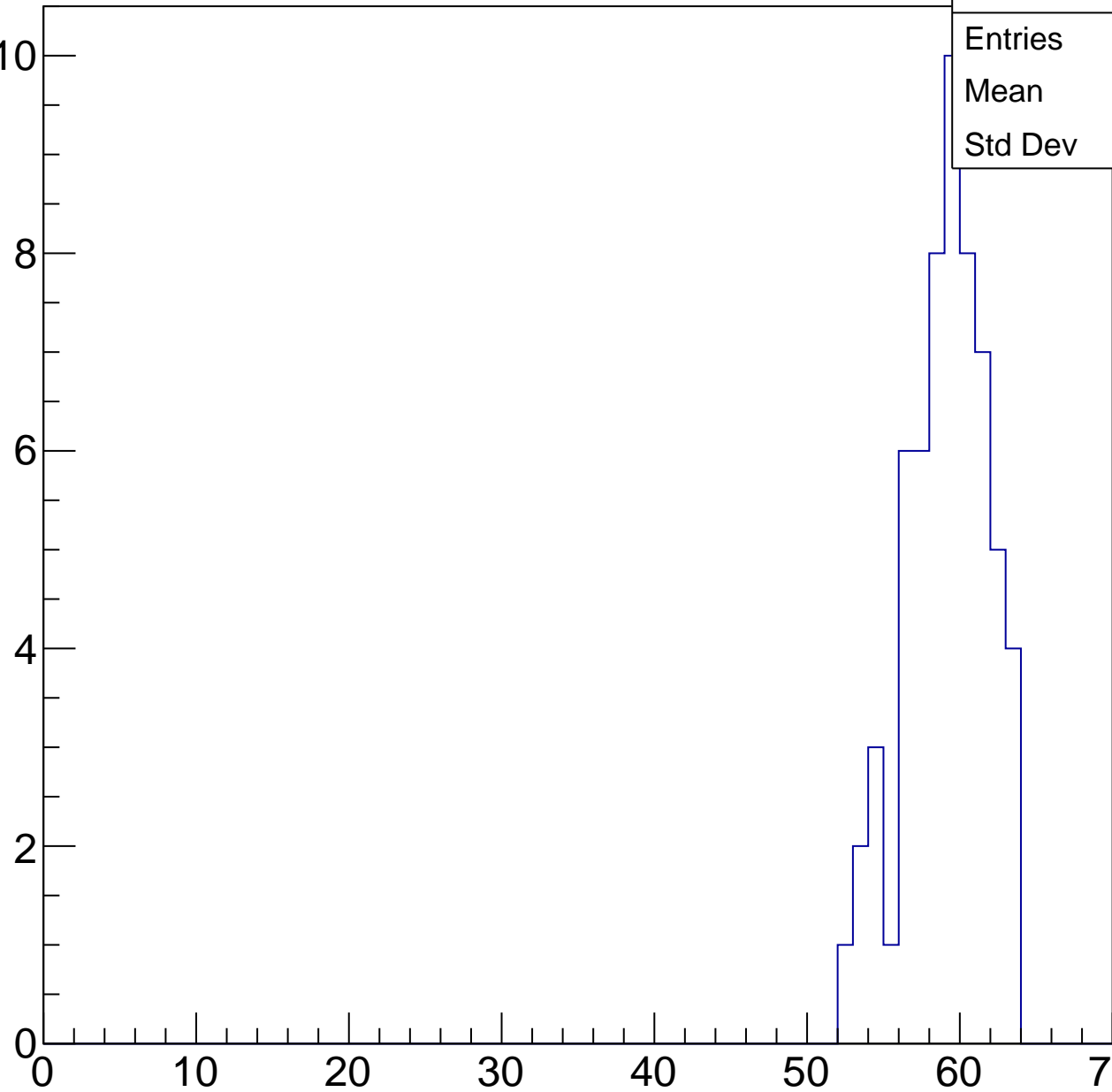
Entries	61
Mean	58.62
Std Dev	2.668

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

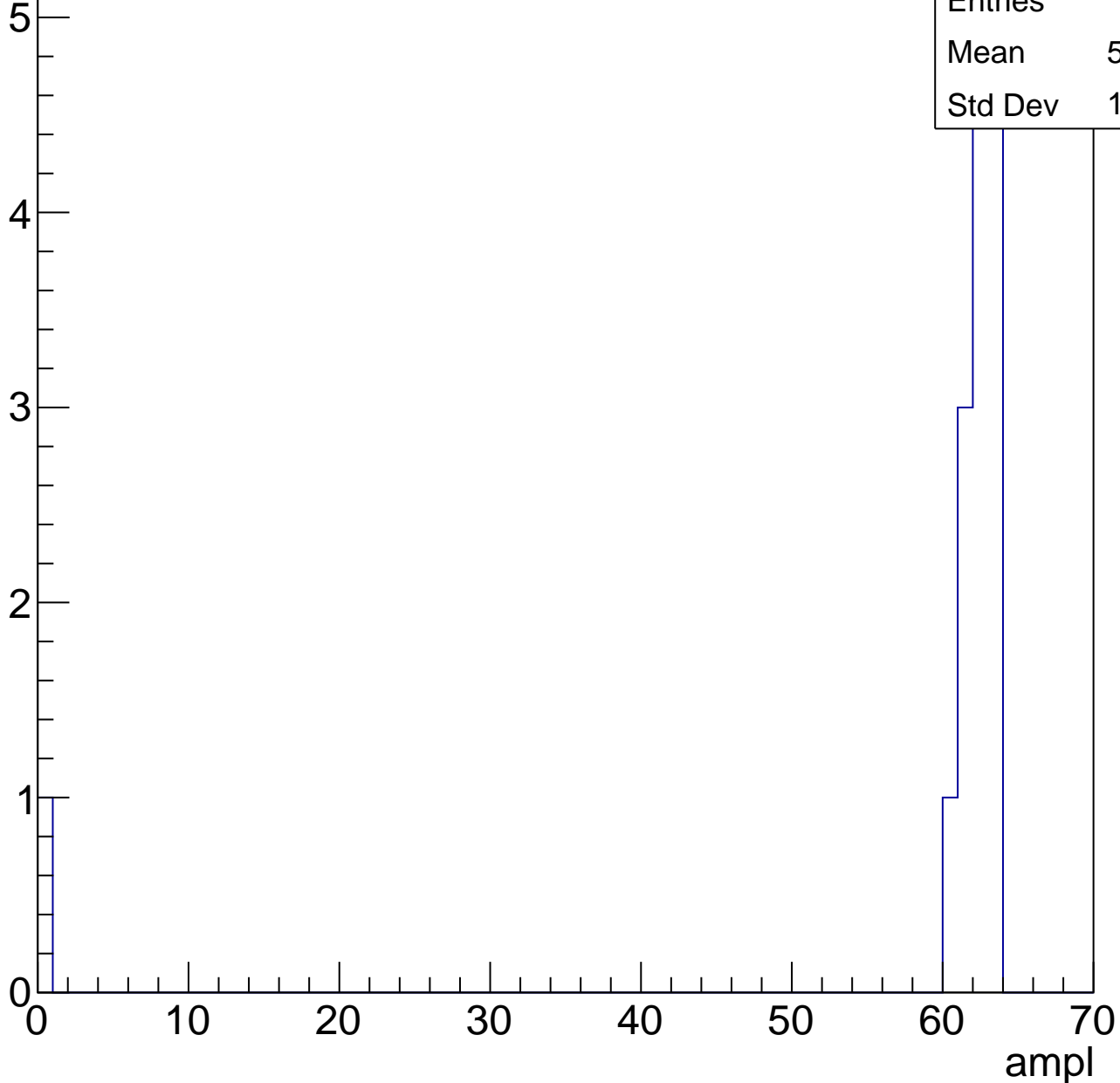


# B1L000S, U25-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.87
Std Dev	15.49

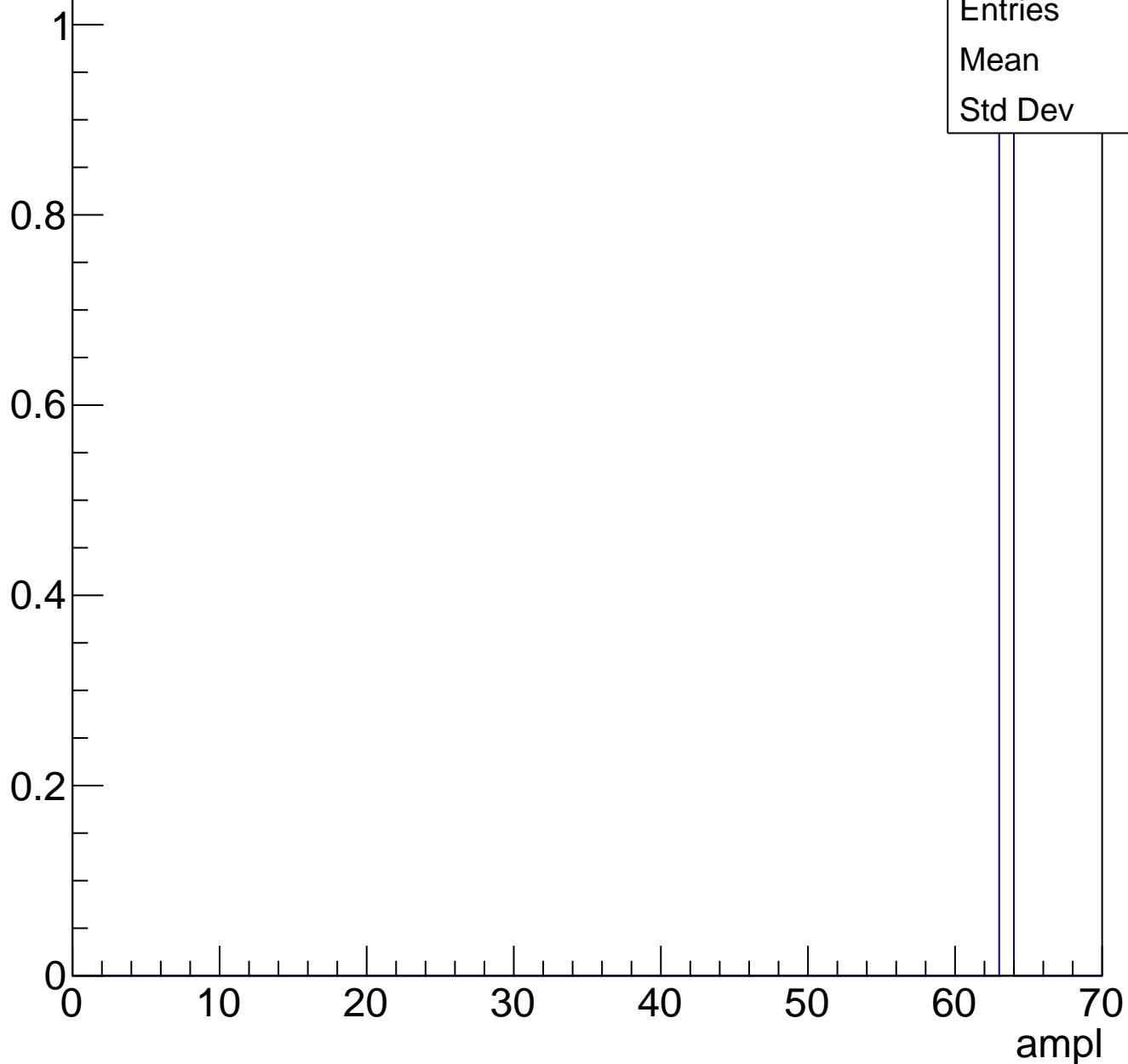




# B1L000S, U25-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch115, adc0

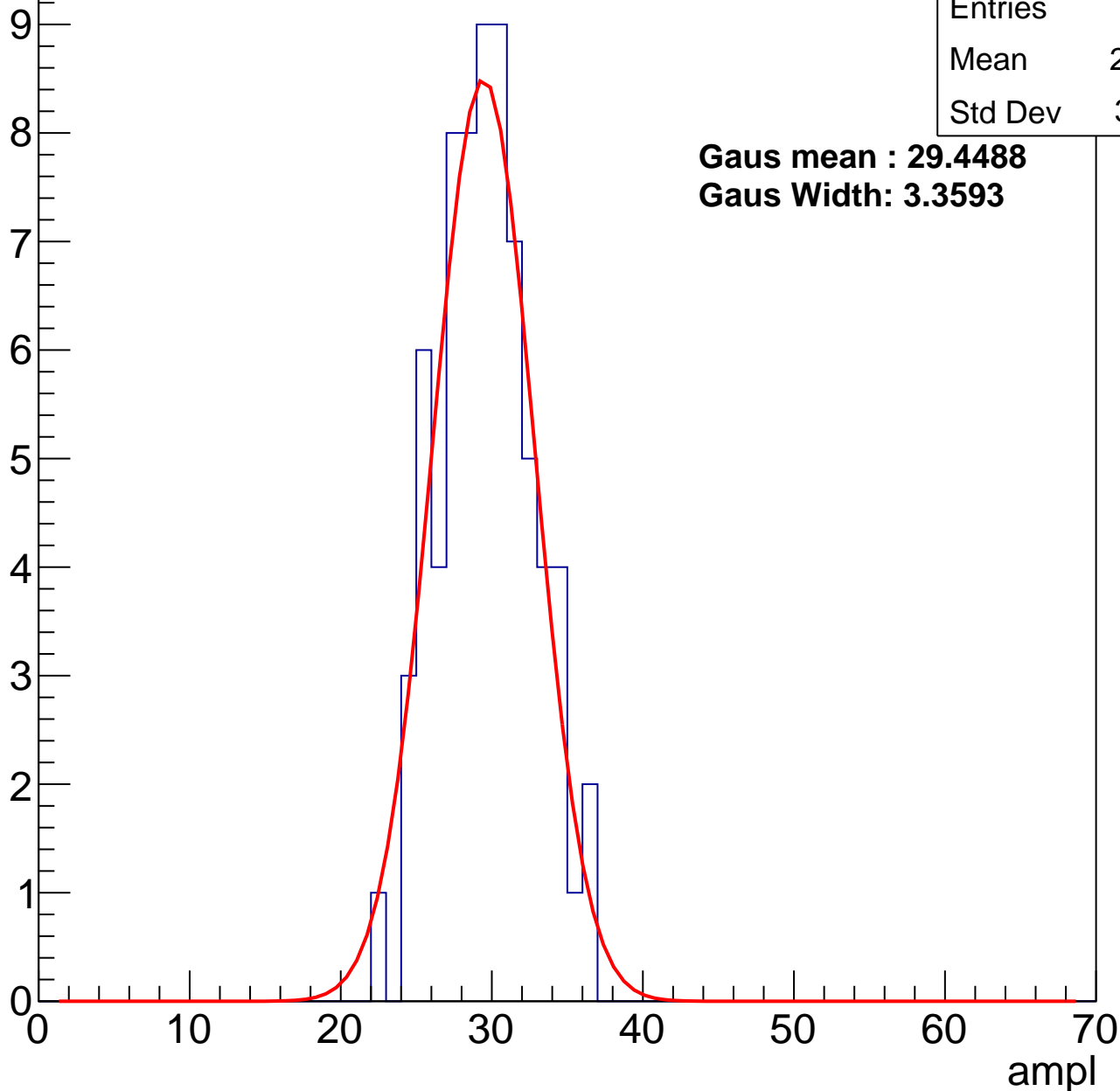
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	29.17
Std Dev	3.081

**Gaus mean : 29.4488**

**Gaus Width: 3.3593**



# B1L000S, U25-ch115, adc1

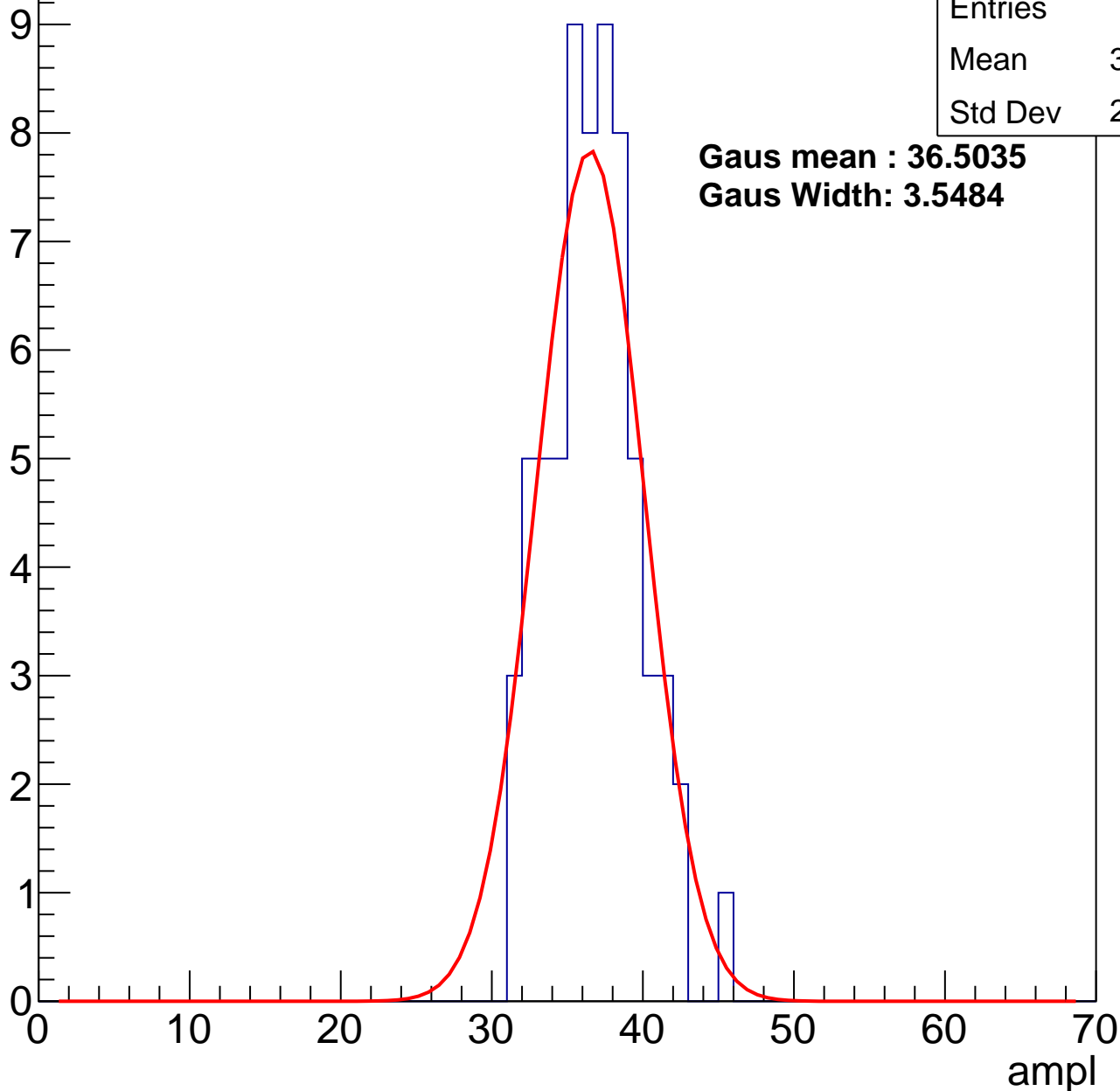
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	36.29
Std Dev	2.979

**Gaus mean : 36.5035**

**Gaus Width: 3.5484**



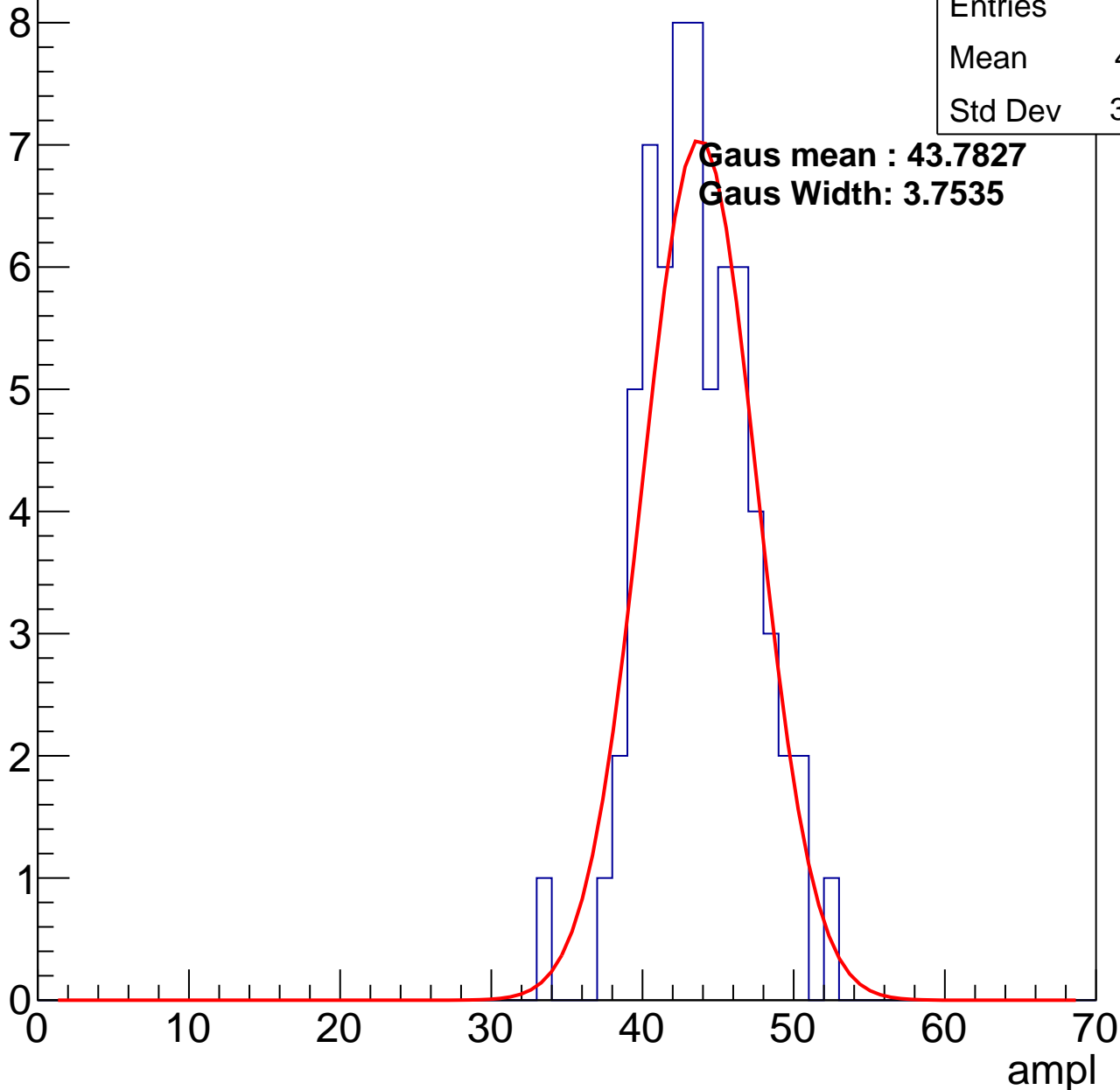
# B1L000S, U25-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	43.21
Std Dev	3.522

**Gaus mean : 43.7827**  
**Gaus Width: 3.7535**

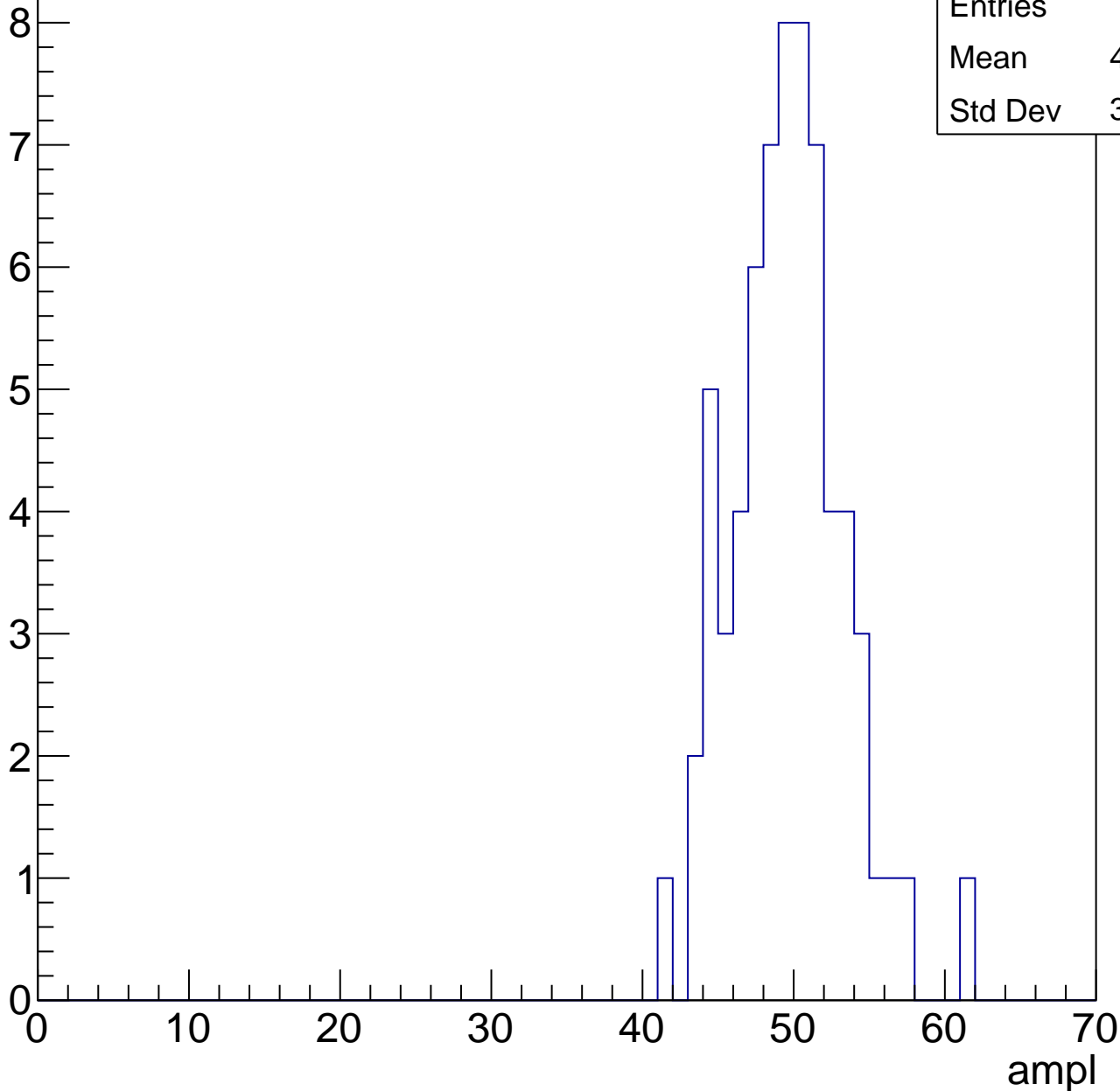


# B1L000S, U25-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	66
Mean	49.15
Std Dev	3.648

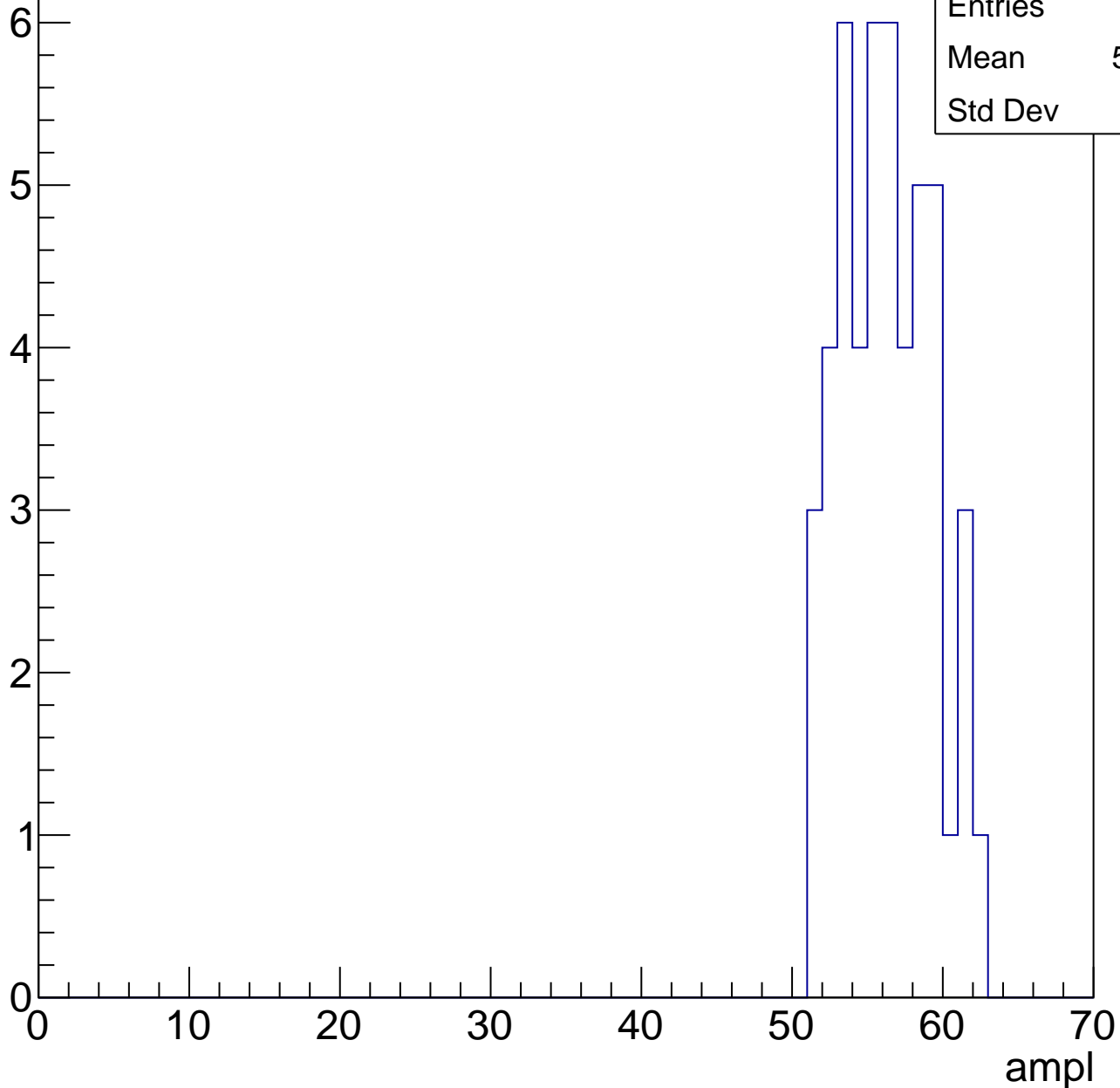


# B1L000S, U25-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	55.81
Std Dev	2.92

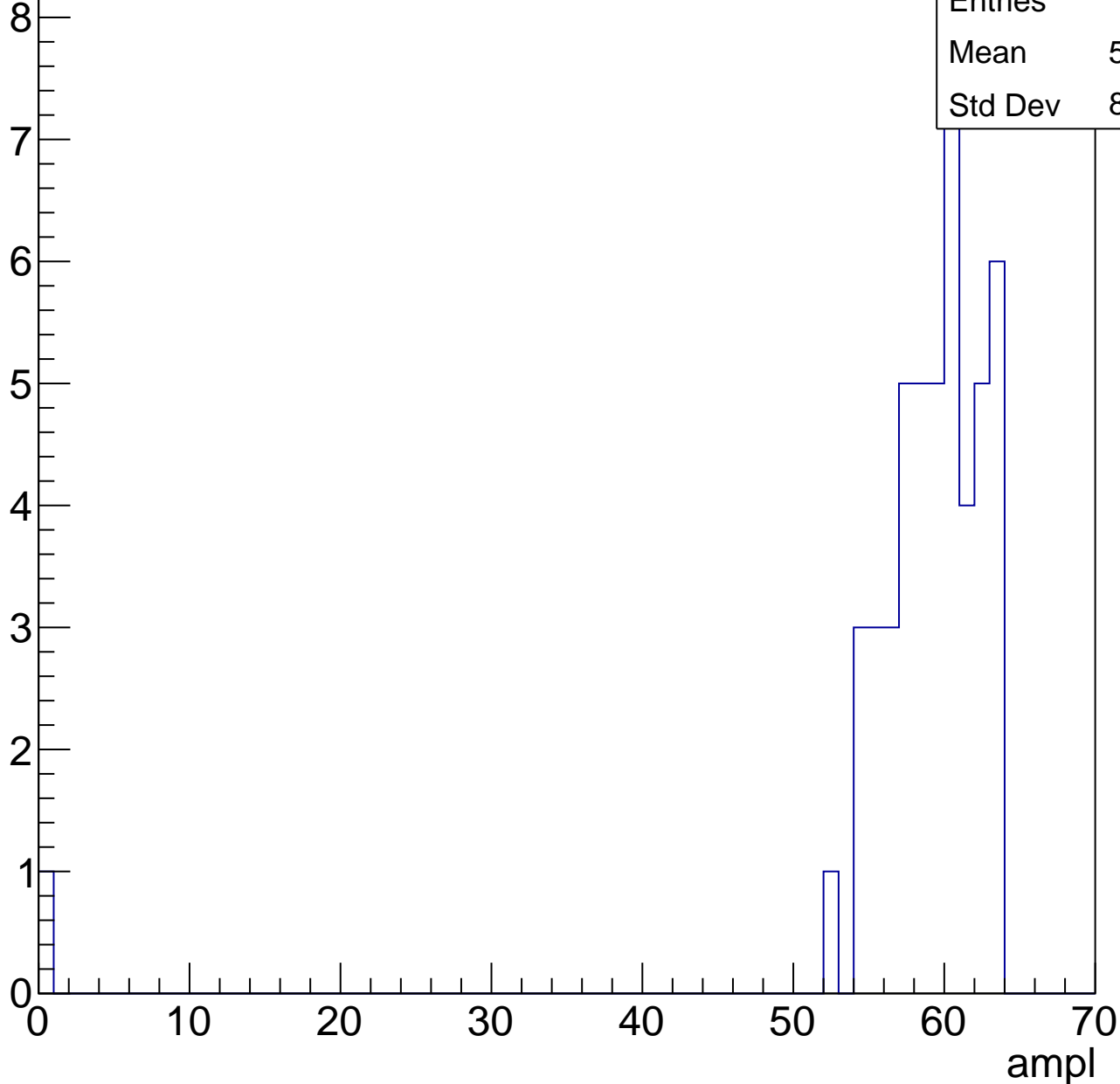


# B1L000S, U25-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

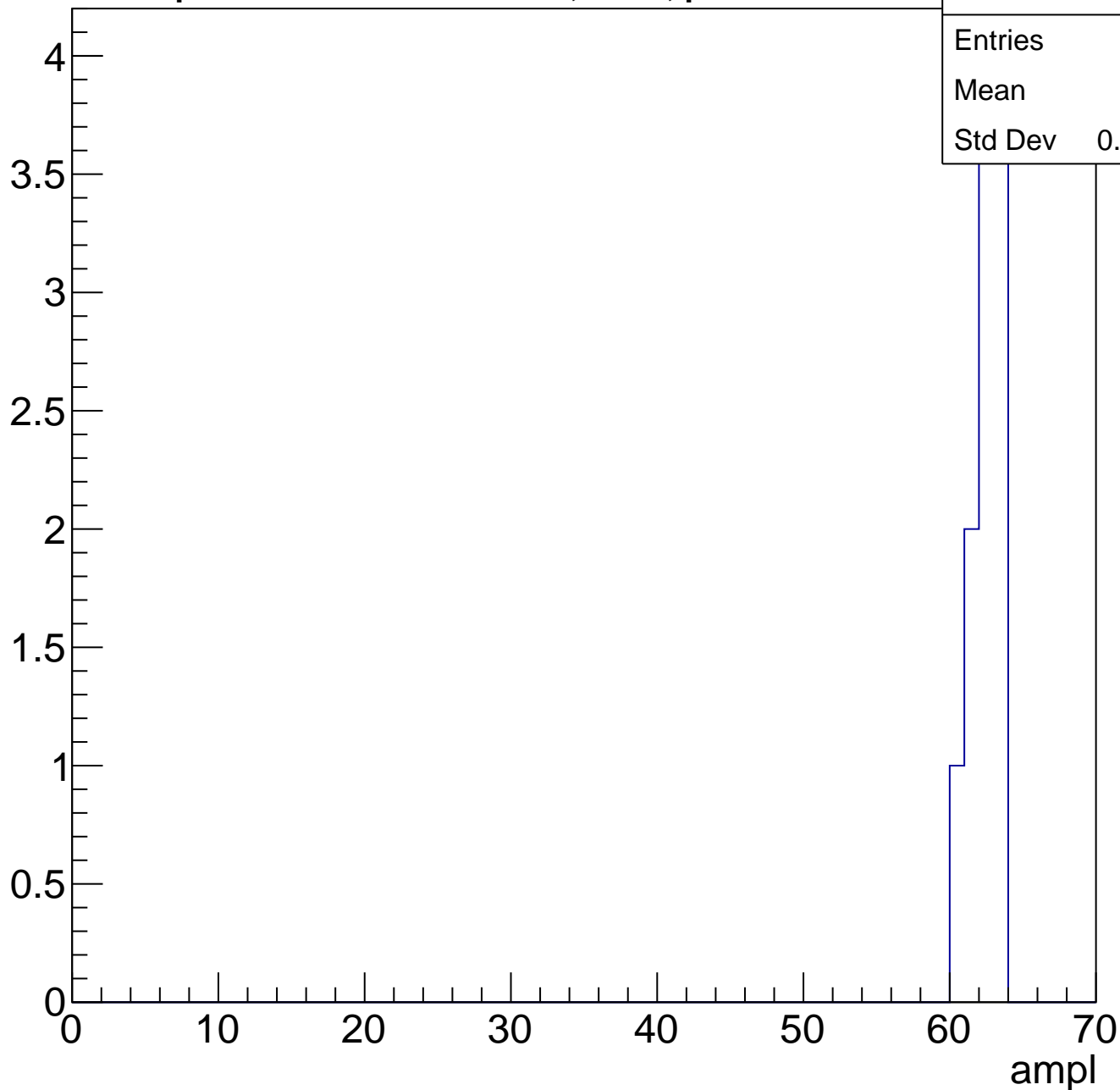
Entries	49
Mean	57.73
Std Dev	8.797



# B1L000S, U25-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L000S, U25-ch116, adc0

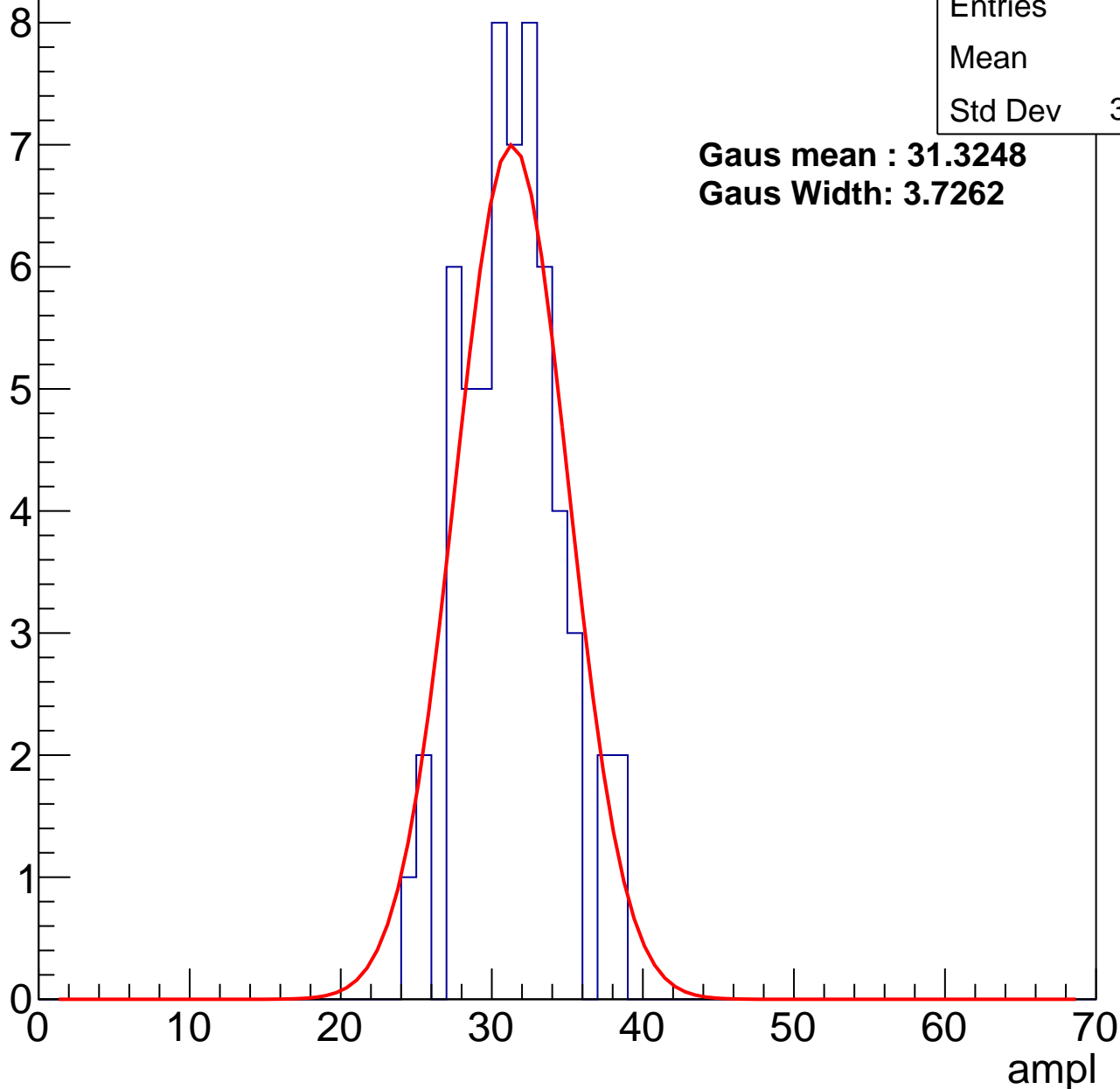
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	30.9
Std Dev	3.123

**Gaus mean : 31.3248**

**Gaus Width: 3.7262**



# B1L000S, U25-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	63
Mean	37.1
Std Dev	3.517

**Gaus mean : 37.7617**

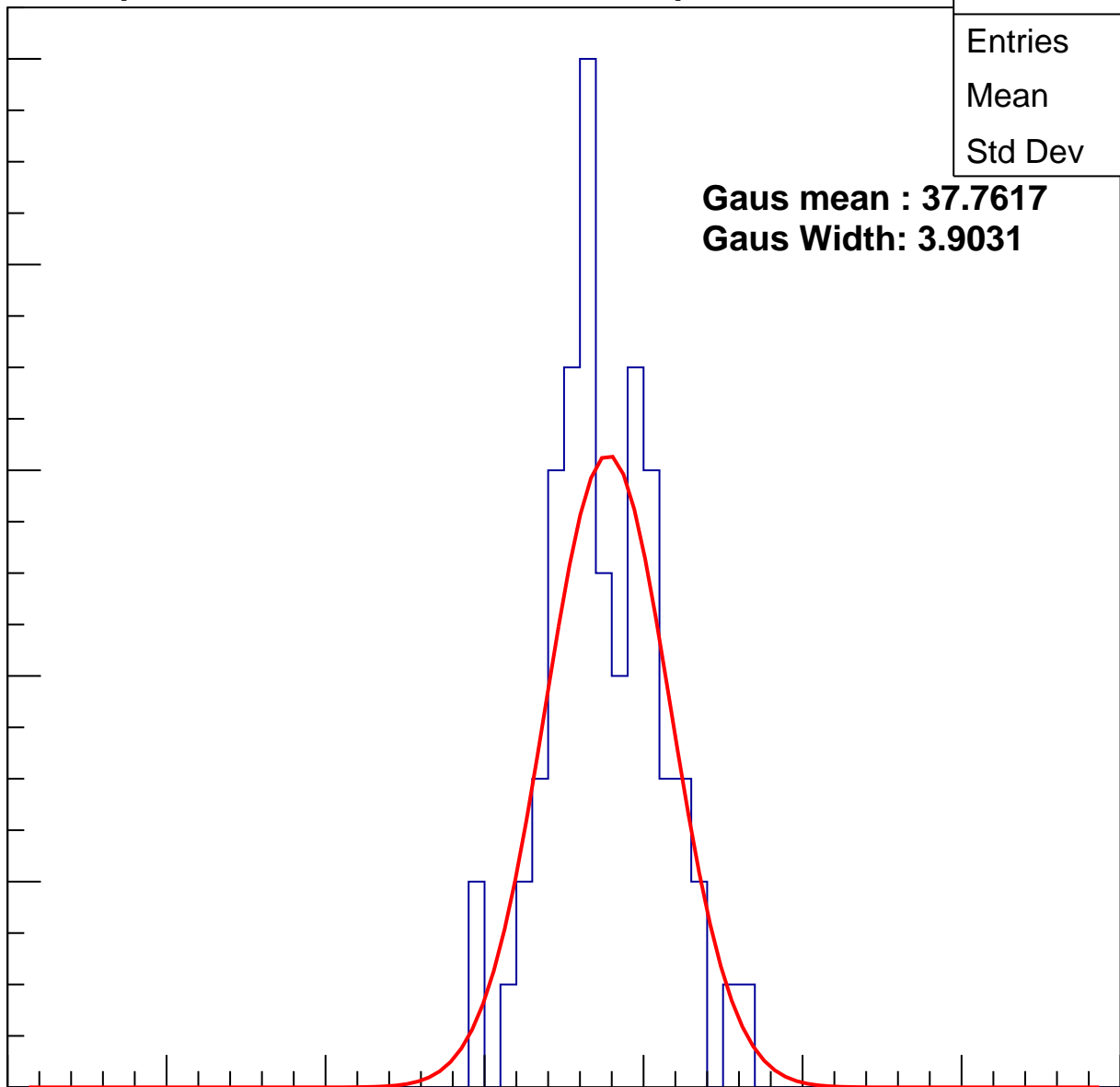
**Gaus Width: 3.9031**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch116, adc2

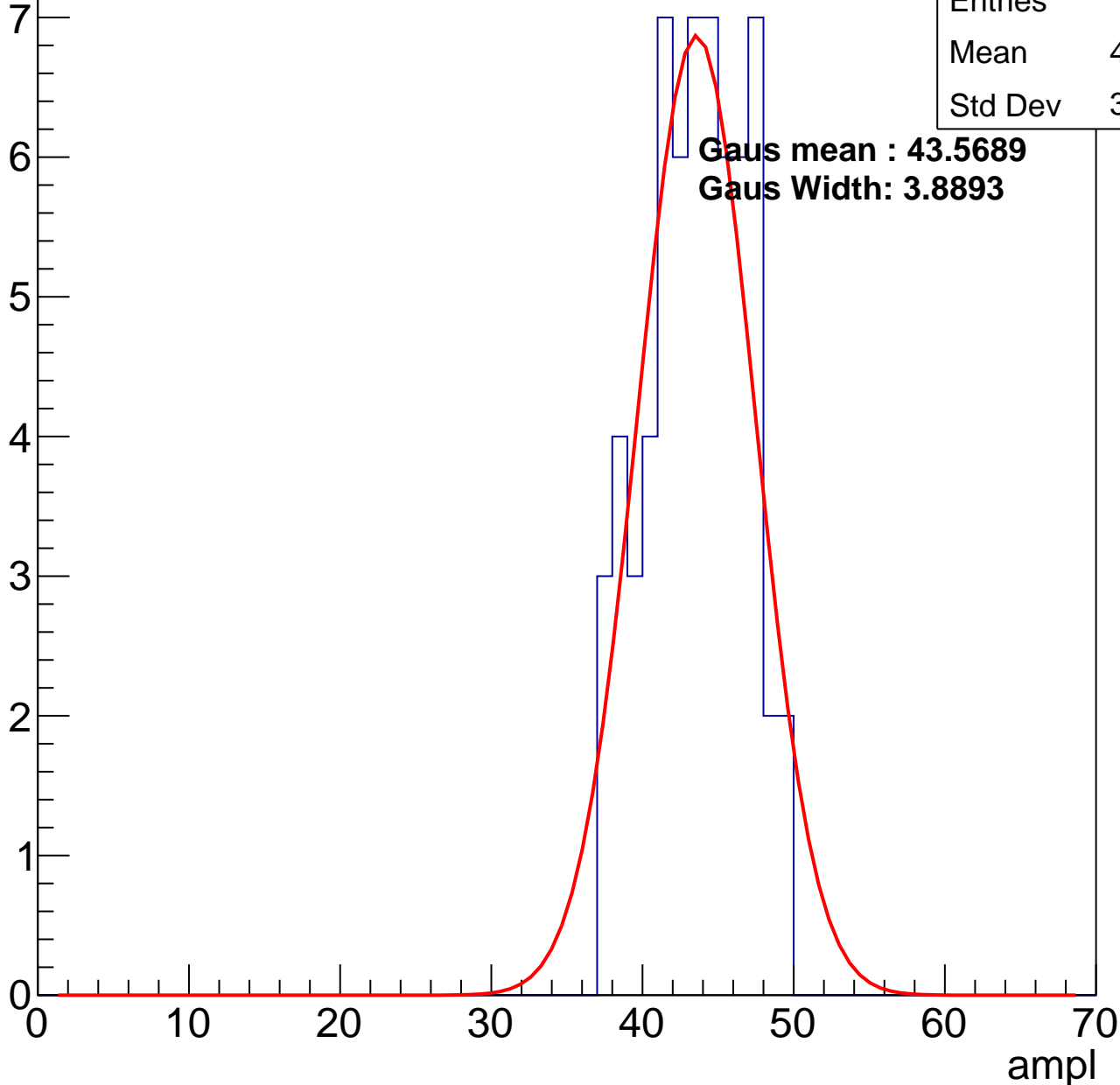
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	43.08
Std Dev	3.174

**Gaus mean : 43.5689**

**Gaus Width: 3.8893**

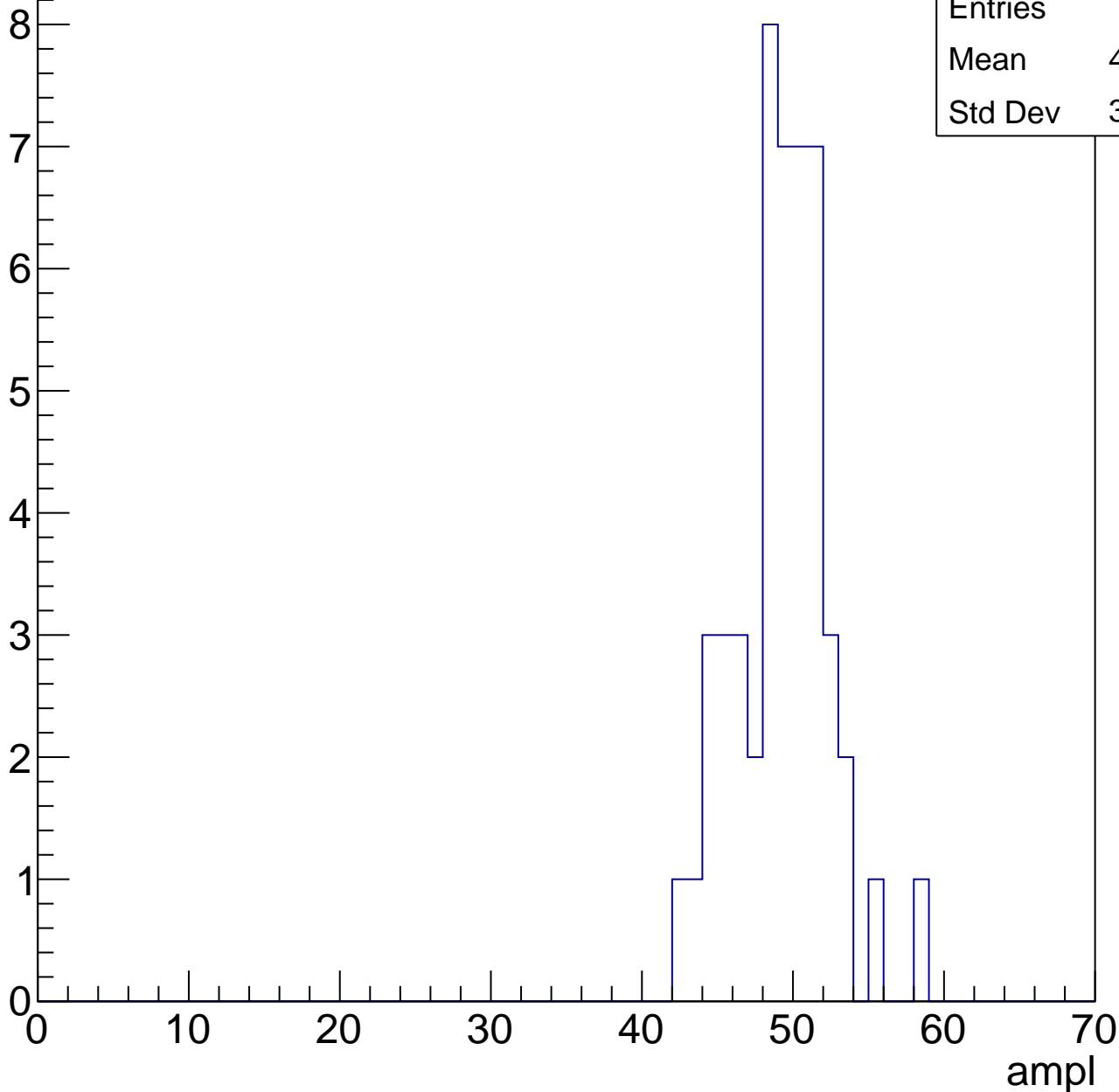


# B1L000S, U25-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	48.84
Std Dev	3.066

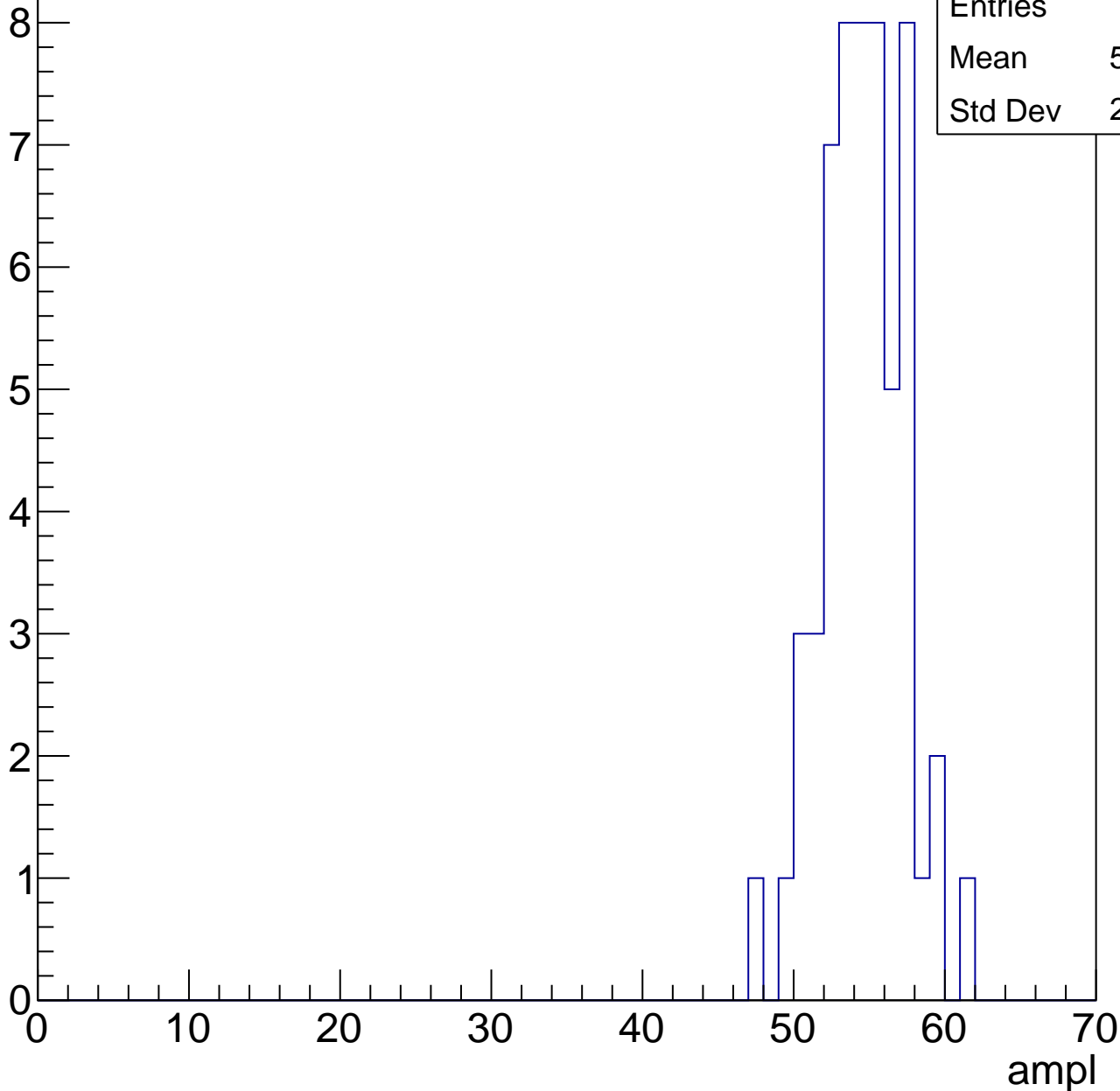


# B1L000S, U25-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	54.14
Std Dev	2.669

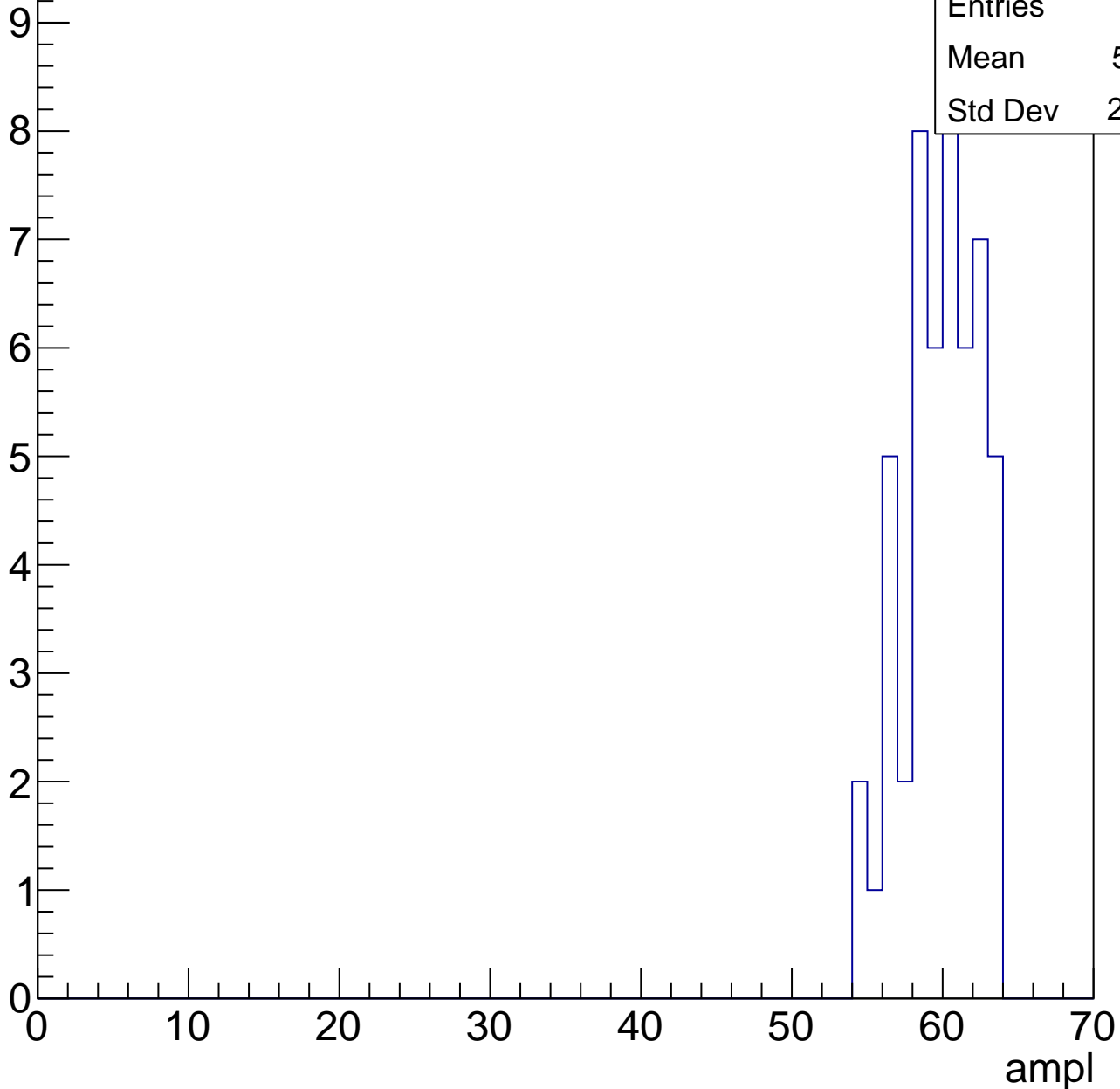


# B1L000S, U25-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	51
Mean	59.41
Std Dev	2.402

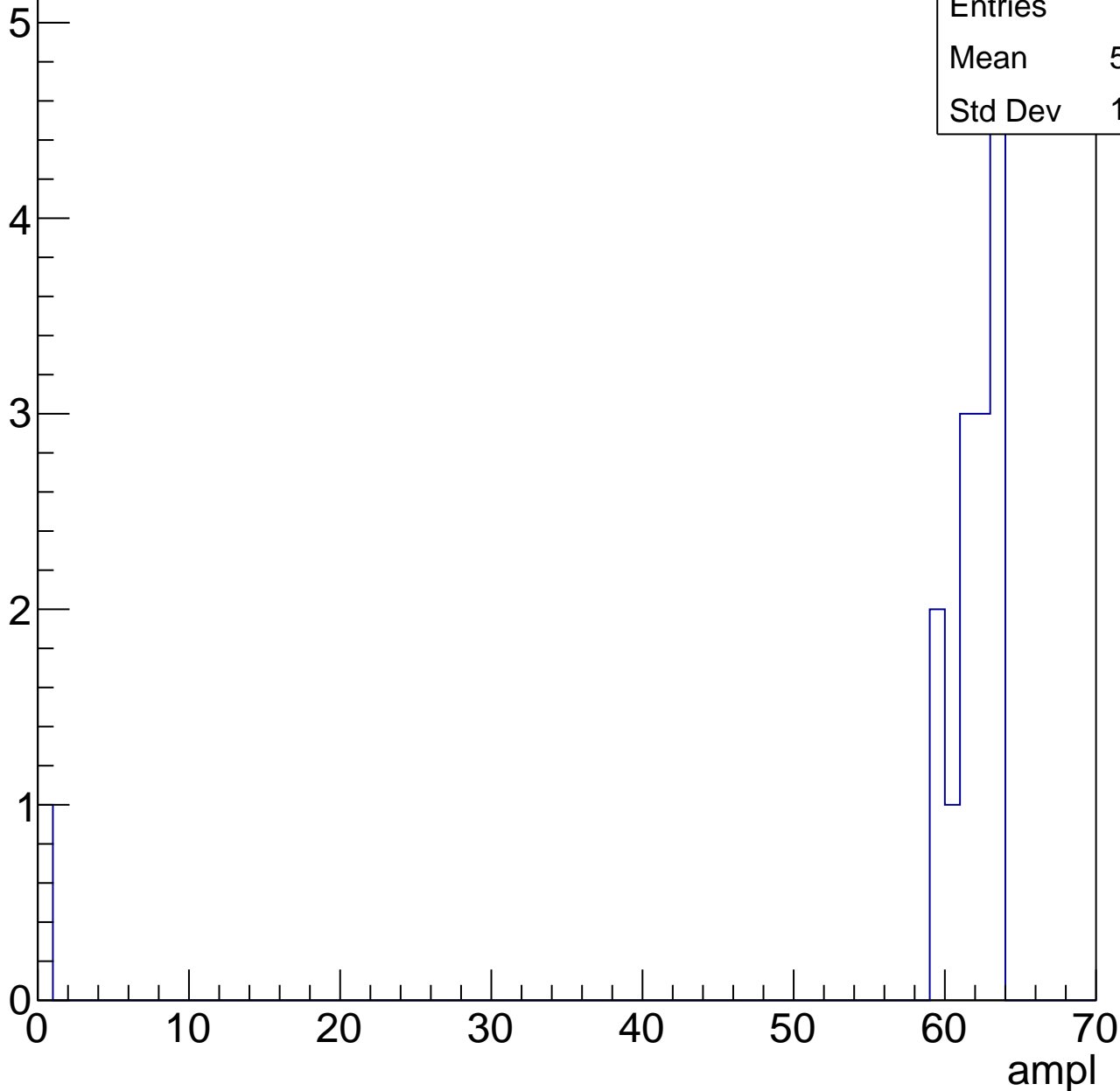


# B1L000S, U25-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	15
Mean	57.47
Std Dev	15.42





# B1L000S, U25-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch117, adc0

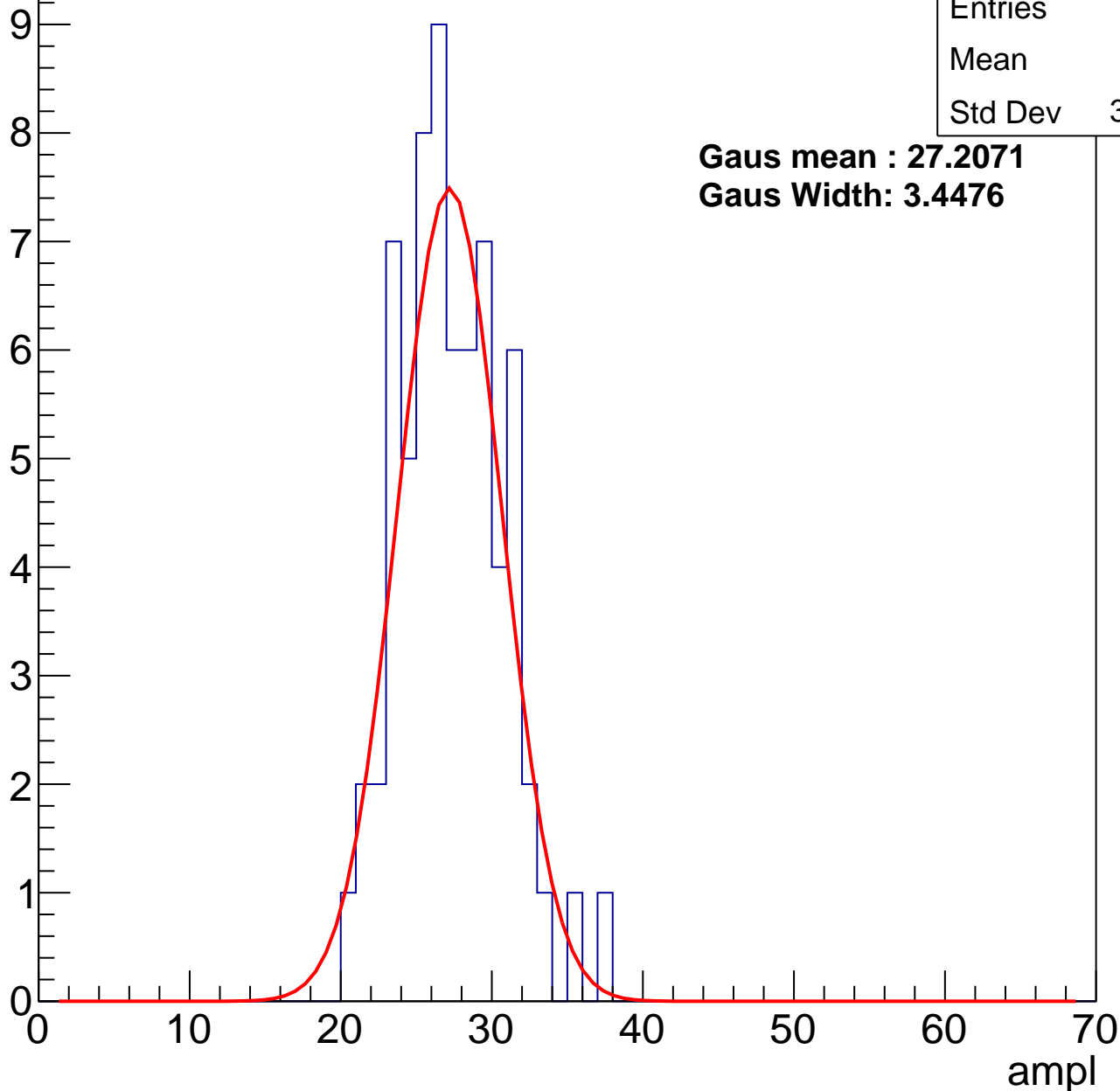
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	68
Mean	26.9
Std Dev	3.396

**Gaus mean : 27.2071**

**Gaus Width: 3.4476**



# B1L000S, U25-ch117, adc1

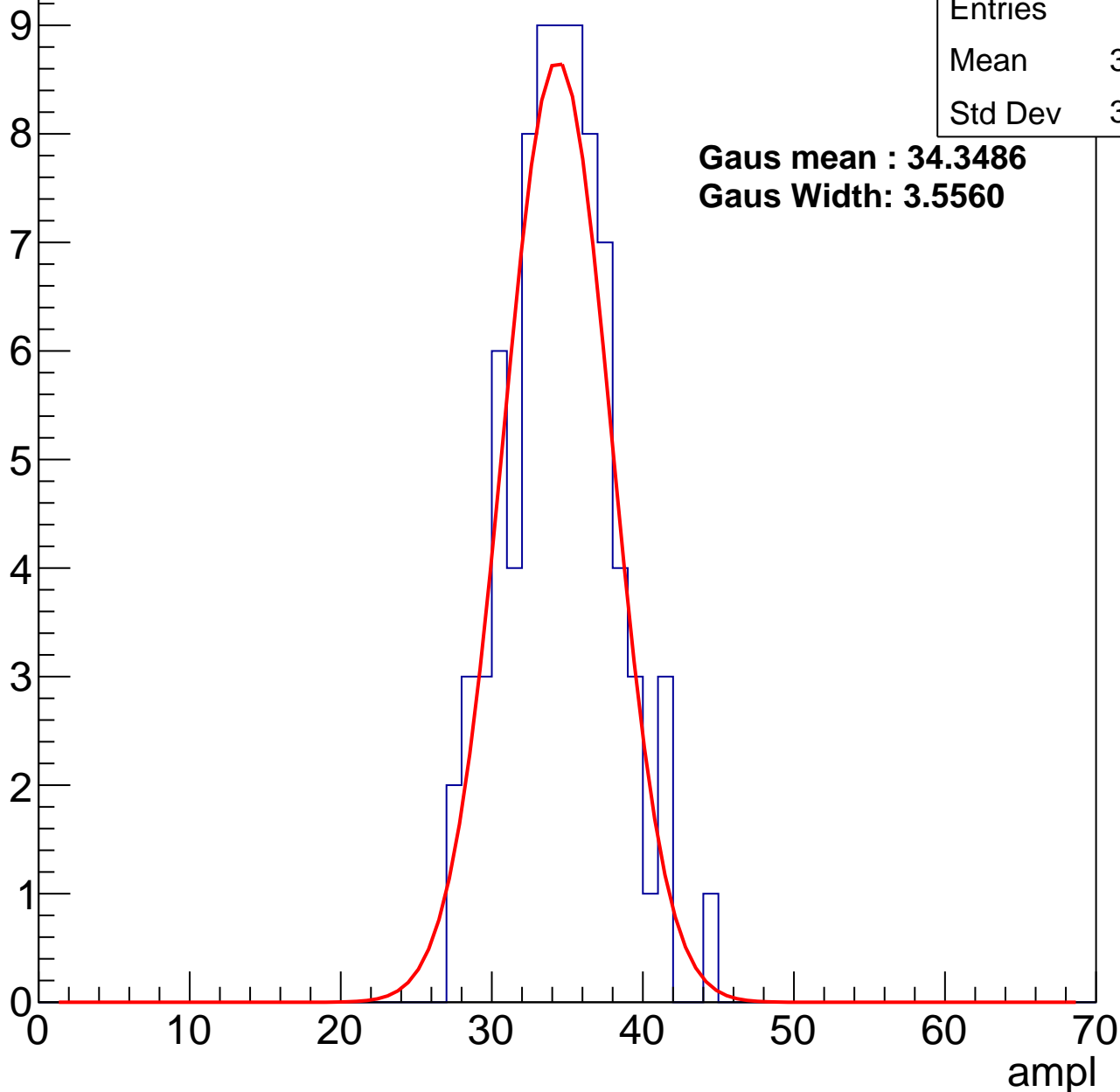
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	34.08
Std Dev	3.499

**Gaus mean : 34.3486**

**Gaus Width: 3.5560**



# B1L000S, U25-ch117, adc2

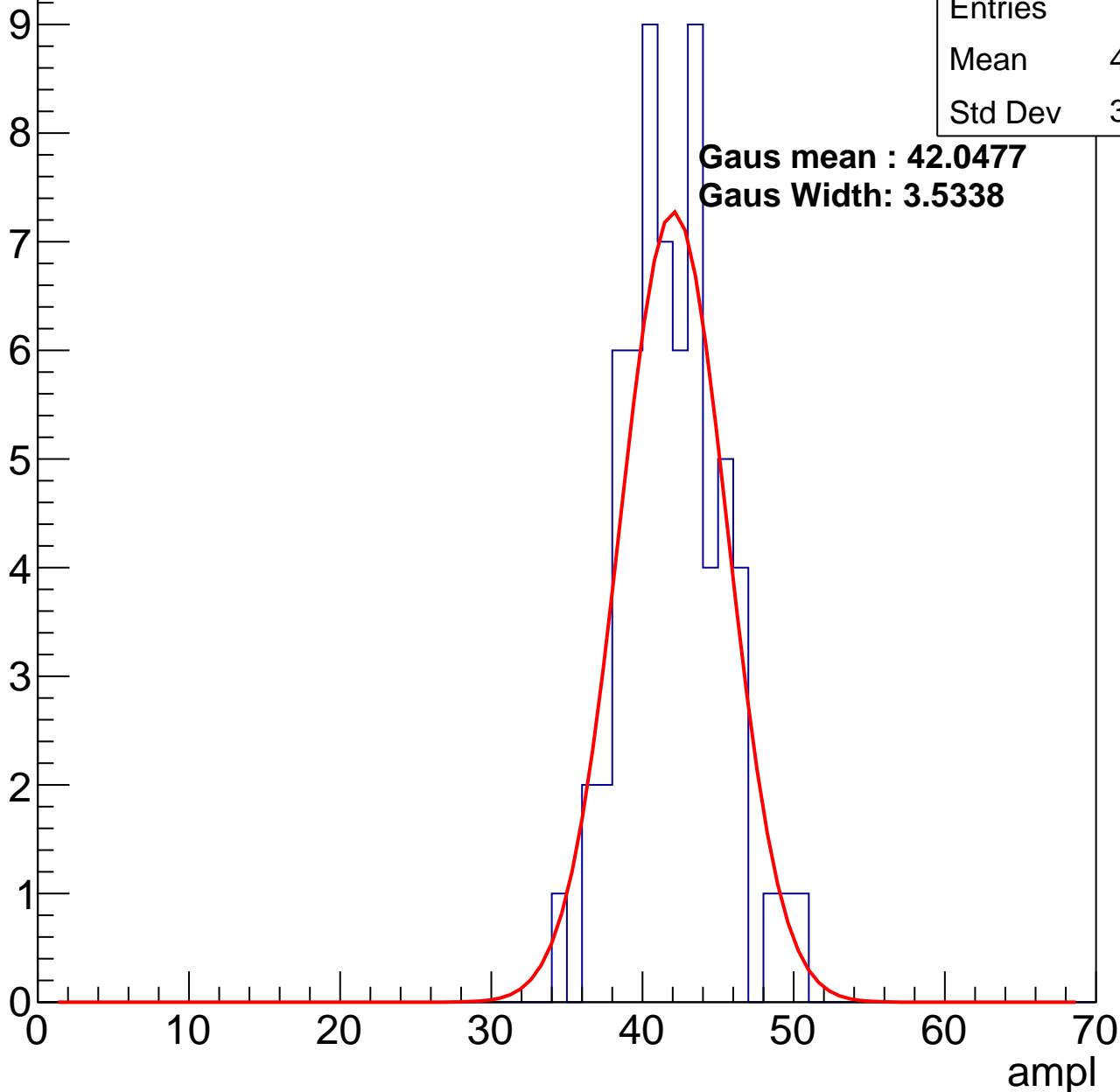
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.56
Std Dev	3.186

**Gaus mean : 42.0477**

**Gaus Width: 3.5338**

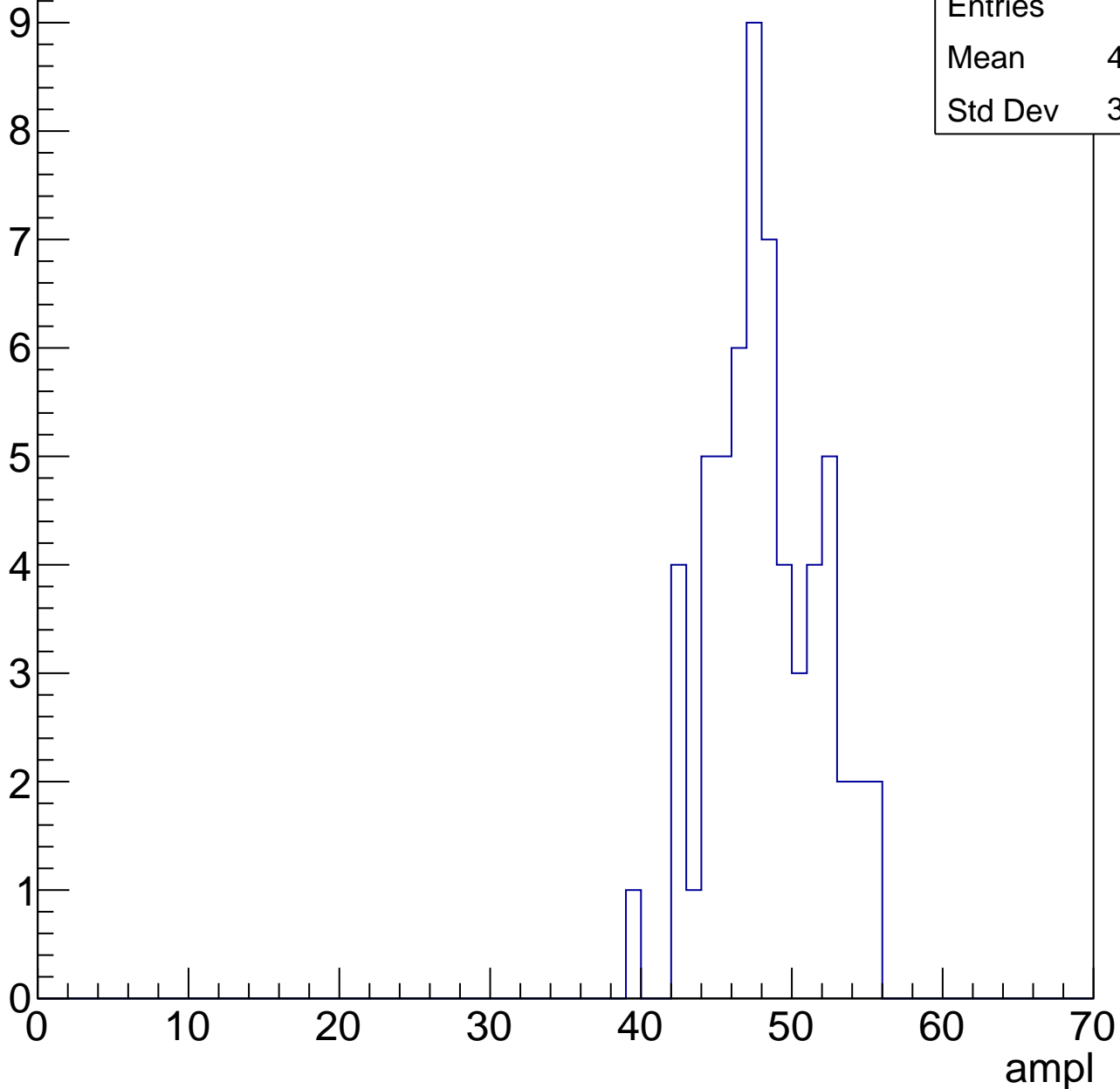


# B1L000S, U25-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	47.73
Std Dev	3.549

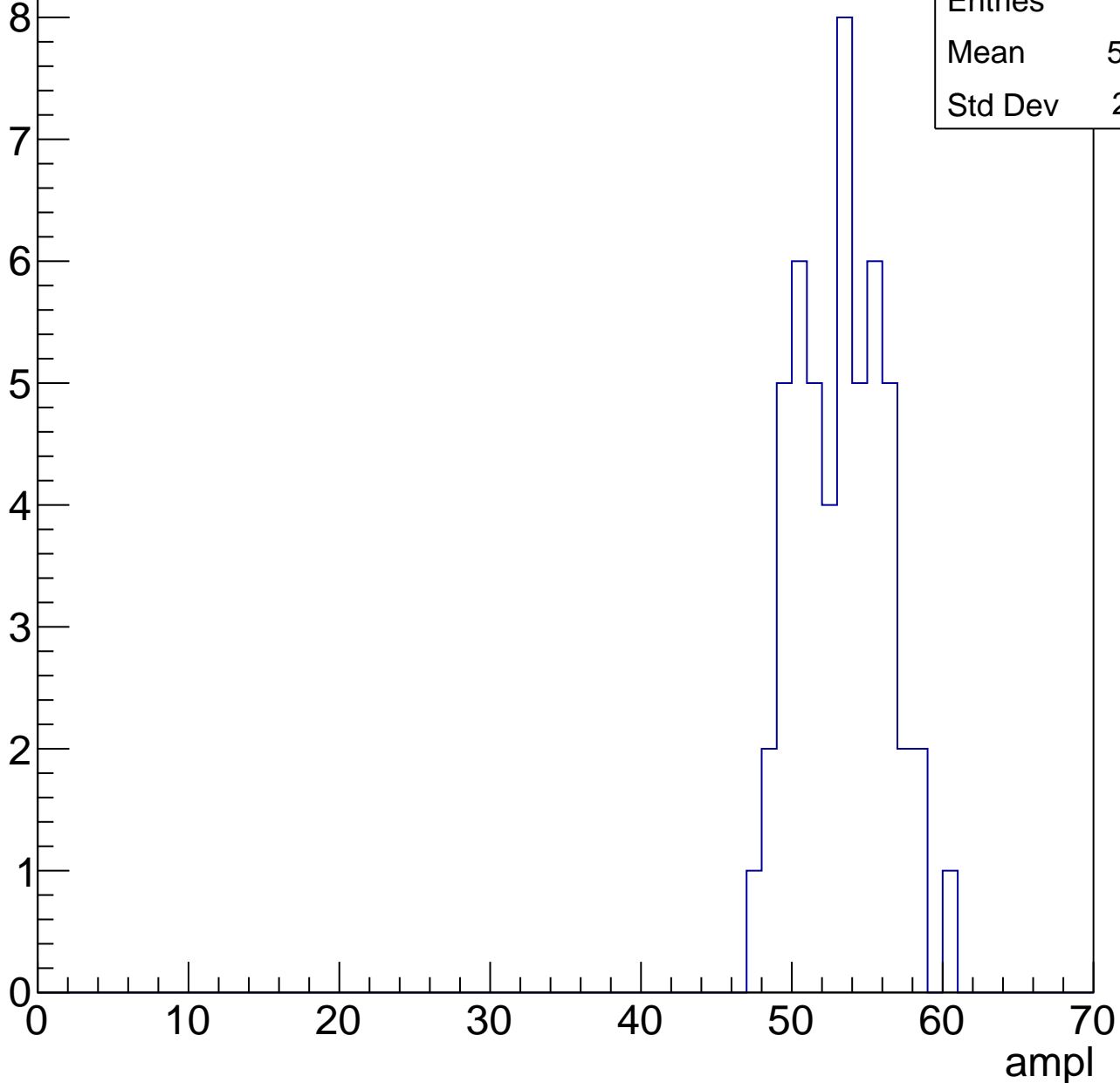


# B1L000S, U25-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	52.79
Std Dev	2.931



# B1L000S, U25-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	55
Mean	58.49
Std Dev	2.635

Entry

10

8

6

4

2

0

0

10

20

30

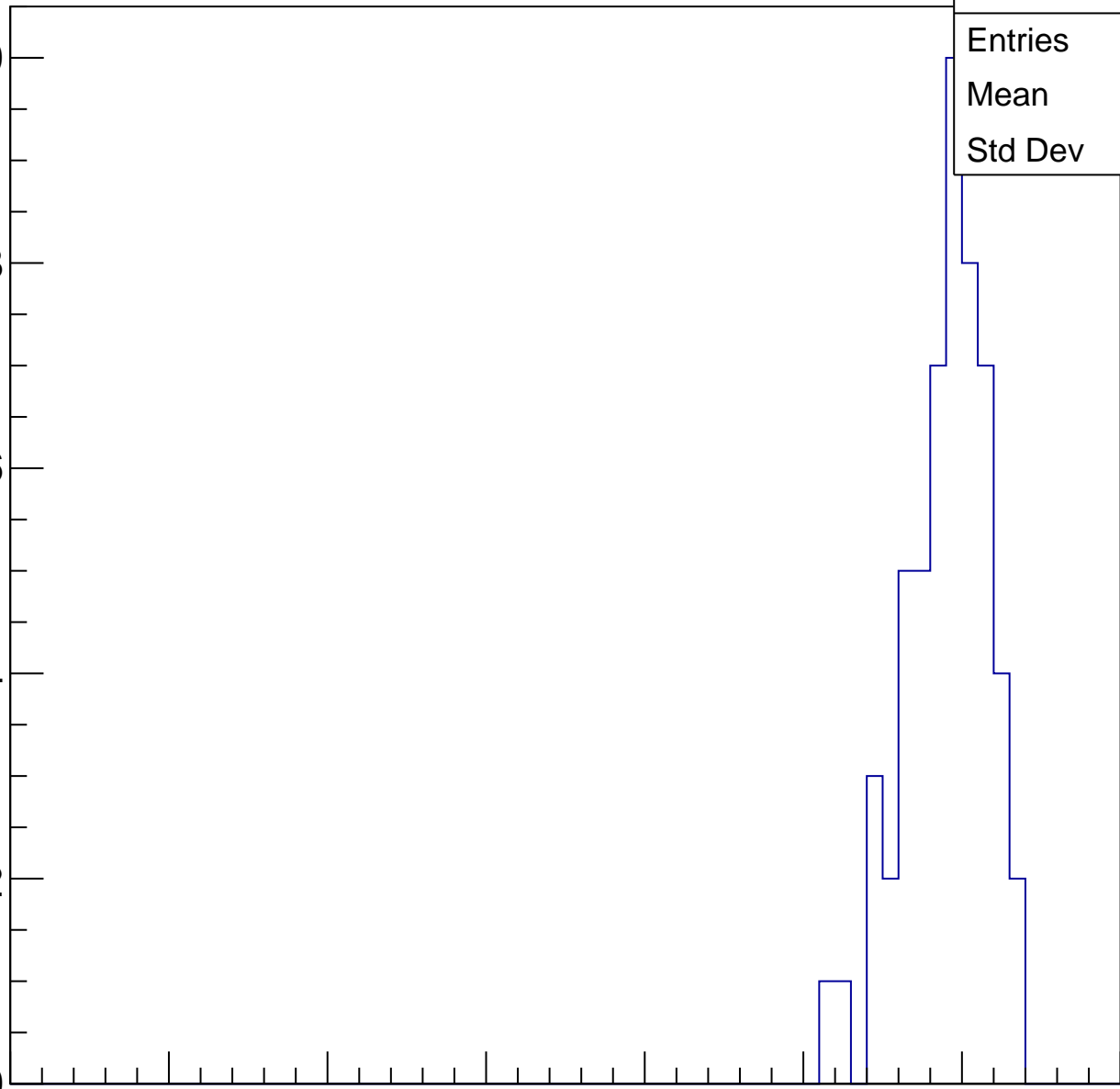
40

50

60

70

ampl

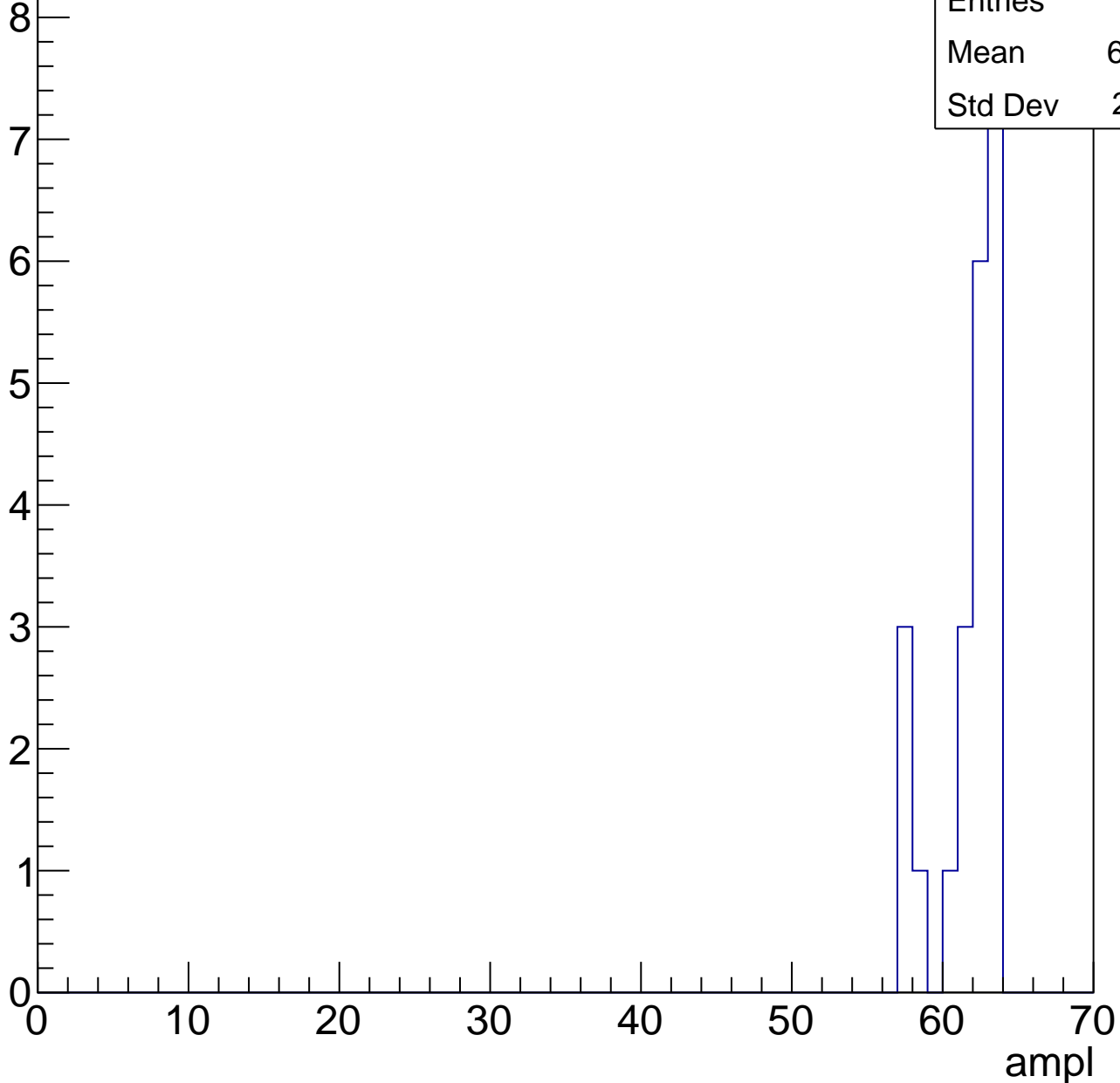


# B1L000S, U25-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	22
Mean	61.27
Std Dev	2.071





# B1L000S, U25-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch118, adc0

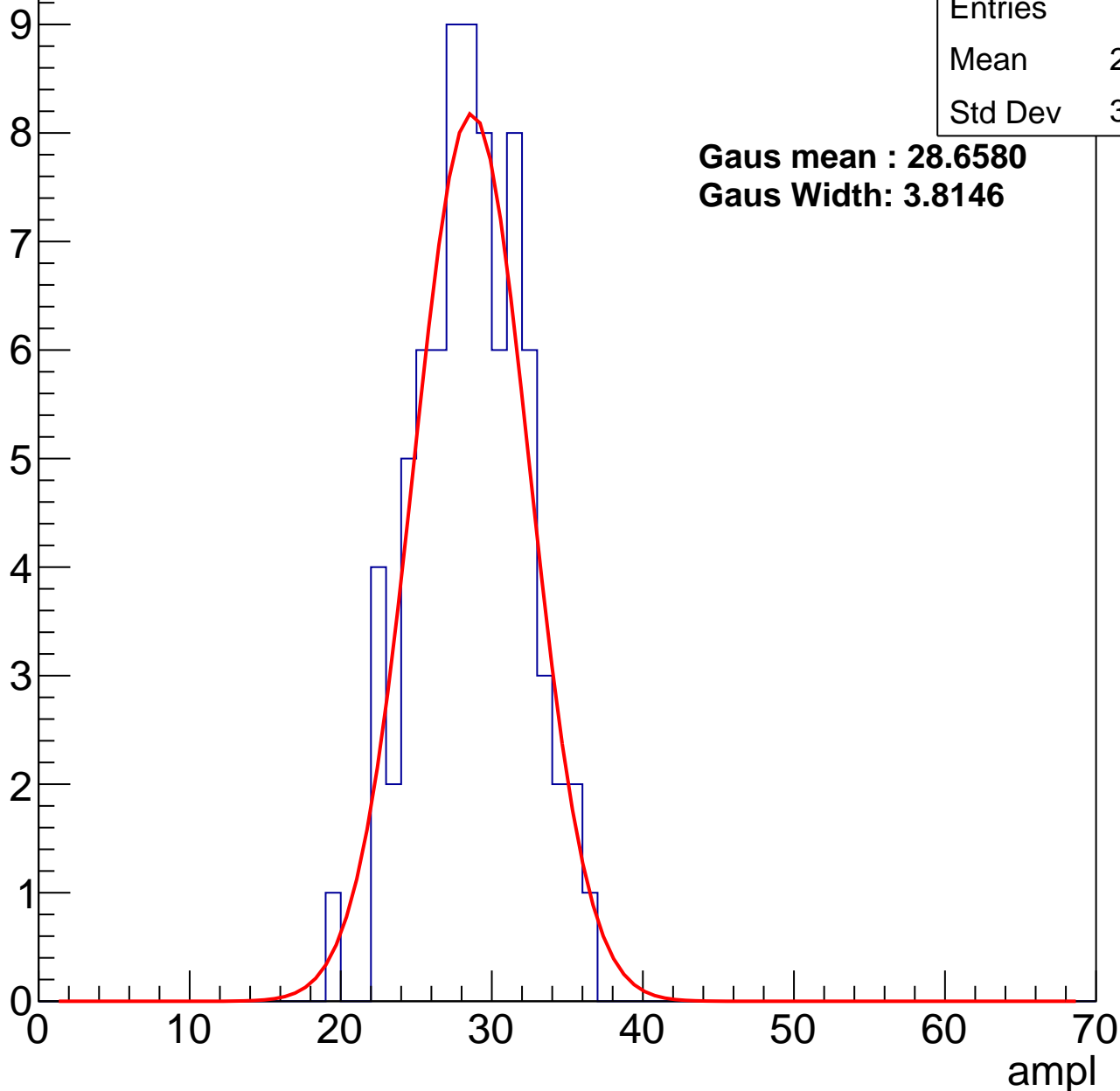
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	78
Mean	28.19
Std Dev	3.486

**Gaus mean : 28.6580**

**Gaus Width: 3.8146**



# B1L000S, U25-ch118, adc1

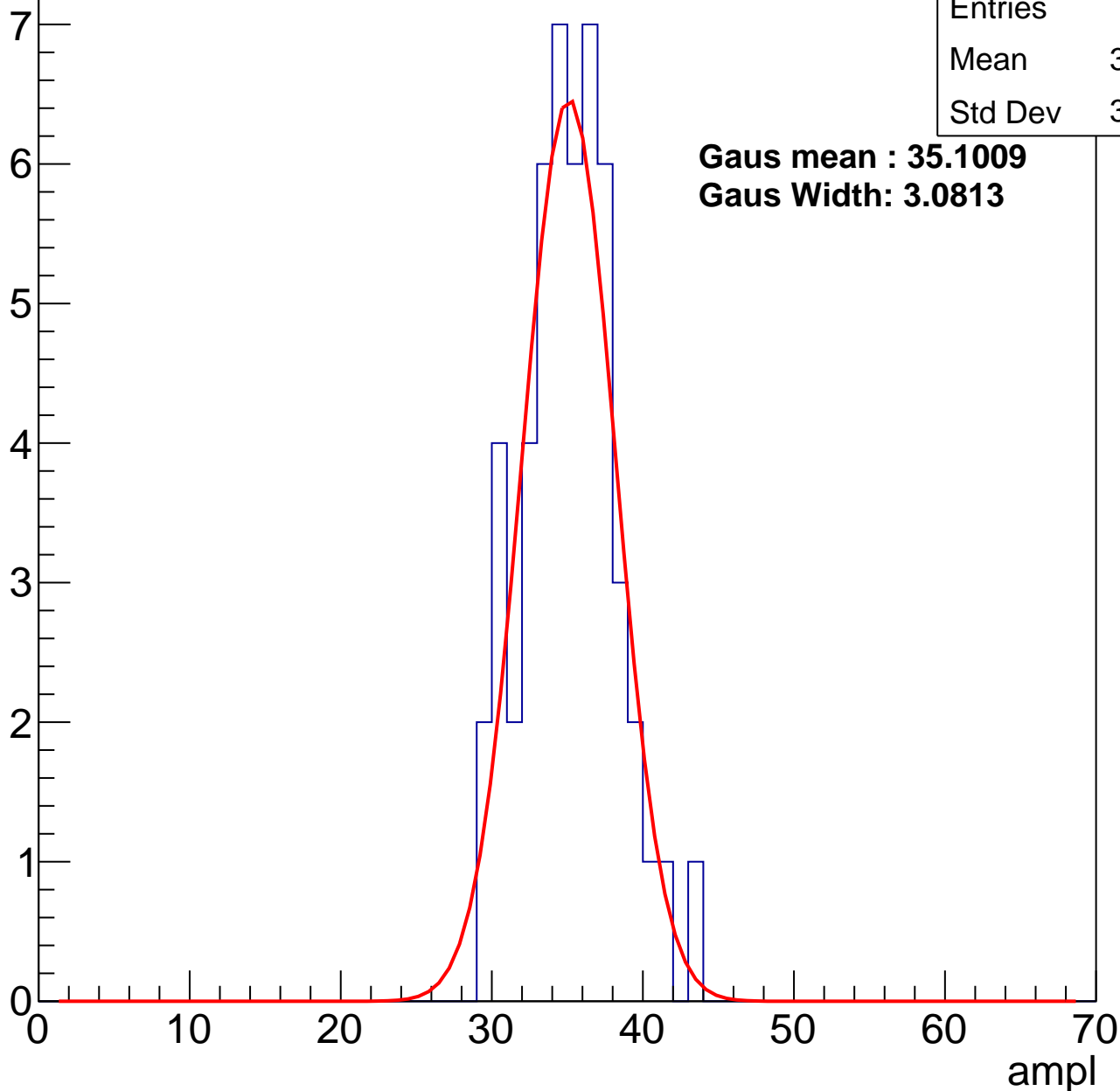
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	34.69
Std Dev	3.042

**Gaus mean : 35.1009**

**Gaus Width: 3.0813**



# B1L000S, U25-ch118, adc2

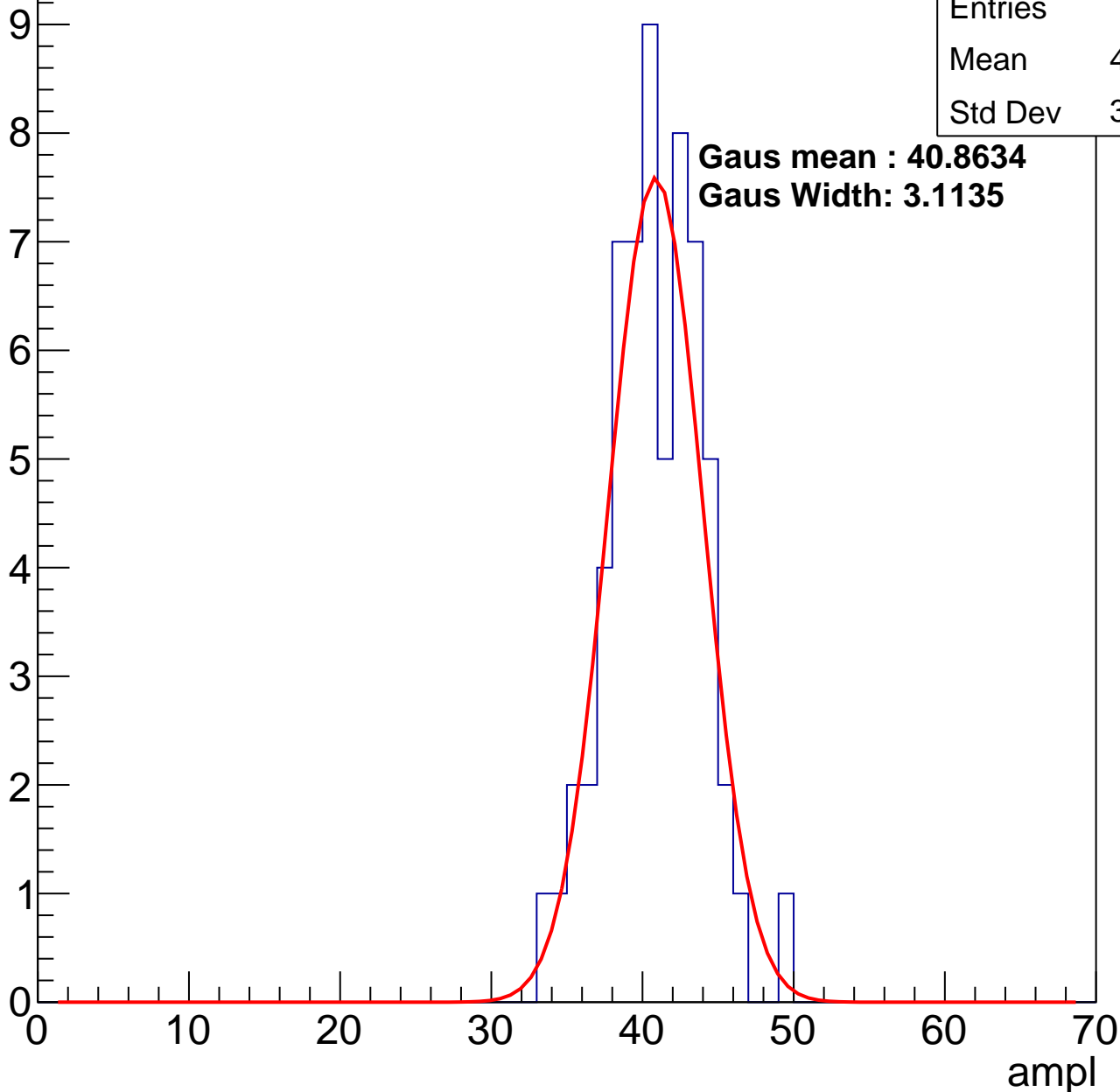
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	40.37
Std Dev	3.049

**Gaus mean : 40.8634**

**Gaus Width: 3.1135**

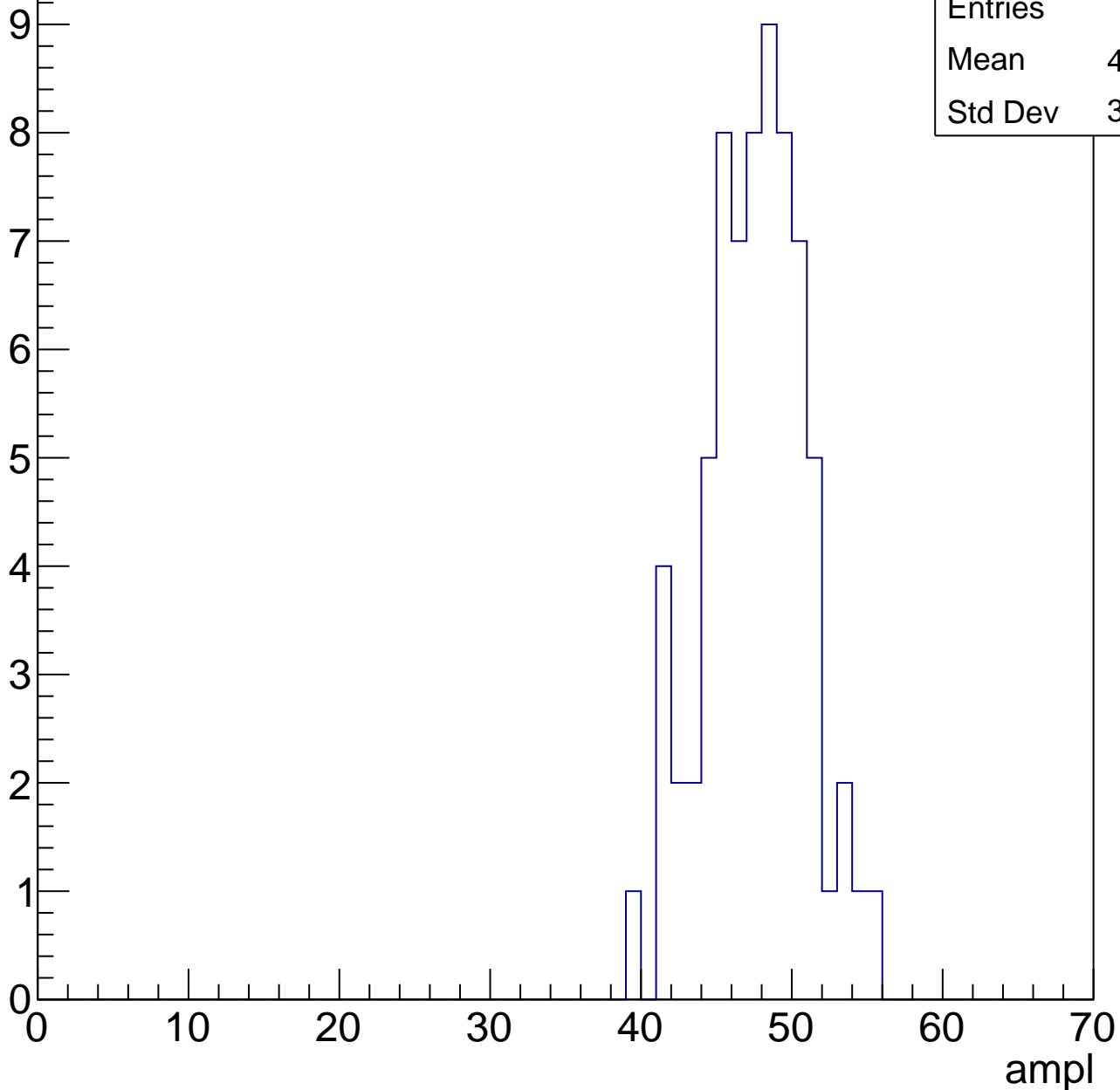


# B1L000S, U25-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	71
Mean	47.14
Std Dev	3.286

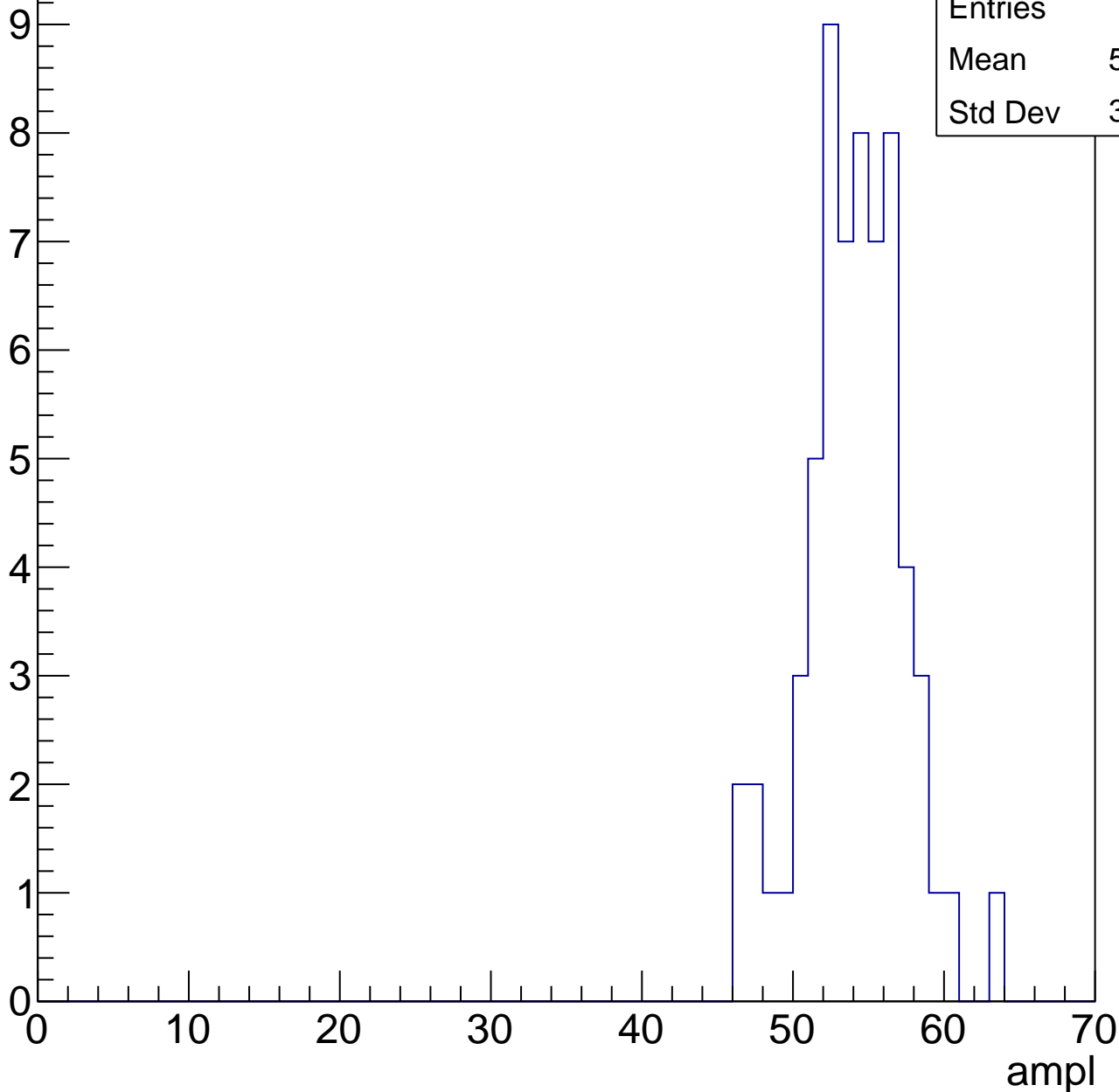


# B1L000S, U25-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	53.59
Std Dev	3.279

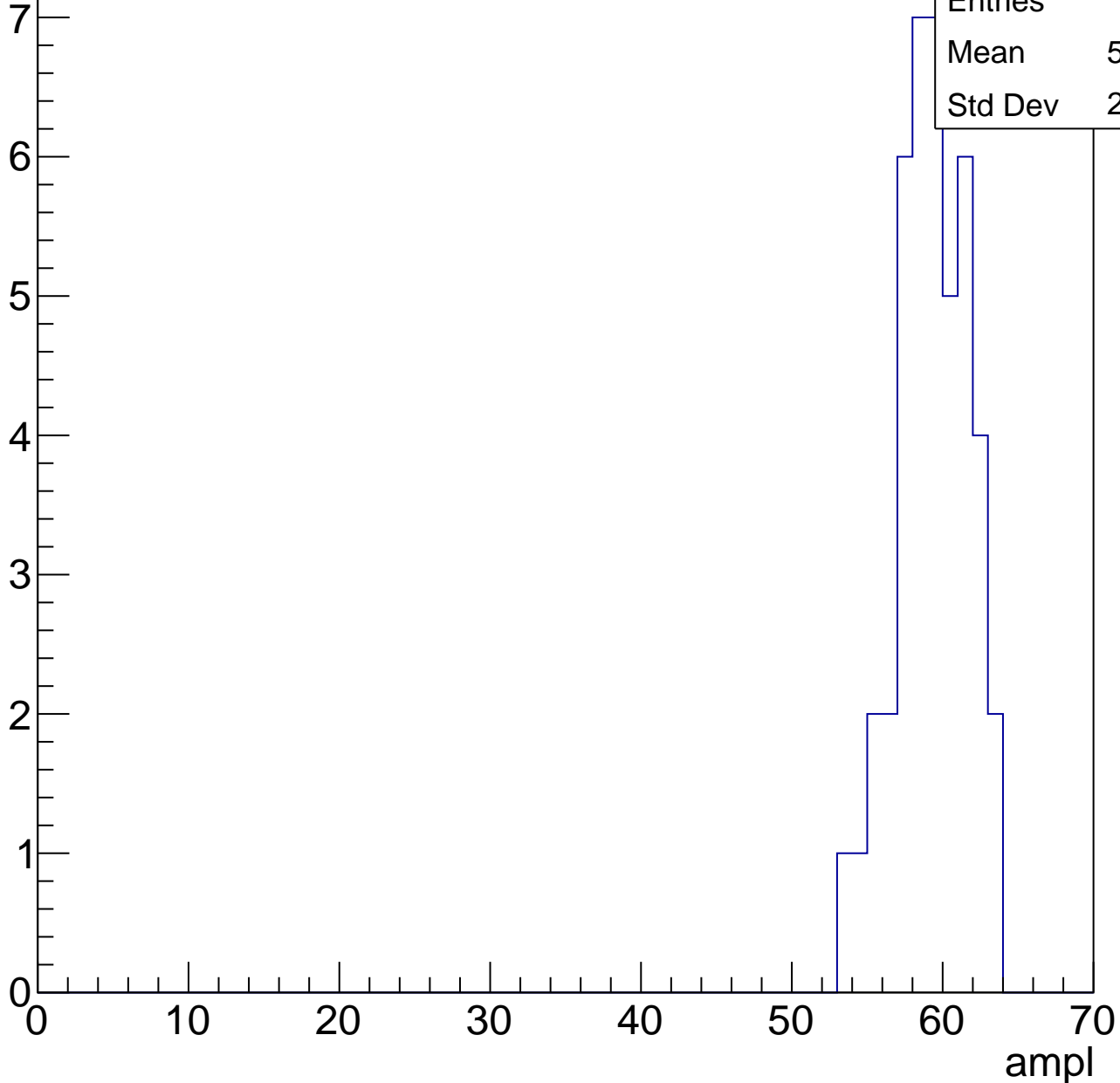


# B1L000S, U25-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	58.84
Std Dev	2.352

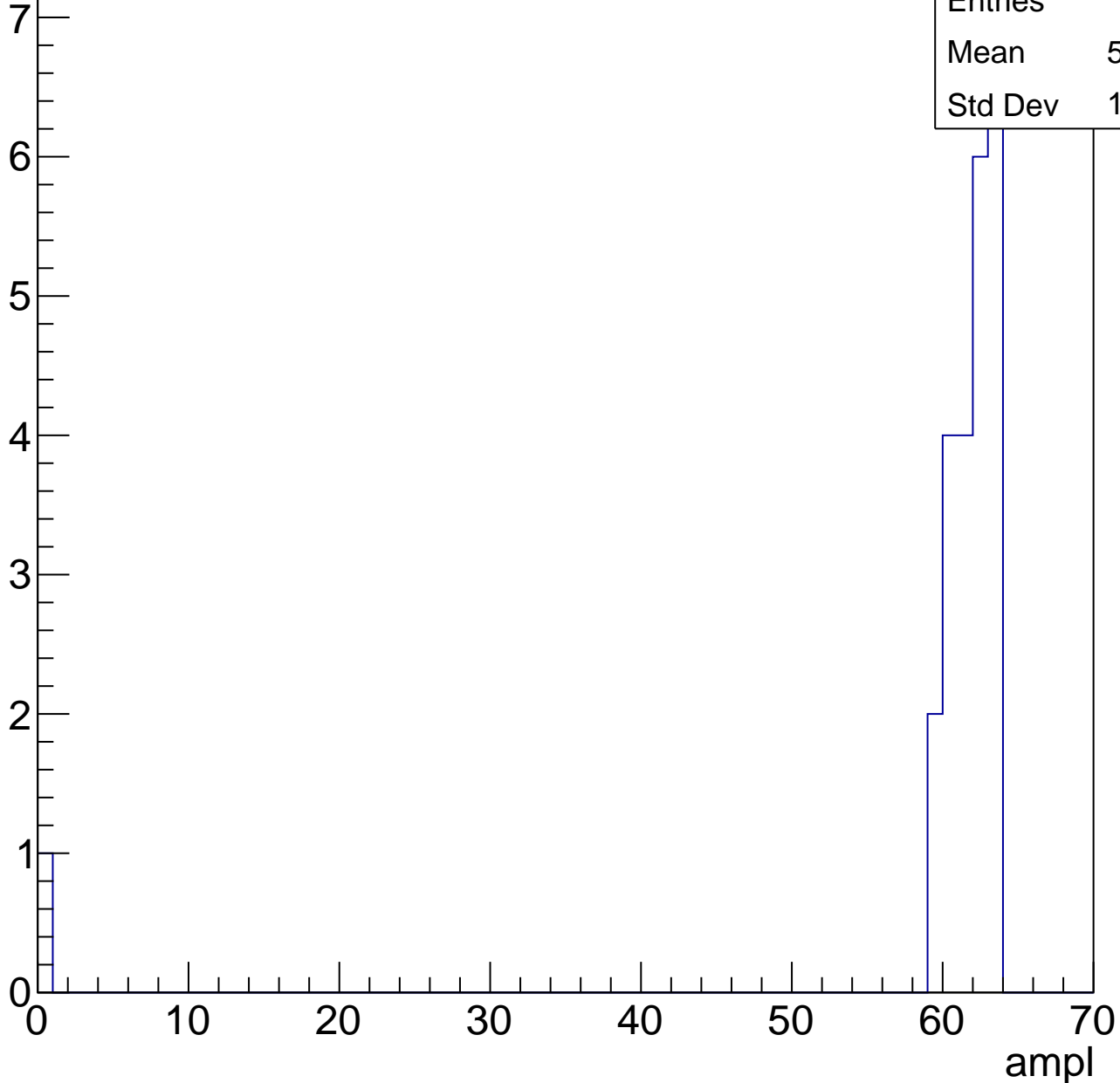


# B1L000S, U25-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	24
Mean	58.96
Std Dev	12.36

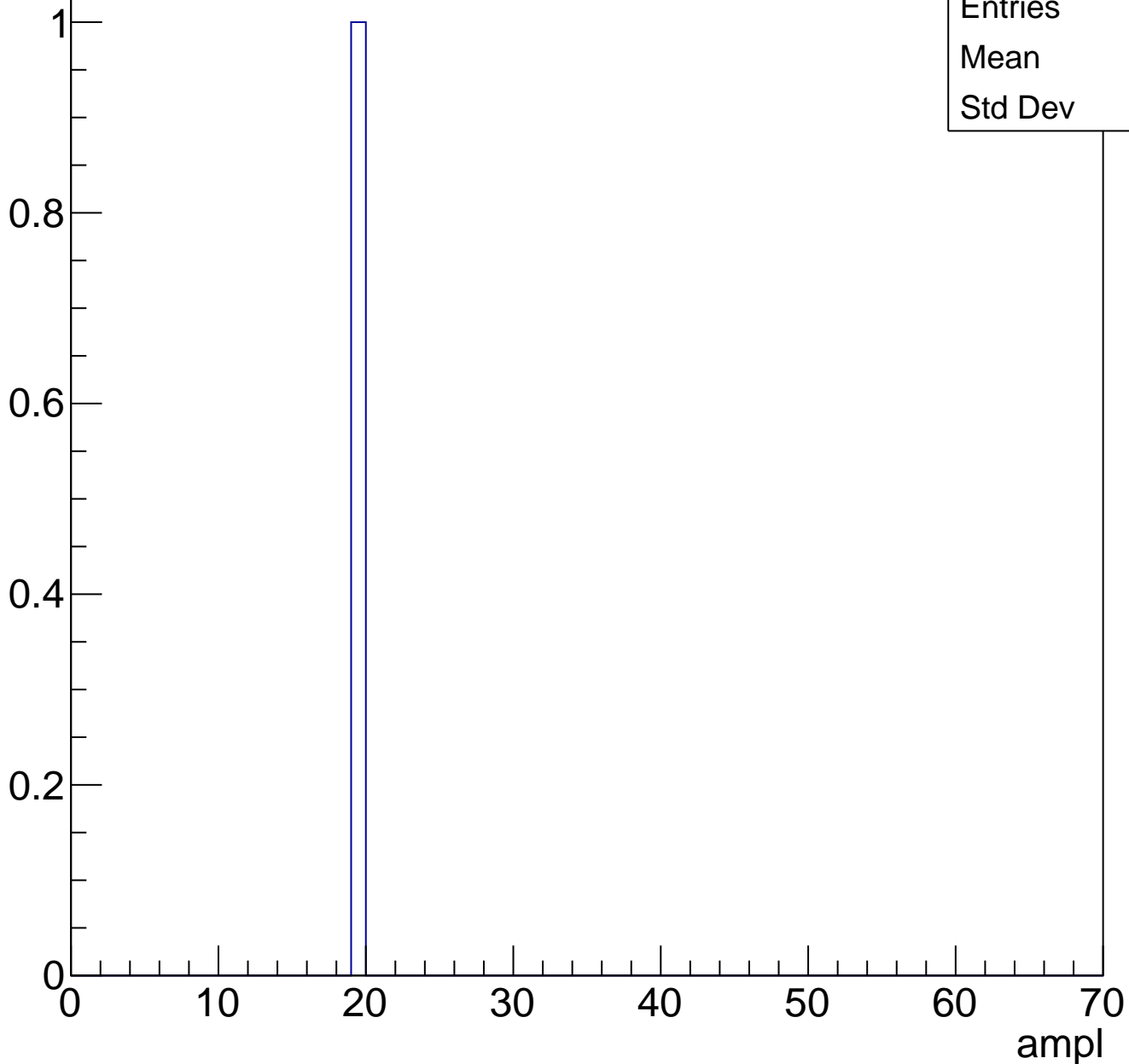




# B1L000S, U25-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	73
Mean	28.95
Std Dev	4.614

**Gaus mean : 29.7023**

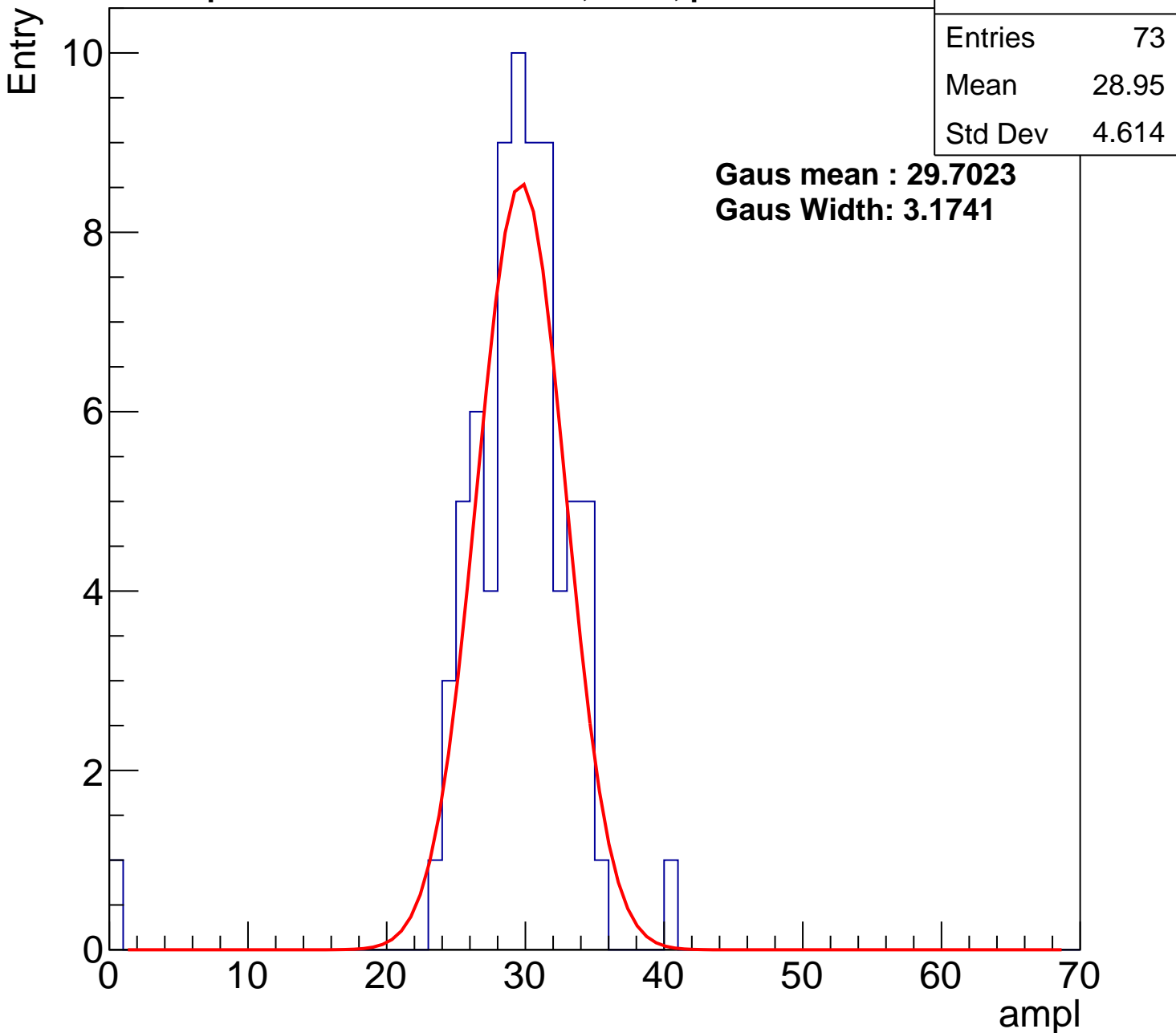
**Gaus Width: 3.1741**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	65
Mean	36.34
Std Dev	3.105

**Gaus mean : 37.0412**

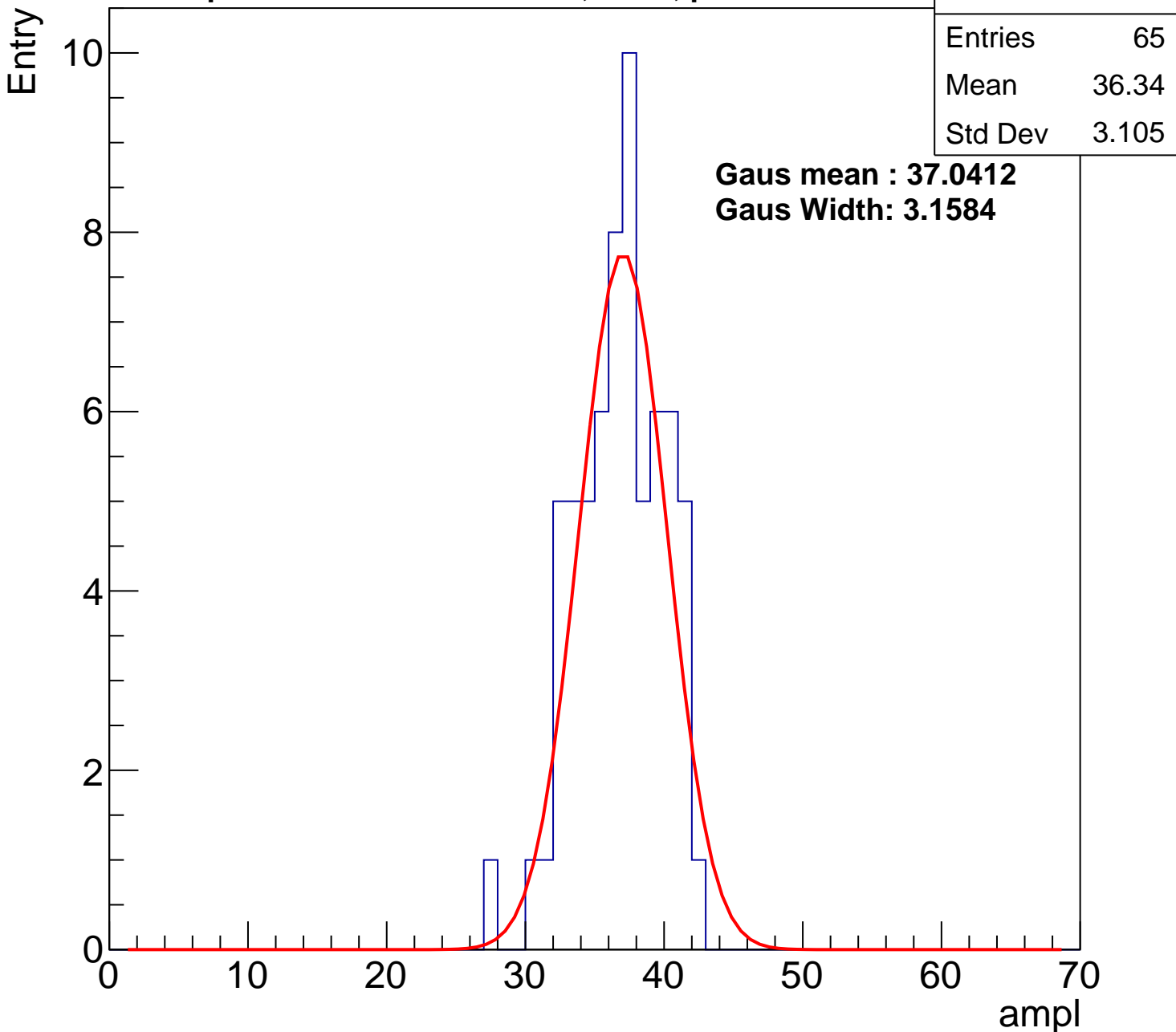
**Gaus Width: 3.1584**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch119, adc2

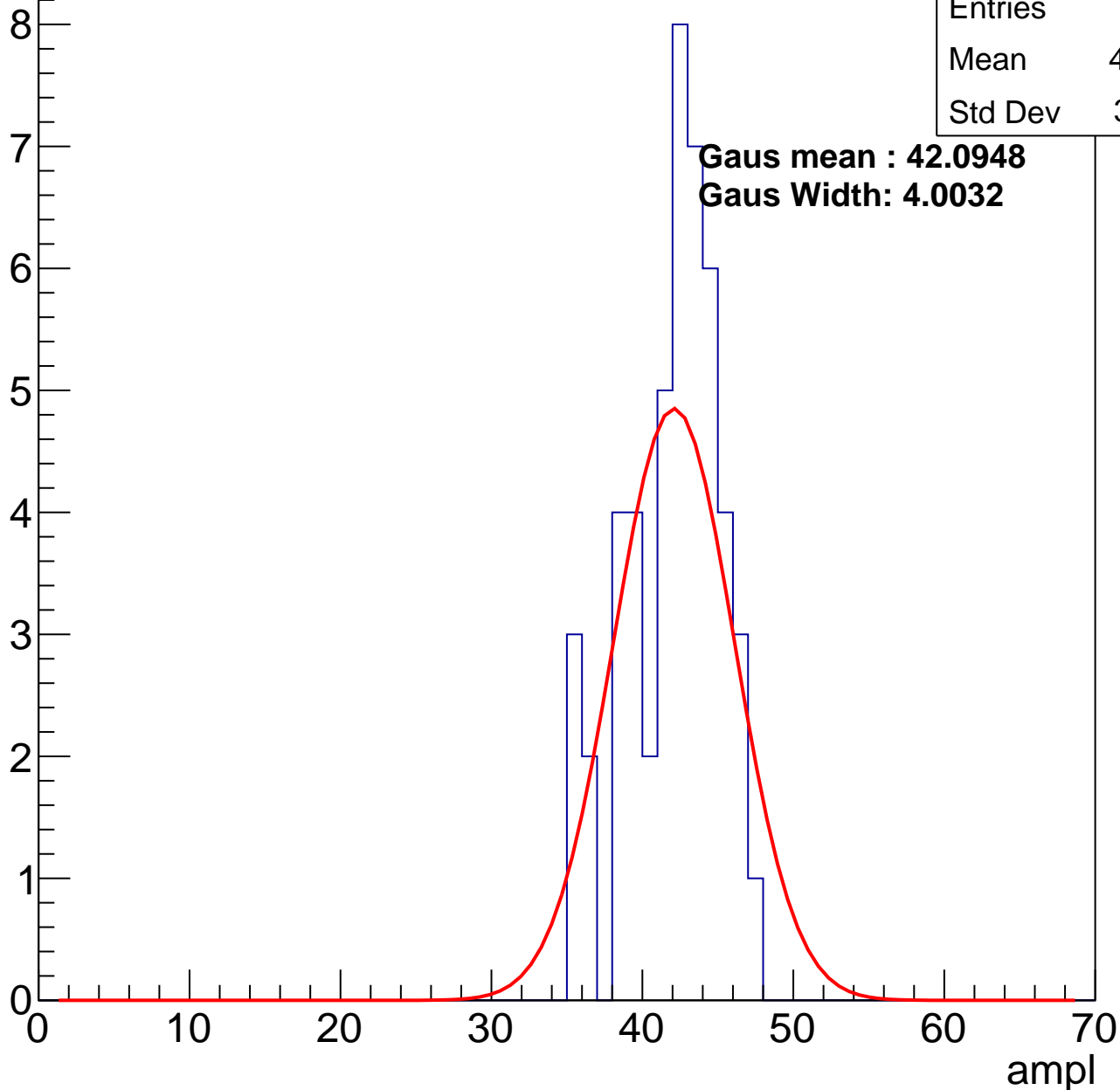
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	49
Mean	41.55
Std Dev	3.071

**Gaus mean : 42.0948**

**Gaus Width: 4.0032**

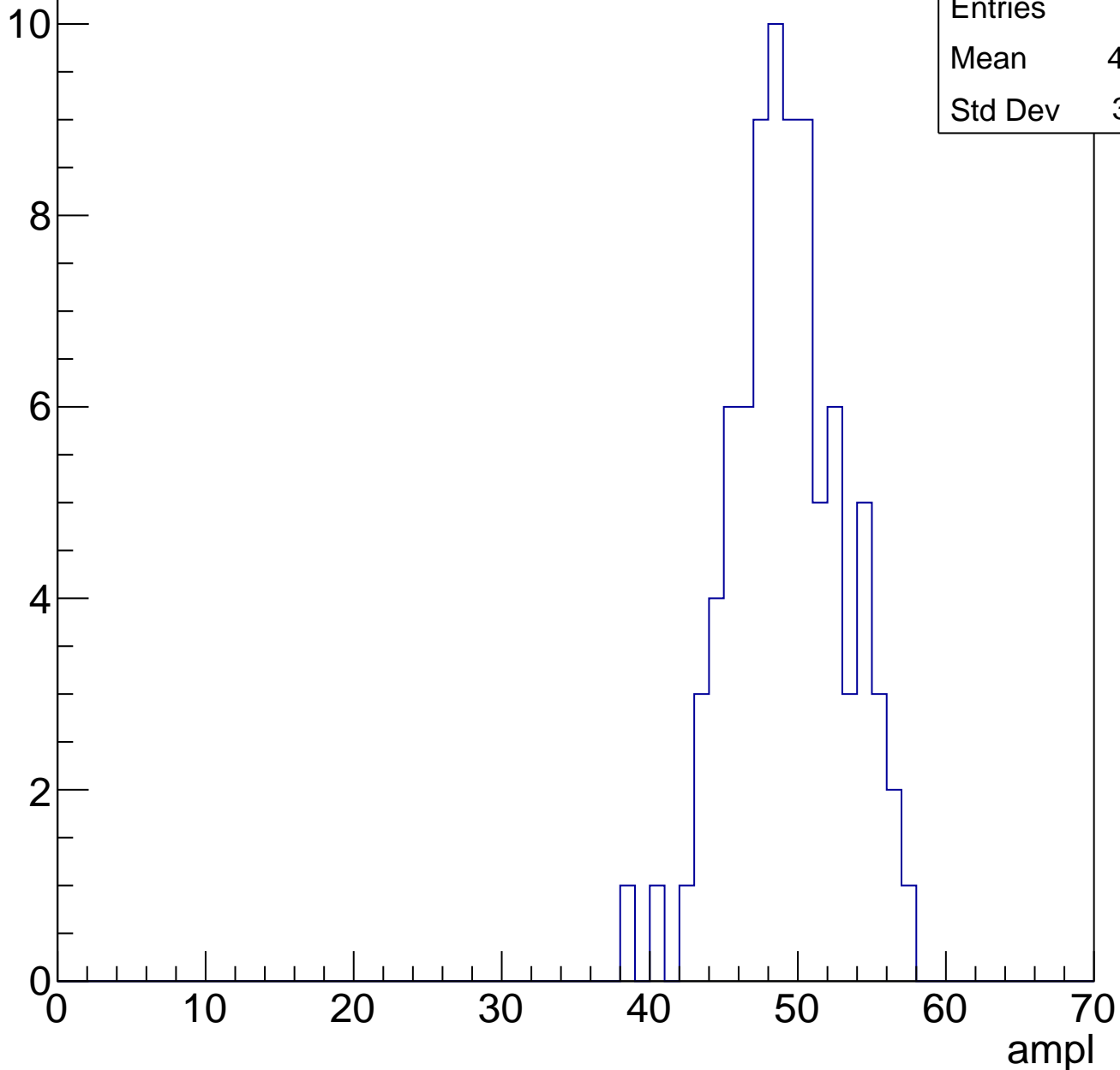


# B1L000S, U25-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	84
Mean	48.75
Std Dev	3.741

Entry

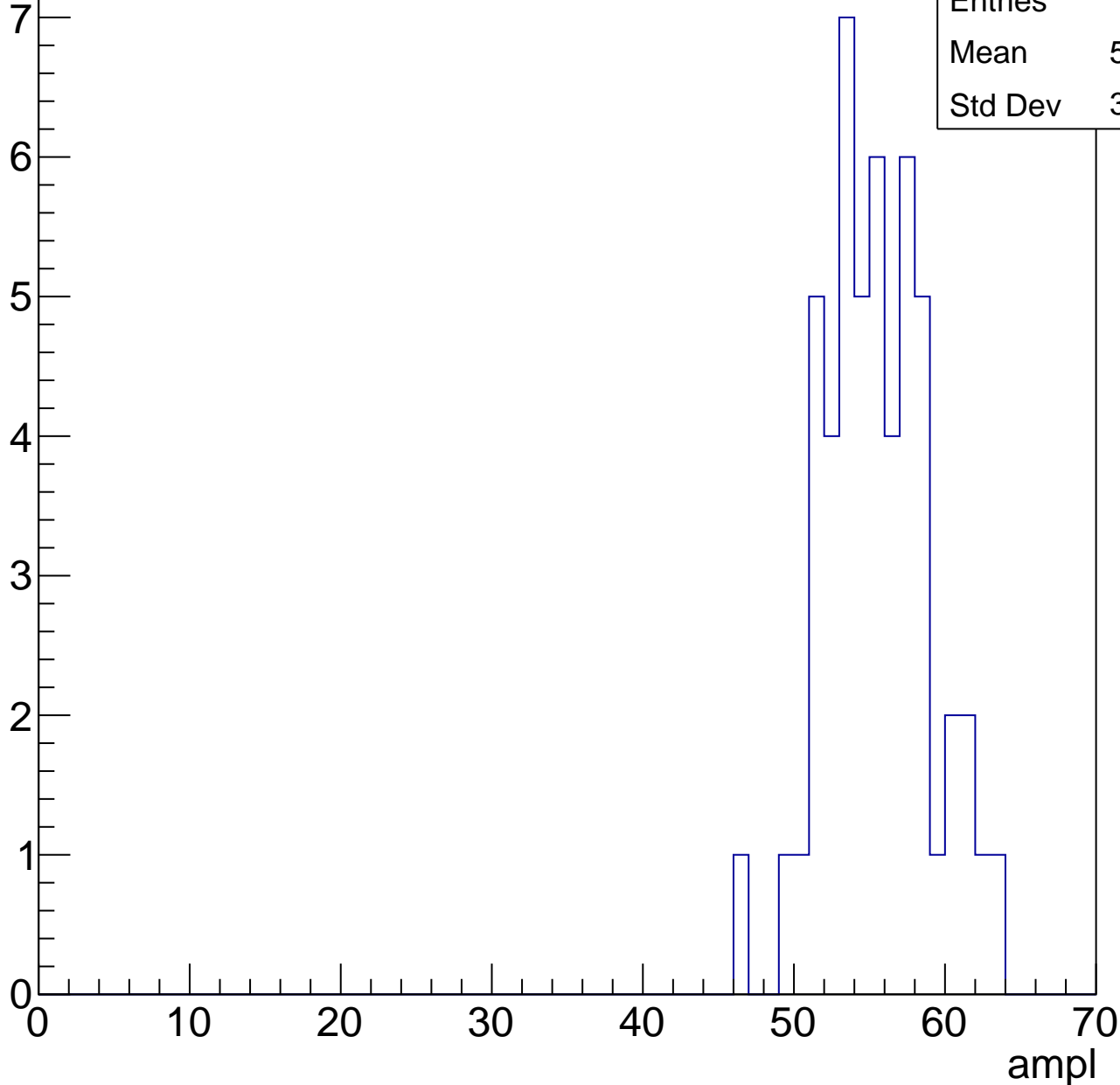


# B1L000S, U25-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	52
Mean	55.02
Std Dev	3.439



# B1L000S, U25-ch119, adc5

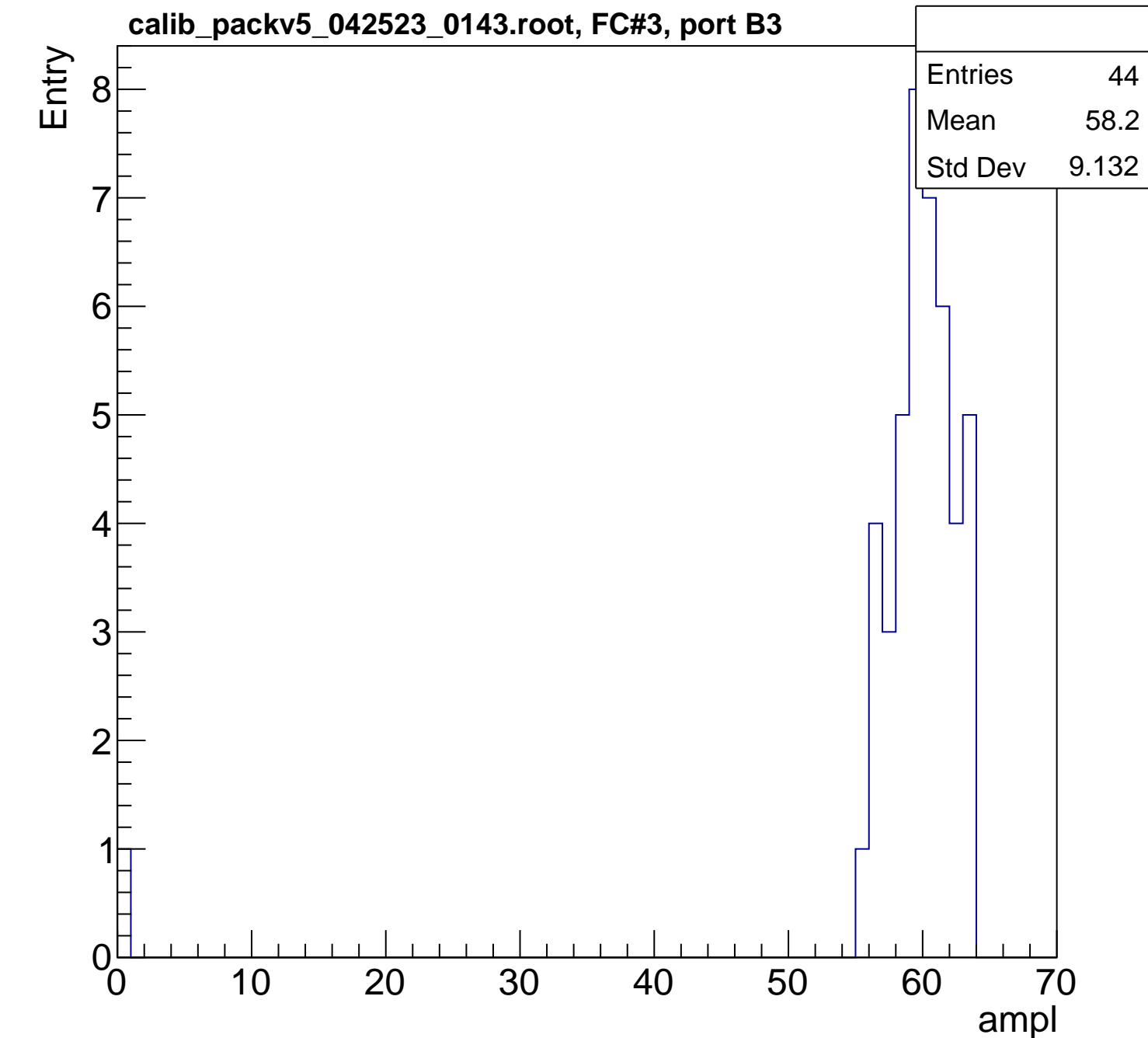
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	58.2
Std Dev	9.132

ampl

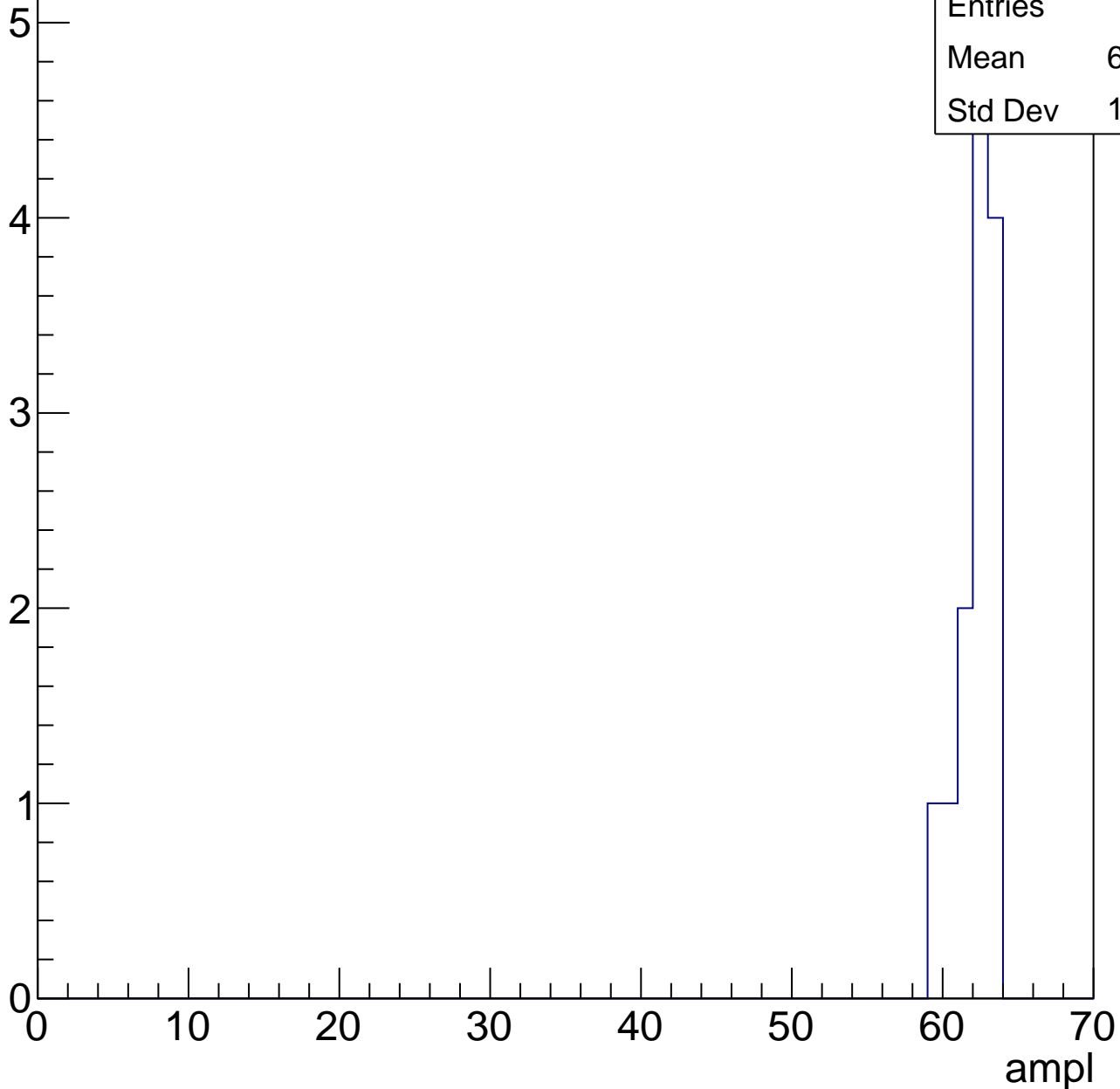


# B1L000S, U25-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	13
Mean	61.77
Std Dev	1.187





# B1L000S, U25-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch120, adc0

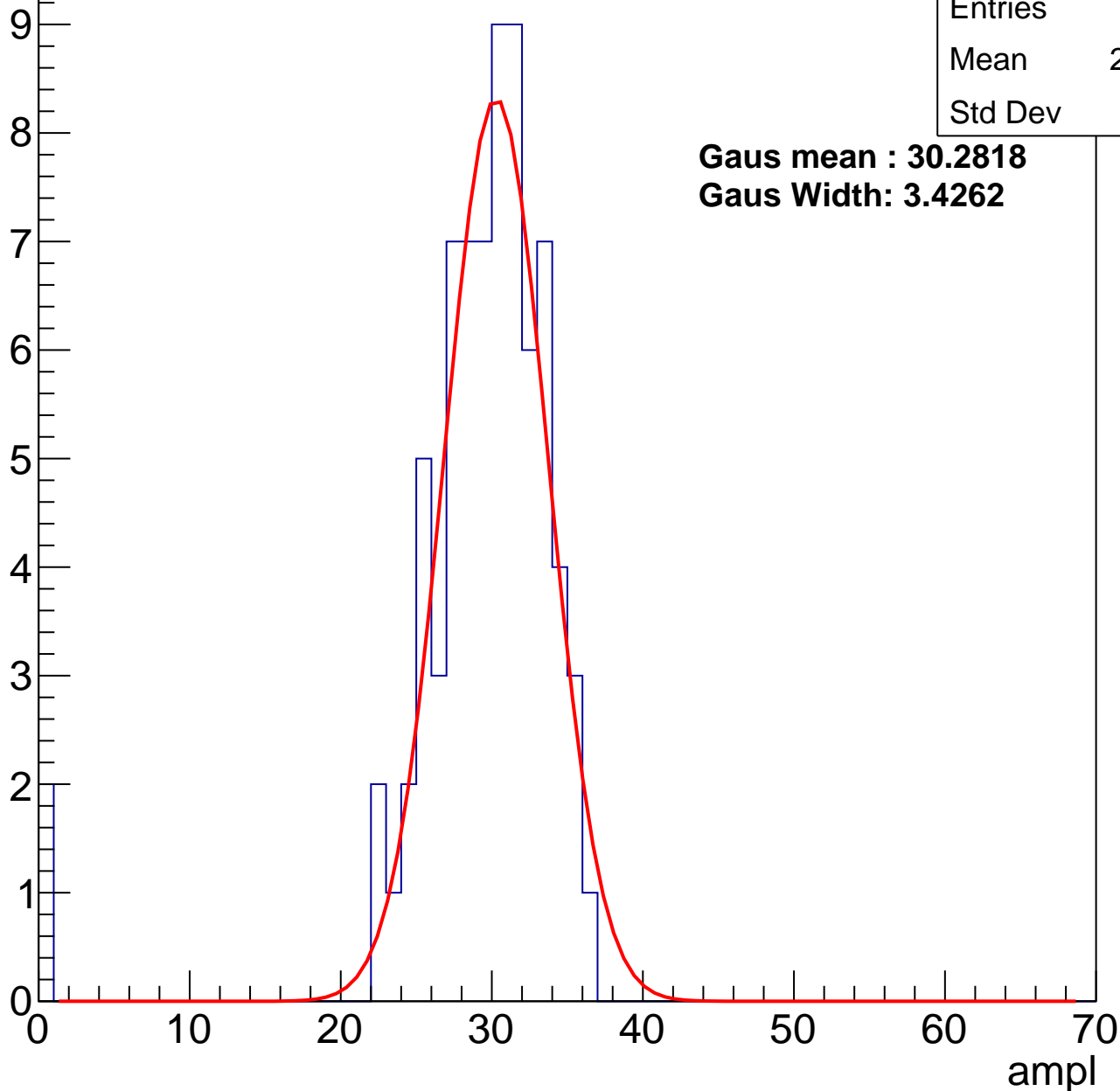
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	28.73
Std Dev	5.74

**Gaus mean : 30.2818**

**Gaus Width: 3.4262**



# B1L000S, U25-ch120, adc1

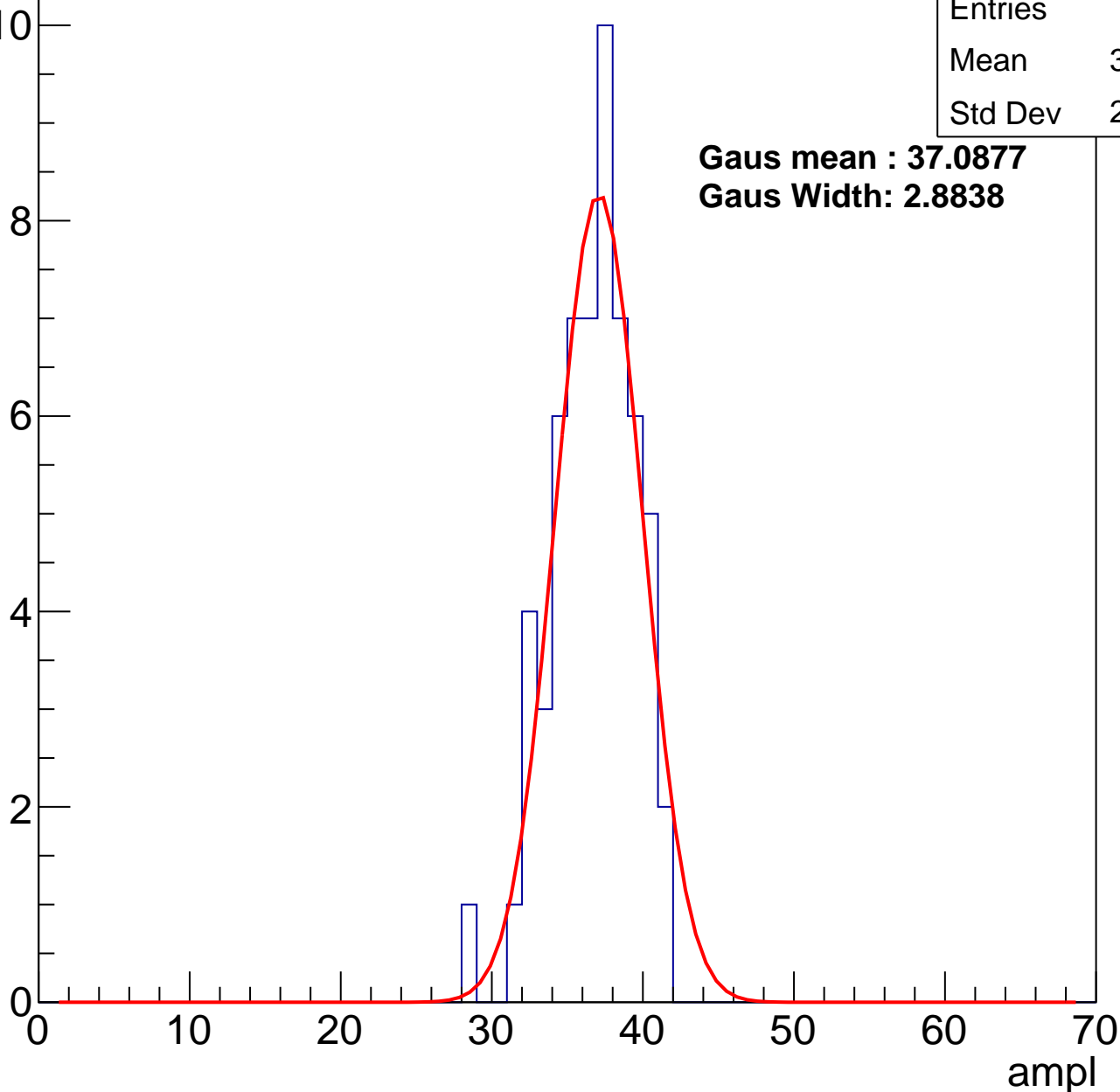
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	36.25
Std Dev	2.697

**Gaus mean : 37.0877**

**Gaus Width: 2.8838**



# B1L000S, U25-ch120, adc2

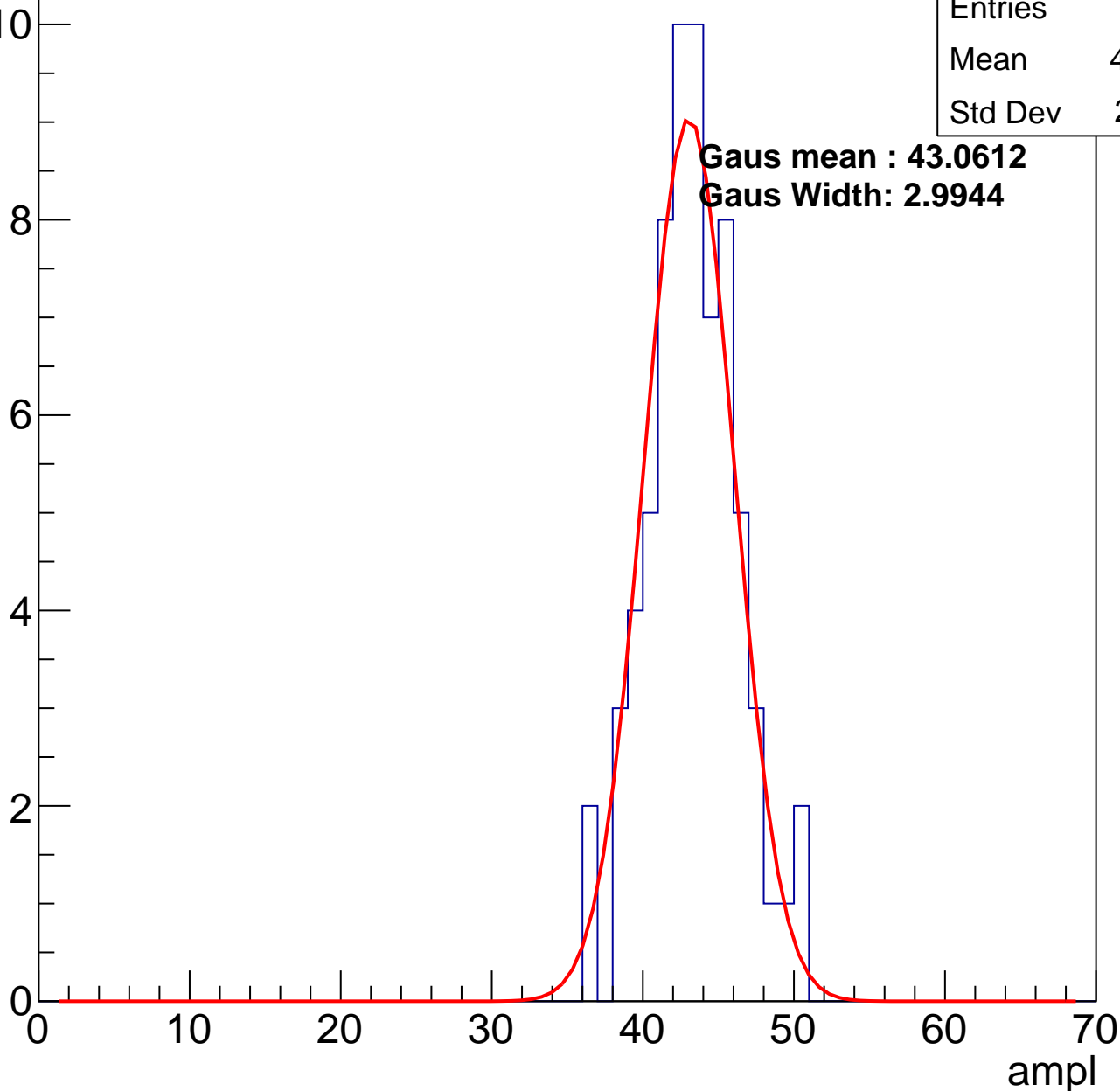
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	42.84
Std Dev	2.981

**Gaus mean : 43.0612**

**Gaus Width: 2.9944**

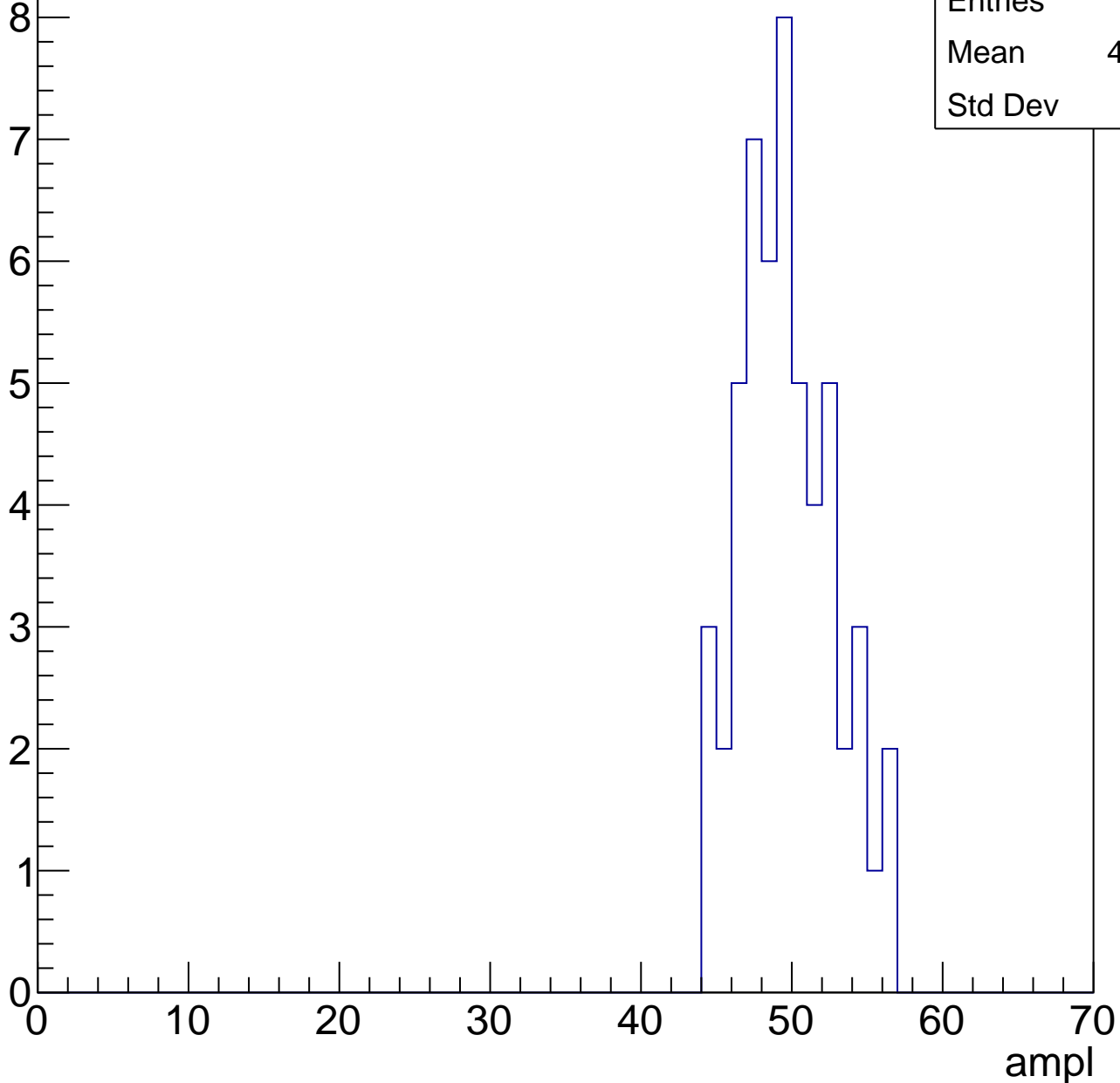


# B1L000S, U25-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	49.25
Std Dev	3.04

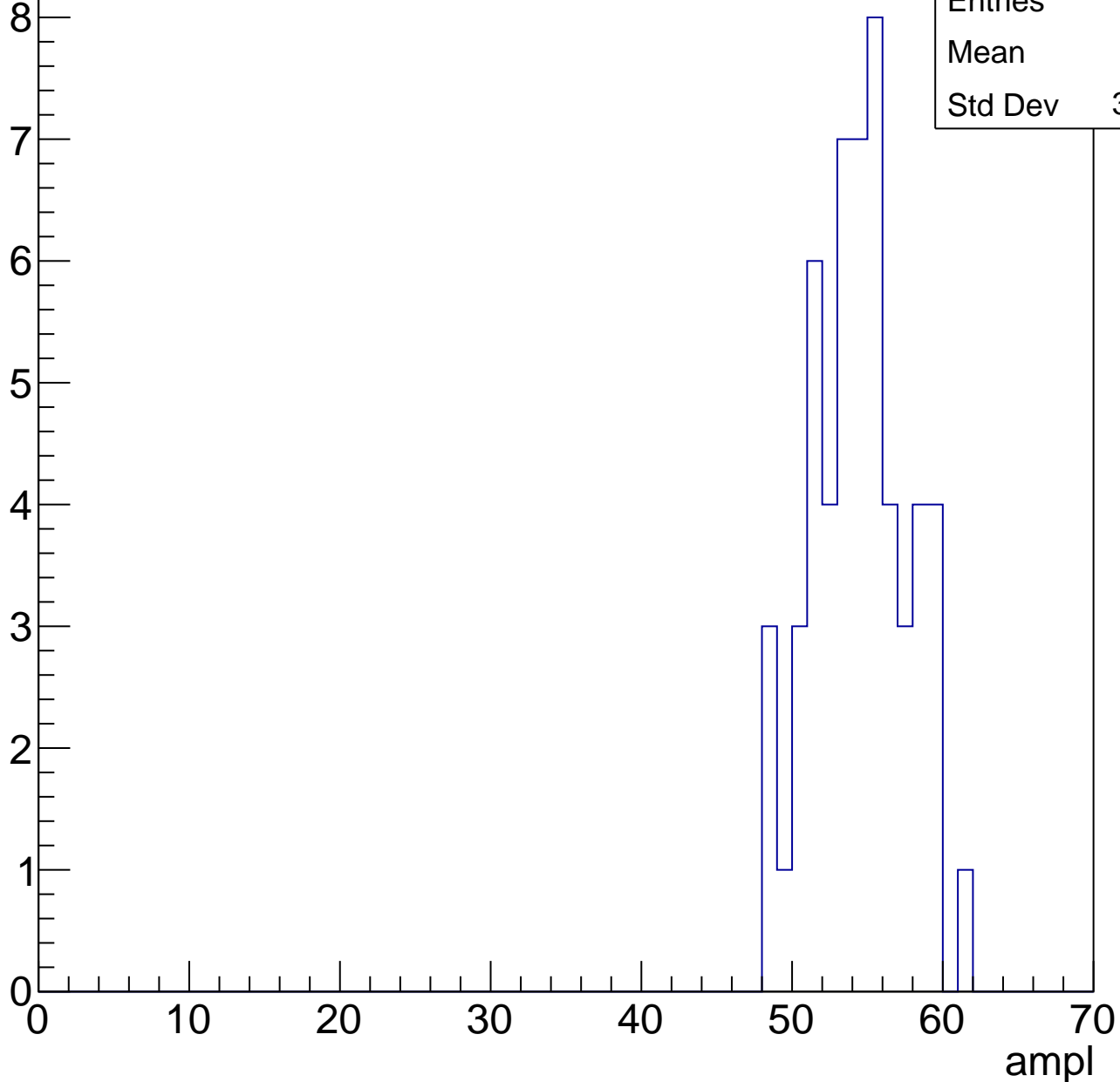


# B1L000S, U25-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	54
Std Dev	3.081

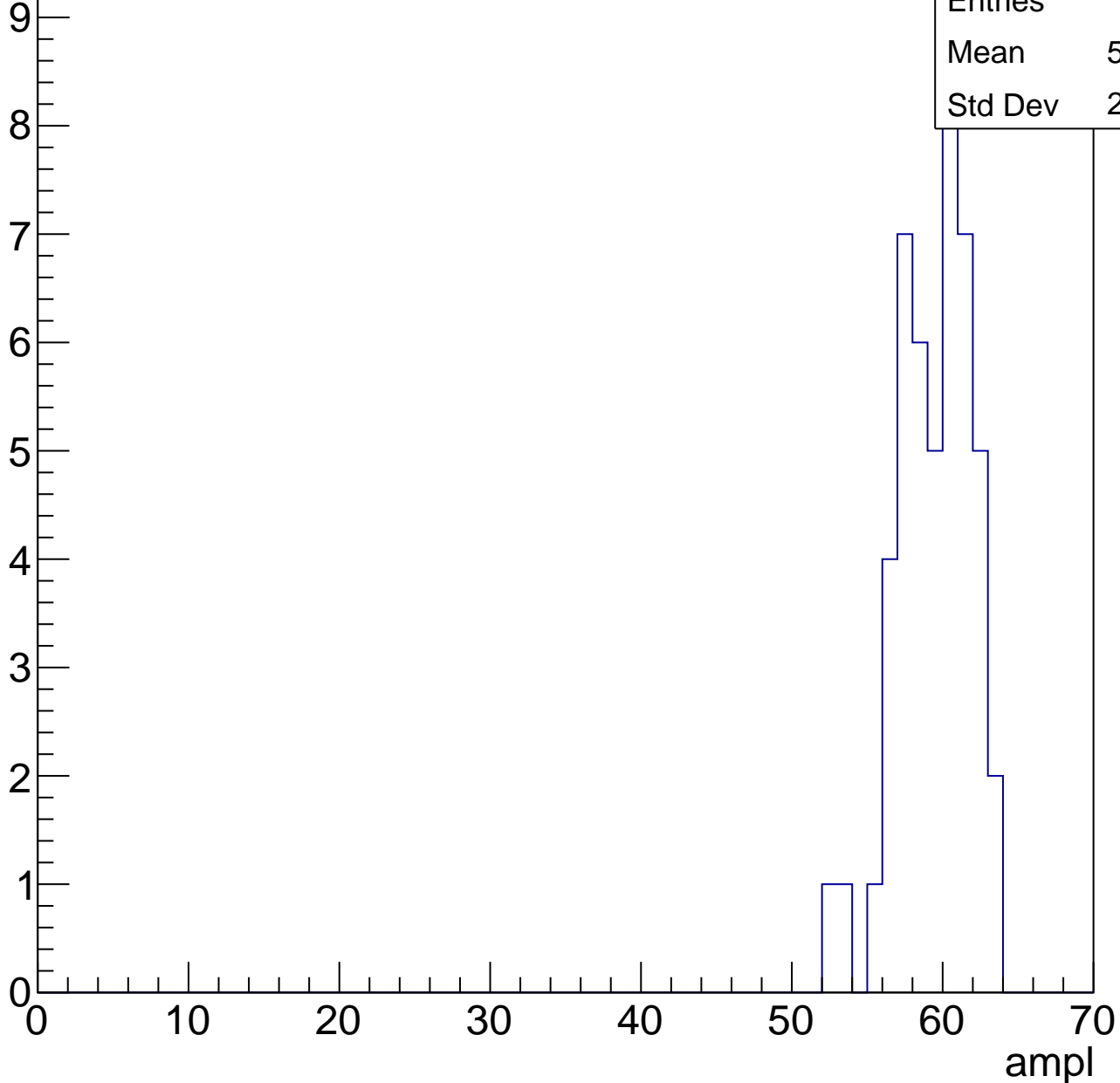


# B1L000S, U25-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	48
Mean	58.94
Std Dev	2.436

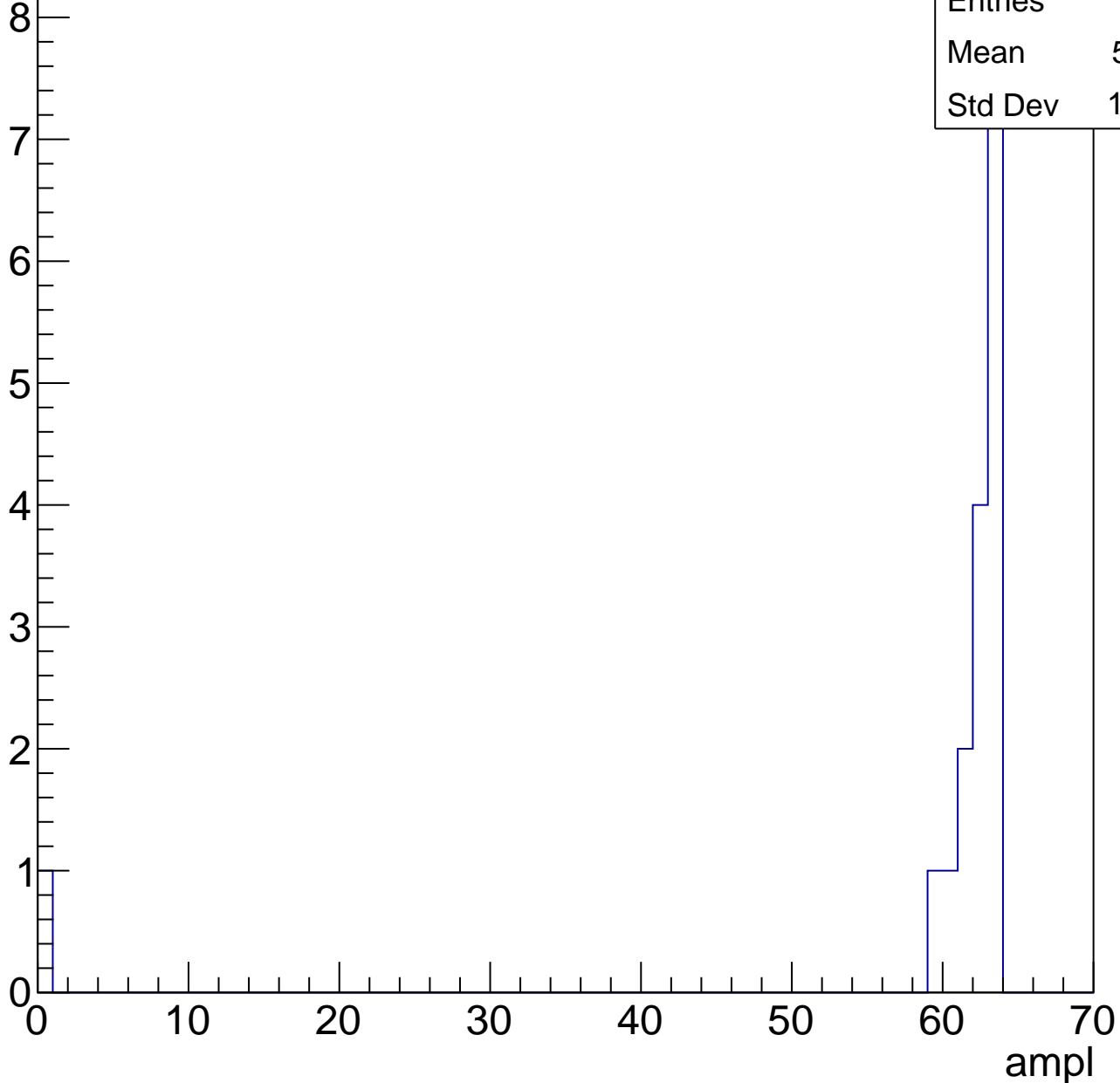


# B1L000S, U25-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	17
Mean	58.41
Std Dev	14.65





# B1L000S, U25-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch121, adc0

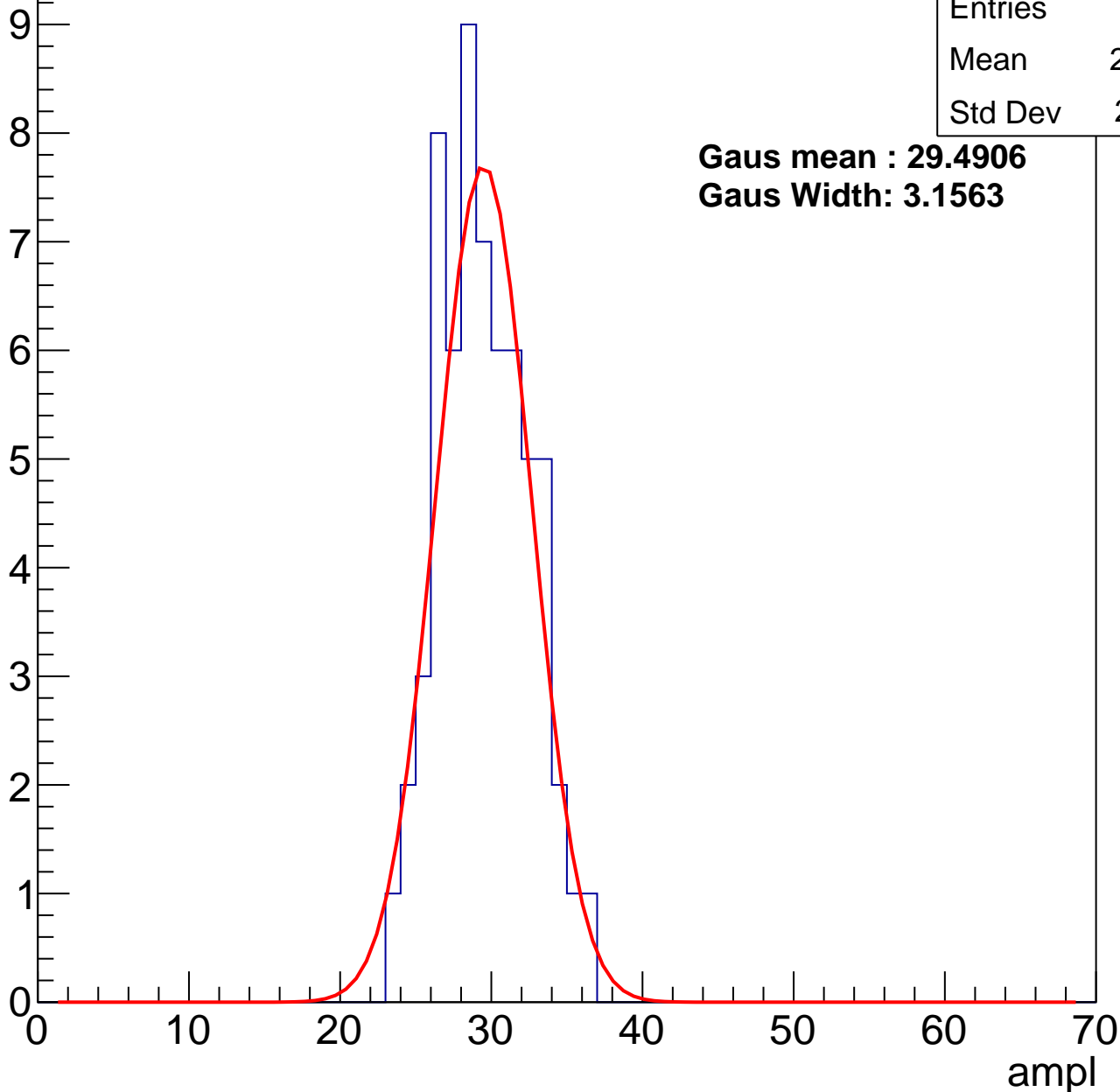
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	29.05
Std Dev	2.921

**Gaus mean : 29.4906**

**Gaus Width: 3.1563**



# B1L000S, U25-ch121, adc1

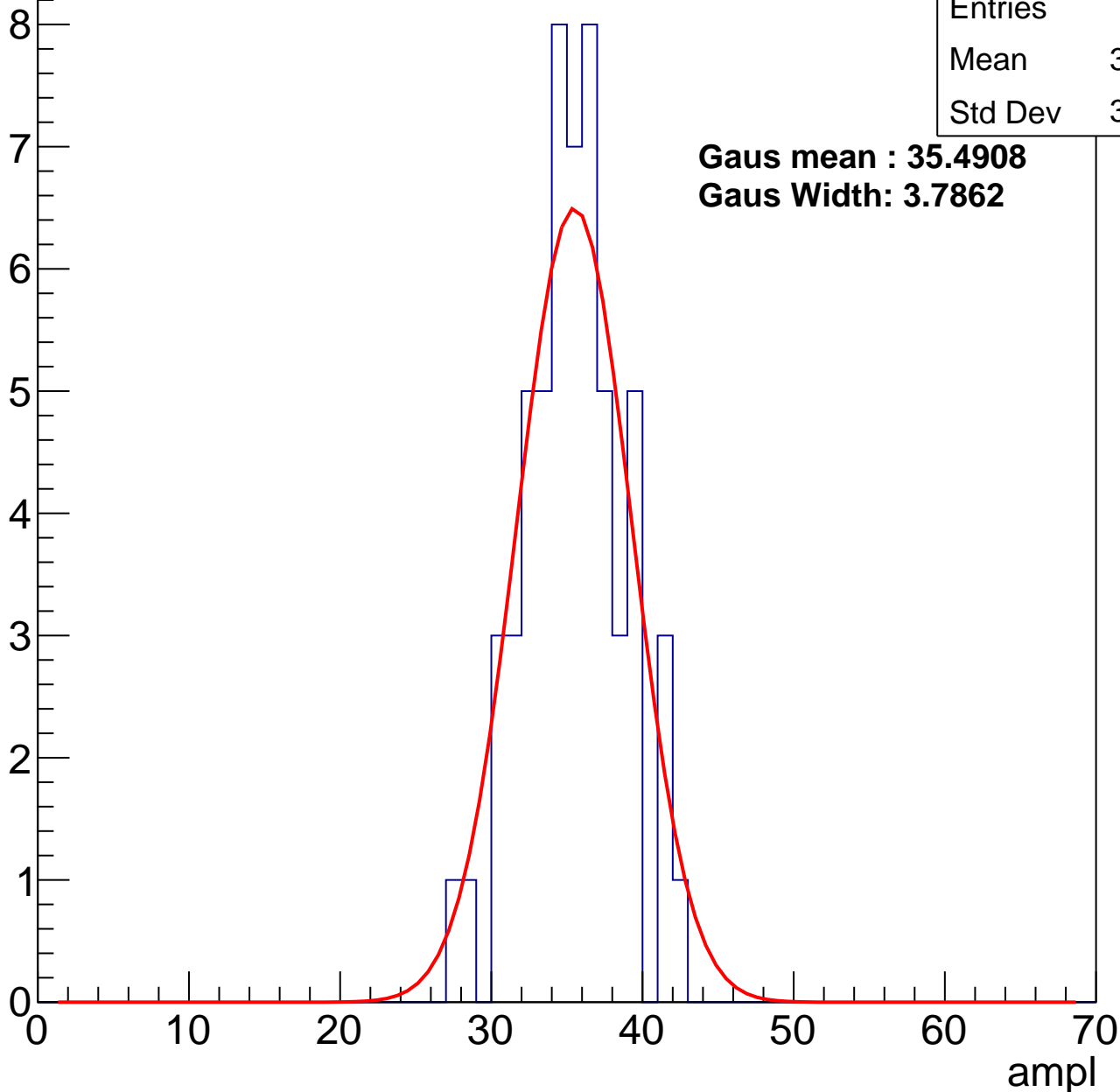
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	34.95
Std Dev	3.219

**Gaus mean : 35.4908**

**Gaus Width: 3.7862**



# B1L000S, U25-ch121, adc2

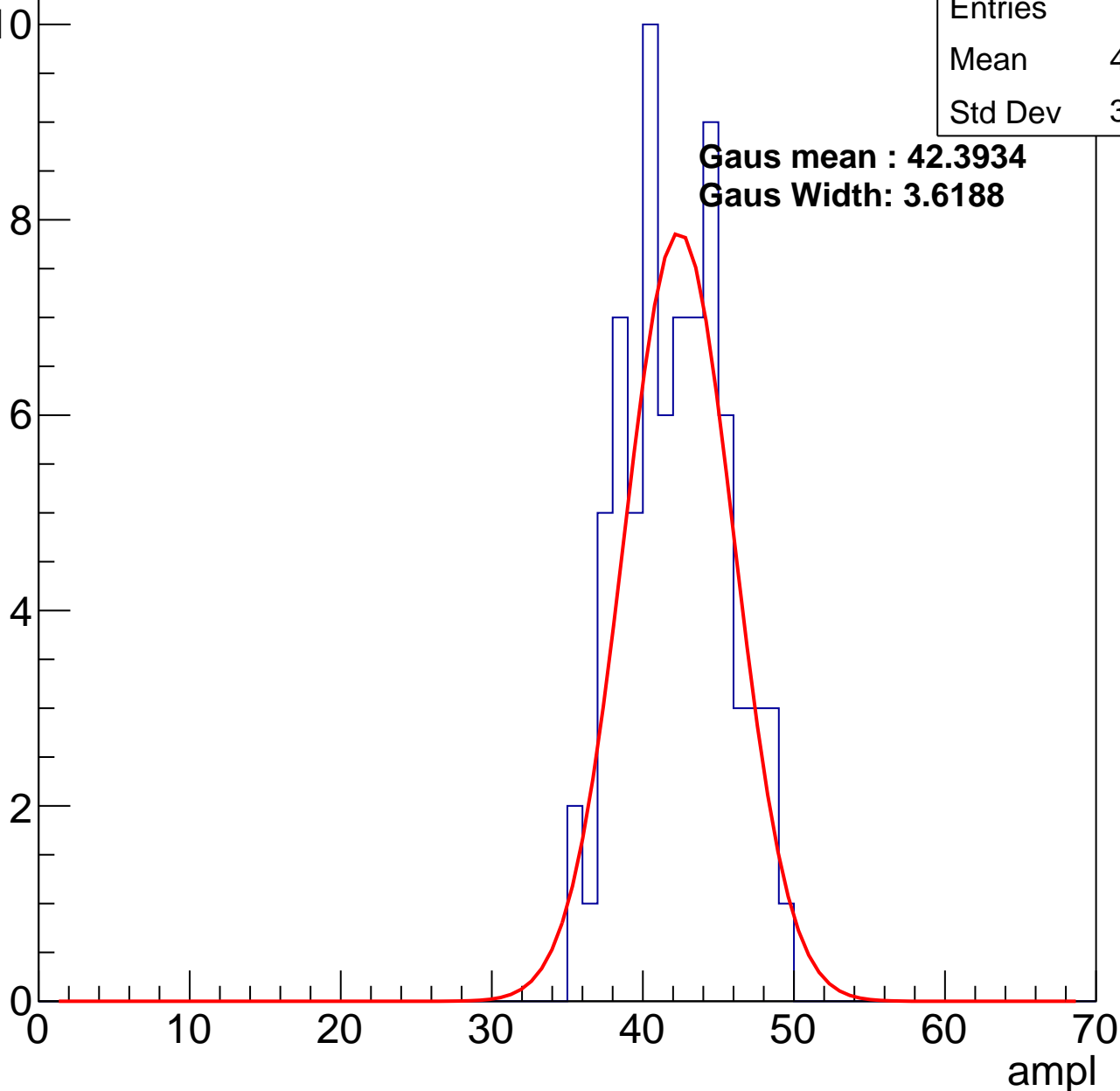
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	75
Mean	41.75
Std Dev	3.335

**Gaus mean : 42.3934**

**Gaus Width: 3.6188**

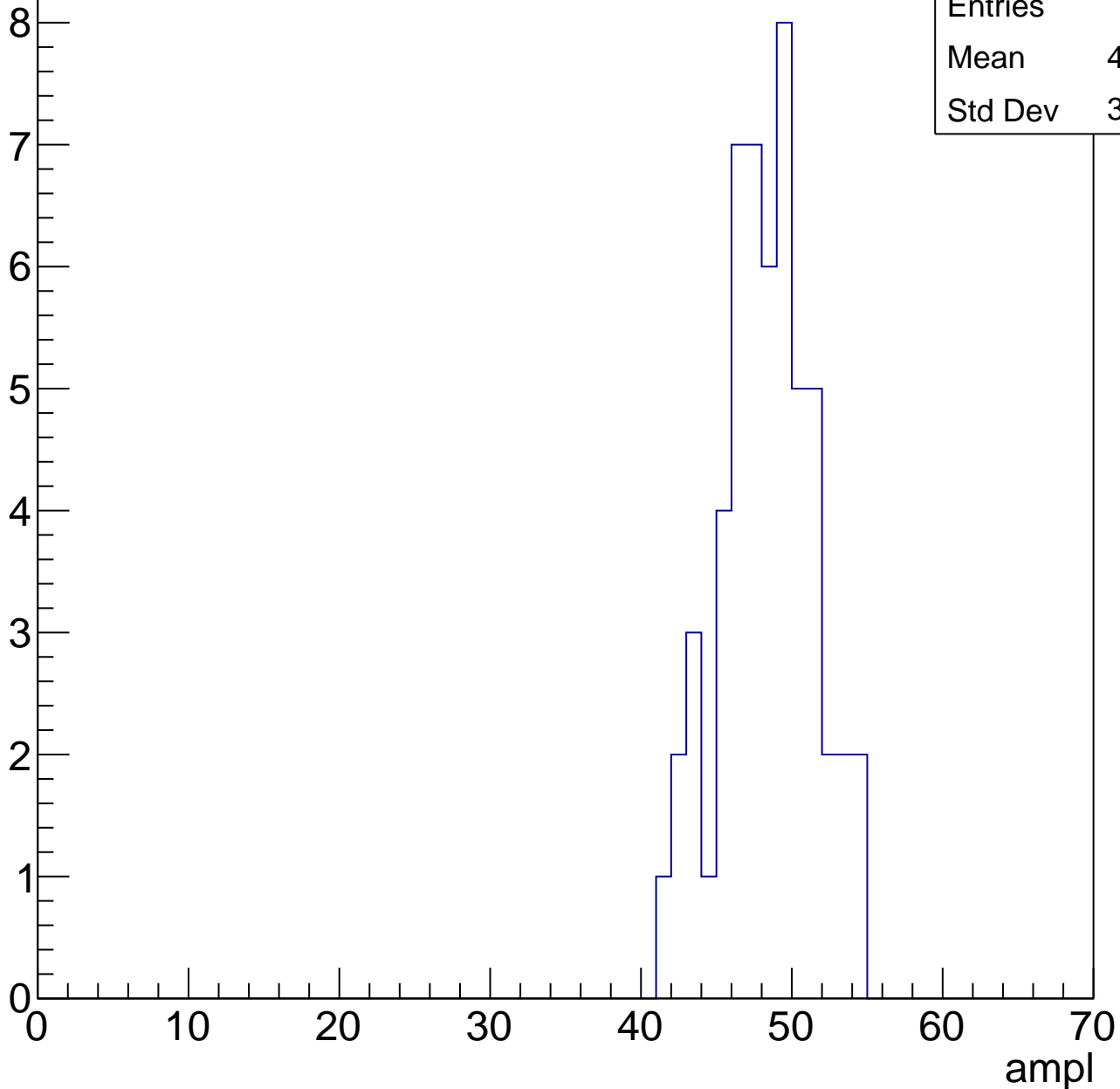


# B1L000S, U25-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	55
Mean	47.85
Std Dev	3.042

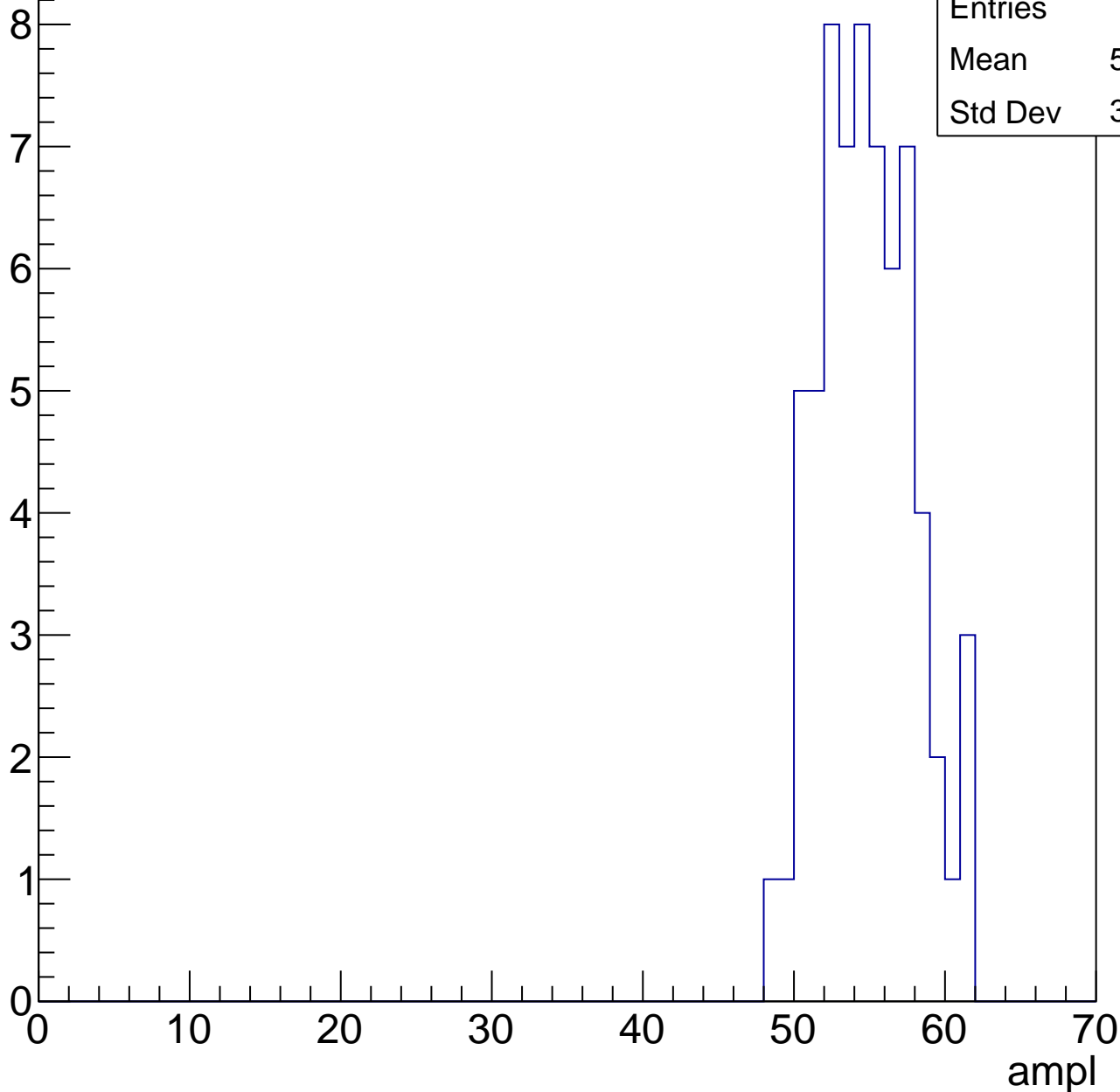


# B1L000S, U25-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.37
Std Dev	3.056

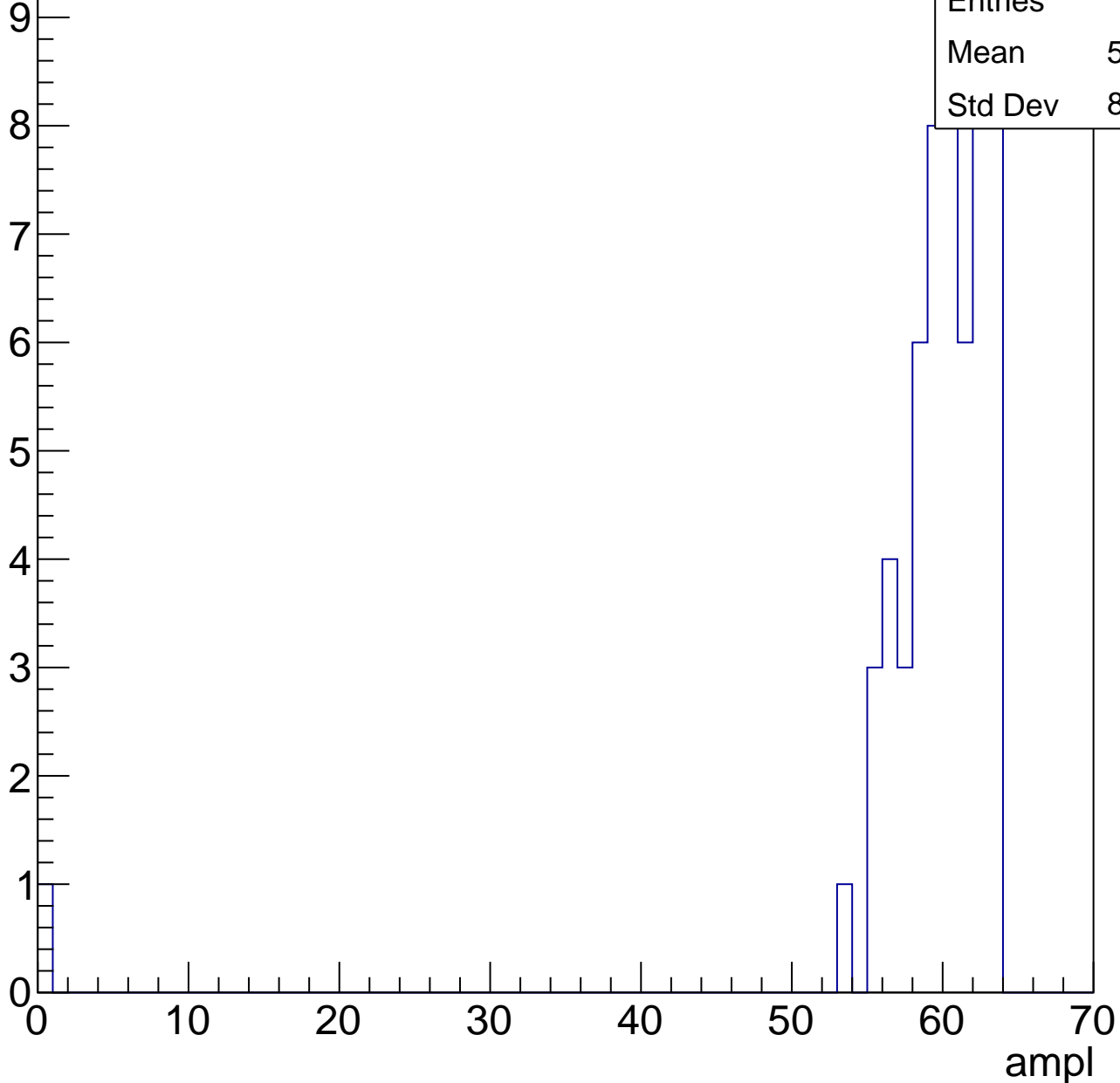


# B1L000S, U25-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	58.69
Std Dev	8.169



# B1L000S, U25-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	86
Mean	28.87
Std Dev	3.484

**Gaus mean : 29.4754**

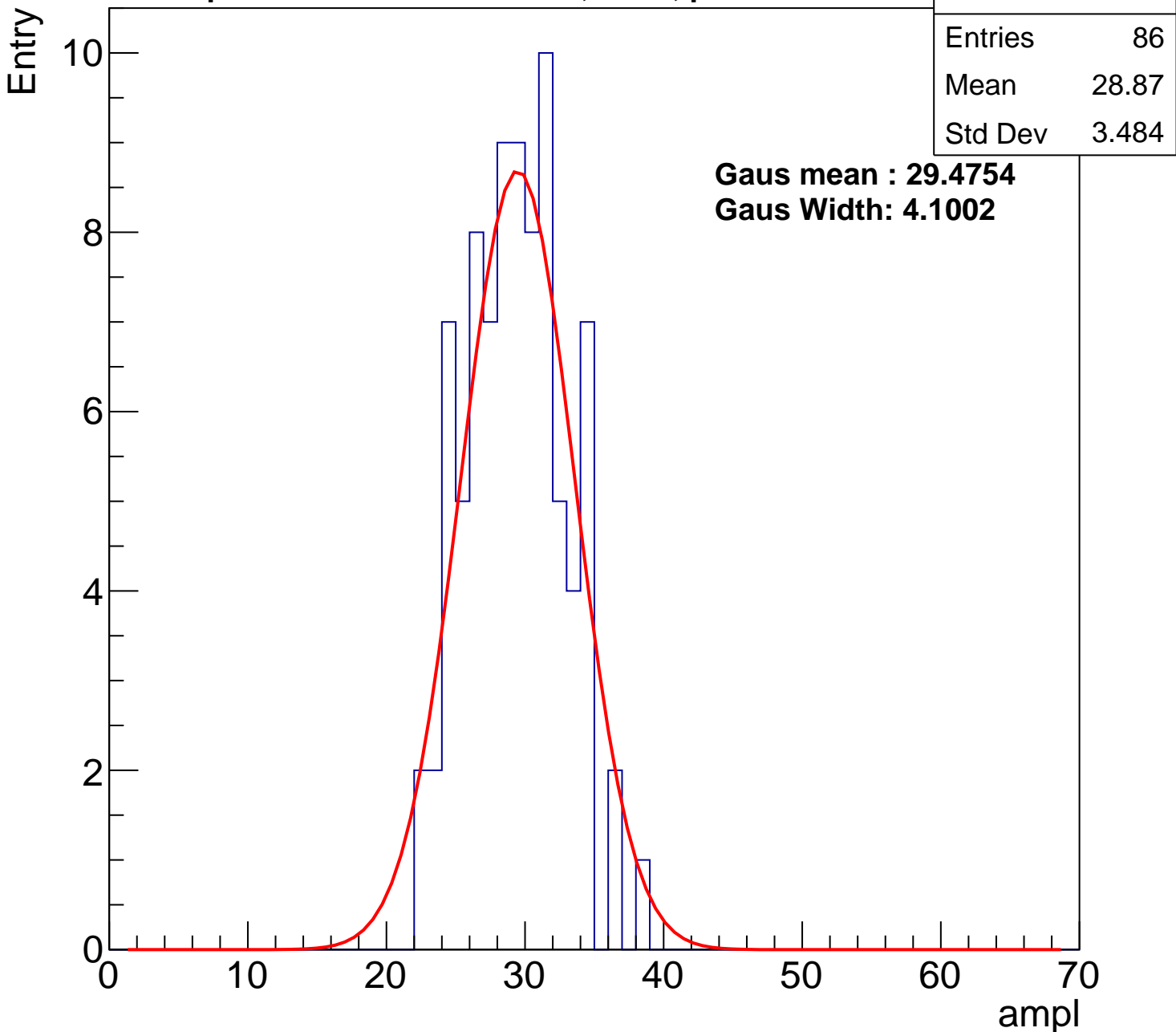
**Gaus Width: 4.1002**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L000S, U25-ch122, adc1

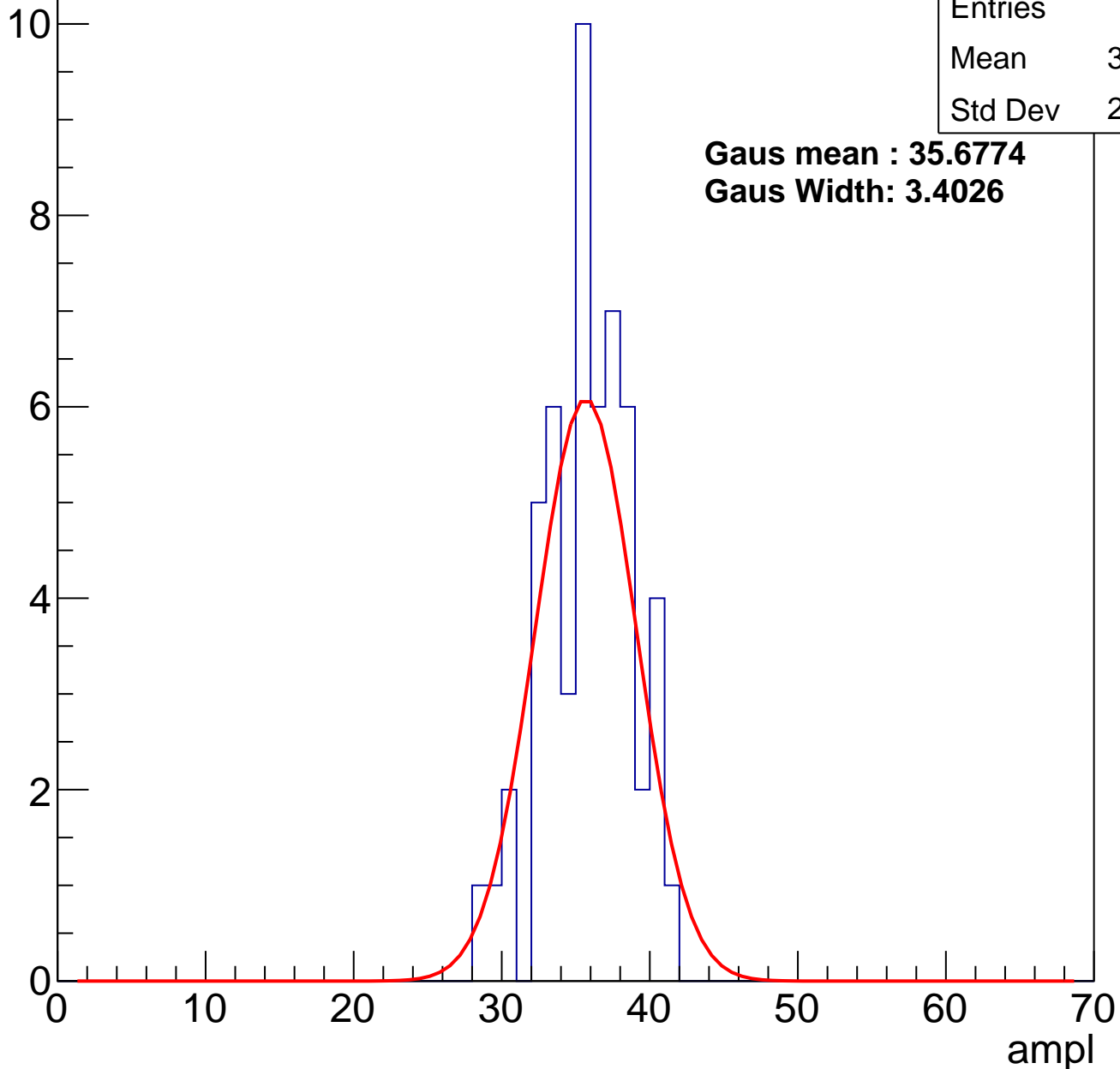
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	54
Mean	35.35
Std Dev	2.907

**Gaus mean : 35.6774**

**Gaus Width: 3.4026**

Entry



# B1L000S, U25-ch122, adc2

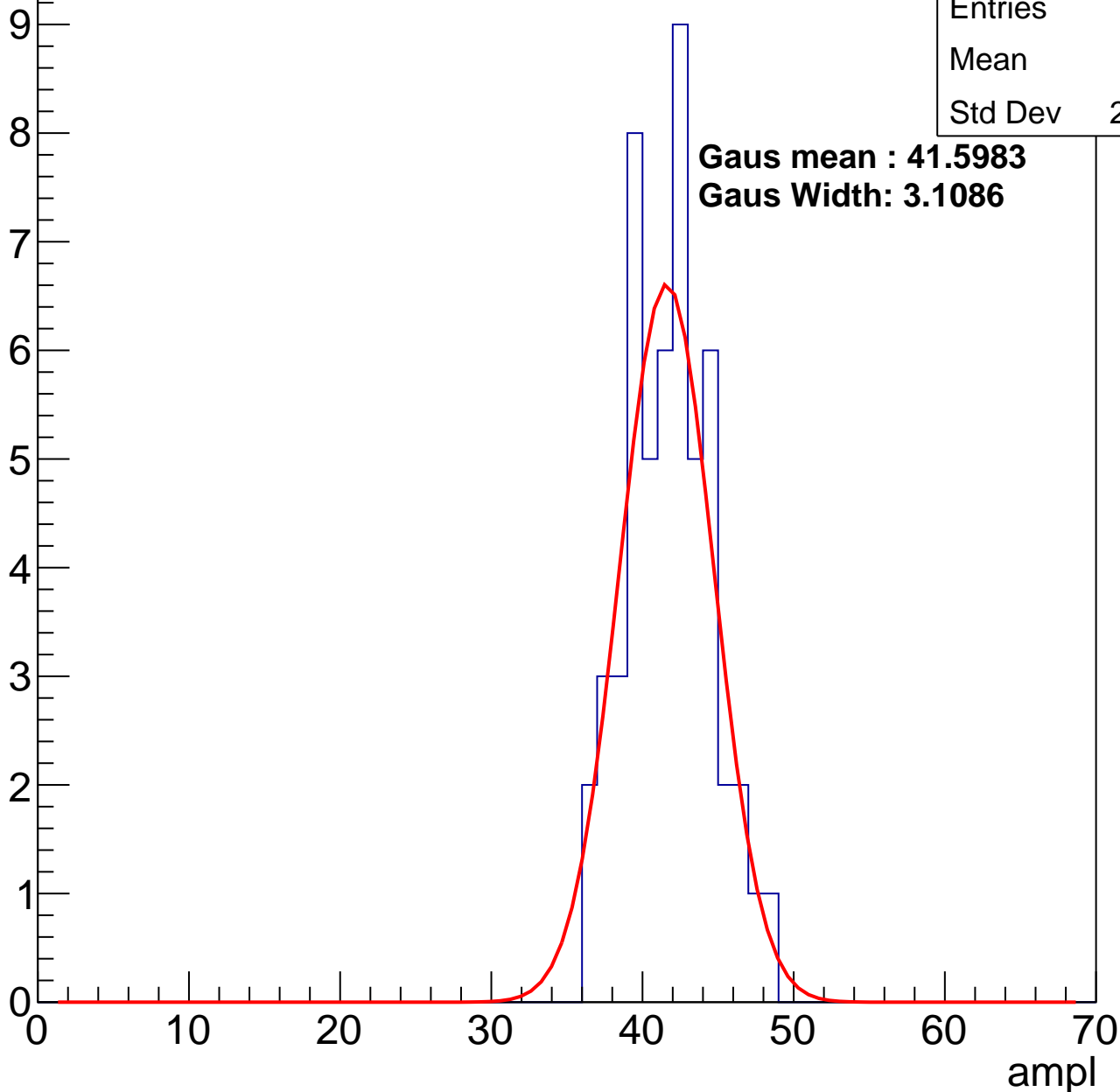
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	53
Mean	41.3
Std Dev	2.772

**Gaus mean : 41.5983**

**Gaus Width: 3.1086**

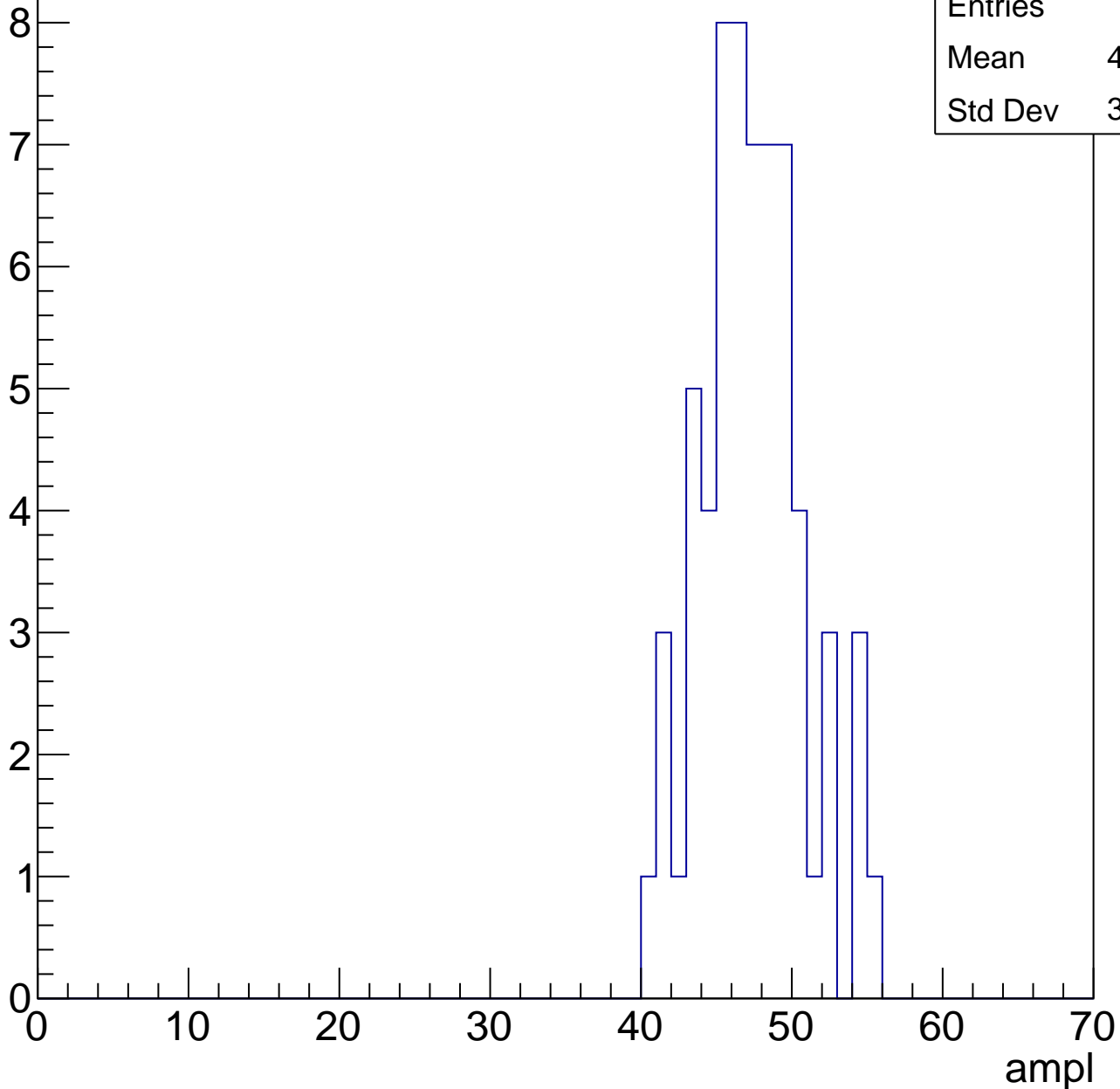


# B1L000S, U25-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	63
Mean	46.92
Std Dev	3.358

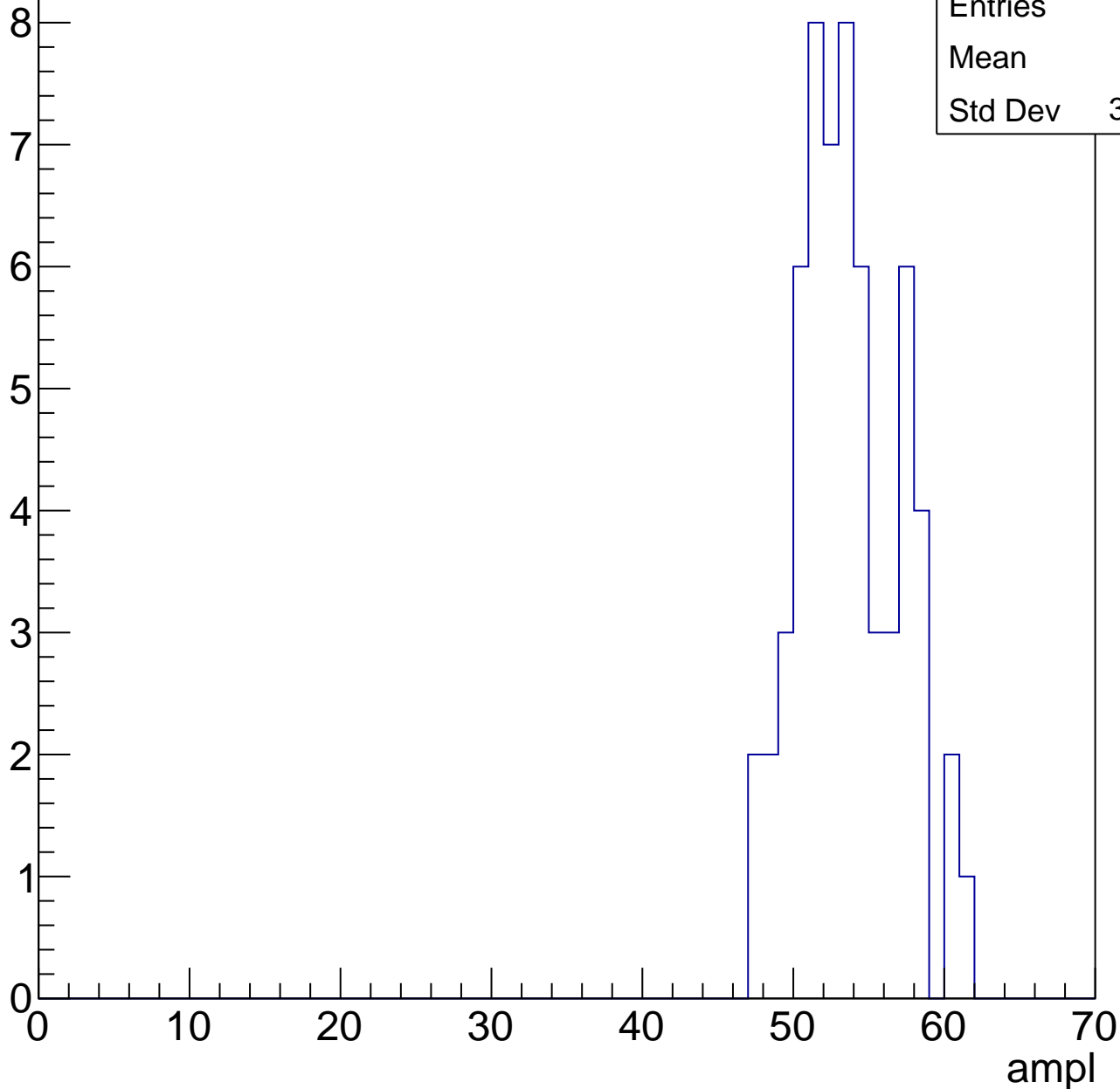


# B1L000S, U25-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	53.2
Std Dev	3.298

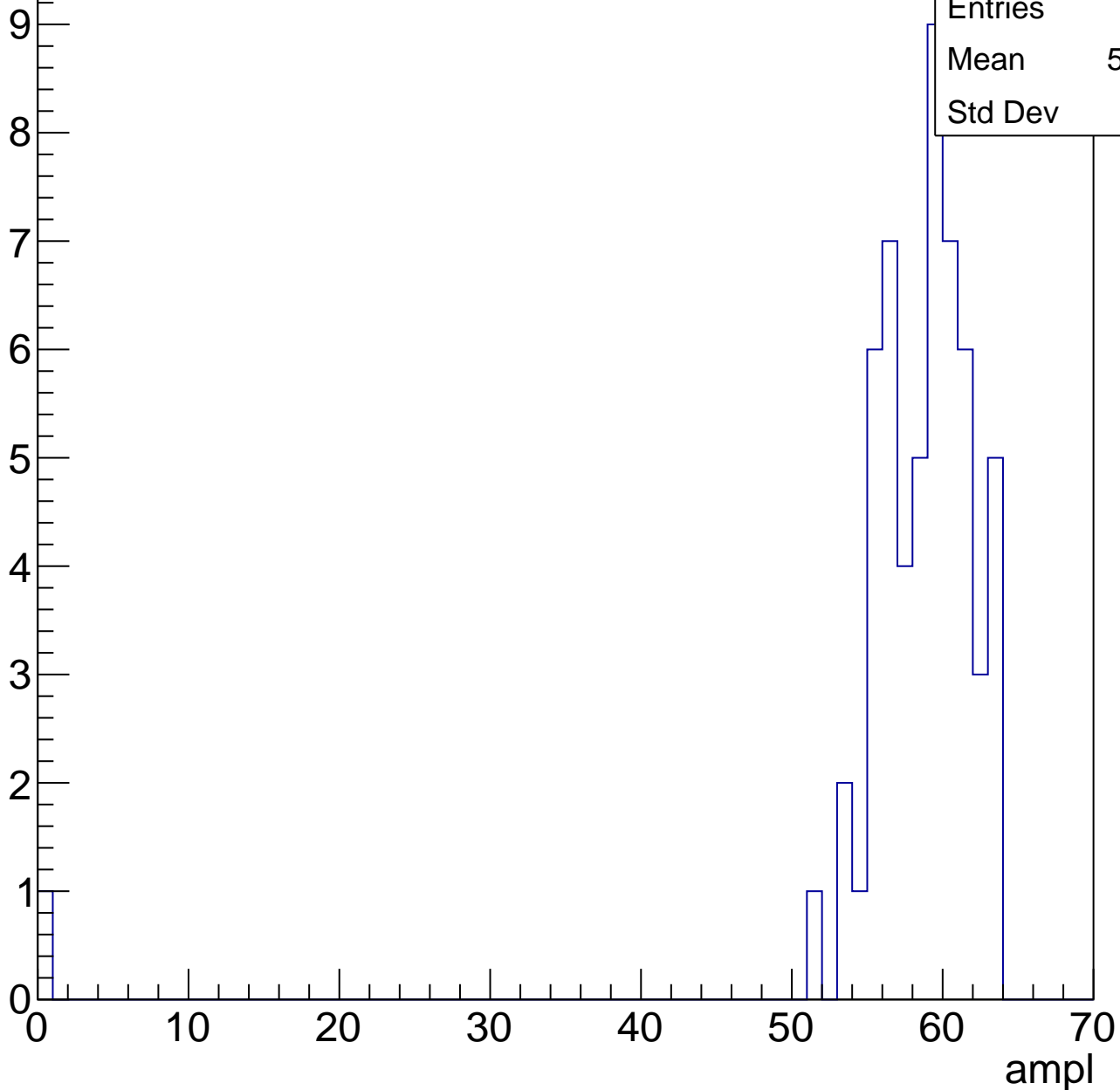


# B1L000S, U25-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	57.35
Std Dev	8.17

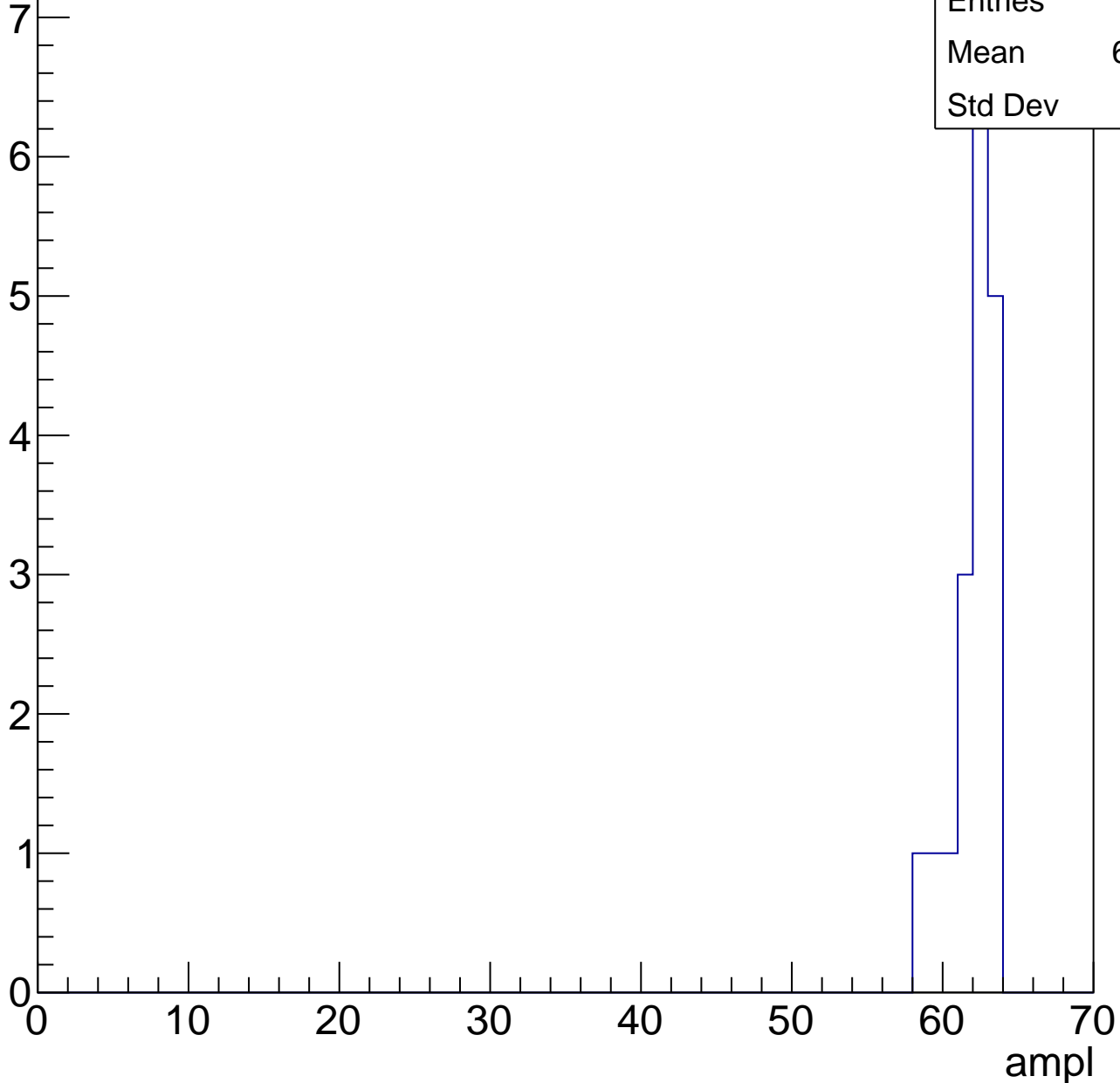


# B1L000S, U25-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	18
Mean	61.61
Std Dev	1.38

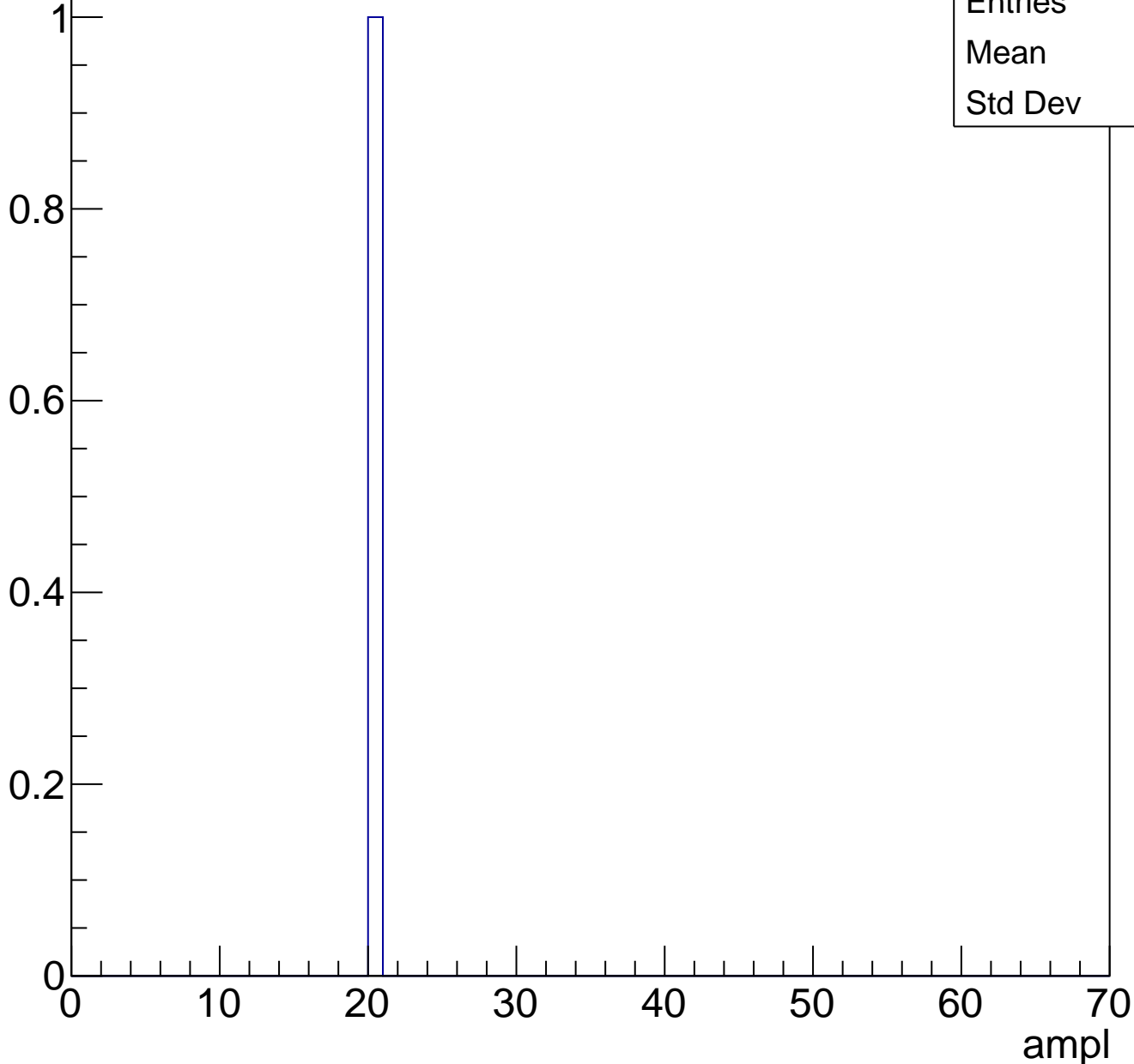




# B1L000S, U25-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch123, adc0

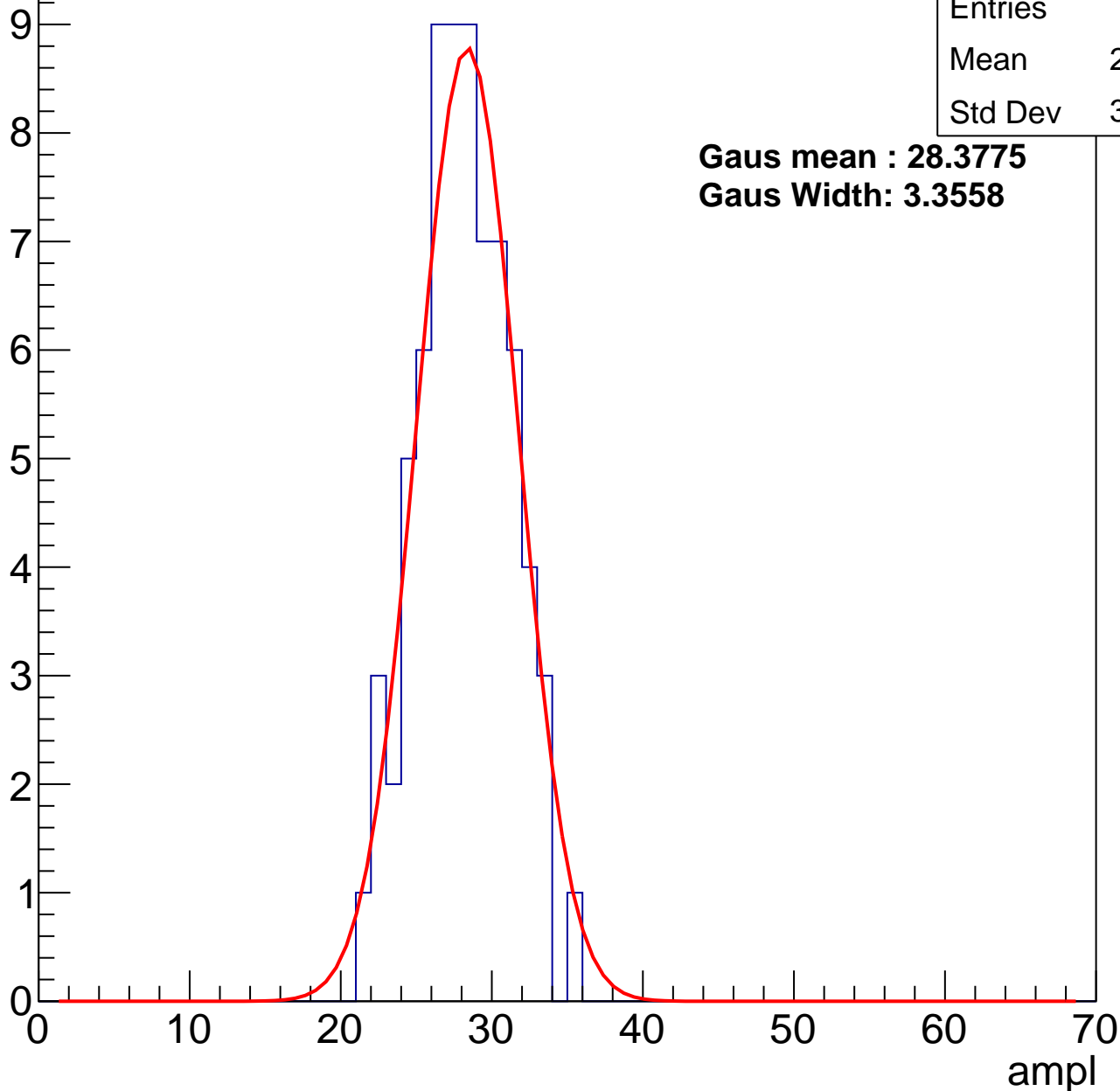
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	72
Mean	27.68
Std Dev	3.018

**Gaus mean : 28.3775**

**Gaus Width: 3.3558**



# B1L000S, U25-ch123, adc1

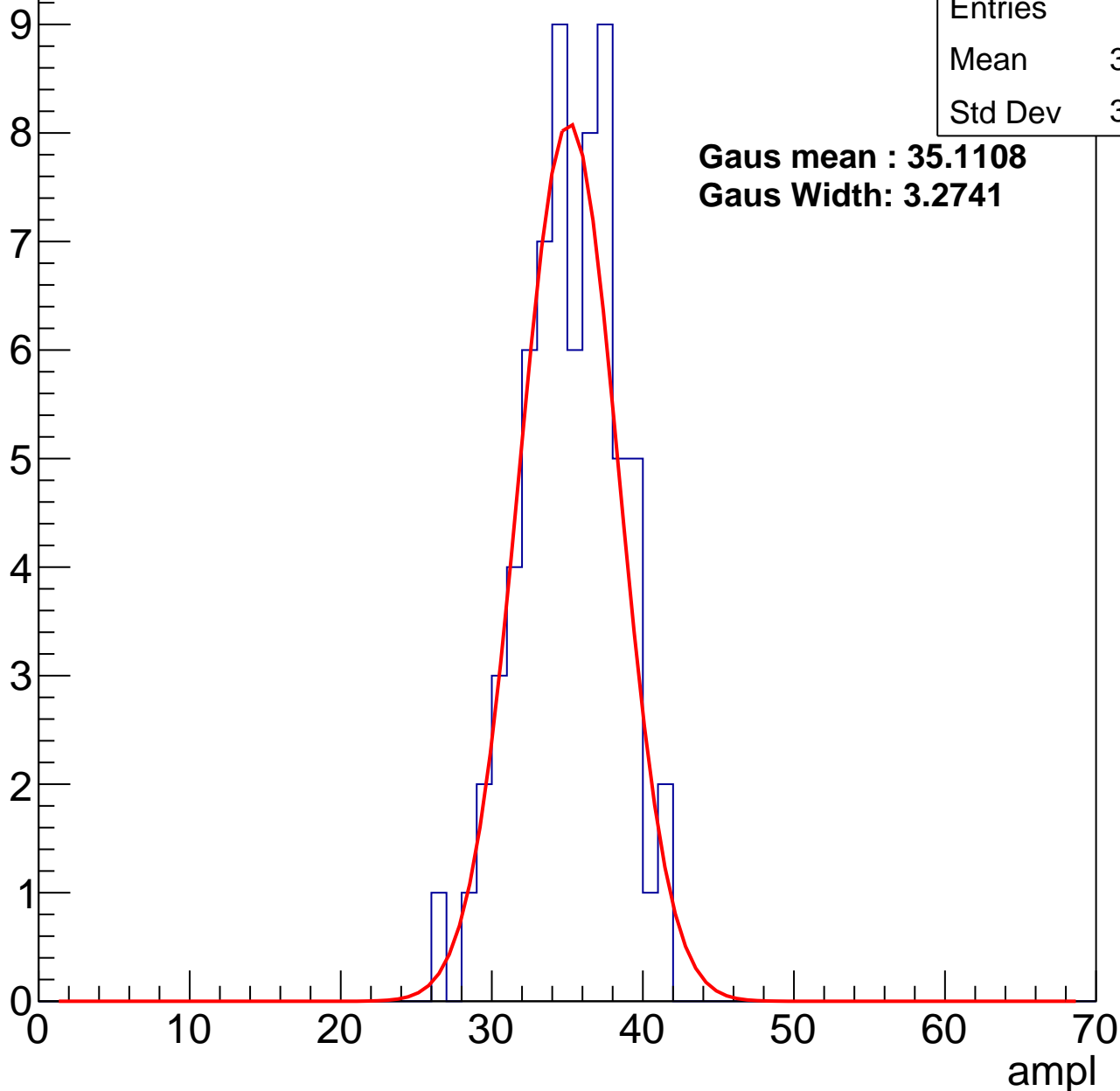
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	69
Mean	34.68
Std Dev	3.165

**Gaus mean : 35.1108**

**Gaus Width: 3.2741**



# B1L000S, U25-ch123, adc2

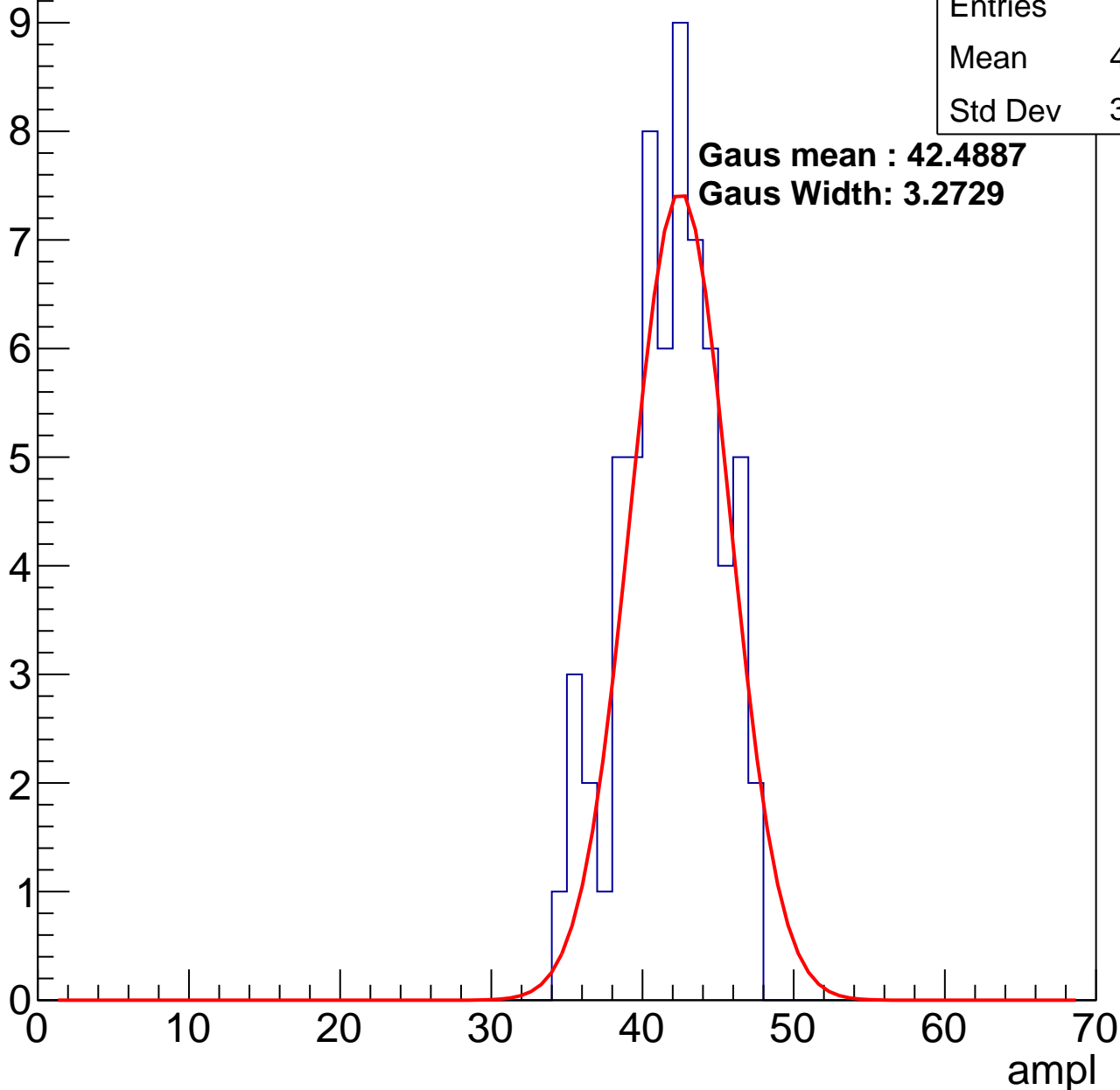
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	64
Mean	41.34
Std Dev	3.163

**Gaus mean : 42.4887**

**Gaus Width: 3.2729**

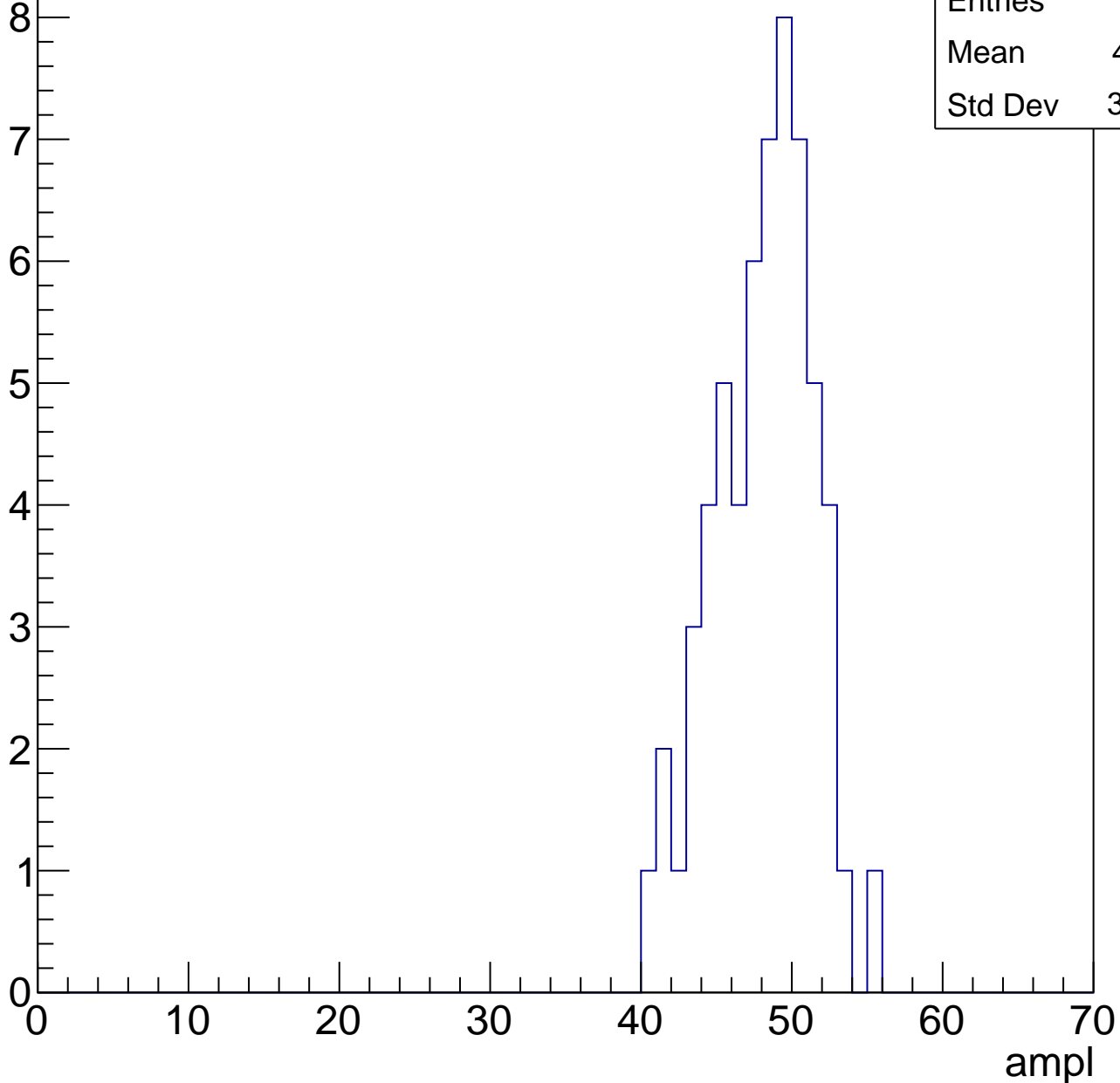


# B1L000S, U25-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	47.61
Std Dev	3.226

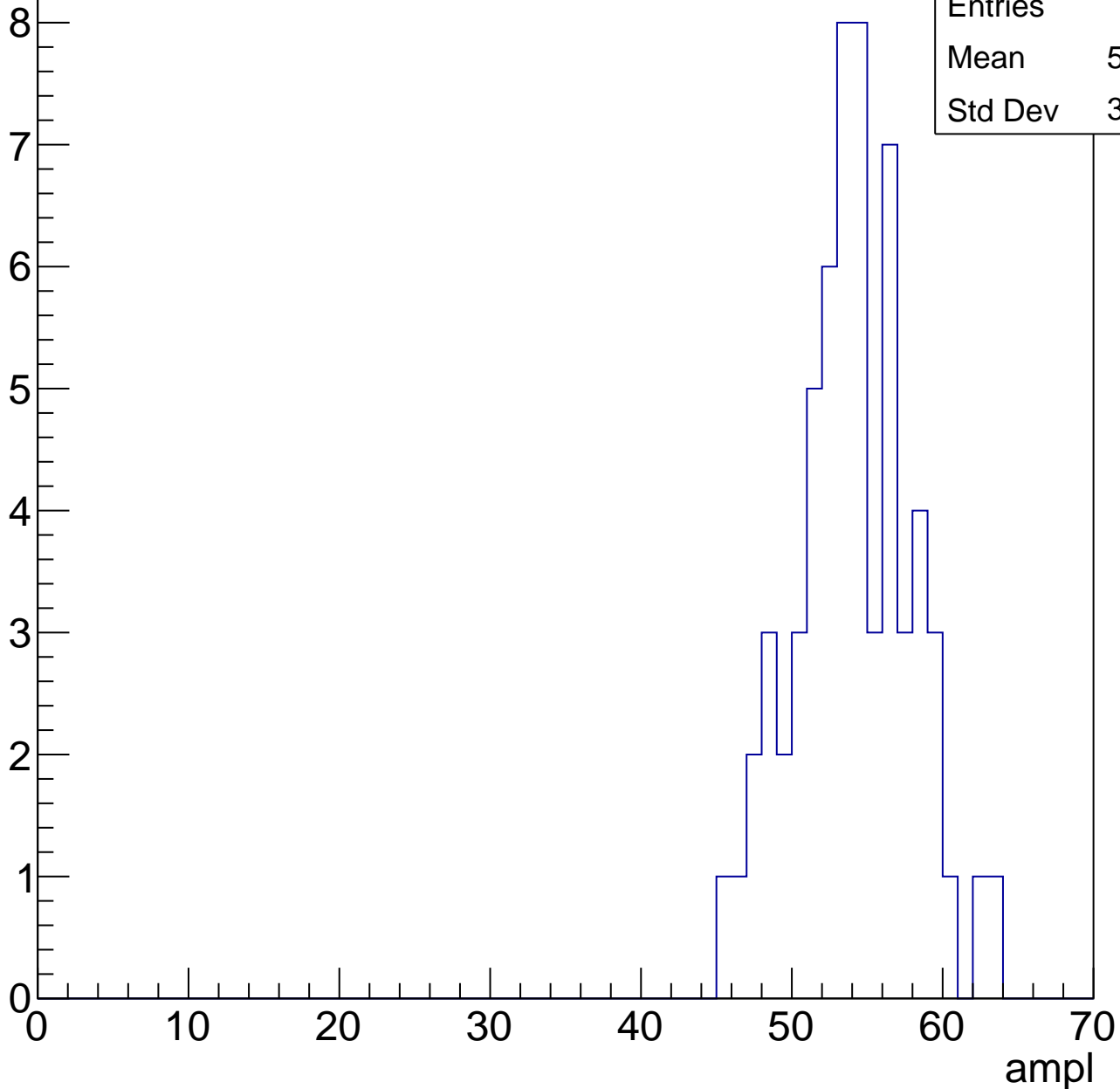


# B1L000S, U25-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	53.58
Std Dev	3.795

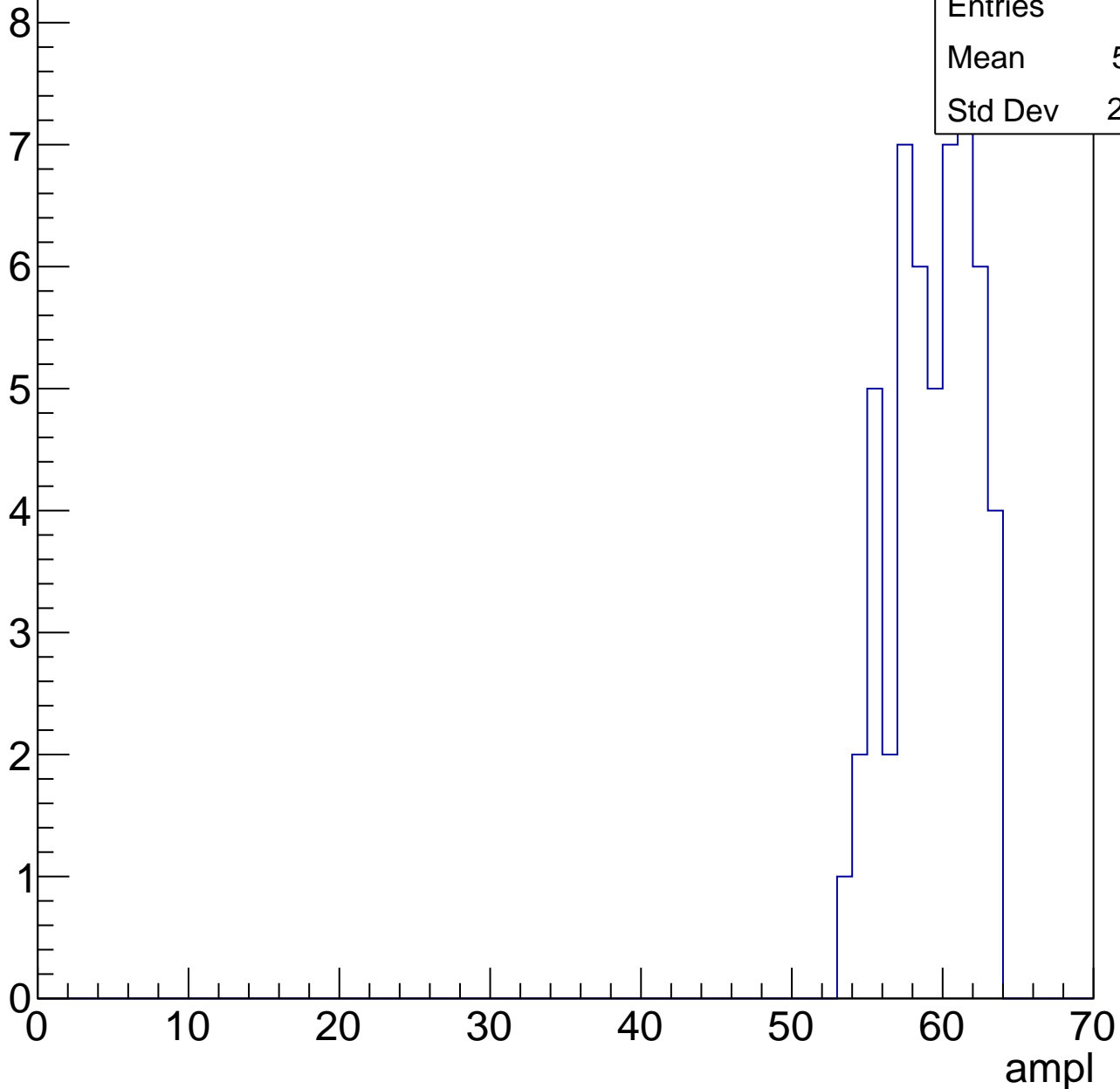


# B1L000S, U25-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

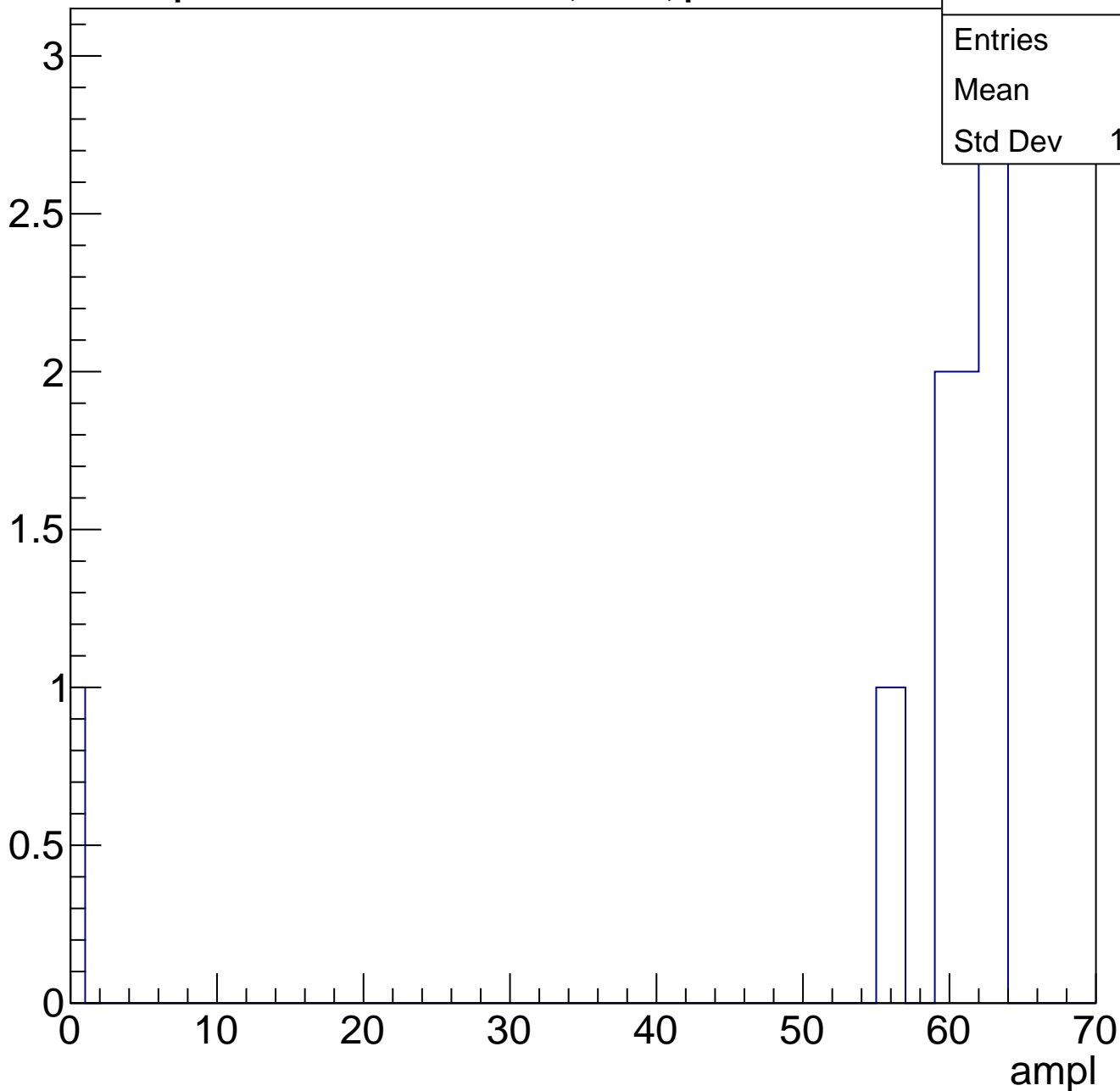
Entries	53
Mean	58.91
Std Dev	2.658



# B1L000S, U25-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch124, adc0

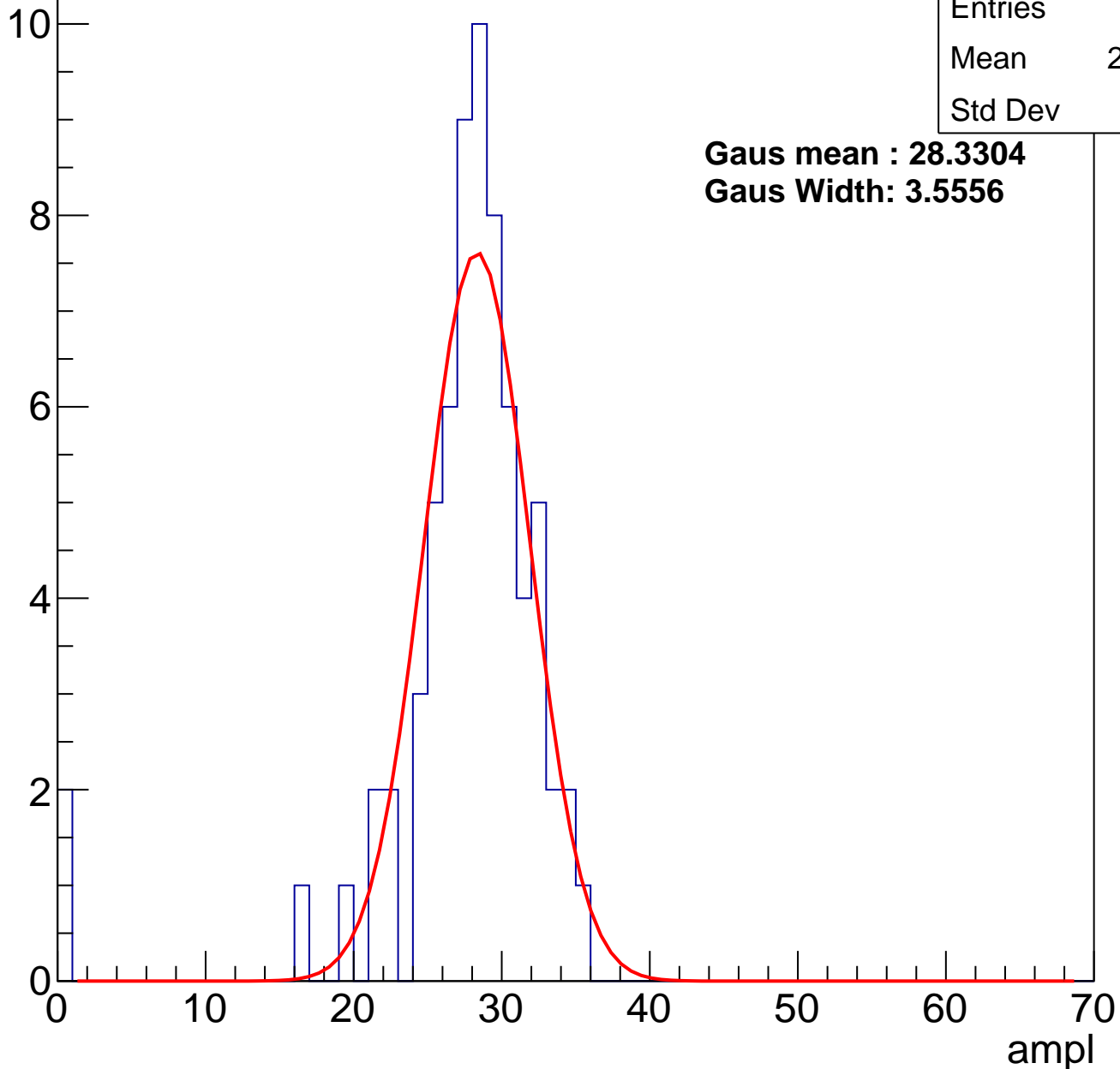
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	69
Mean	26.99
Std Dev	5.82

**Gaus mean : 28.3304**

**Gaus Width: 3.5556**

Entry



# B1L000S, U25-ch124, adc1

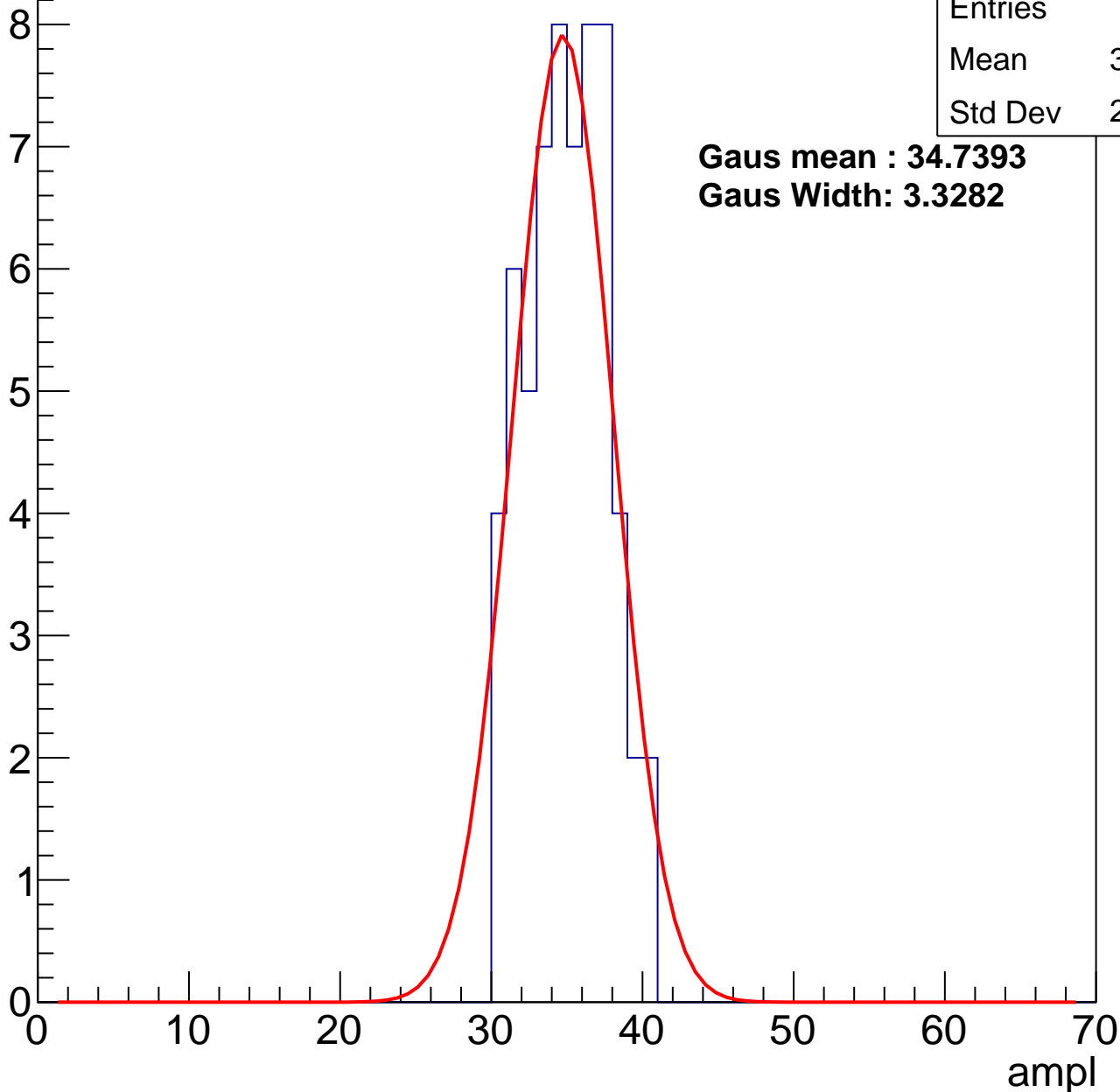
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	61
Mean	34.56
Std Dev	2.633

**Gaus mean : 34.7393**

**Gaus Width: 3.3282**



# B1L000S, U25-ch124, adc2

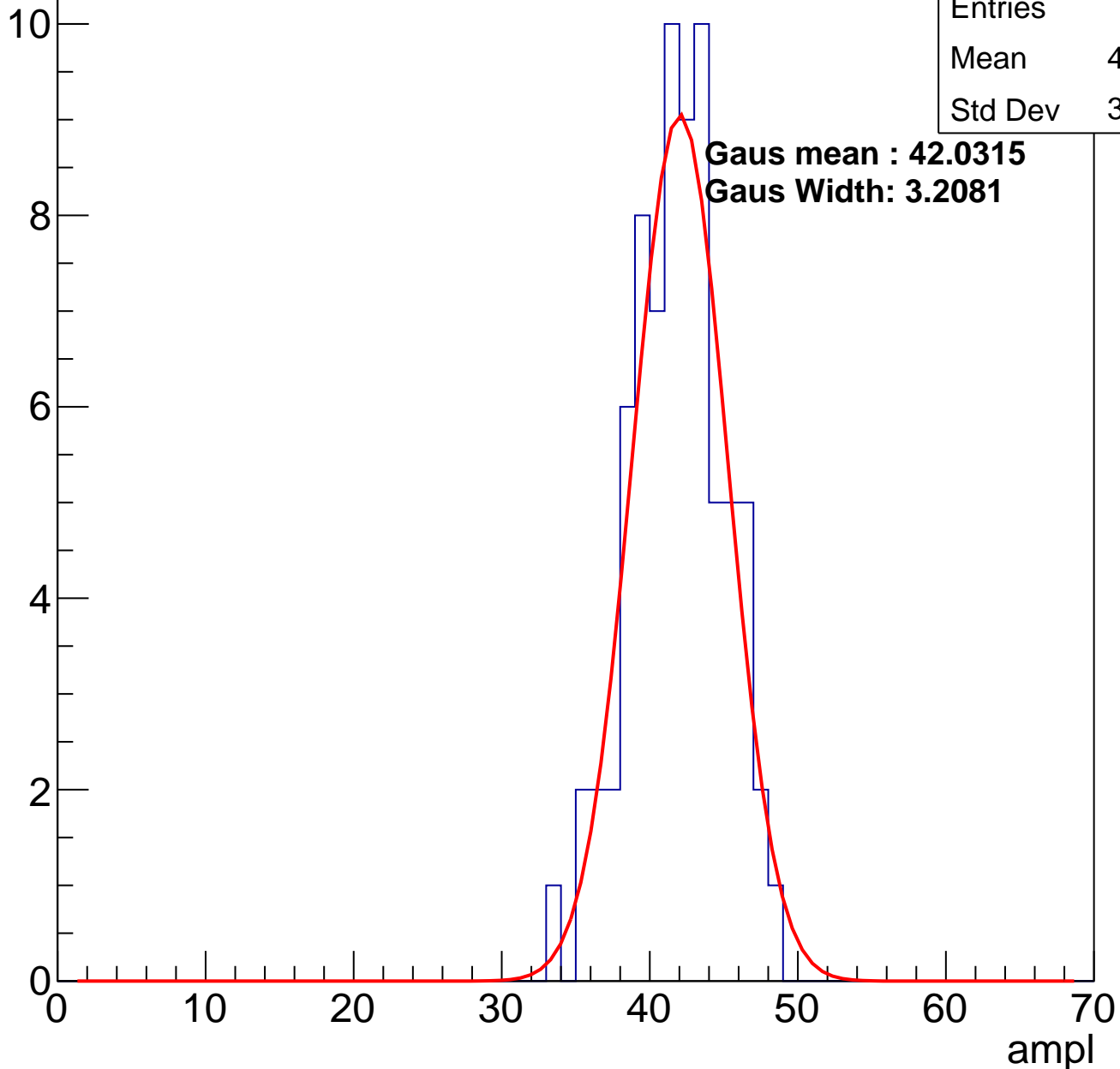
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	75
Mean	41.39
Std Dev	3.098

**Gaus mean : 42.0315**

**Gaus Width: 3.2081**

Entry

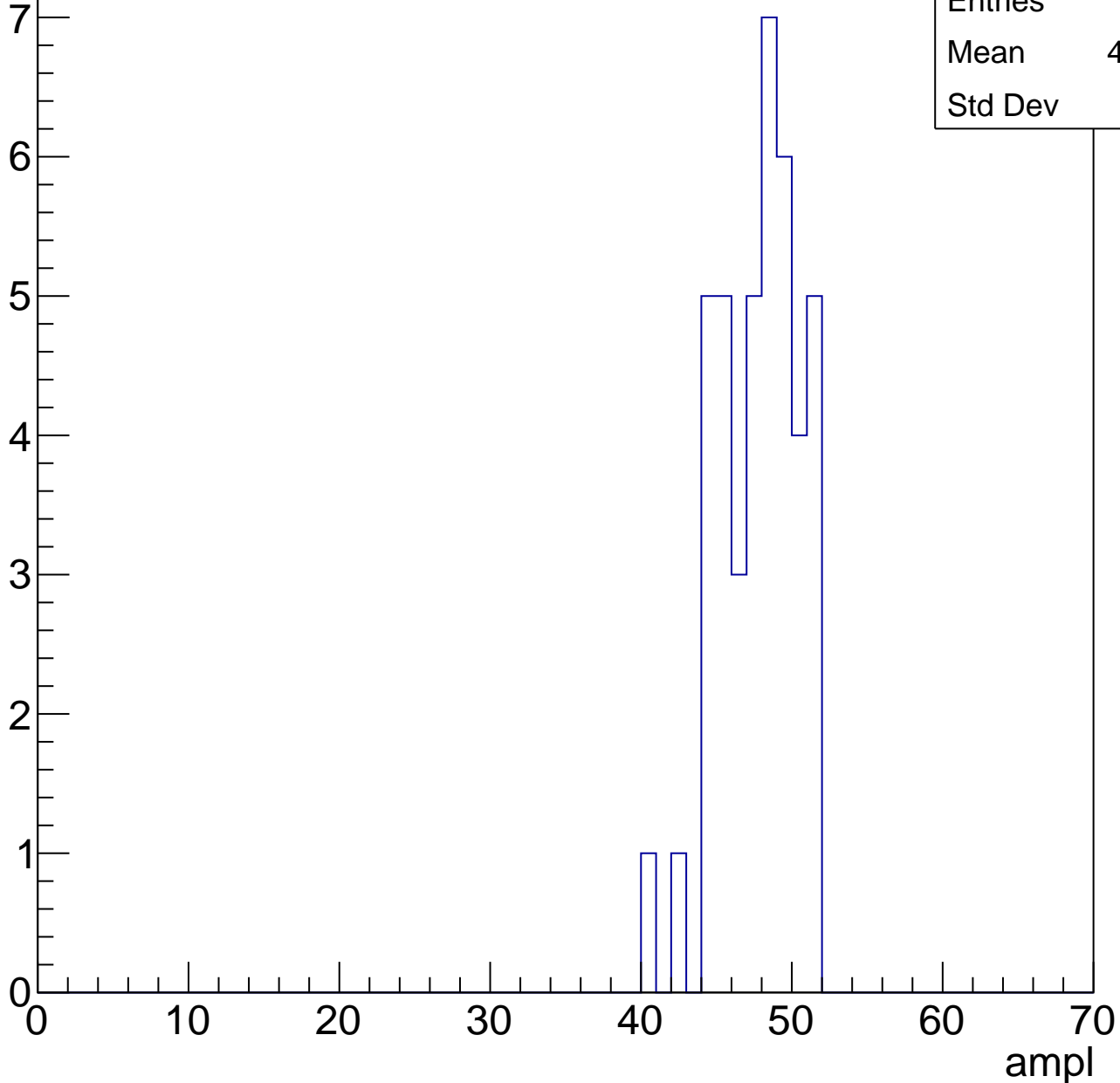


# B1L000S, U25-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

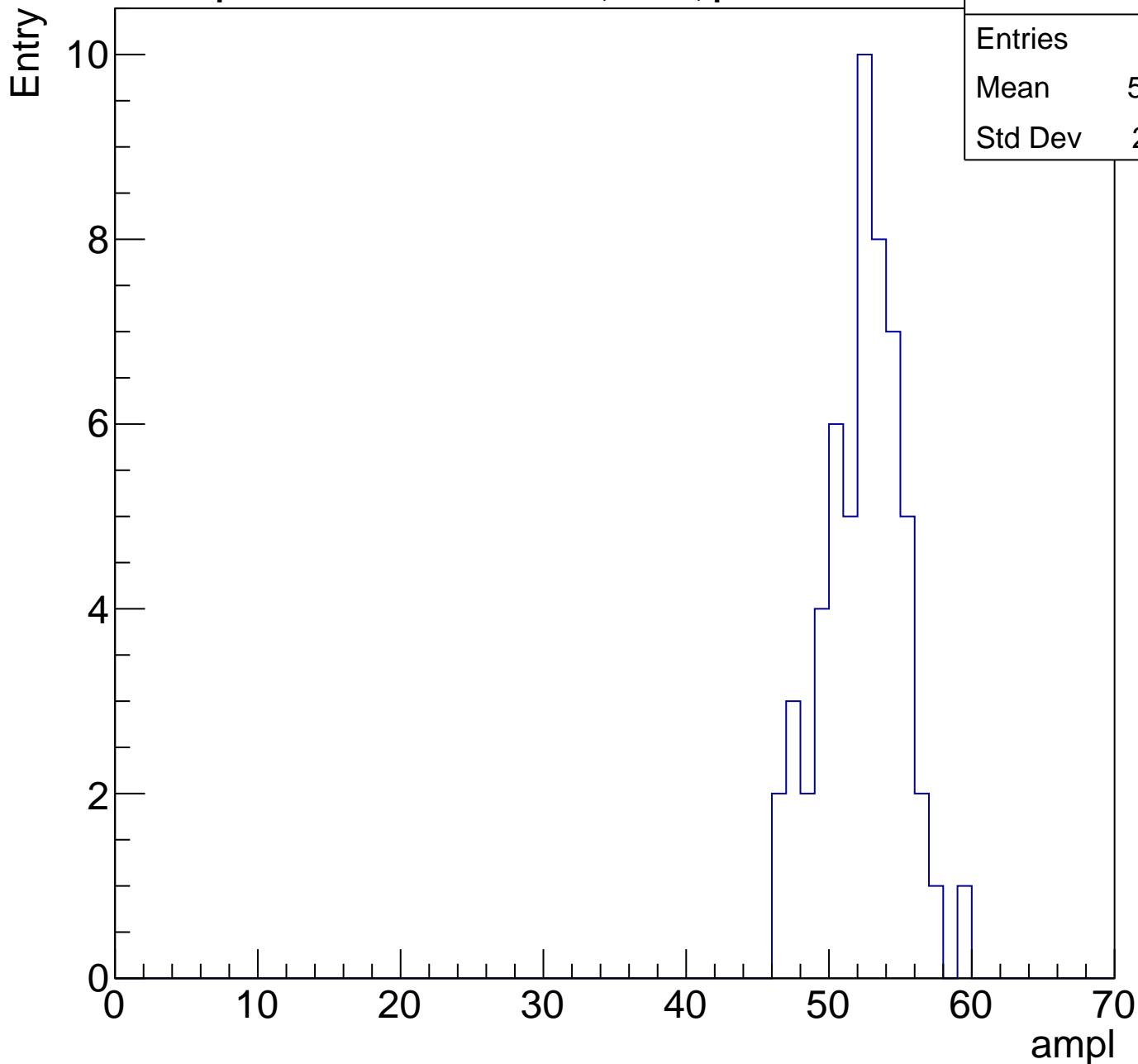
Entries	42
Mean	47.26
Std Dev	2.61



# B1L000S, U25-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	56
Mean	51.88
Std Dev	2.771

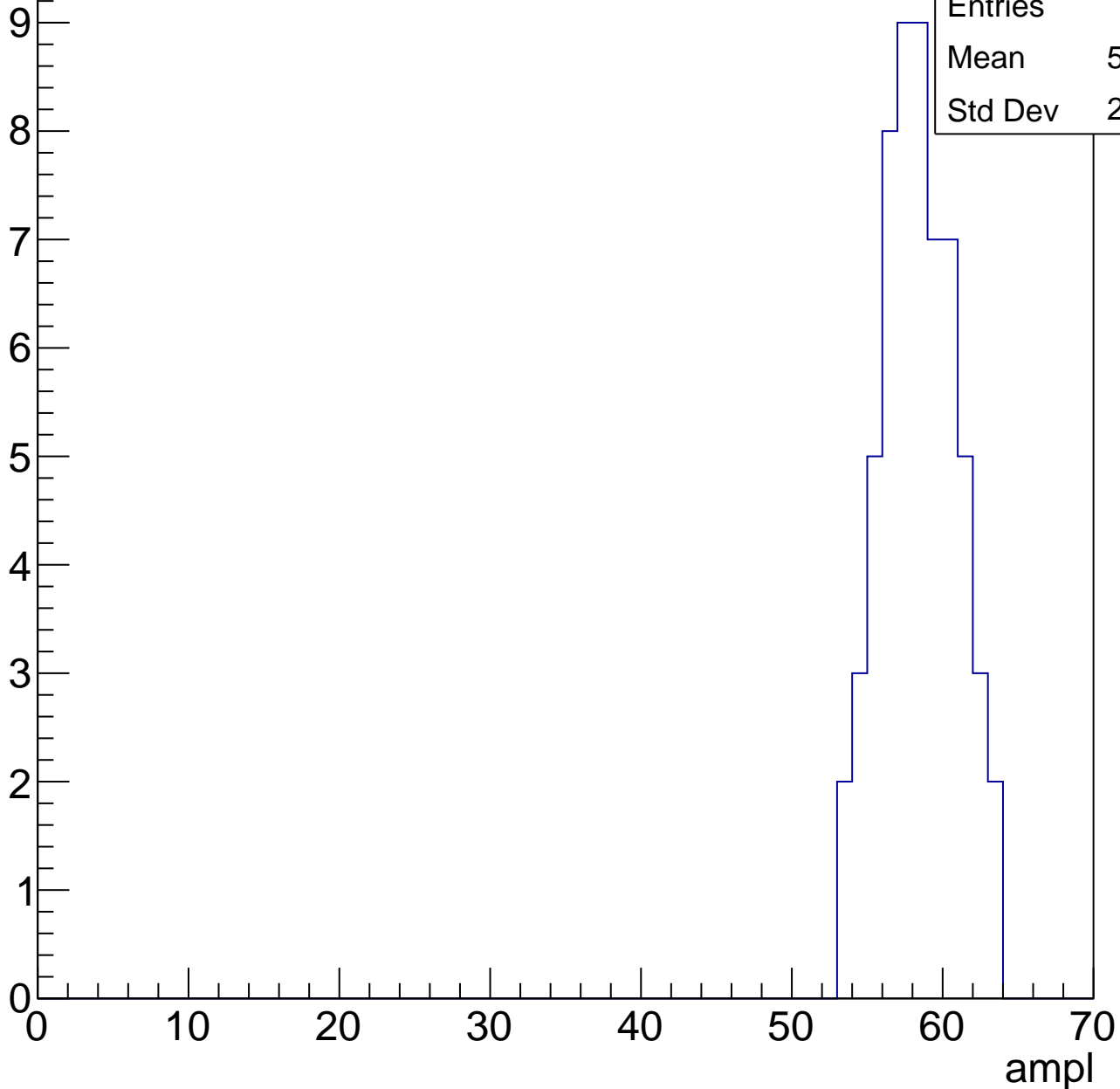


# B1L000S, U25-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	60
Mean	57.93
Std Dev	2.455

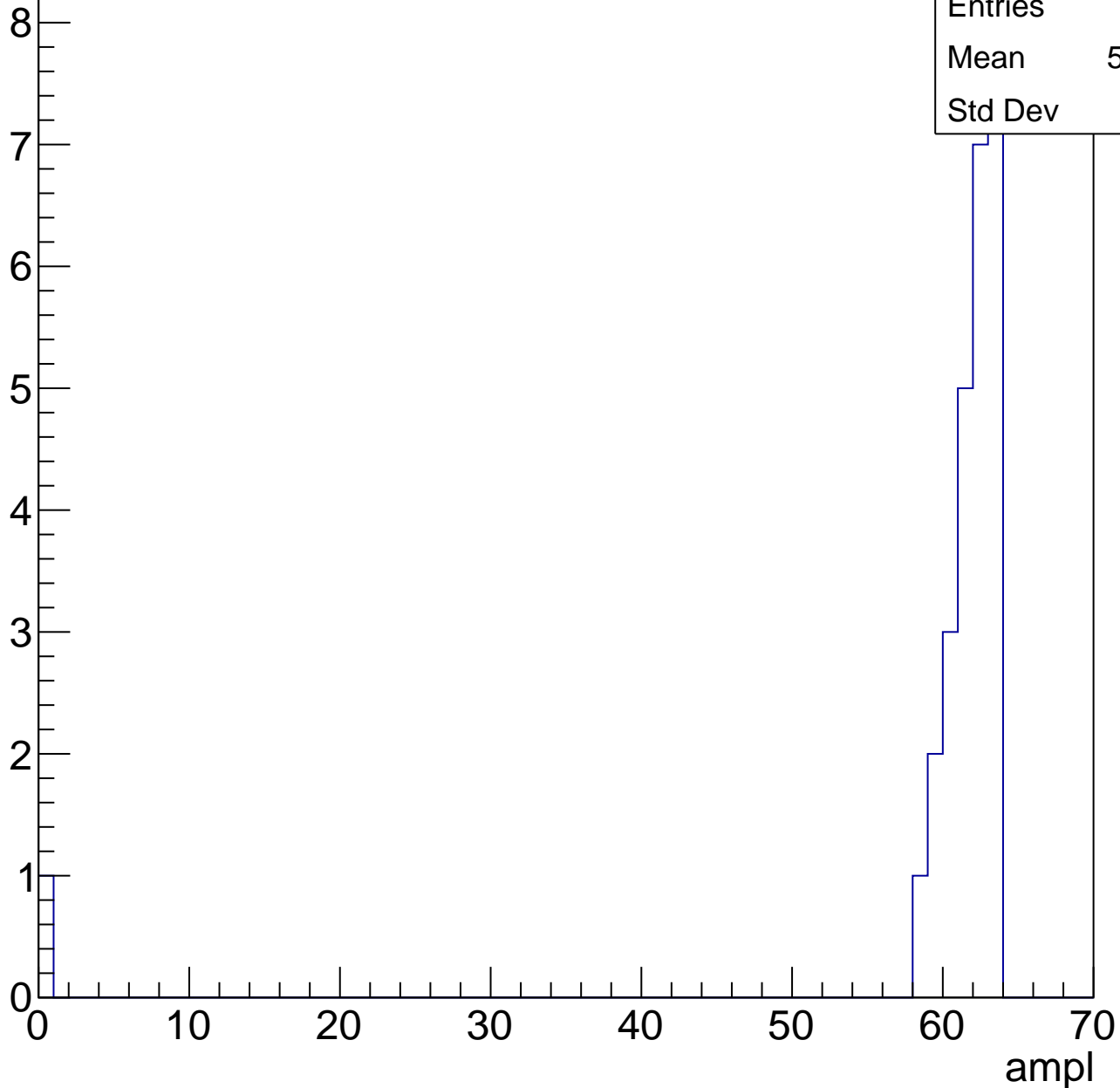


# B1L000S, U25-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	27
Mean	59.22
Std Dev	11.7





# B1L000S, U25-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch125, adc0

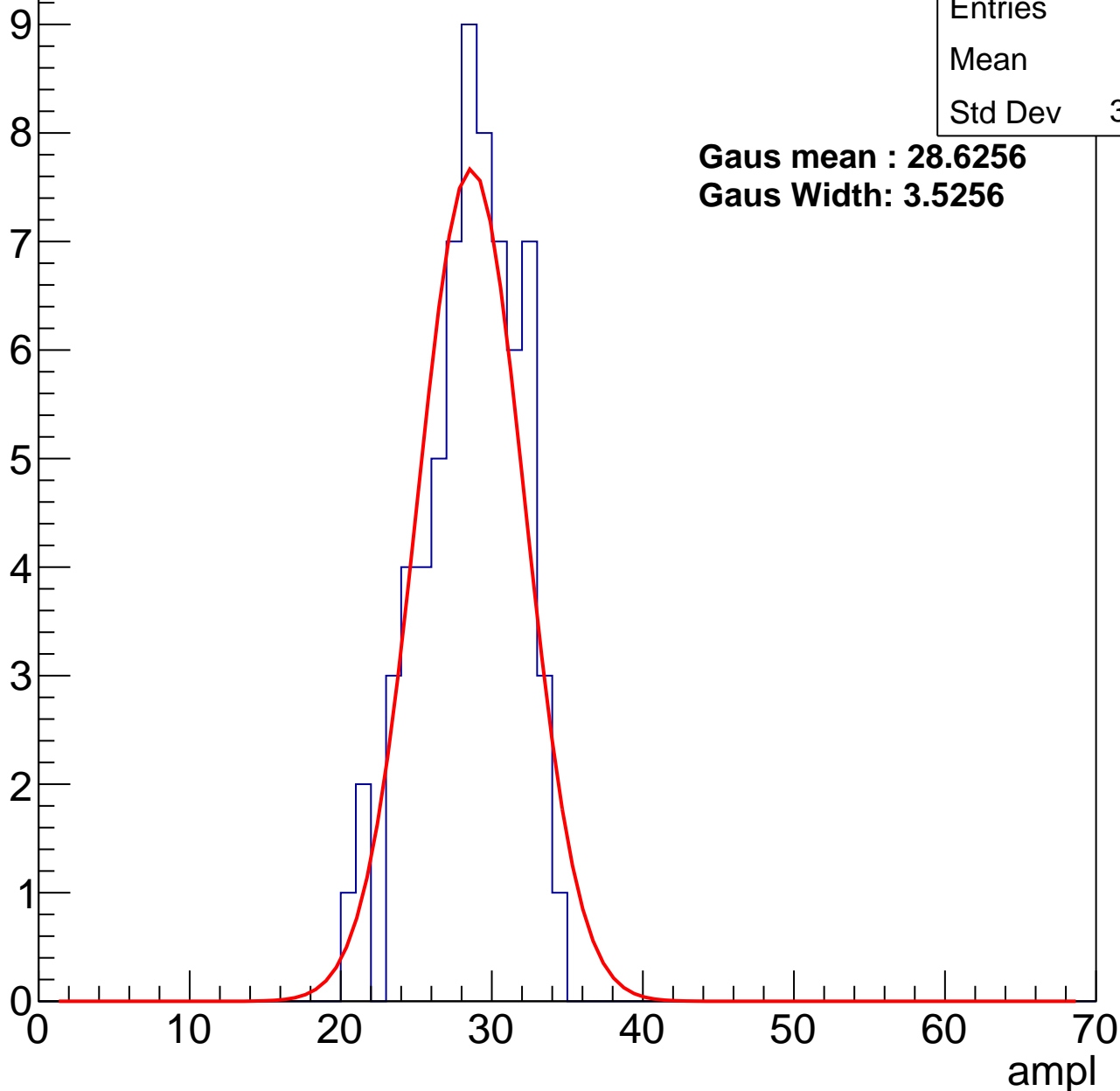
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	28.1
Std Dev	3.177

**Gaus mean : 28.6256**

**Gaus Width: 3.5256**



# B1L000S, U25-ch125, adc1

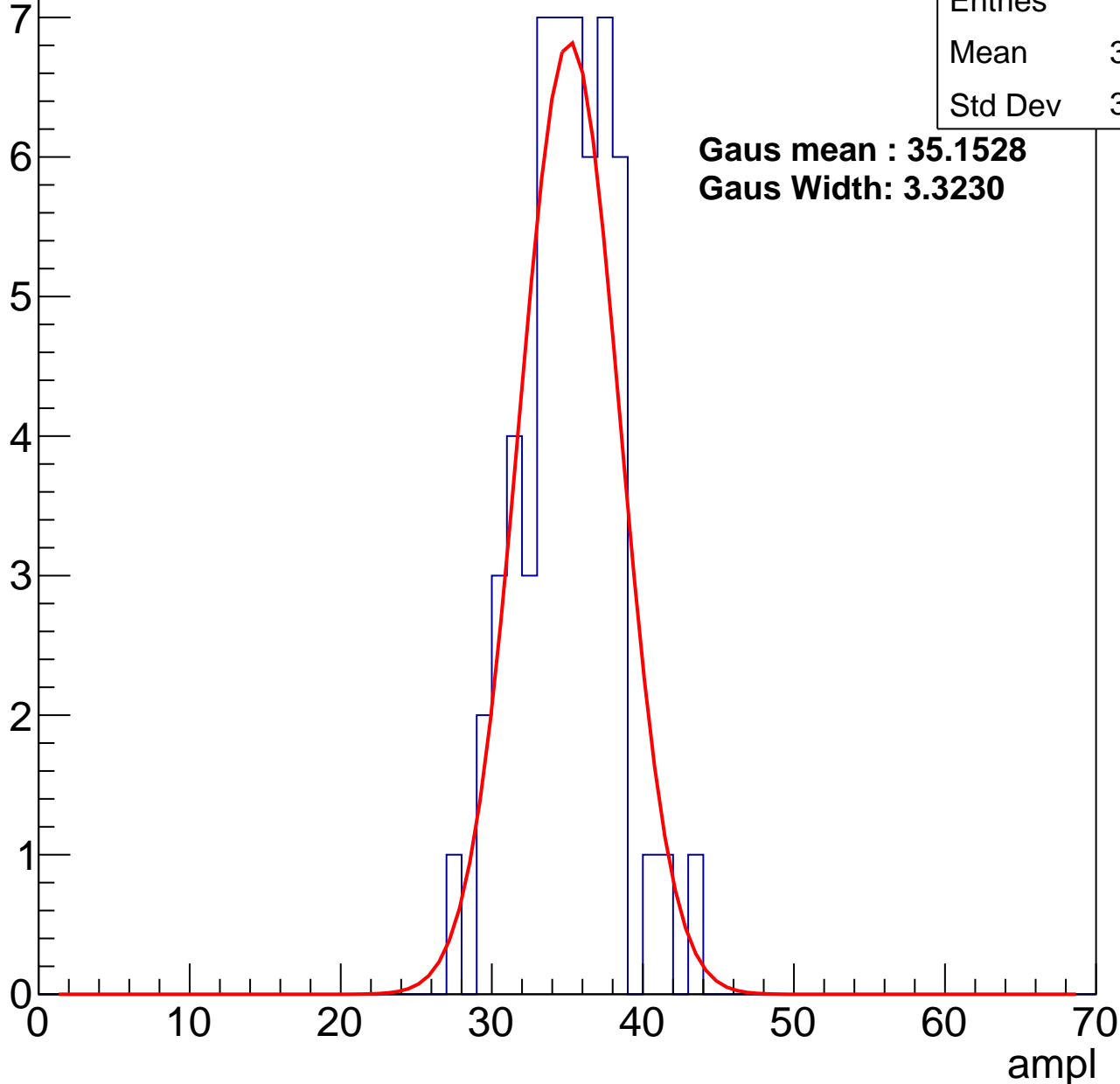
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	56
Mean	34.57
Std Dev	3.104

**Gaus mean : 35.1528**

**Gaus Width: 3.3230**



# B1L000S, U25-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	70
Mean	40.69
Std Dev	3.37

**Gaus mean : 41.4194**

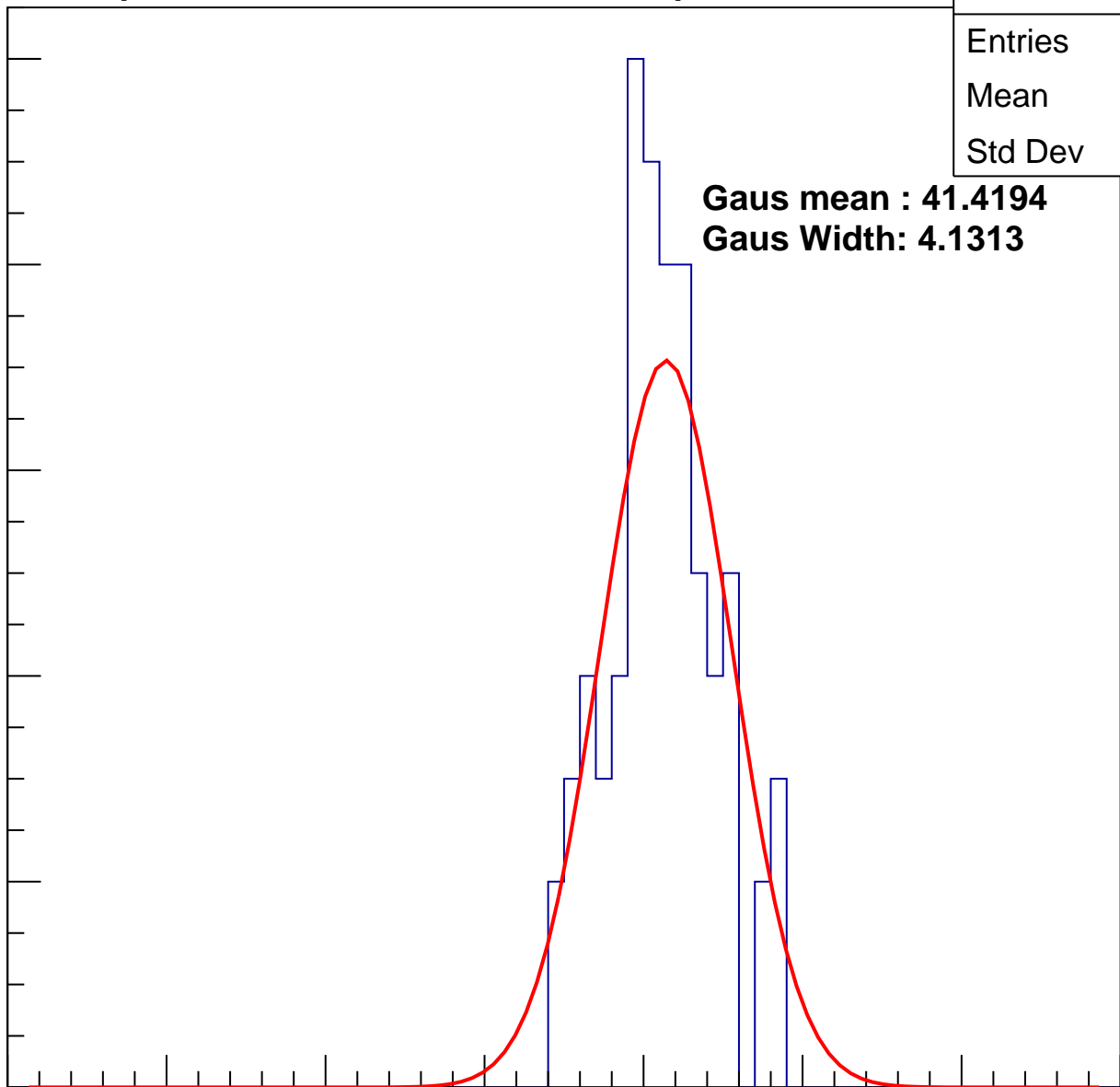
**Gaus Width: 4.1313**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L000S, U25-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entries	72
Mean	47.69
Std Dev	3.222

Entry

10

8

6

4

2

0

0

10

20

30

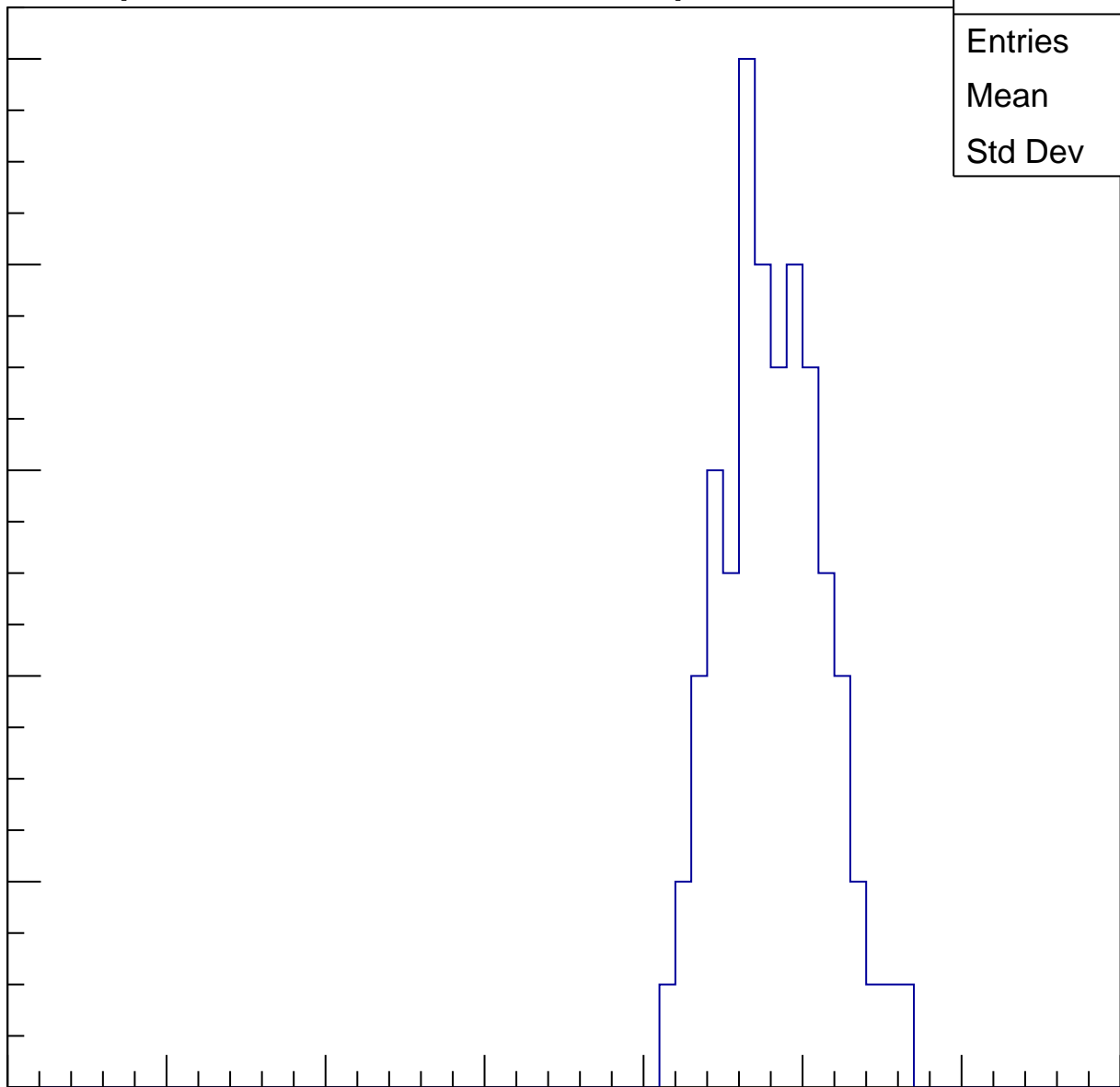
40

50

60

70

ampl

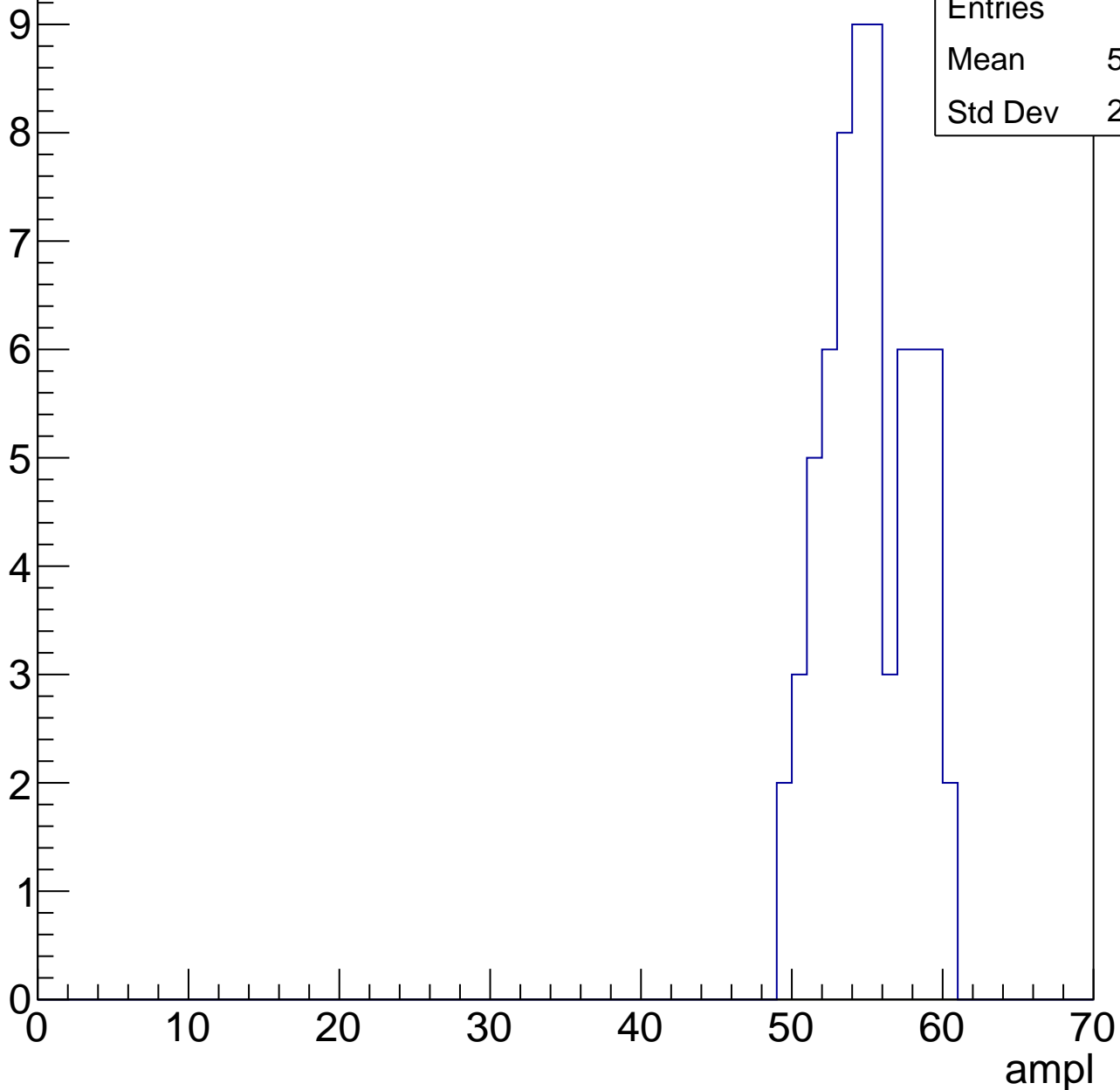


# B1L000S, U25-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	65
Mean	54.65
Std Dev	2.885

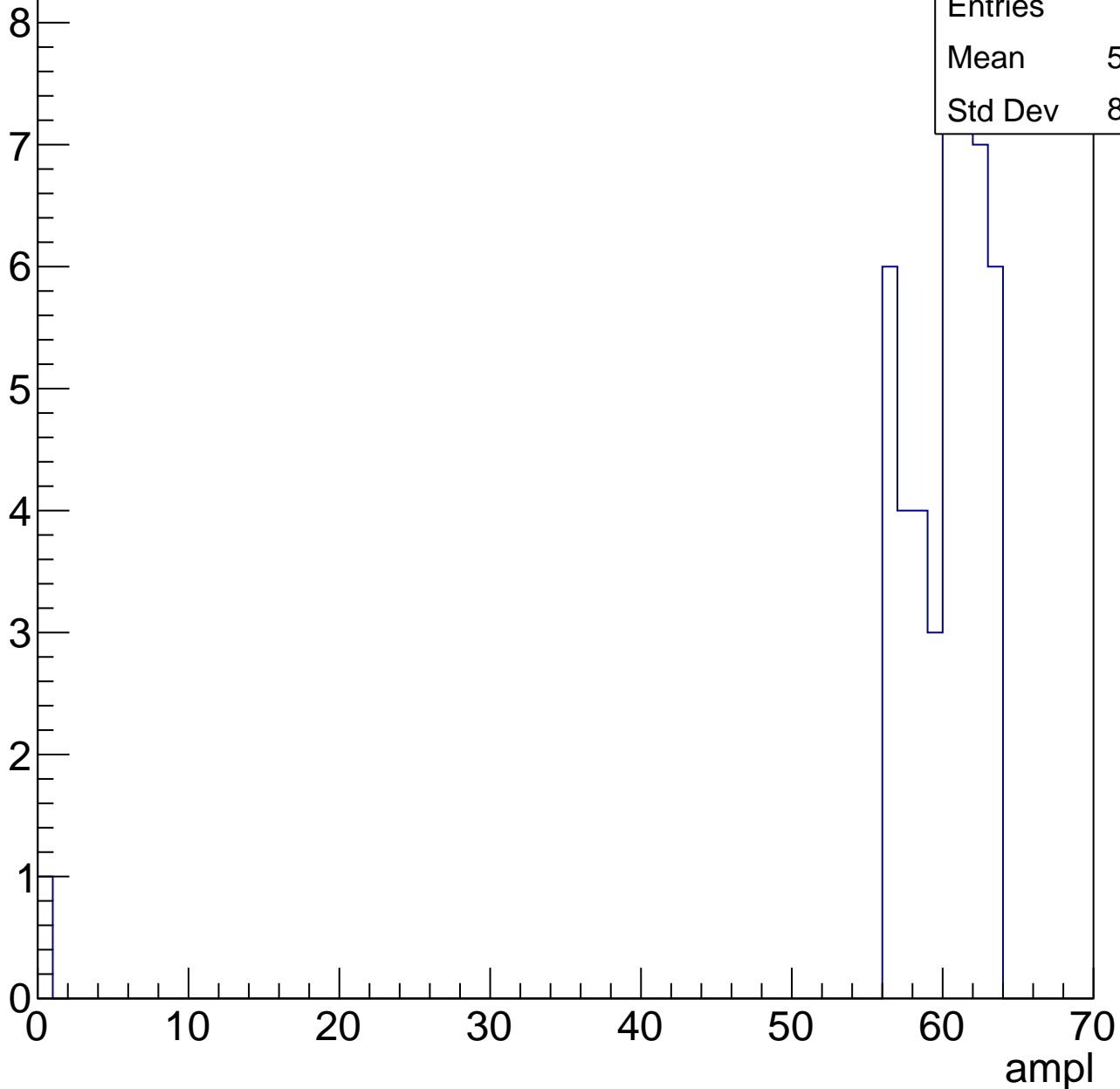


# B1L000S, U25-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

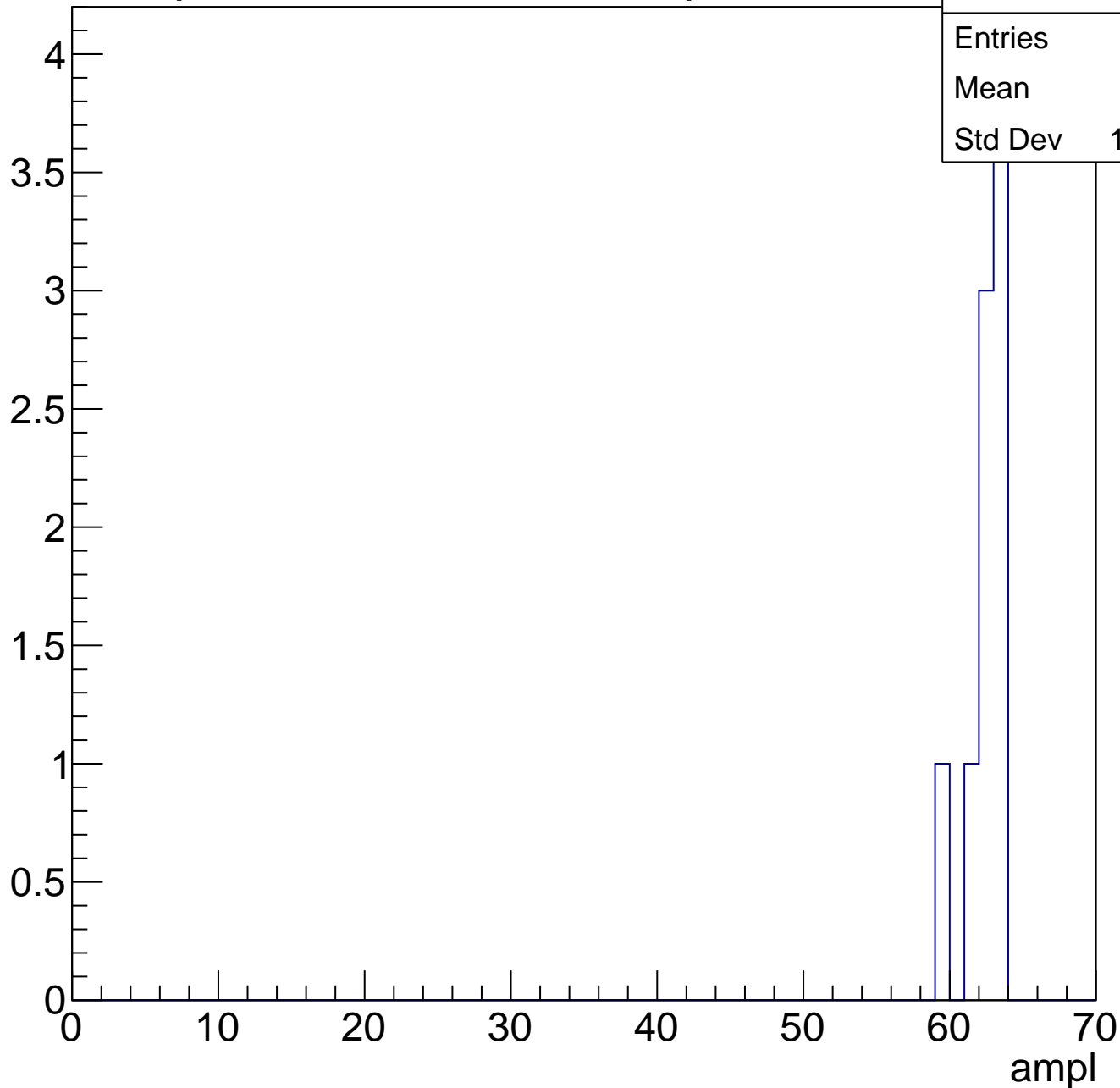
Entries	47
Mean	58.57
Std Dev	8.927



# B1L000S, U25-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry





# B1L000S, U25-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch126, adc0

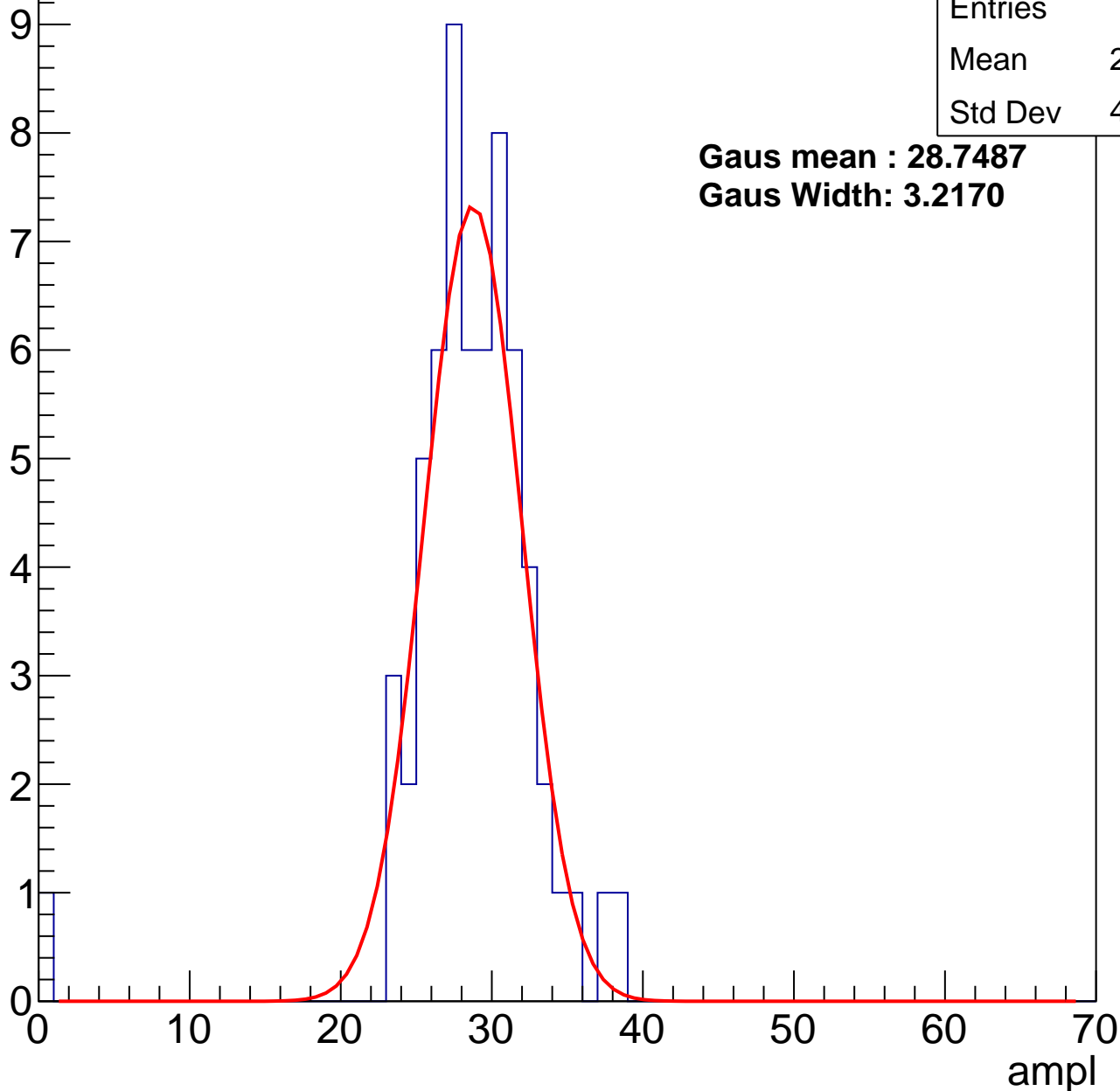
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	62
Mean	28.18
Std Dev	4.818

**Gaus mean : 28.7487**

**Gaus Width: 3.2170**



# B1L000S, U25-ch126, adc1

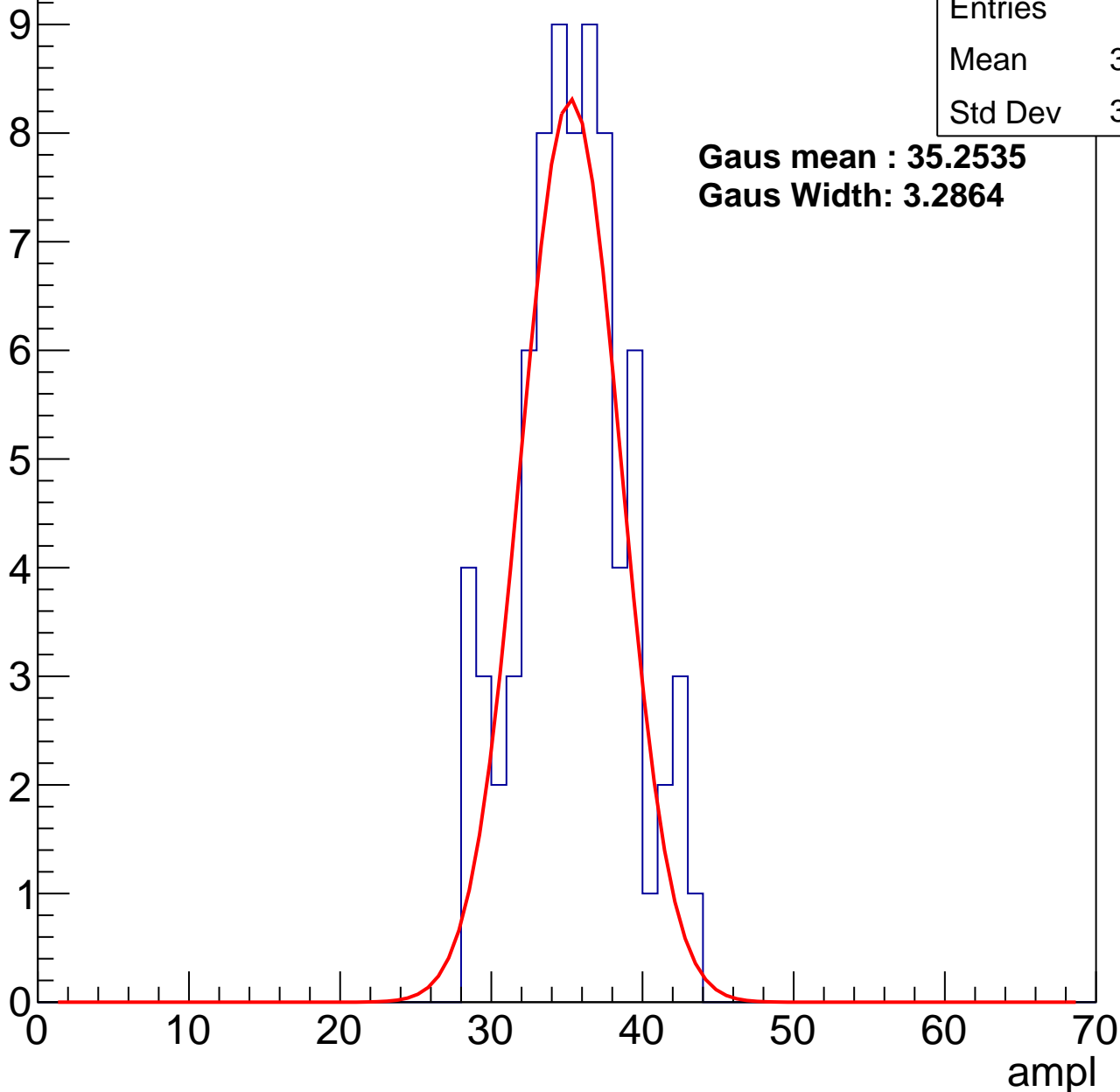
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	77
Mean	34.95
Std Dev	3.564

**Gaus mean : 35.2535**

**Gaus Width: 3.2864**



# B1L000S, U25-ch126, adc2

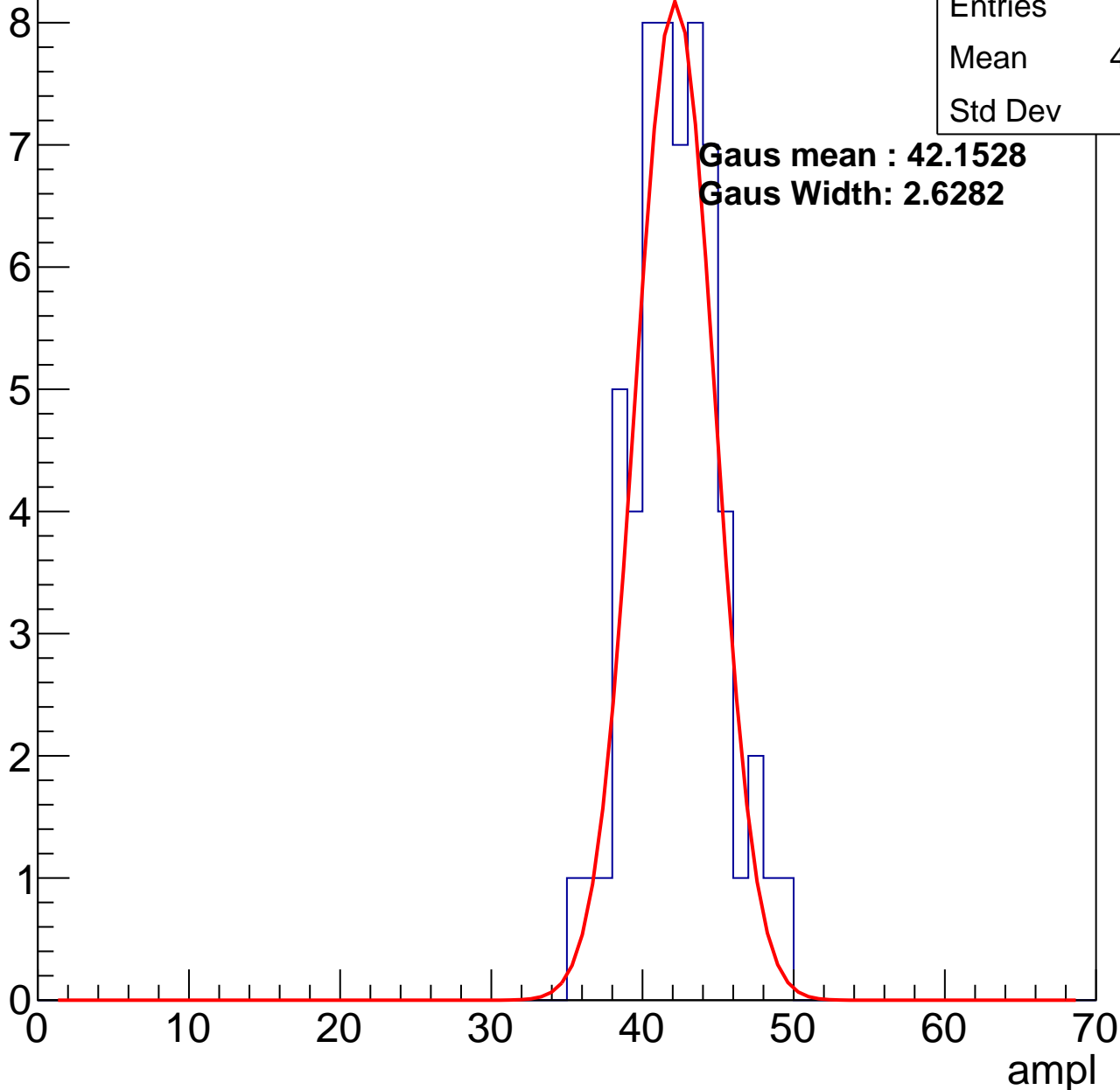
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	41.78
Std Dev	2.87

**Gaus mean : 42.1528**

**Gaus Width: 2.6282**

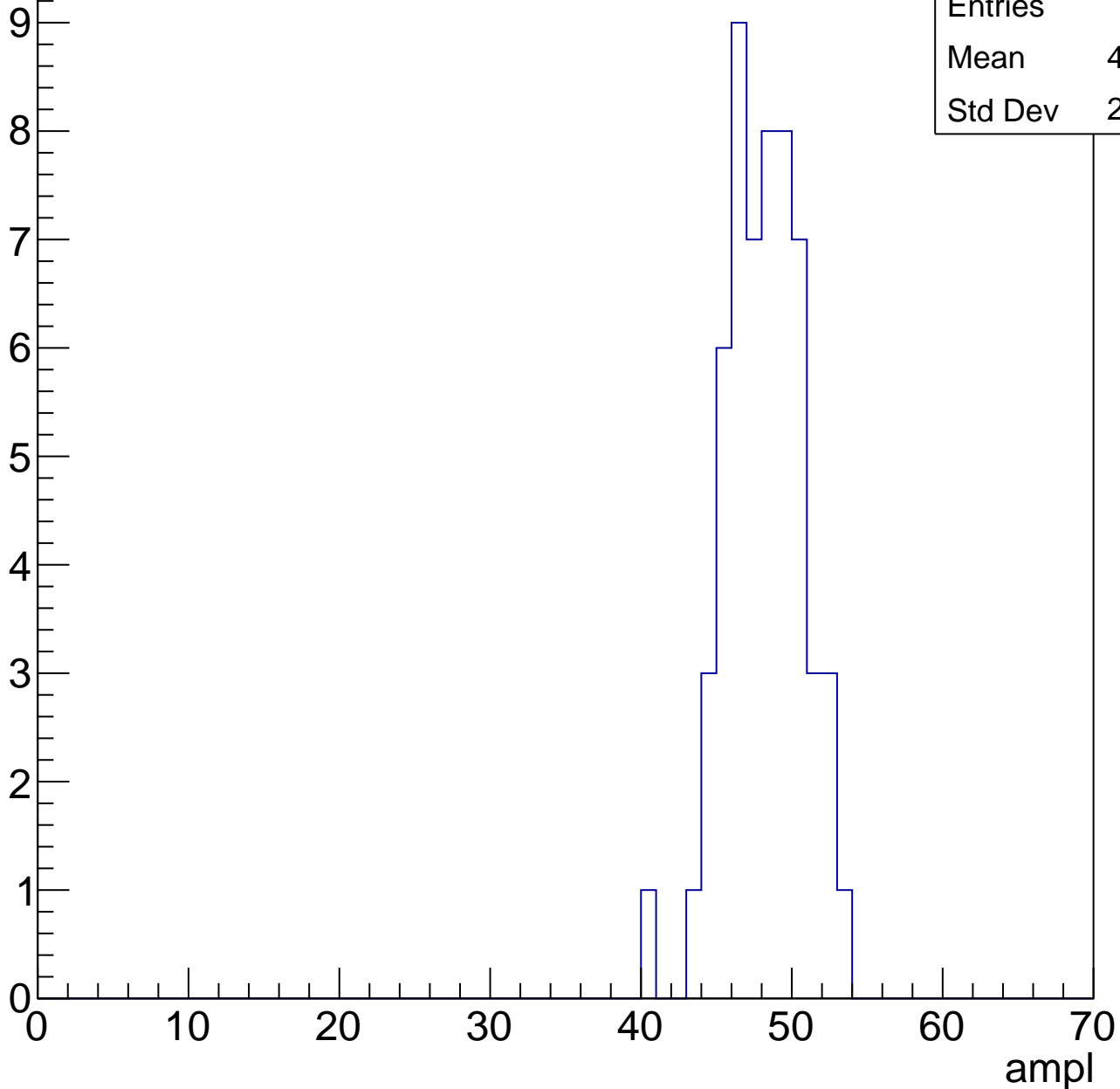


# B1L000S, U25-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	57
Mean	47.65
Std Dev	2.524

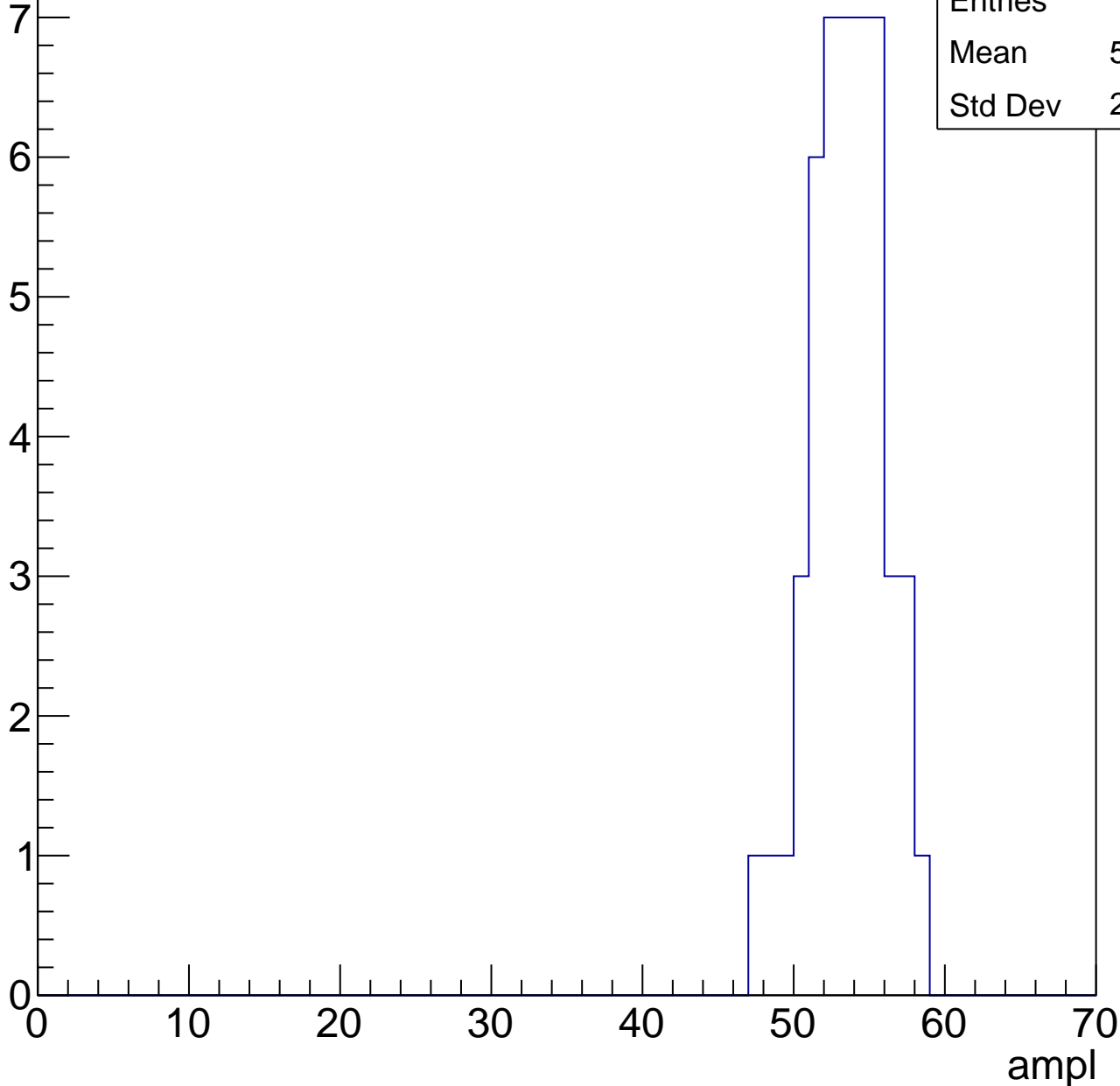


# B1L000S, U25-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	47
Mean	53.09
Std Dev	2.395

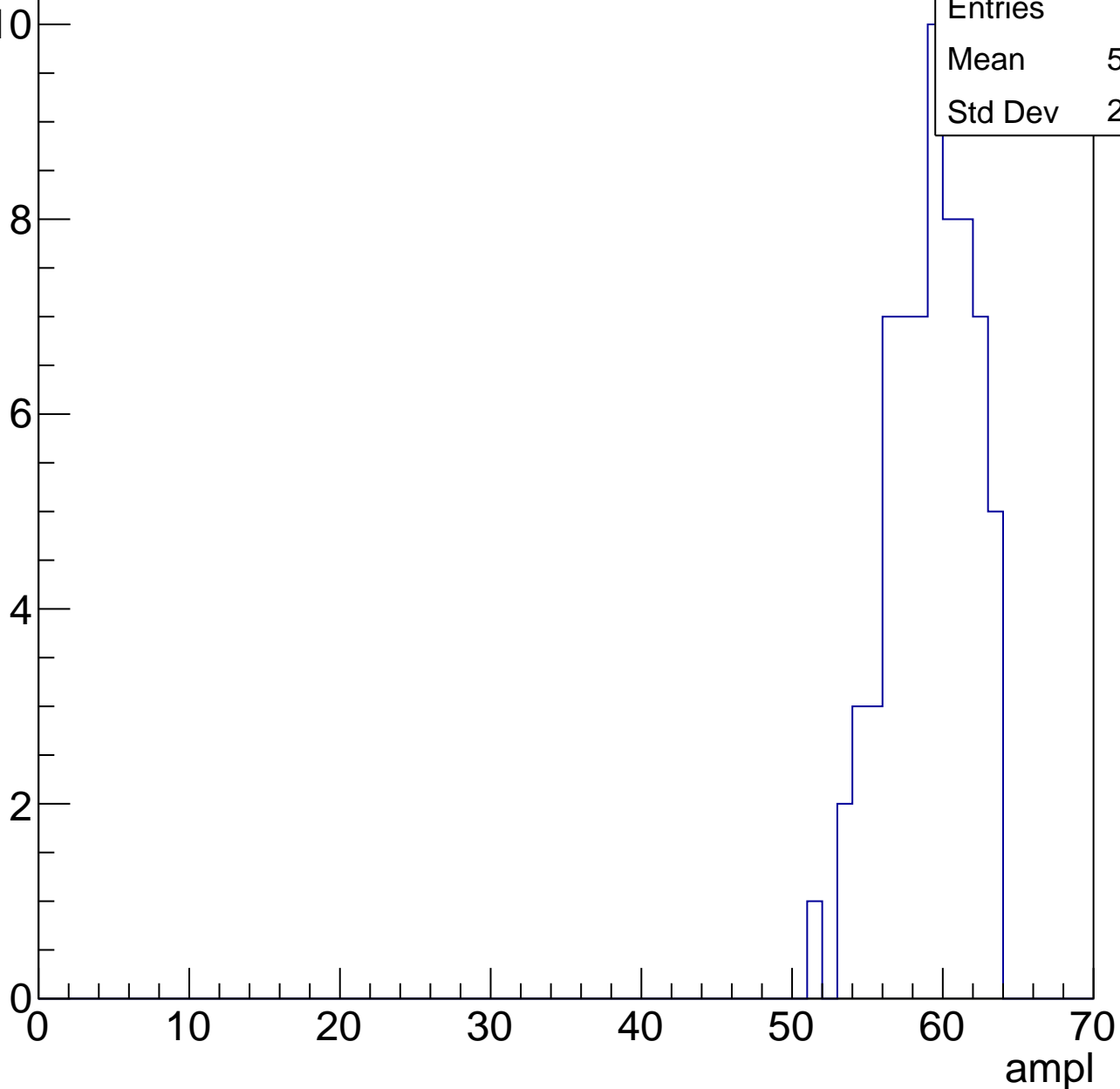


# B1L000S, U25-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

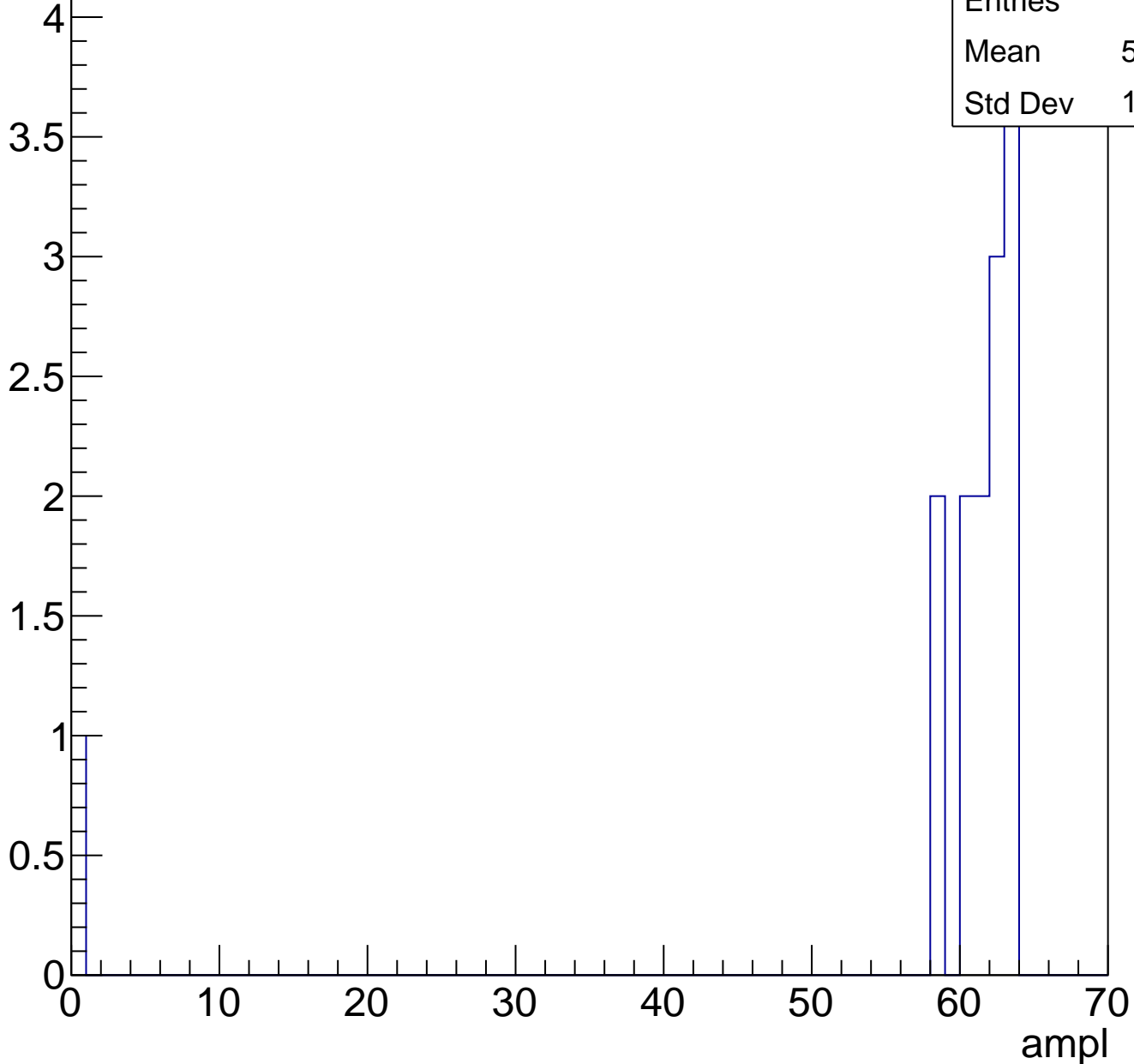
Entries	68
Mean	58.65
Std Dev	2.796



# B1L000S, U25-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



Entries	14
Mean	56.86
Std Dev	15.86



# B1L000S, U25-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch127, adc0

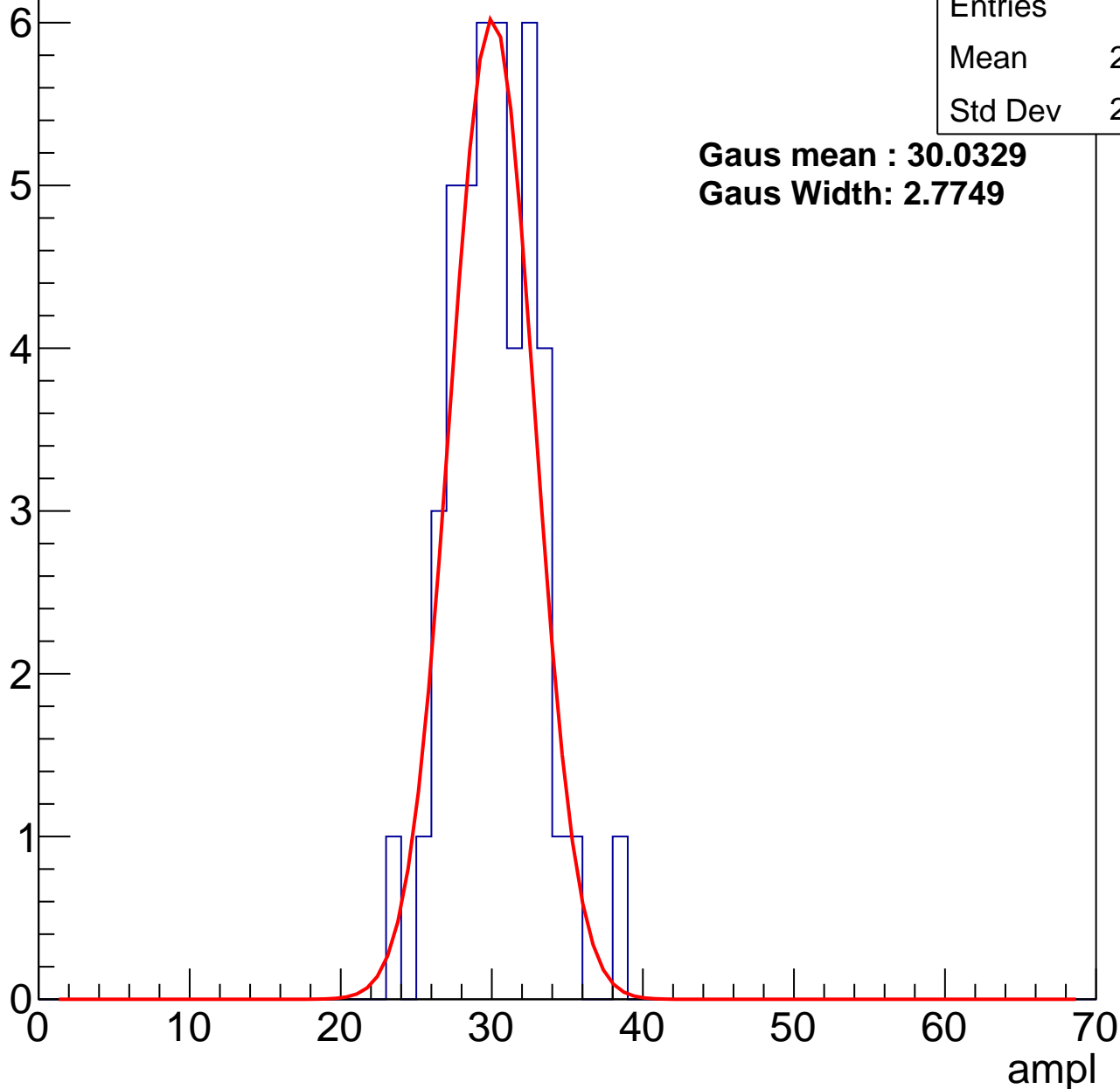
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	44
Mean	29.77
Std Dev	2.859

**Gaus mean : 30.0329**

**Gaus Width: 2.7749**



# B1L000S, U25-ch127, adc1

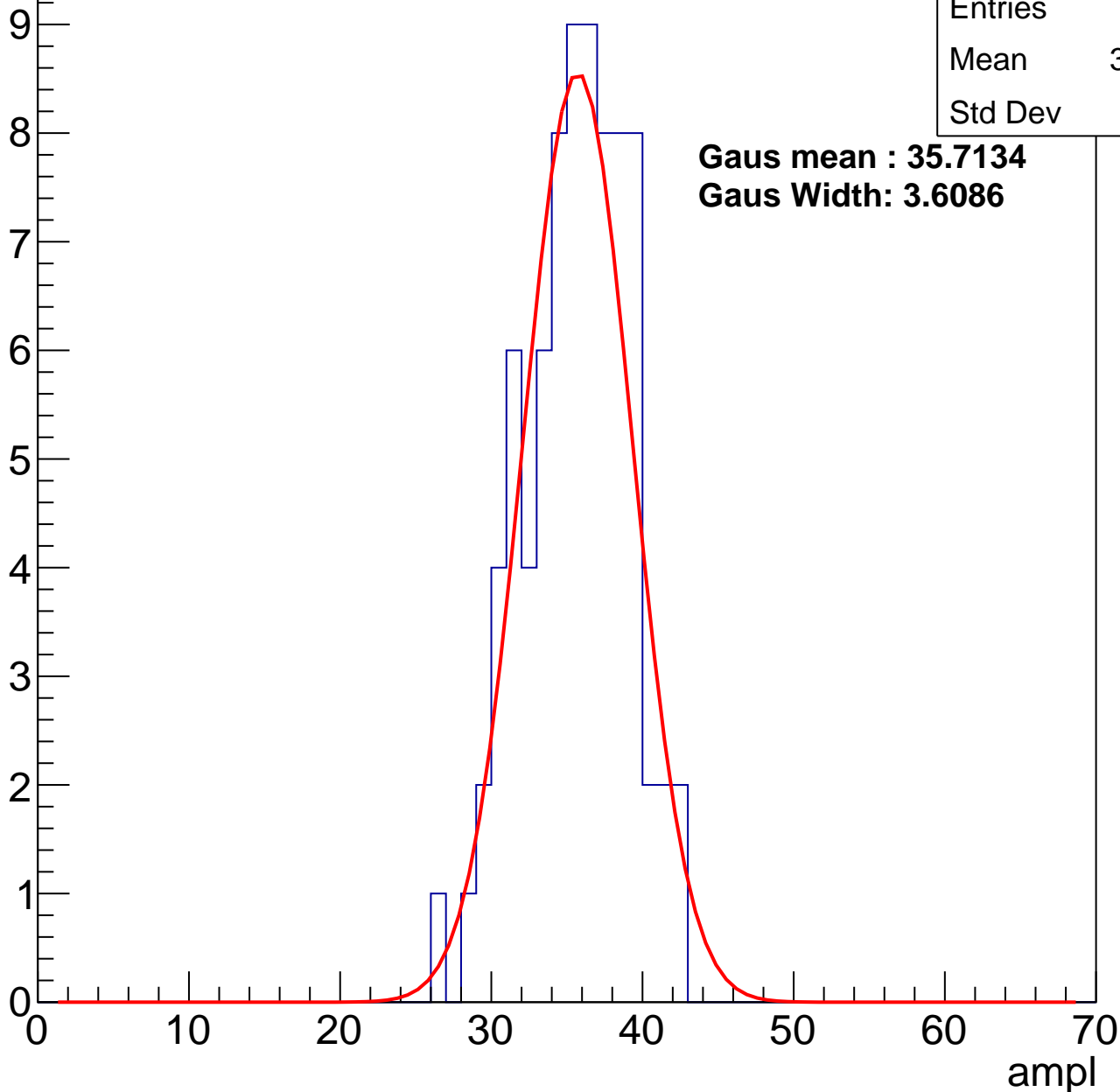
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	80
Mean	35.16
Std Dev	3.4

**Gaus mean : 35.7134**

**Gaus Width: 3.6086**



# B1L000S, U25-ch127, adc2

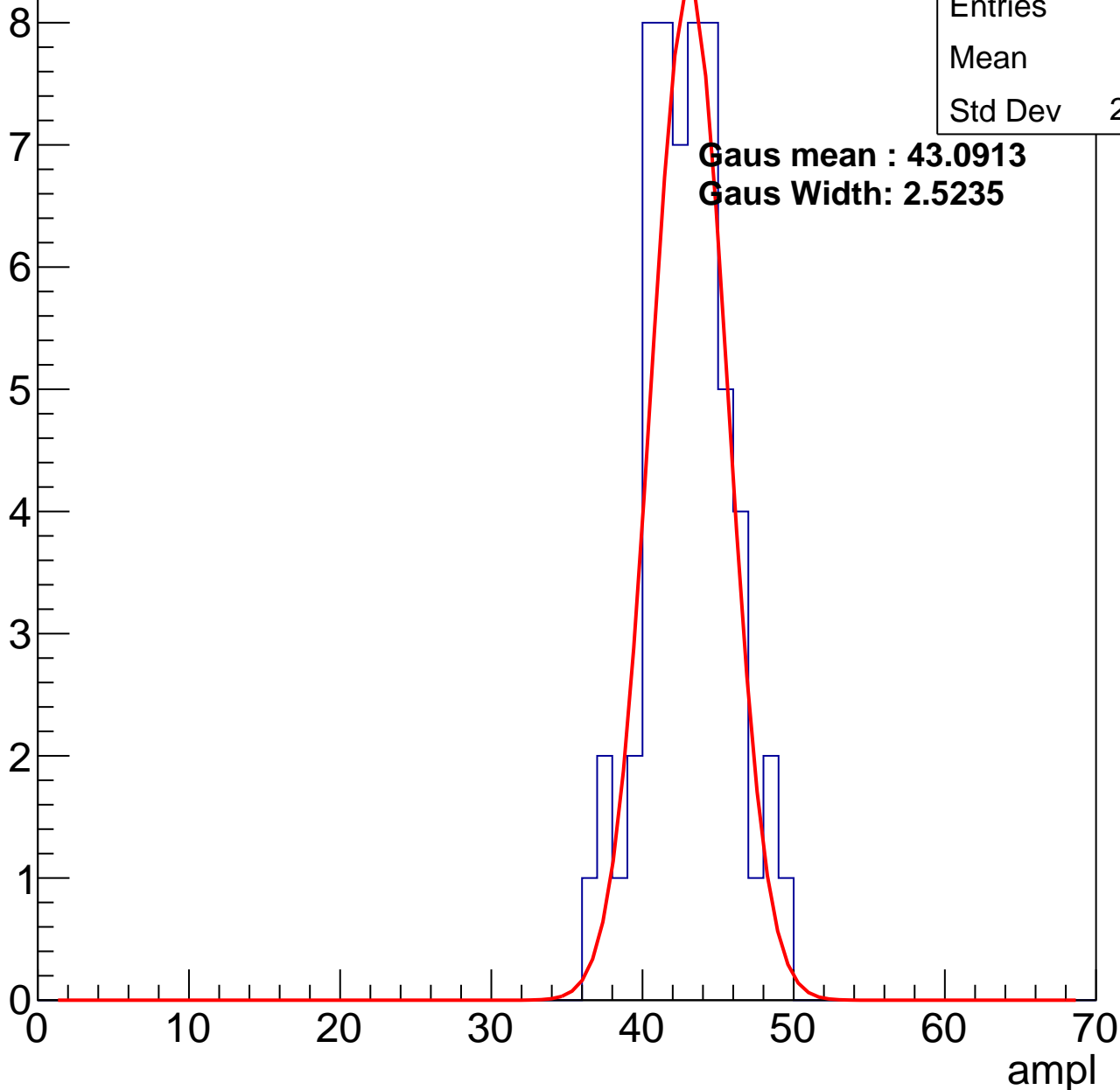
calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	58
Mean	42.5
Std Dev	2.756

**Gaus mean : 43.0913**

**Gaus Width: 2.5235**

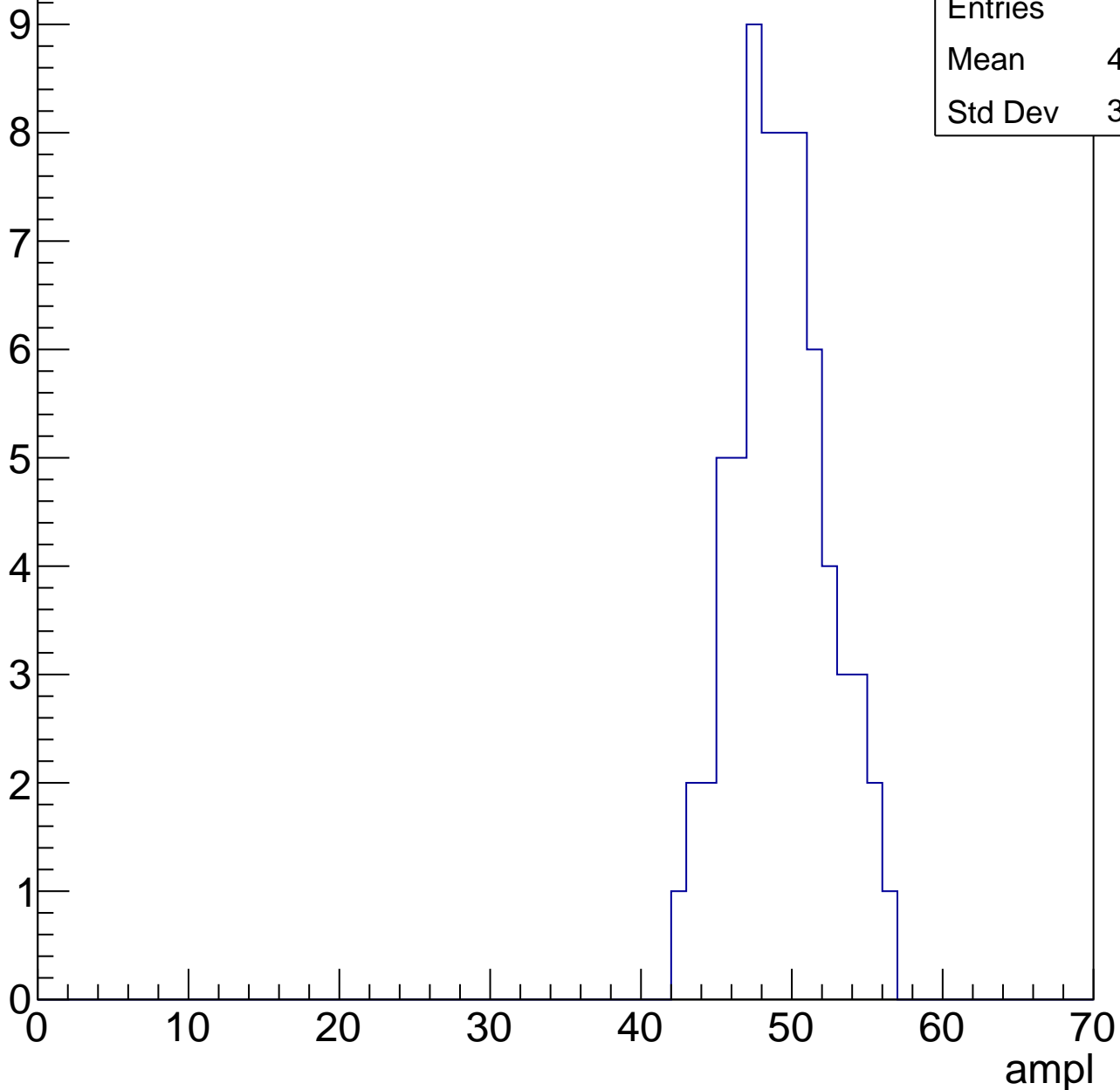


# B1L000S, U25-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	67
Mean	48.82
Std Dev	3.114

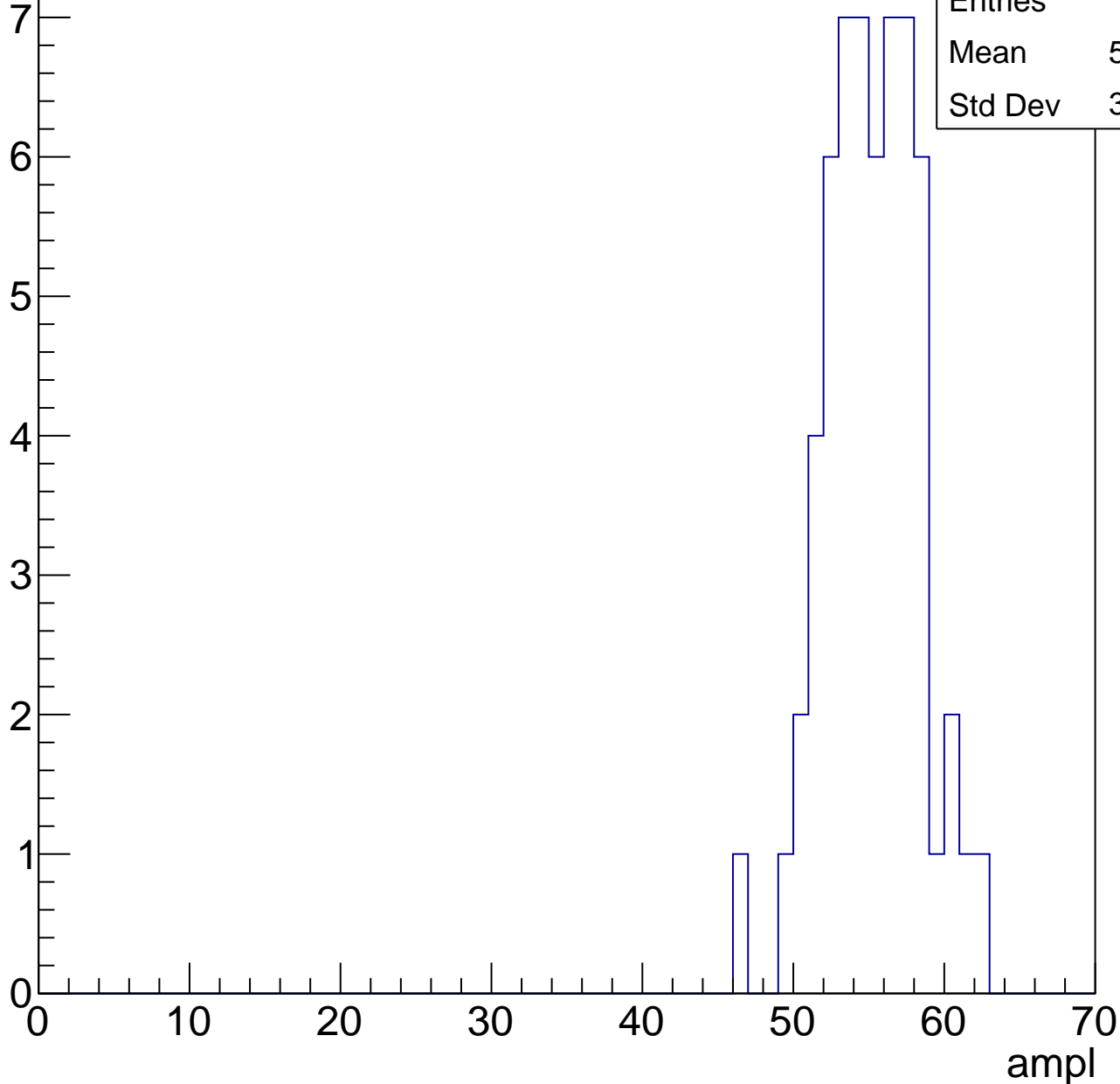


# B1L000S, U25-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	59
Mean	54.76
Std Dev	3.072

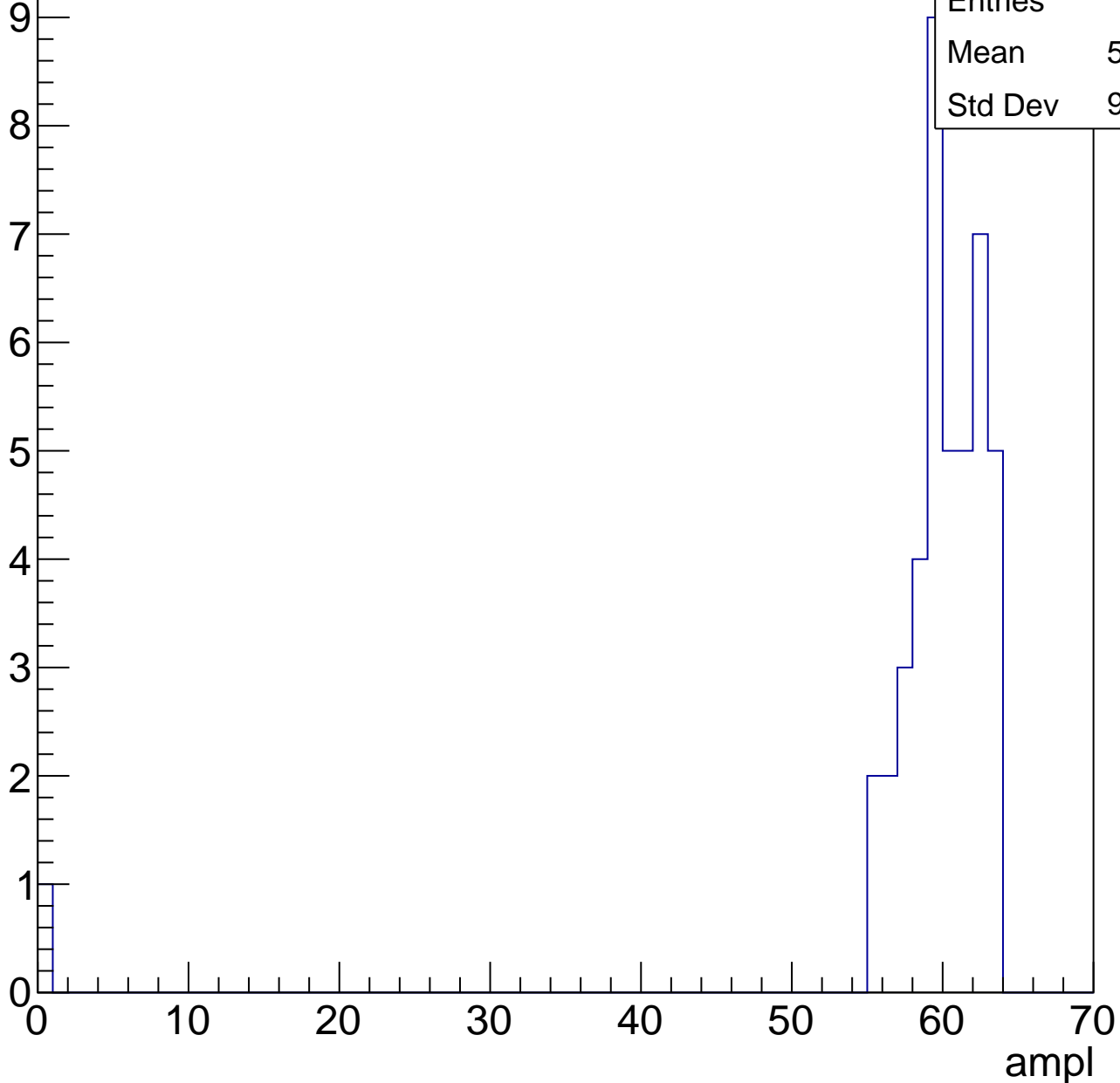


# B1L000S, U25-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	43
Mean	58.37
Std Dev	9.274

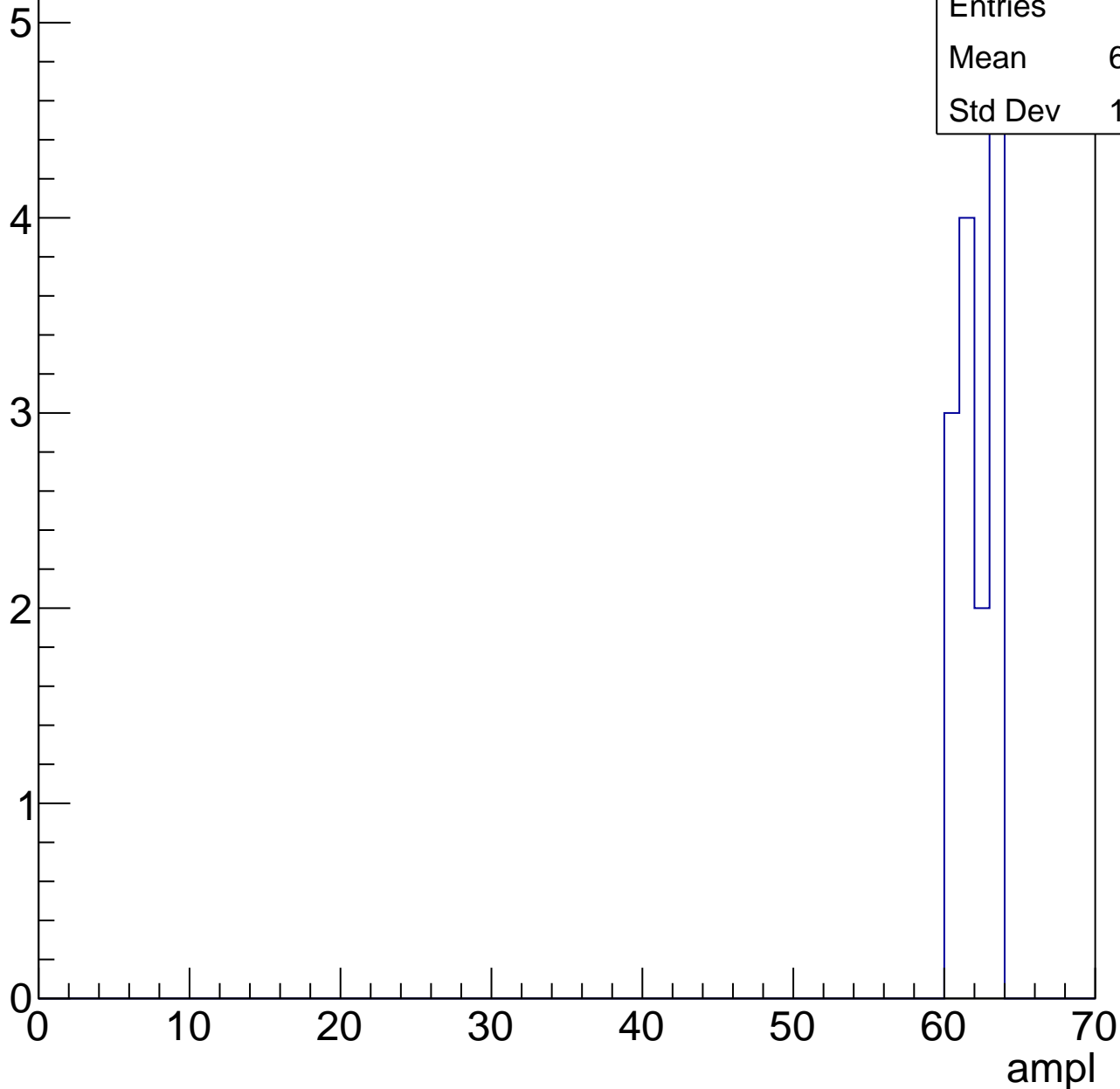


# B1L000S, U25-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

Entries	14
Mean	61.64
Std Dev	1.172





# B1L000S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry



# B1L000S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#3, port B3

Entry

