

B0L101S, U14-ch0, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

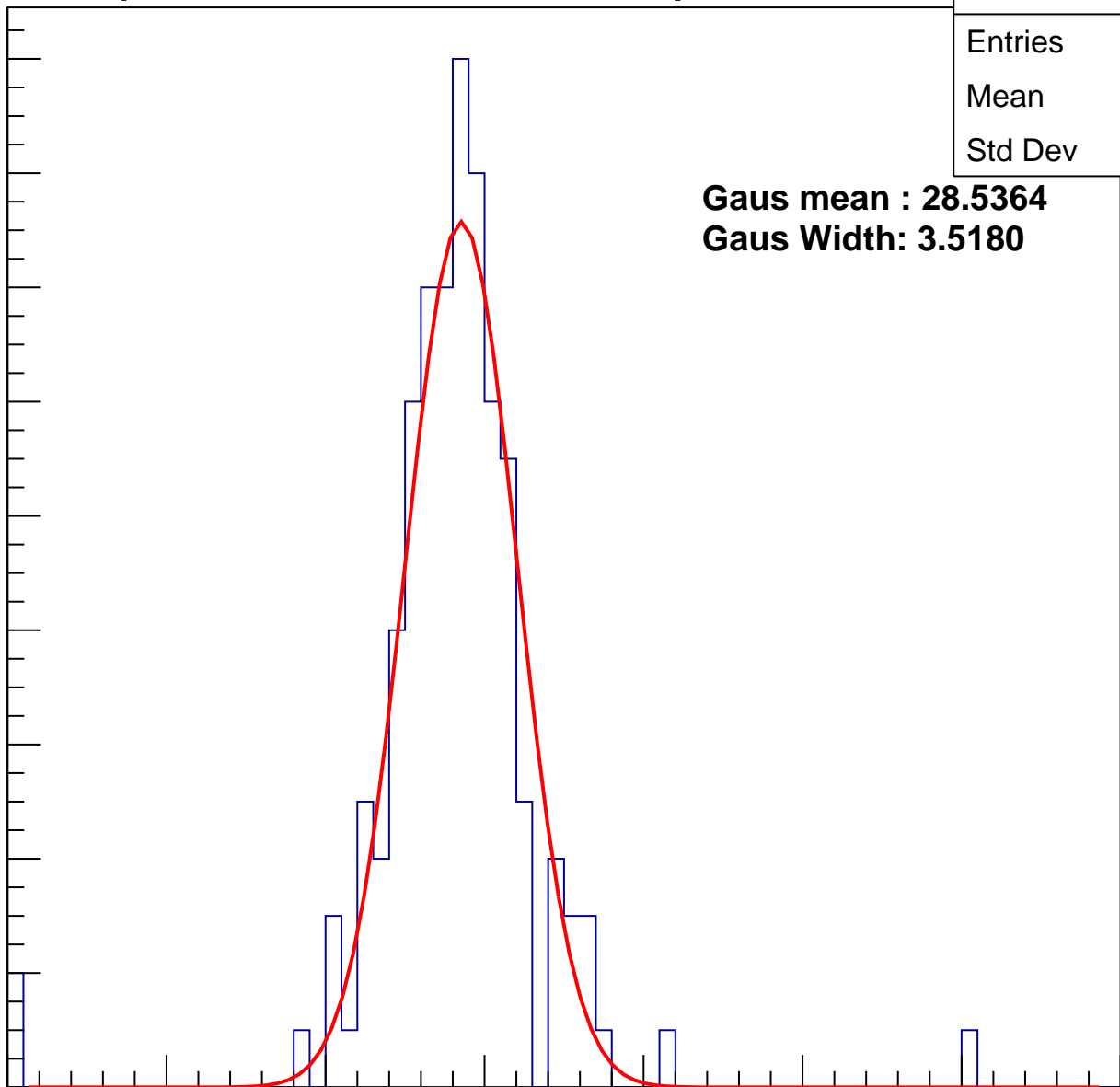
Entries	139
Mean	27.71
Std Dev	5.679

Gaus mean : 28.5364

Gaus Width: 3.5180

0 10 20 30 40 50 60 70

ampl

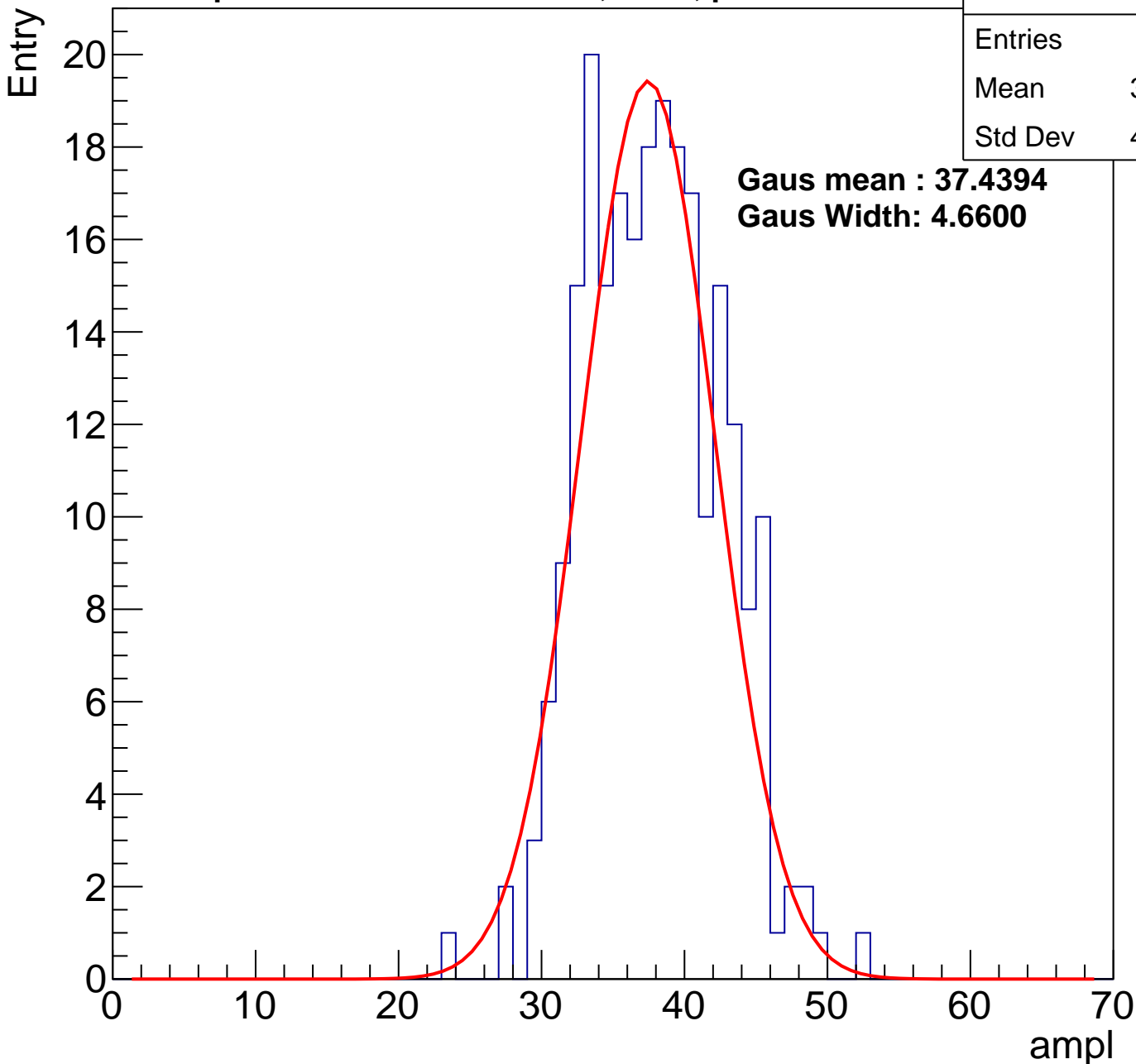


B0L101S, U14-ch0, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	238
Mean	37.43
Std Dev	4.679

Gaus mean : 37.4394
Gaus Width: 4.6600



B0L101S, U14-ch0, adc2

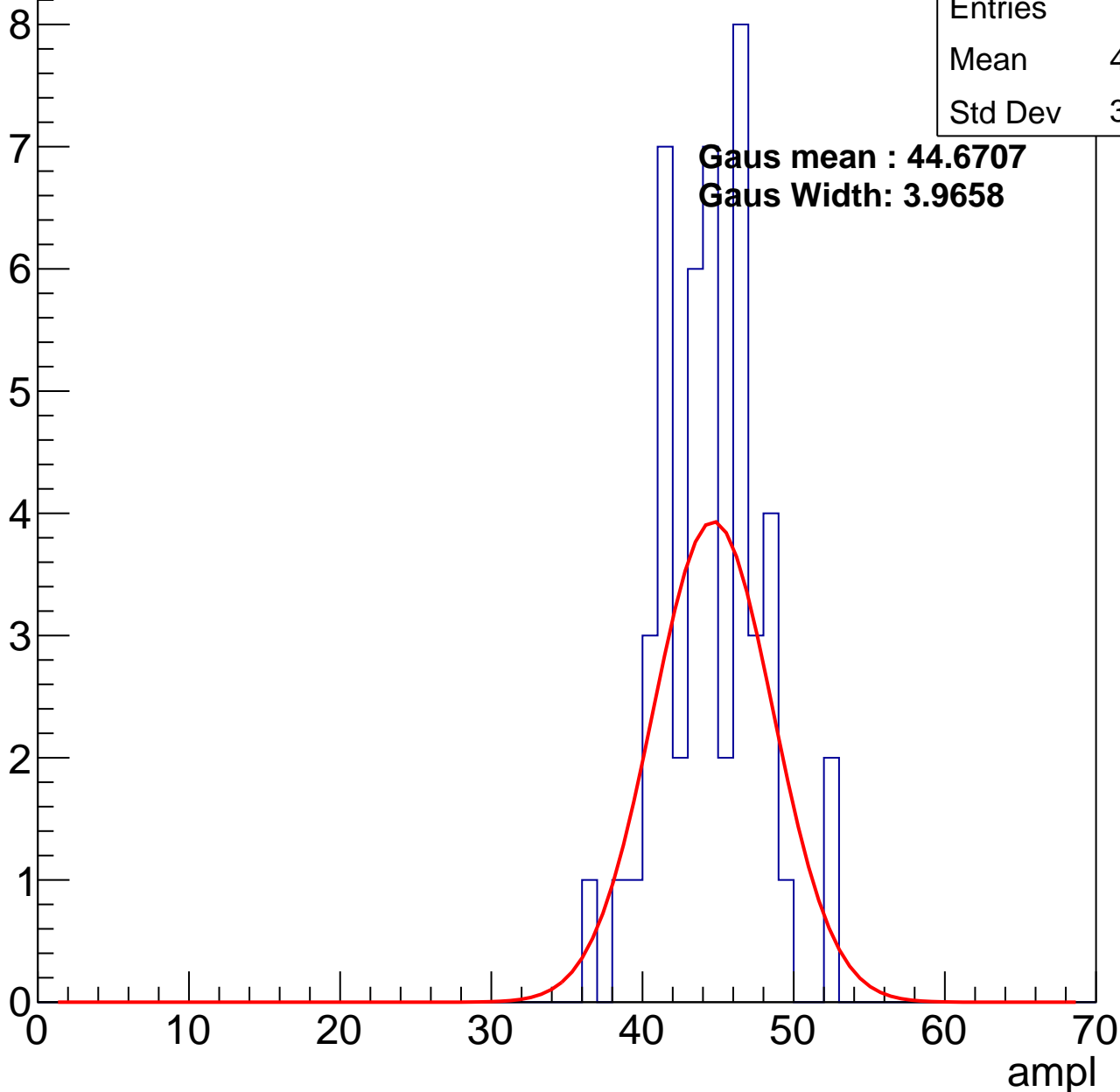
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	48
Mean	44.04
Std Dev	3.316

Gaus mean : 44.6707

Gaus Width: 3.9658

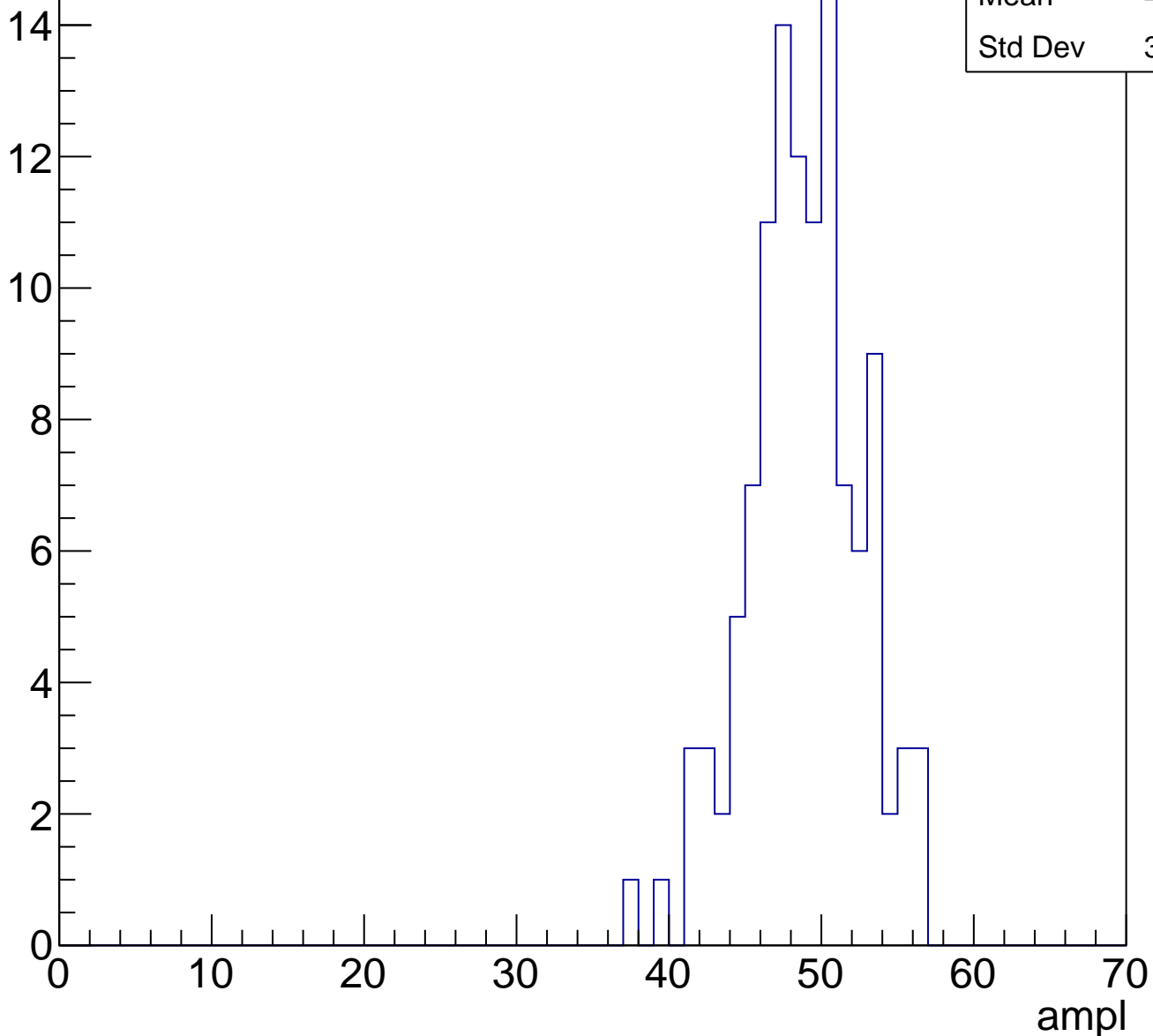


B0L101S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	48.37
Std Dev	3.682

Entry



B0L101S, U14-ch0, adc4

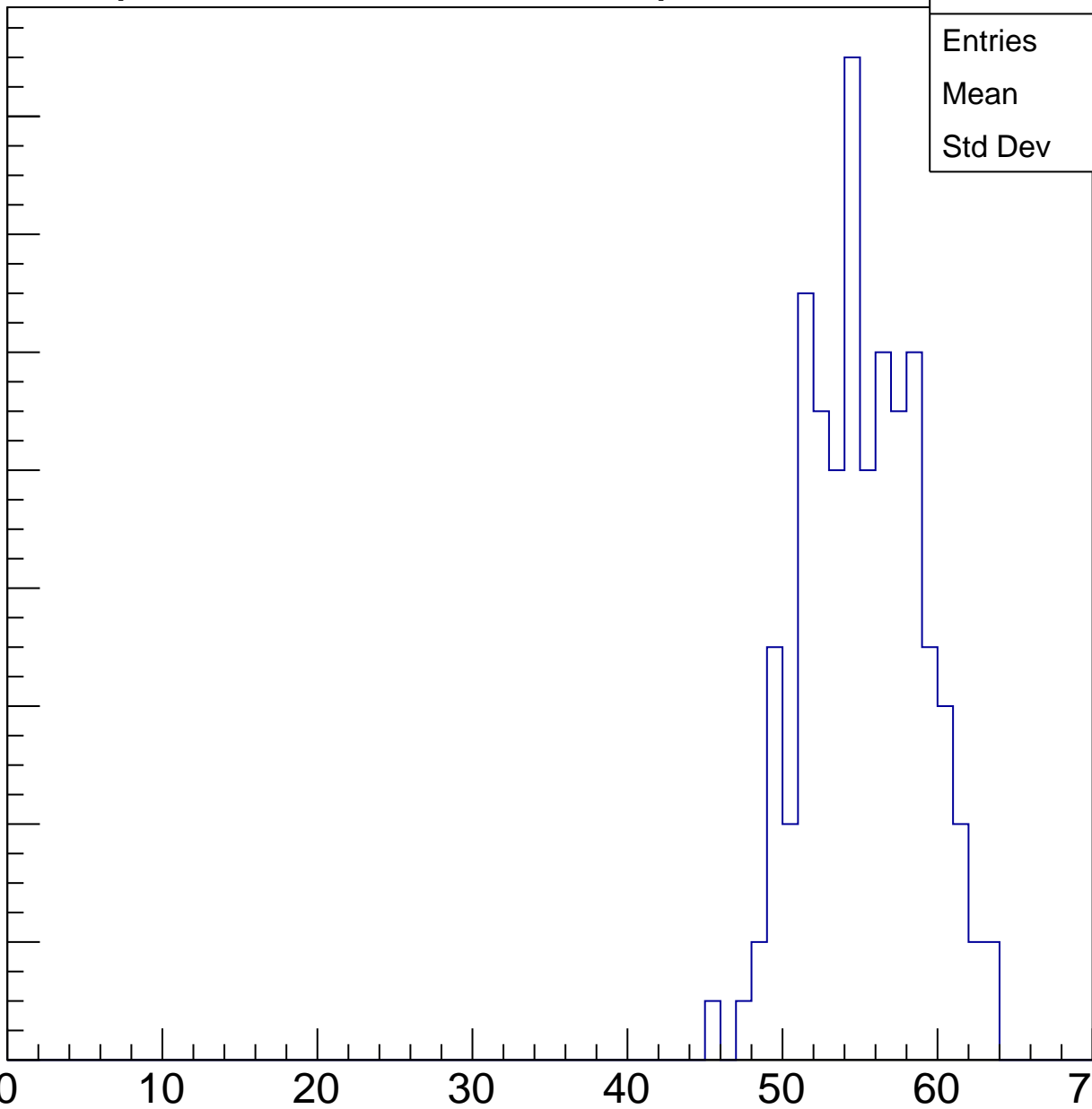
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	54.74
Std Dev	3.657

ampl



B0L101S, U14-ch0, adc5

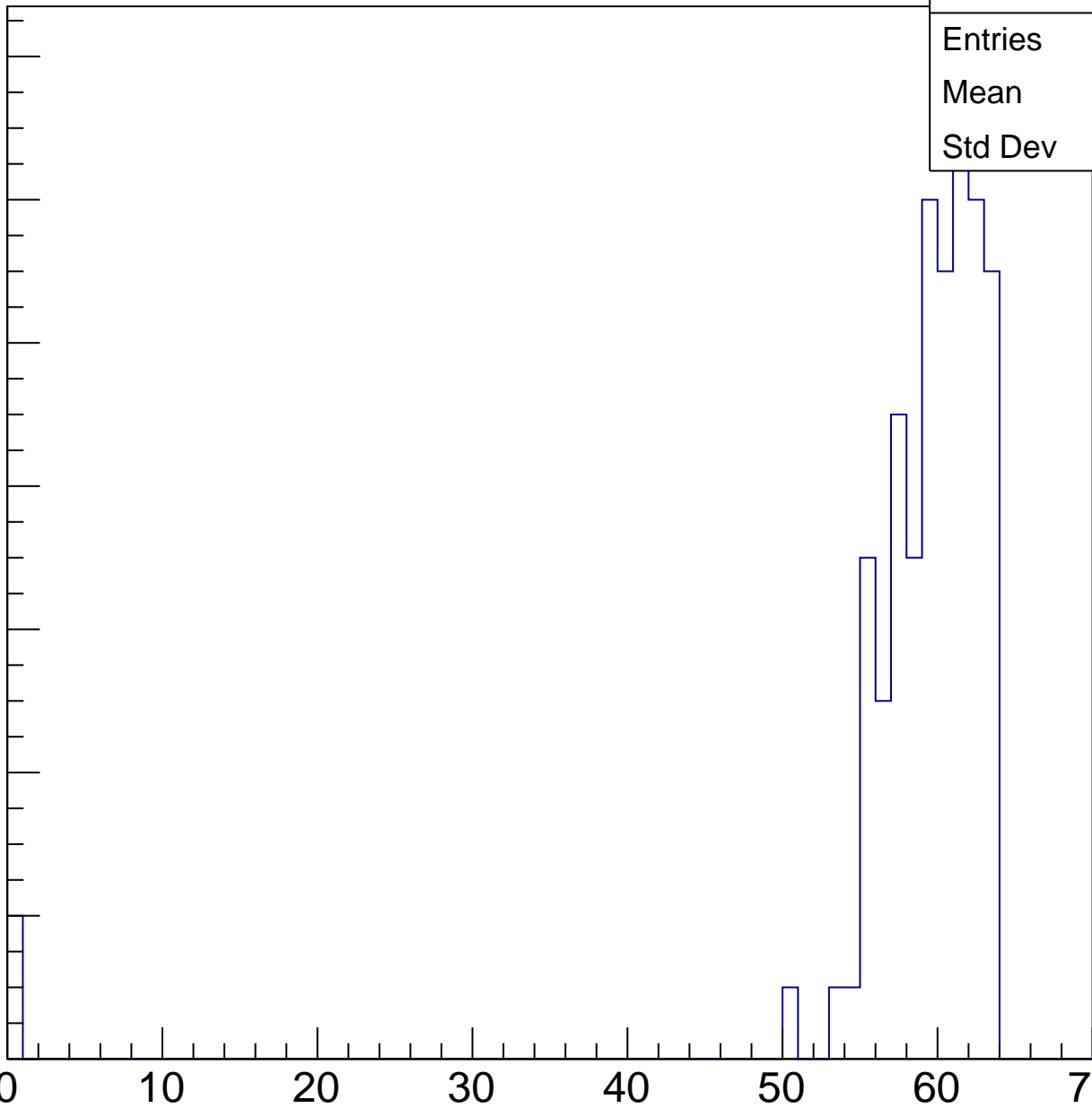
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	58.06
Std Dev	9.024

ampl

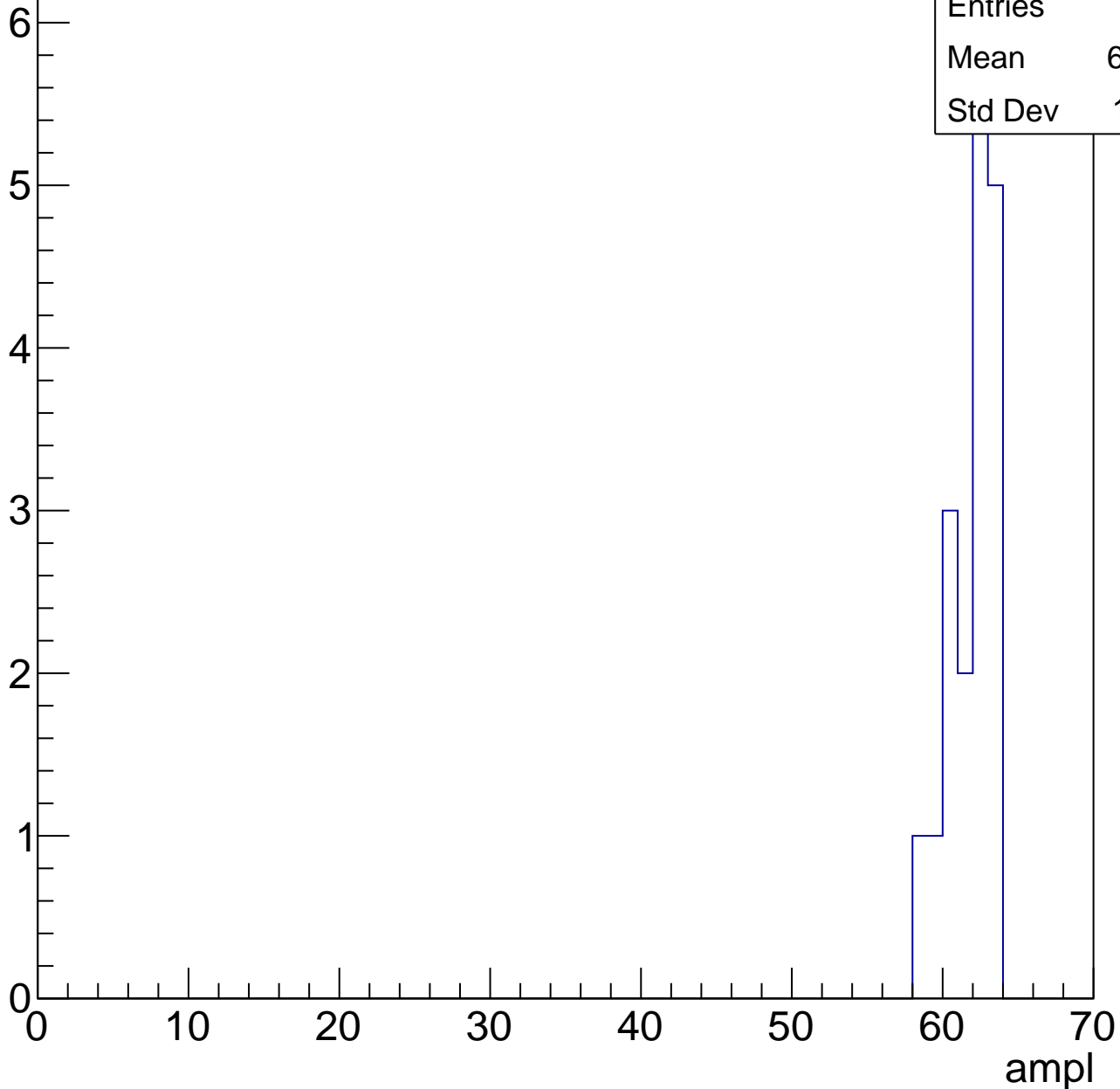


B0L101S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.44
Std Dev	1.461



B0L101S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	30.72
Std Dev	3.802

Gaus mean : 31.3036

Gaus Width: 3.7665

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U14-ch1, adc1

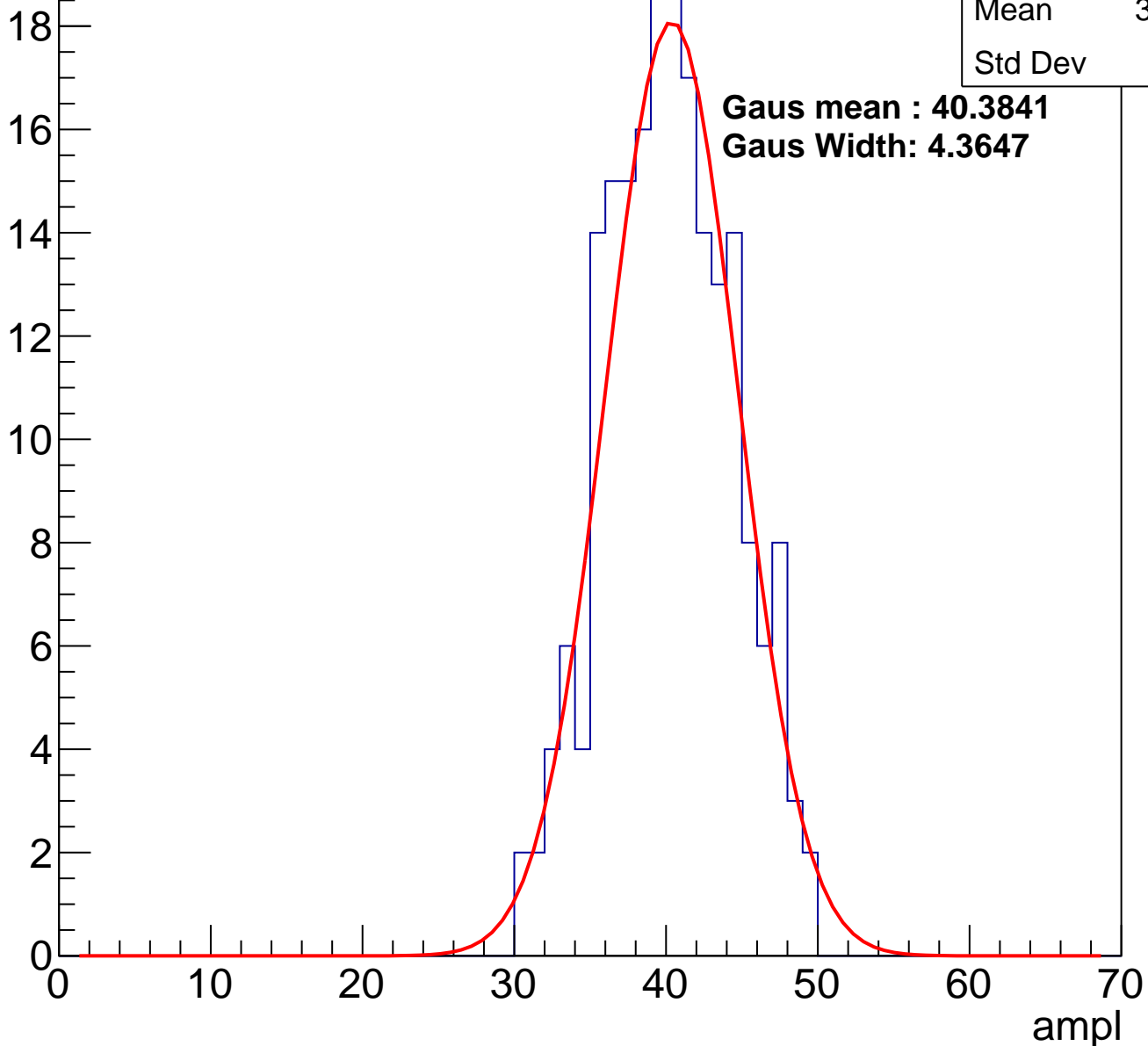
calib_packv5_042523_0143.root, FC#1, port C1

Entries	201
Mean	39.76
Std Dev	4.13

Gaus mean : 40.3841

Gaus Width: 4.3647

Entry



B0L101S, U14-ch1, adc2

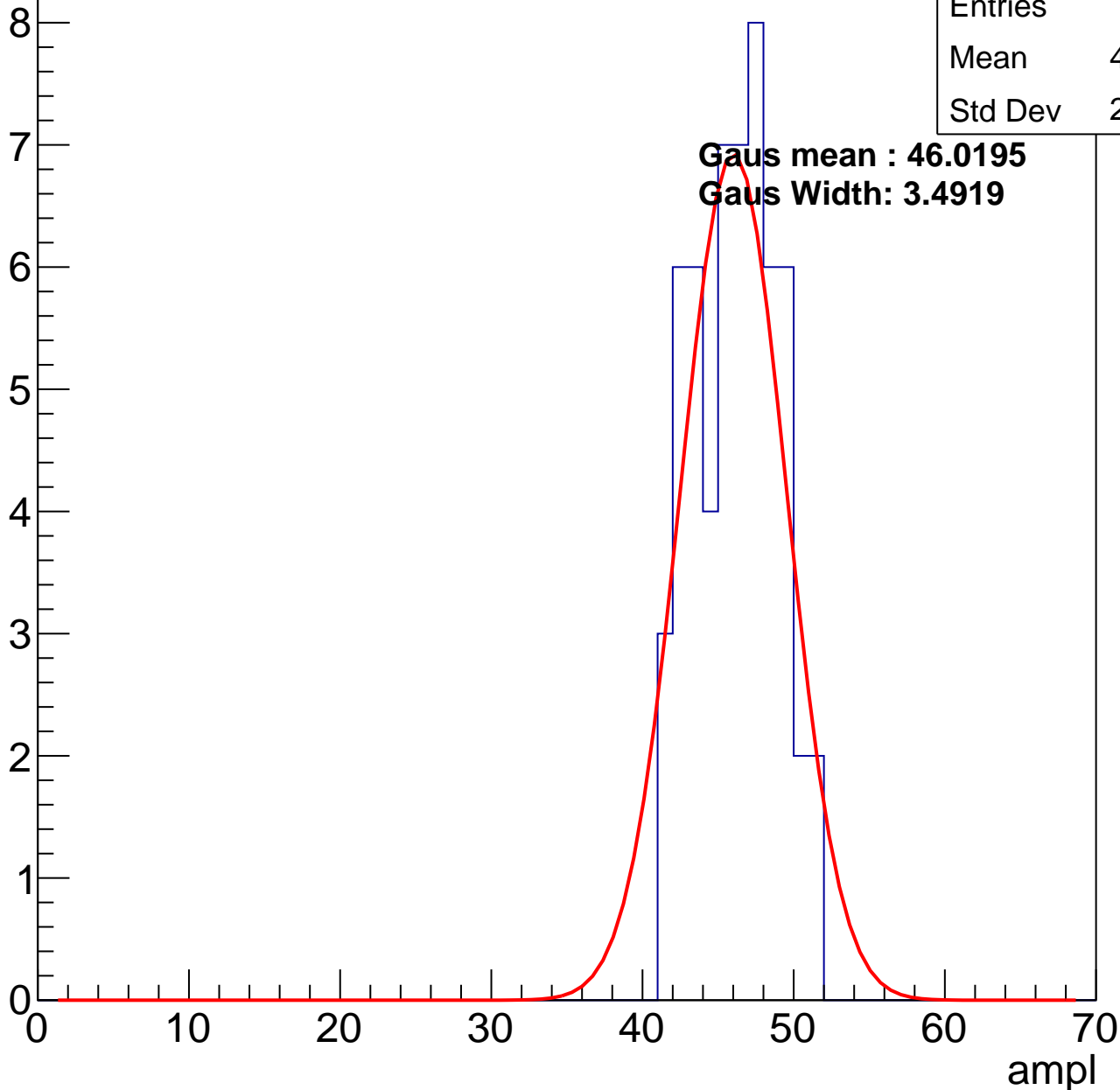
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	57
Mean	45.72
Std Dev	2.687

Gaus mean : 46.0195

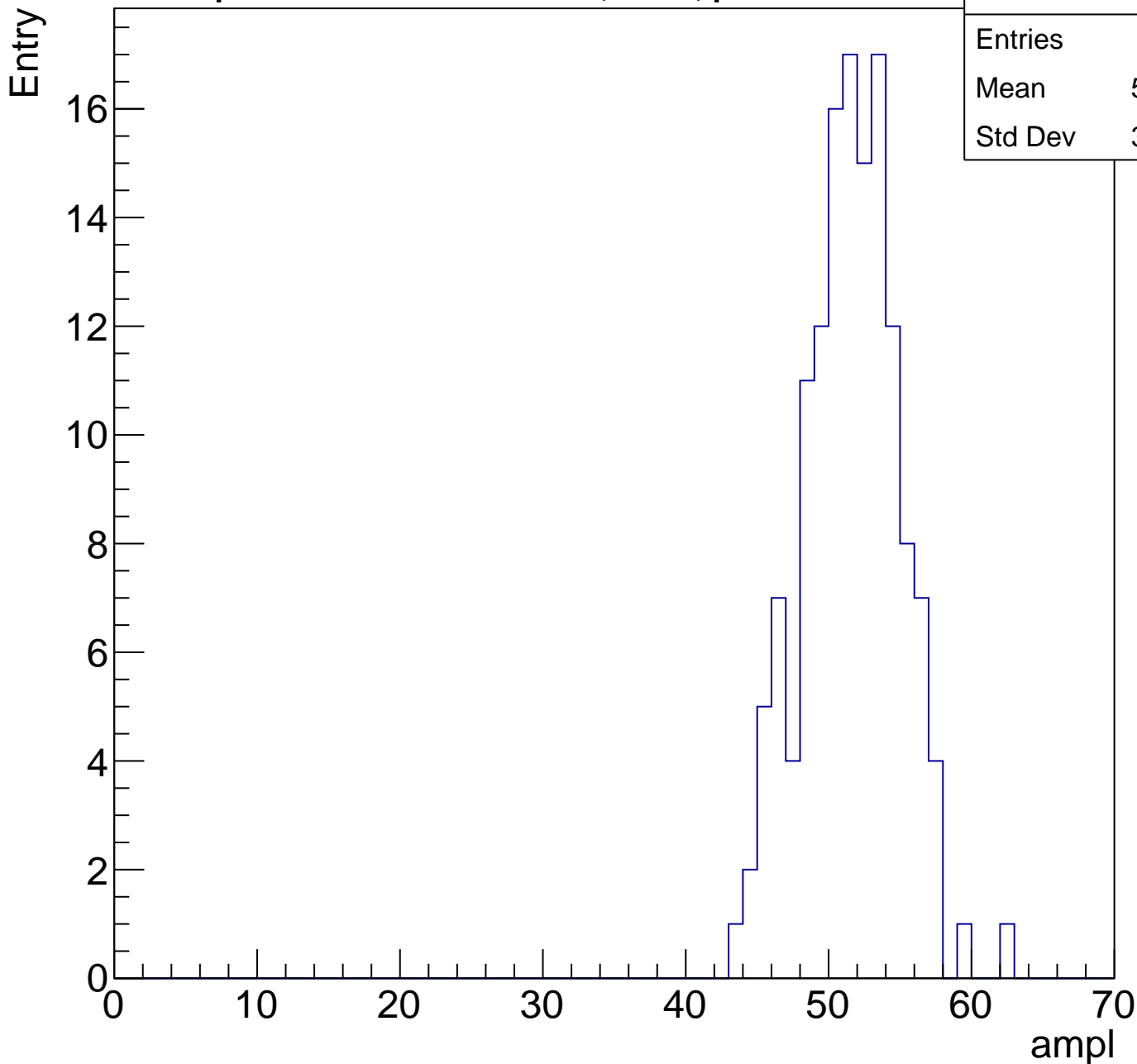
Gaus Width: 3.4919



B0L101S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

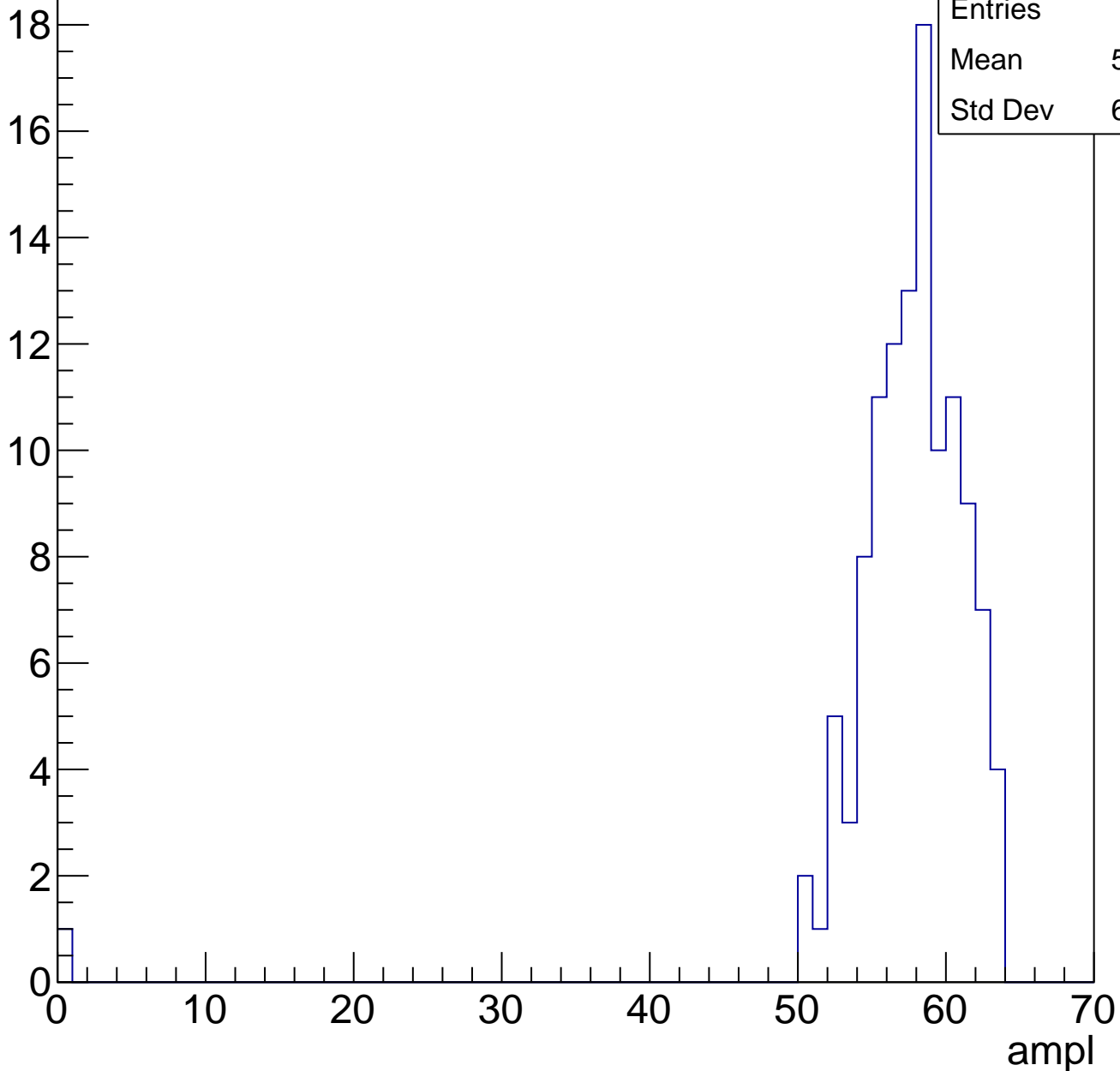
Entries	140
Mean	51.14
Std Dev	3.343



B0L101S, U14-ch1, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch1, adc5

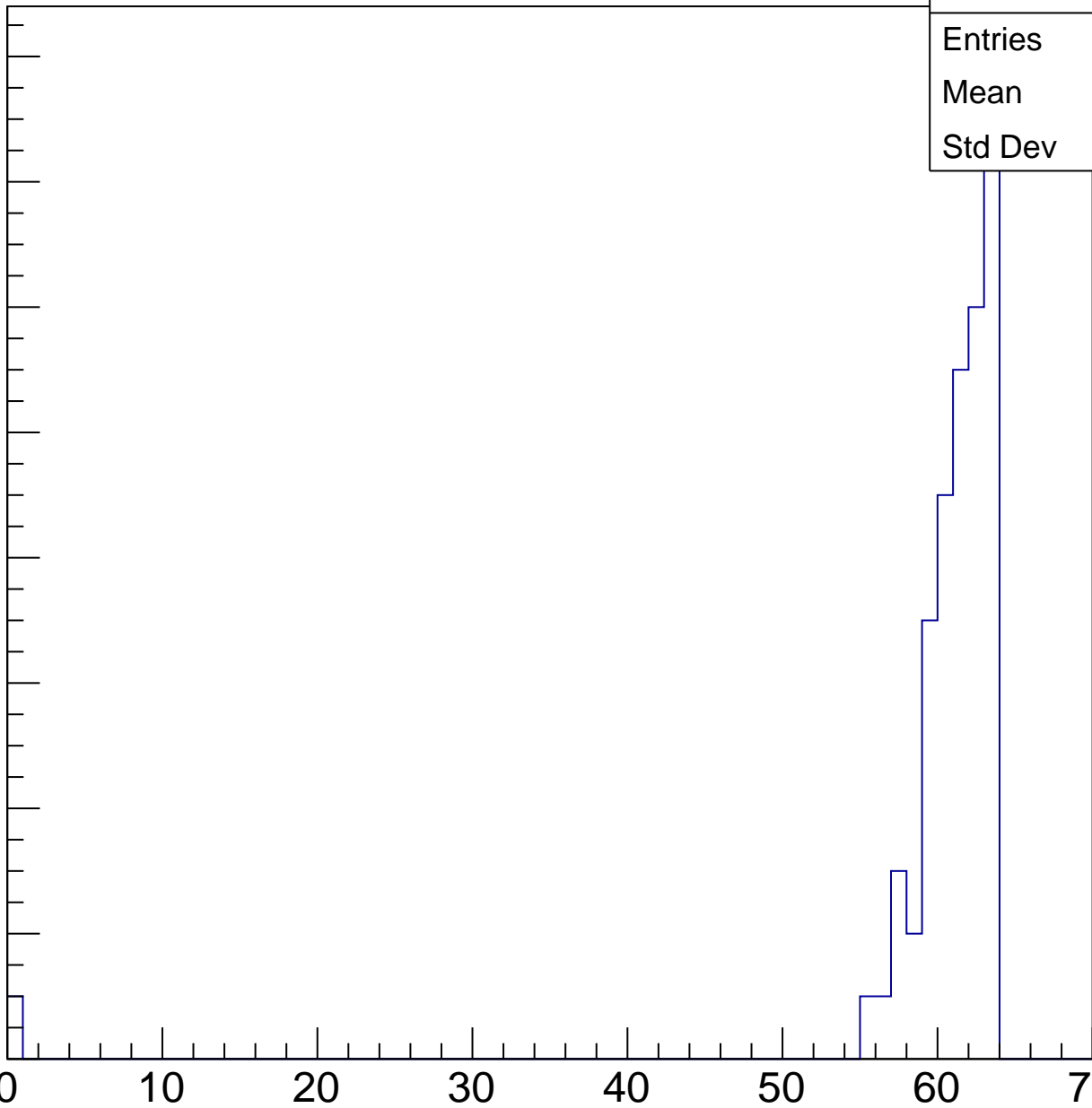
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	59.9
Std Dev	7.853

ampl



B0L101S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

ampl

Entries	2
Mean	59
Std Dev	0

B0L101S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

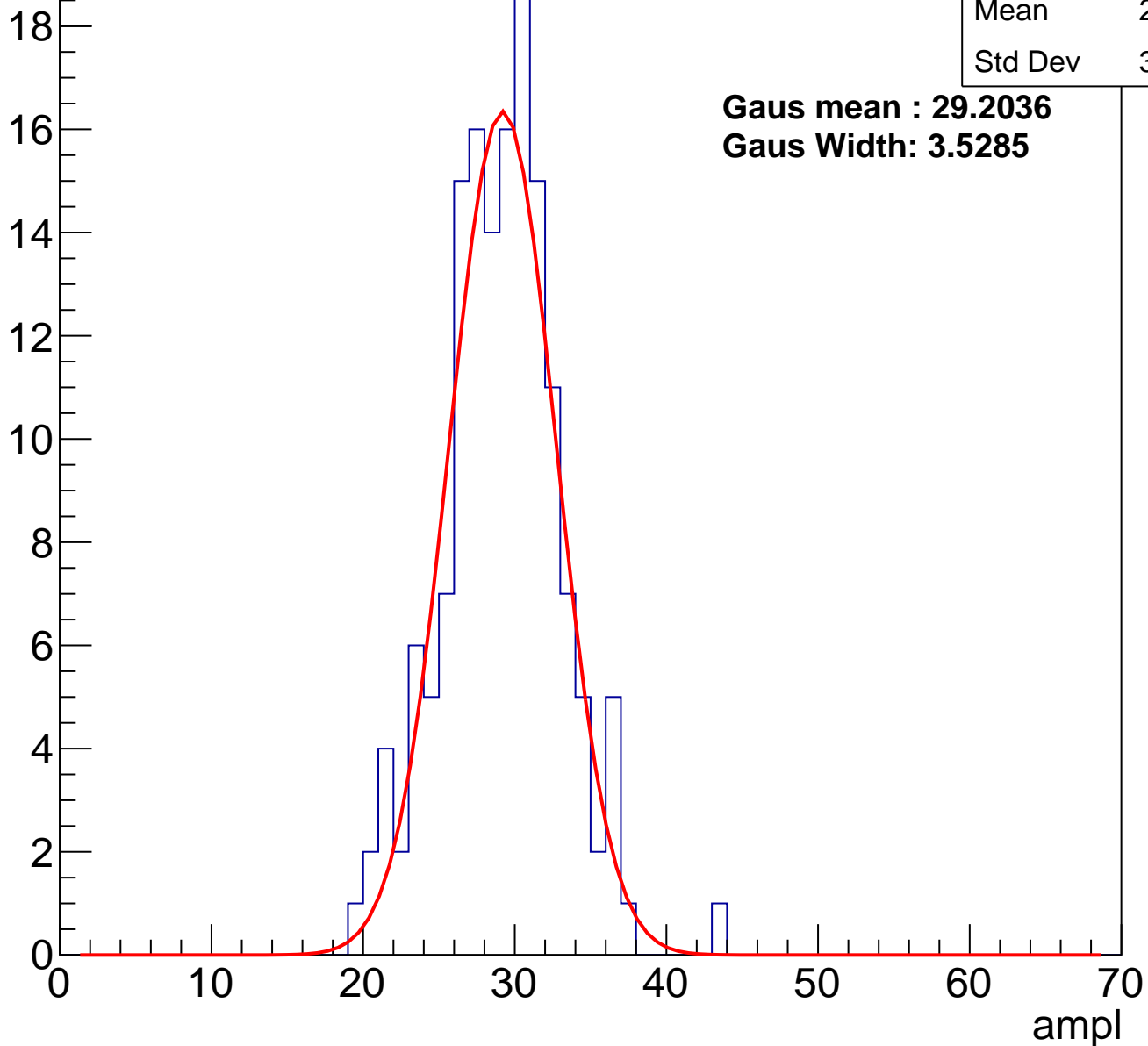


Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



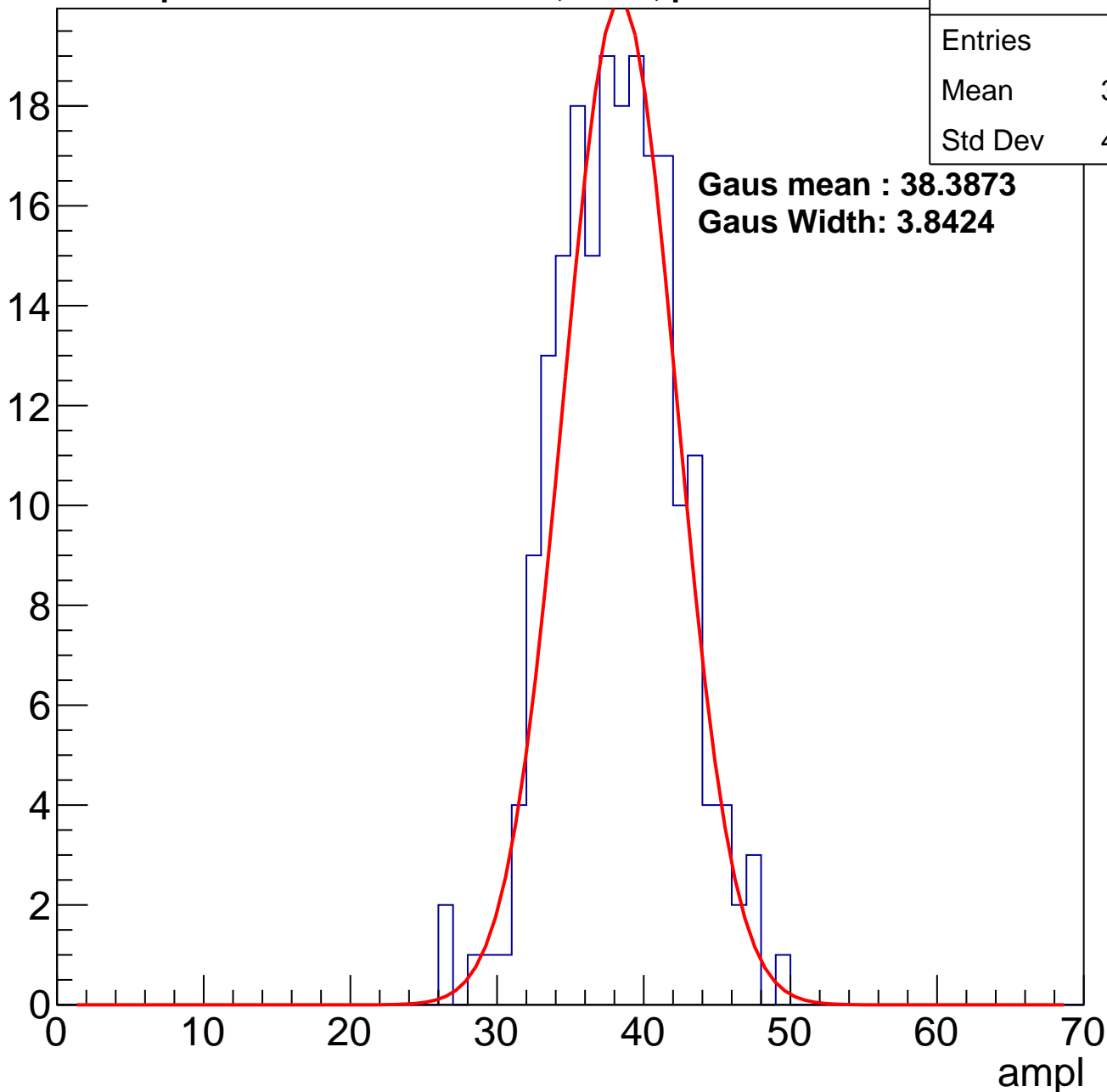
B0L101S, U14-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	204
Mean	37.73
Std Dev	4.053

Gaus mean : 38.3873
Gaus Width: 3.8424

Entry



B0L101S, U14-ch2, adc2

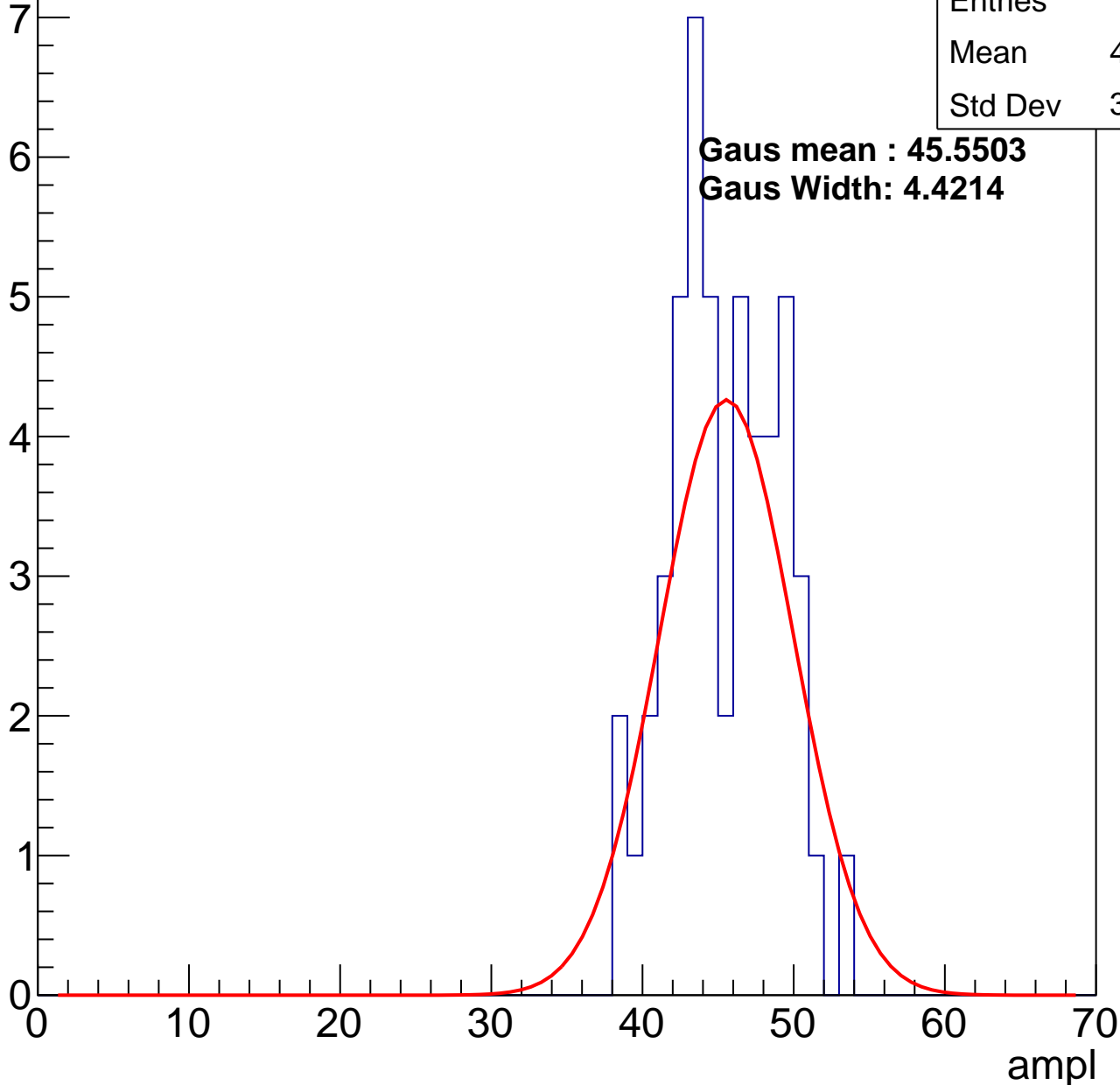
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	50
Mean	44.96
Std Dev	3.527

Gaus mean : 45.5503

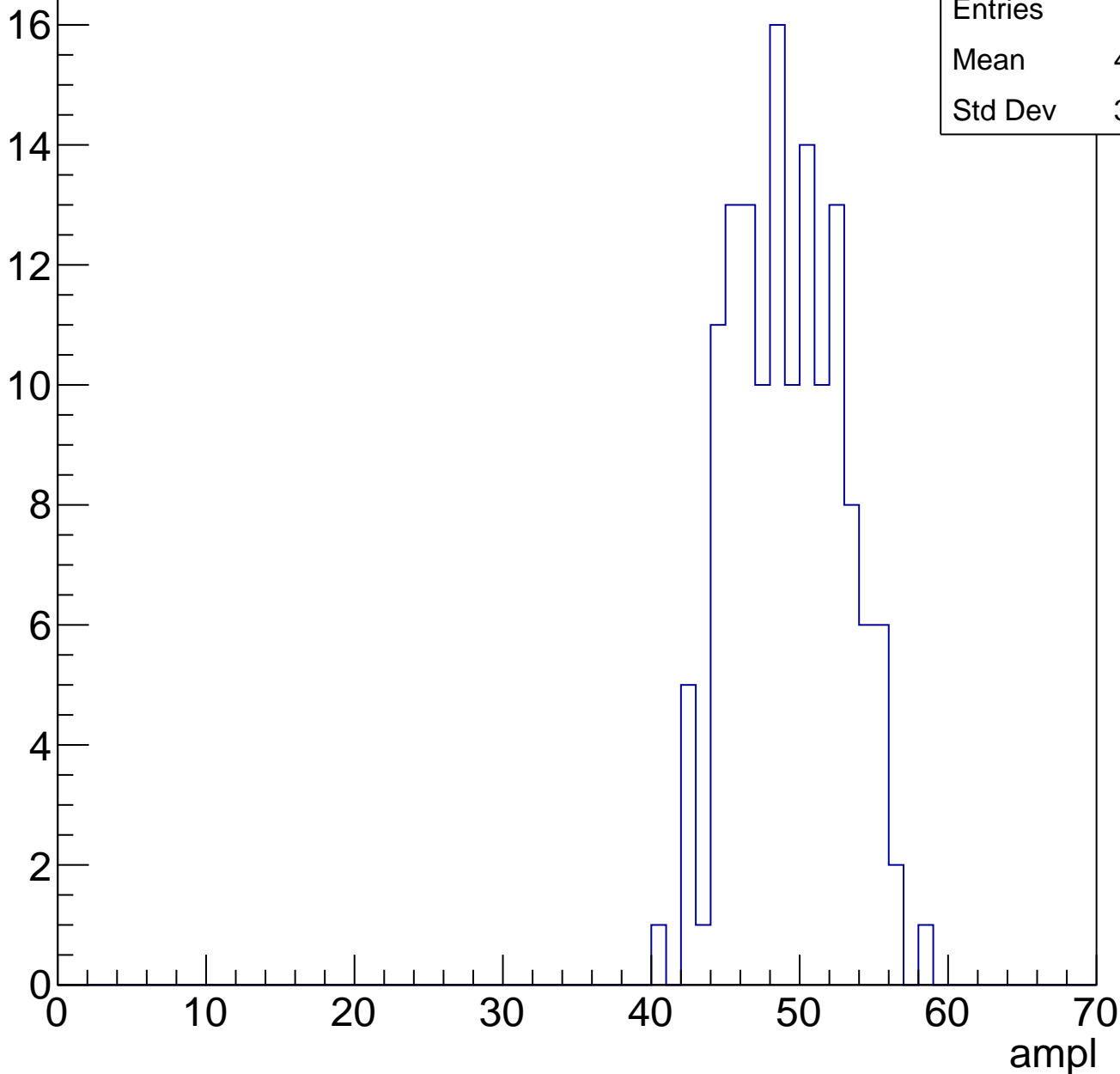
Gaus Width: 4.4214



B0L101S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

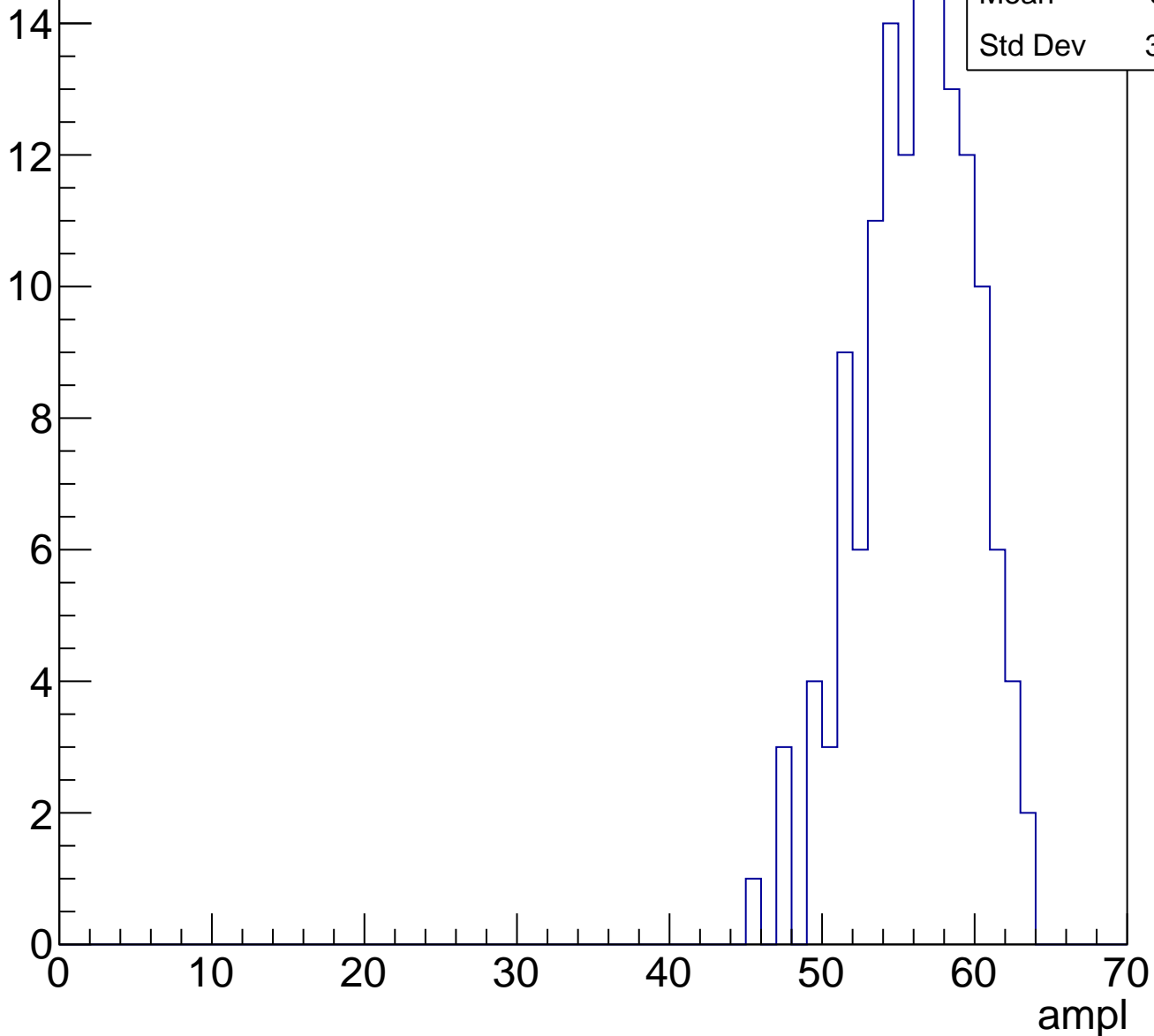
Entry



B0L101S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch2, adc5

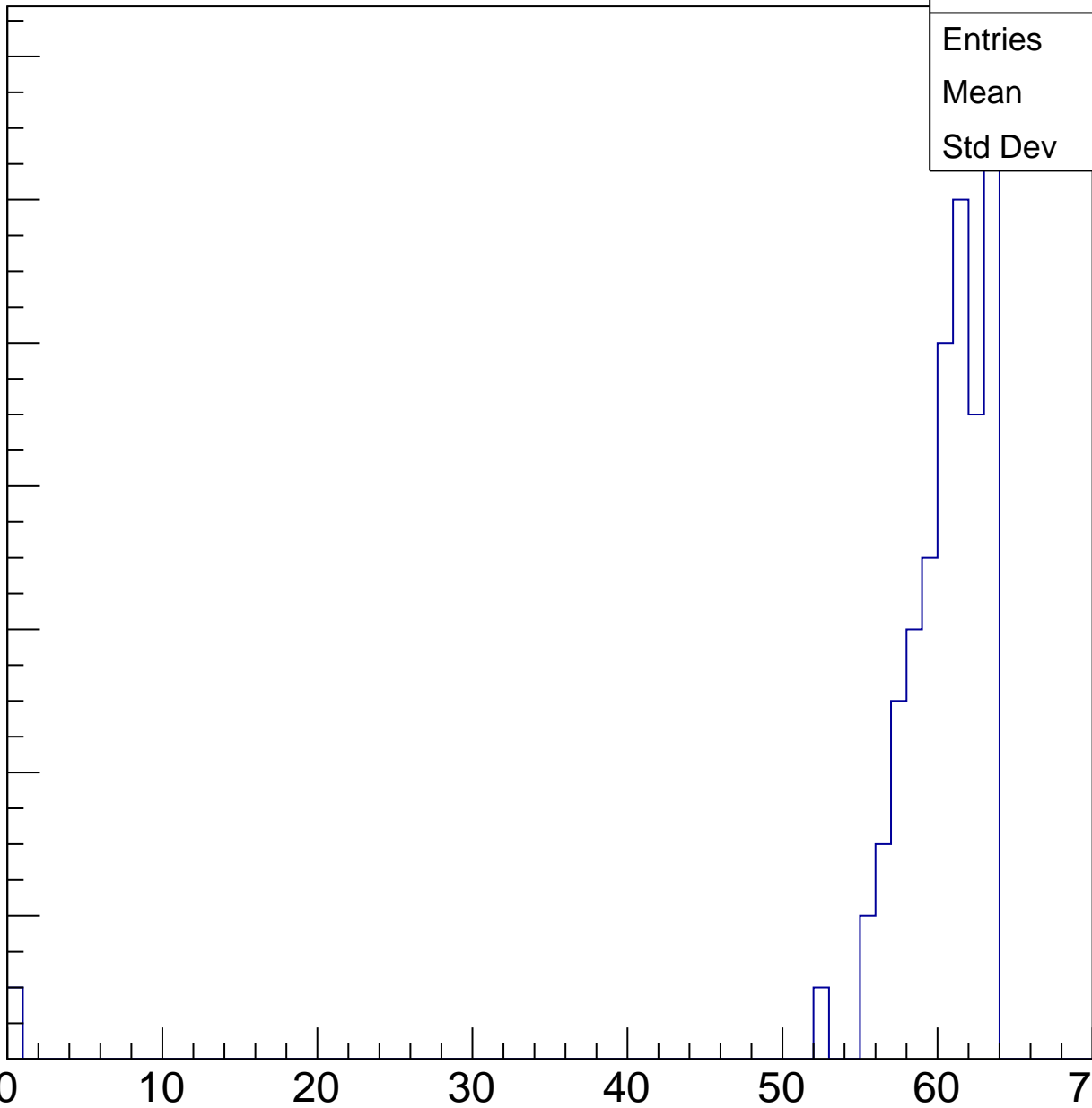
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.26
Std Dev	7.534

ampl

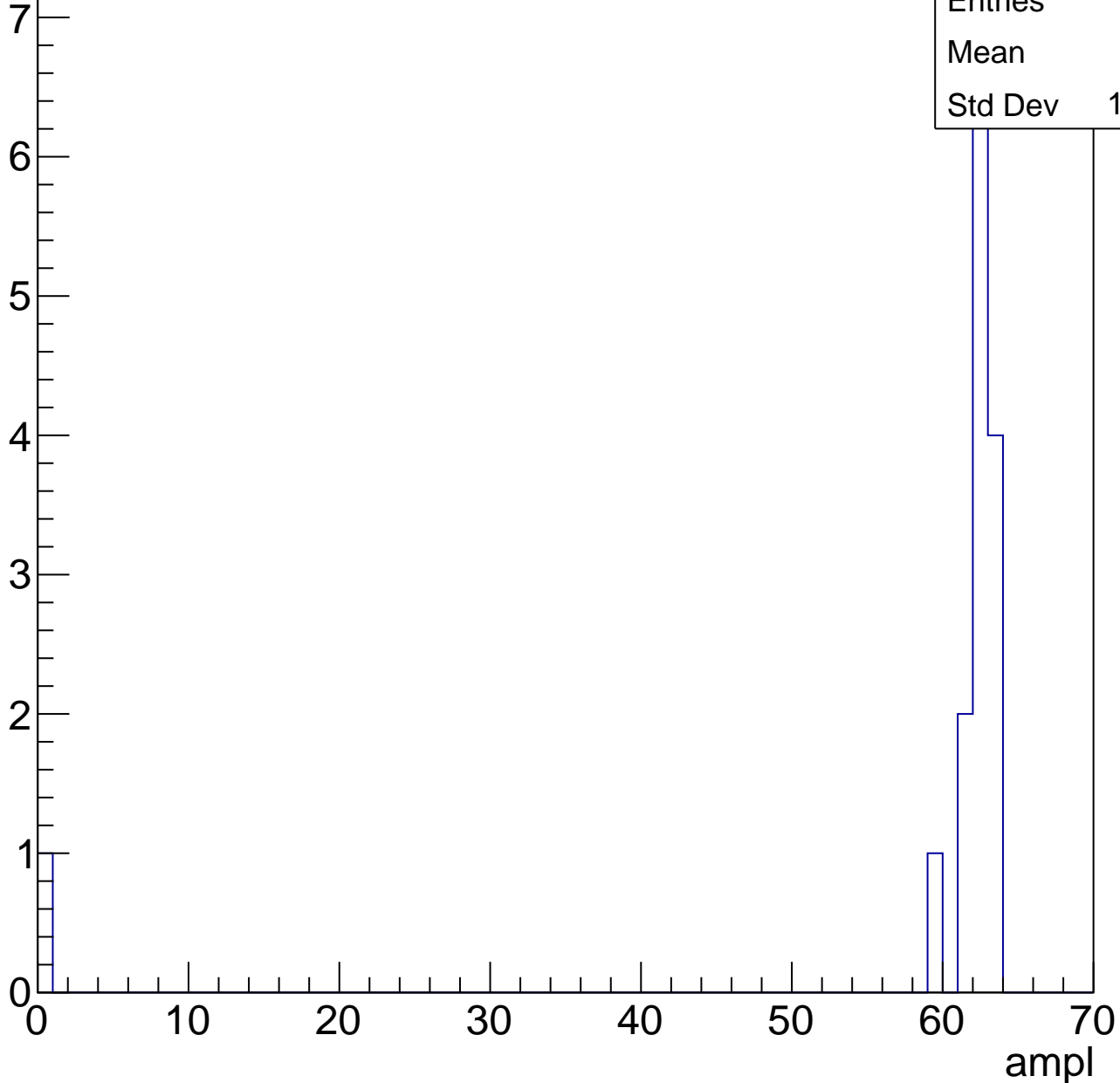


B0L101S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

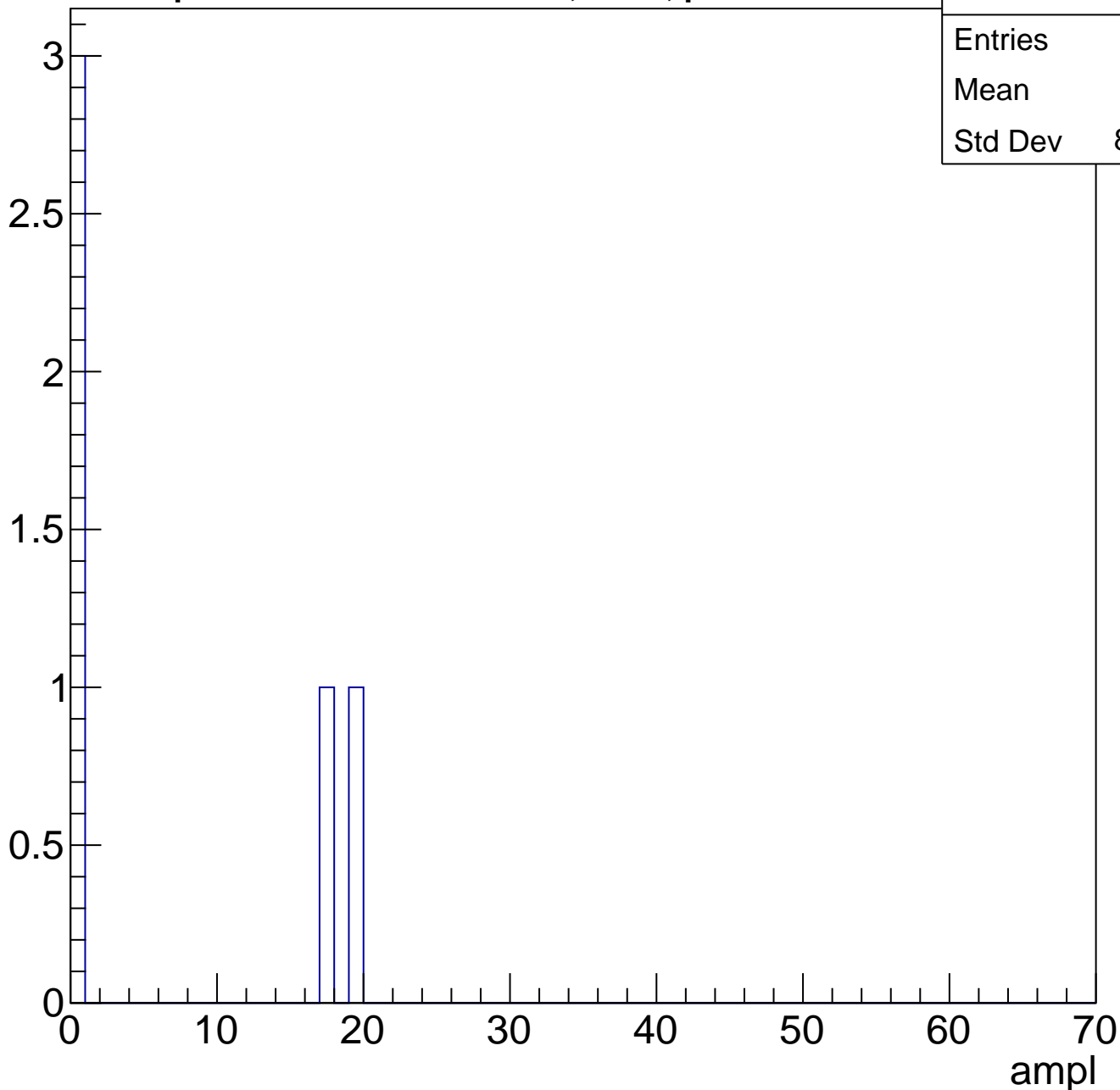
Entries	15
Mean	57.8
Std Dev	15.48



B0L101S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	7.2
Std Dev	8.841

B0L101S, U14-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.6
Std Dev	4.907

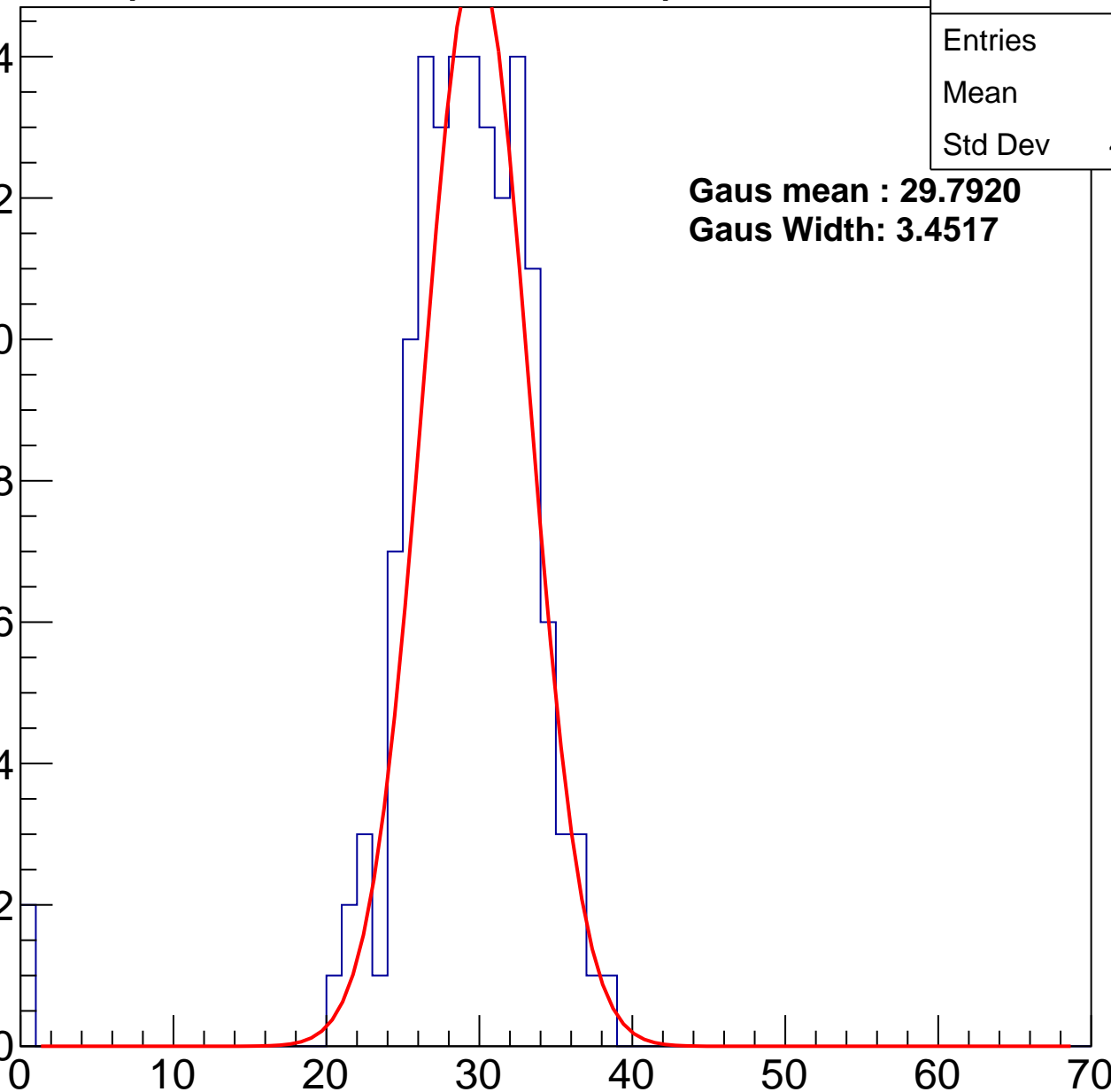
Gaus mean : 29.7920

Gaus Width: 3.4517

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

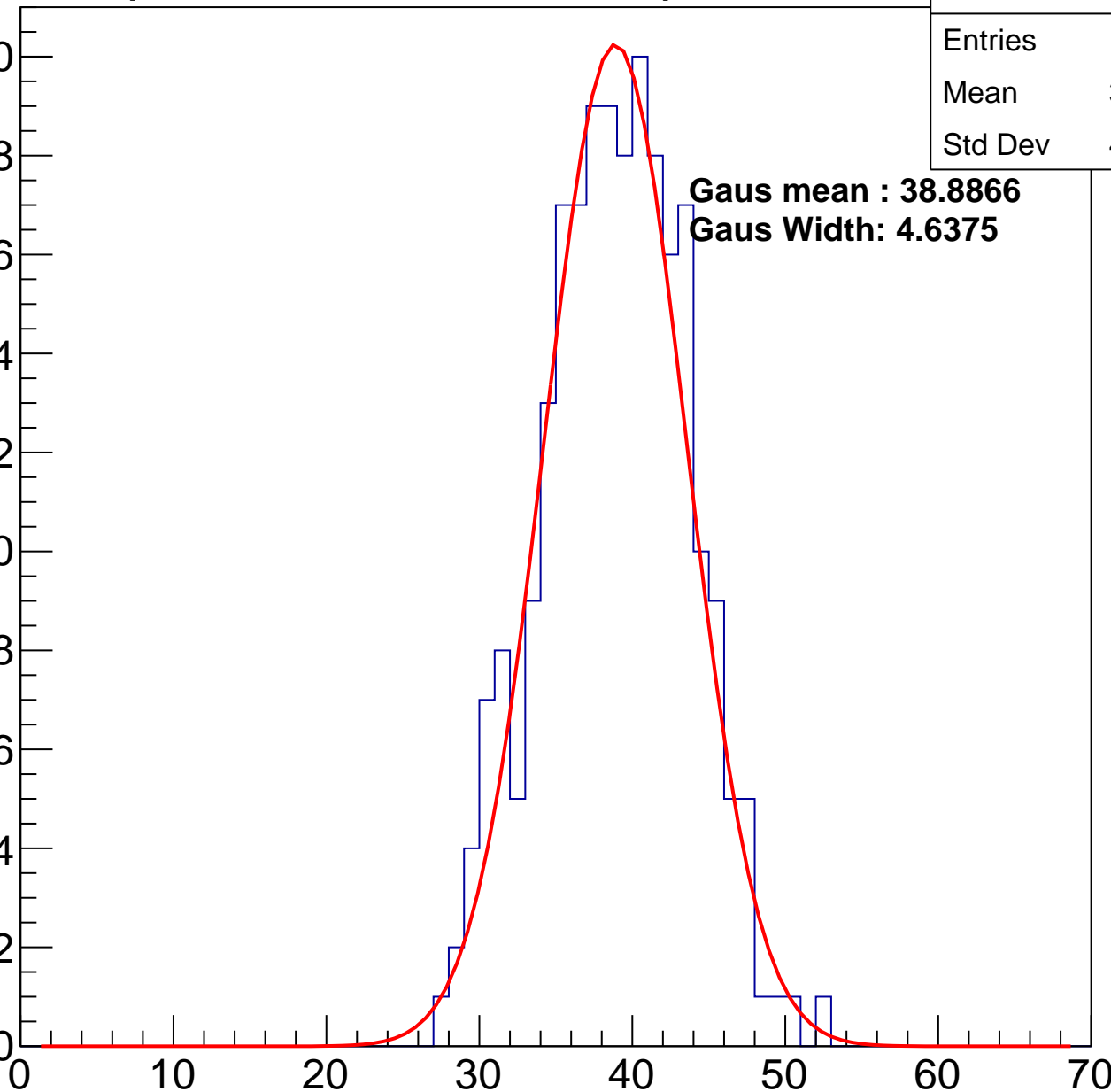
20
18
16
14
12
10
8
6
4
2
0

Entries	243
Mean	38.44
Std Dev	4.679

Gaus mean : 38.8866

Gaus Width: 4.6375

ampl



B0L101S, U14-ch3, adc2

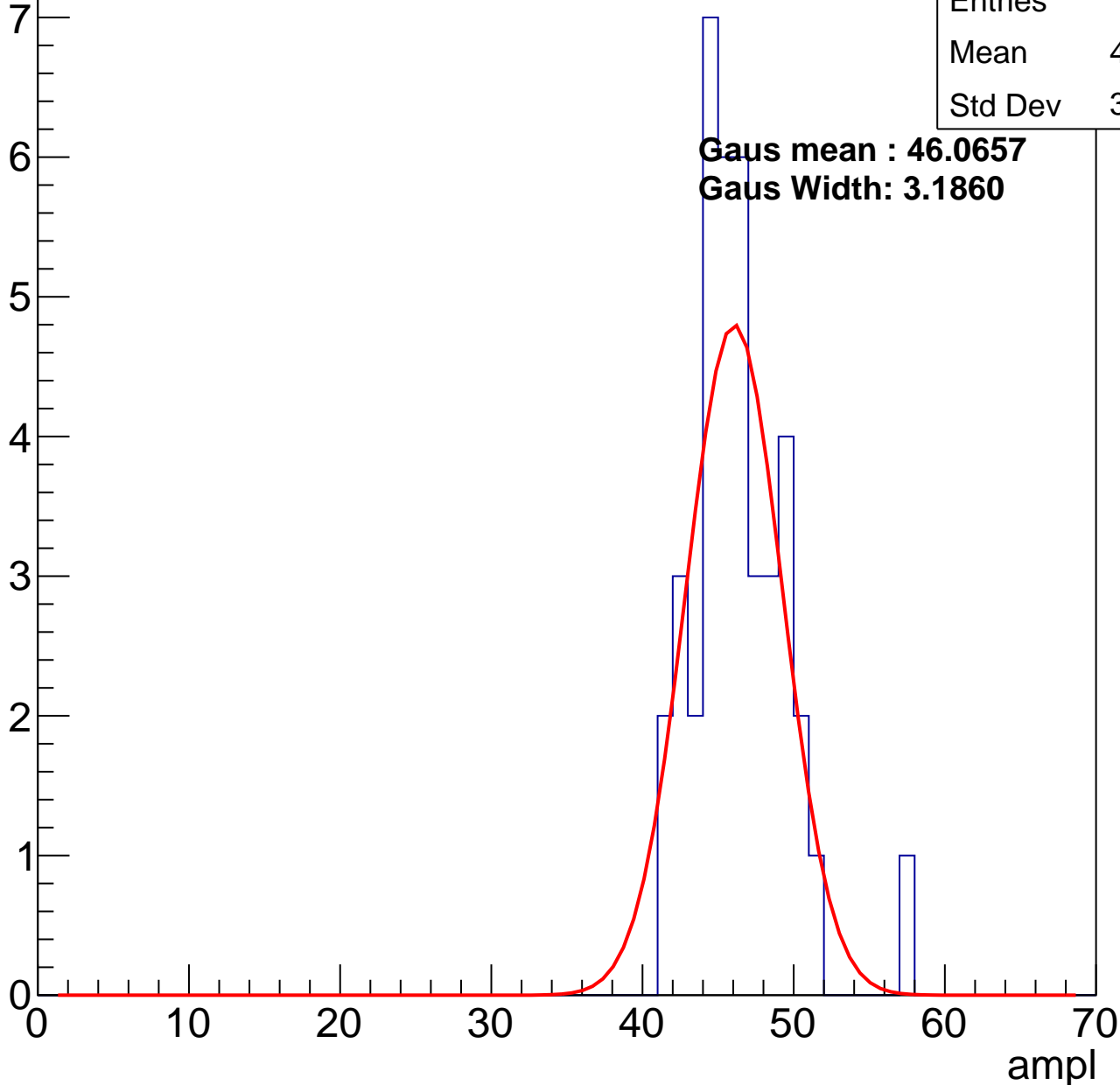
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	40
Mean	45.92
Std Dev	3.077

Gaus mean : 46.0657

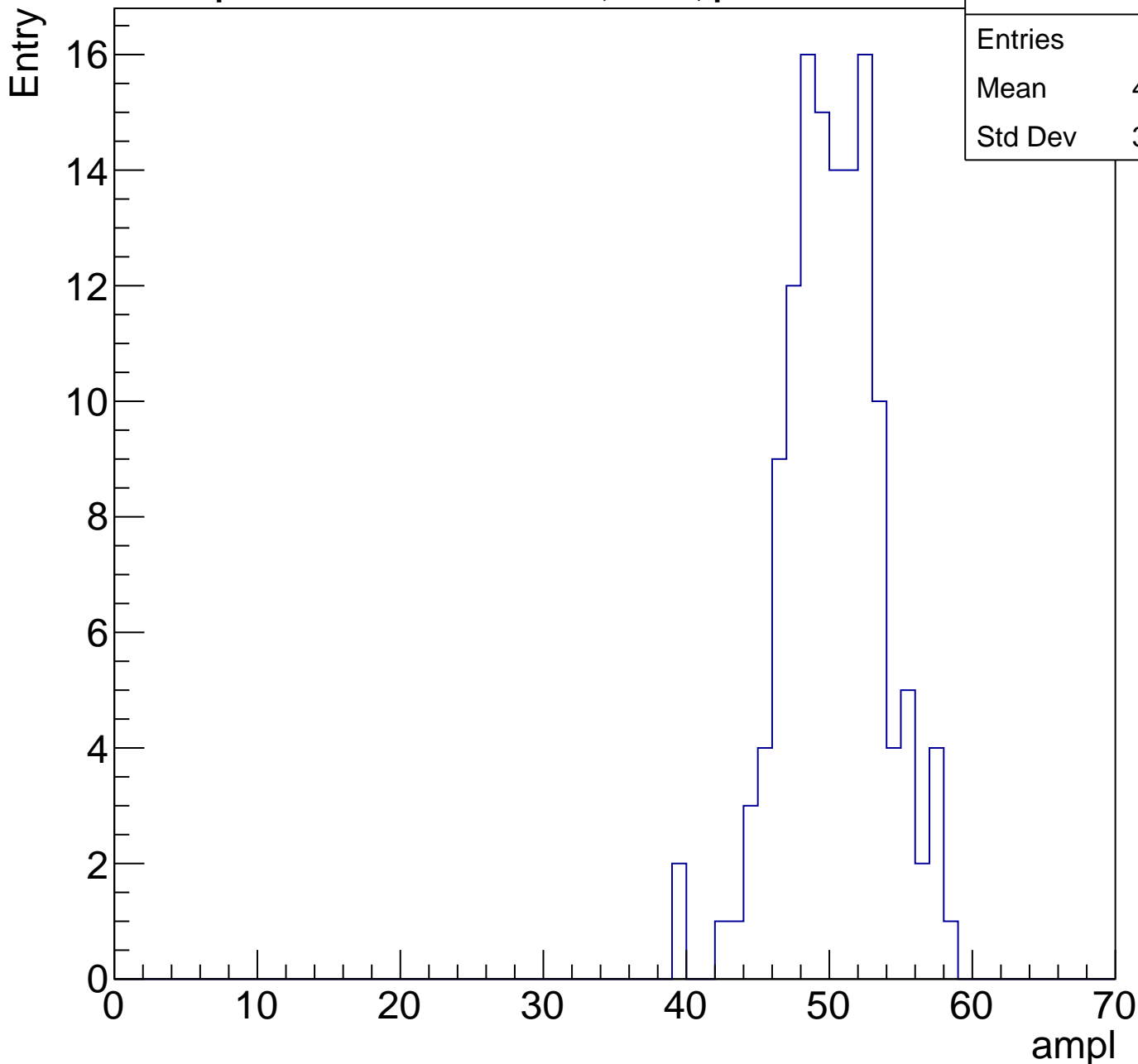
Gaus Width: 3.1860



B0L101S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	49.78
Std Dev	3.443



B0L101S, U14-ch3, adc4

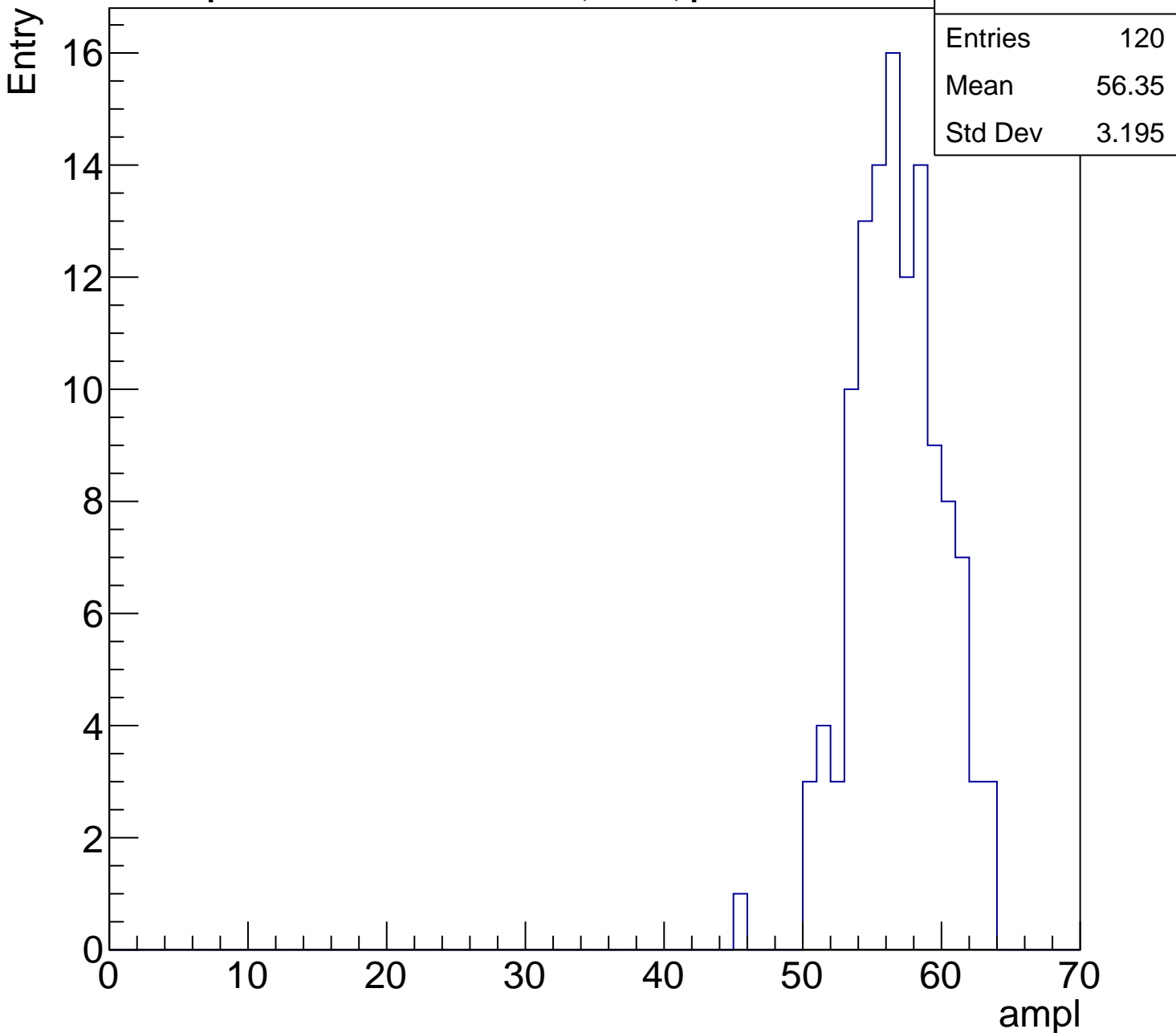
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	56.35
Std Dev	3.195

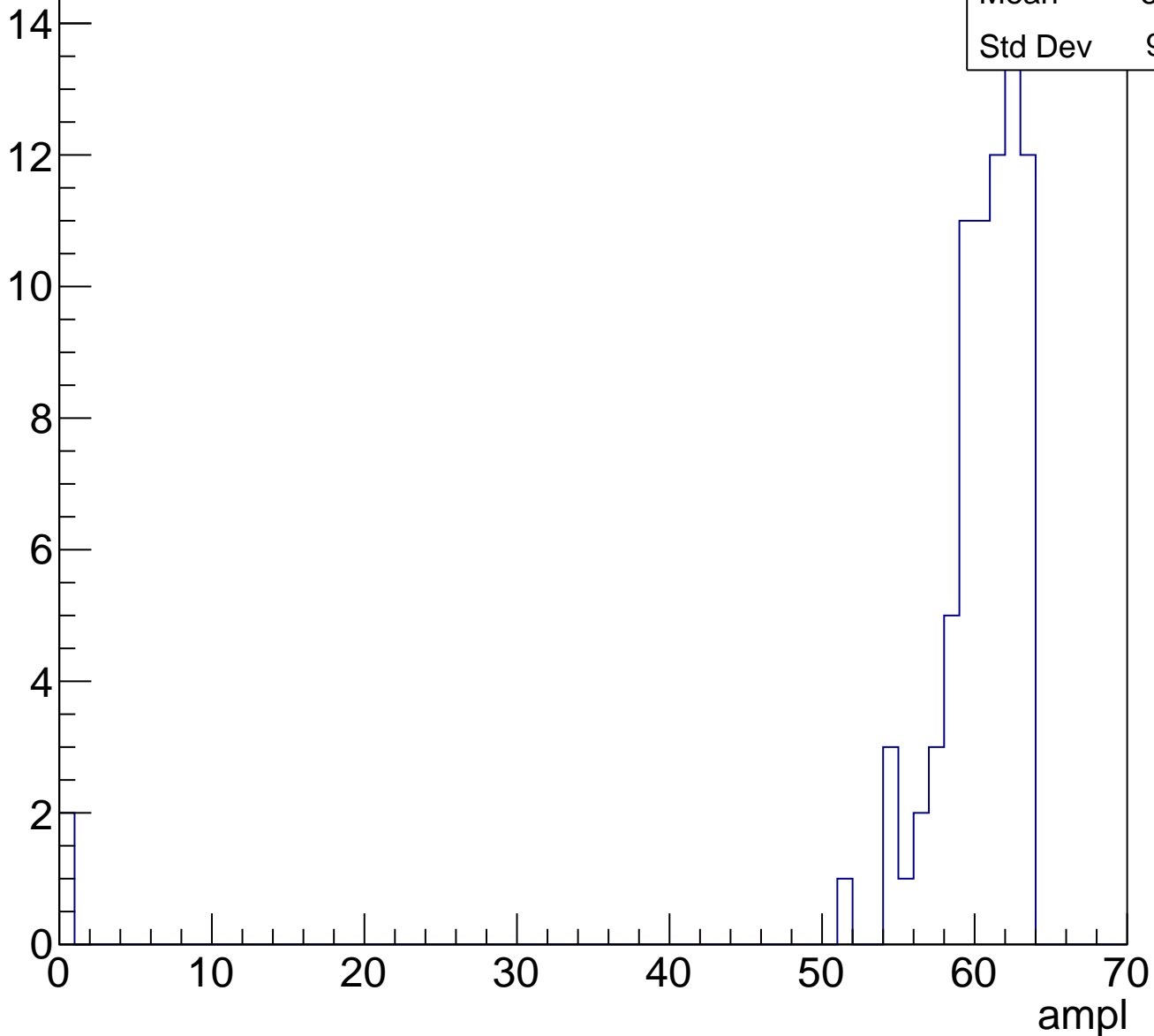
ampl



B0L101S, U14-ch3, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

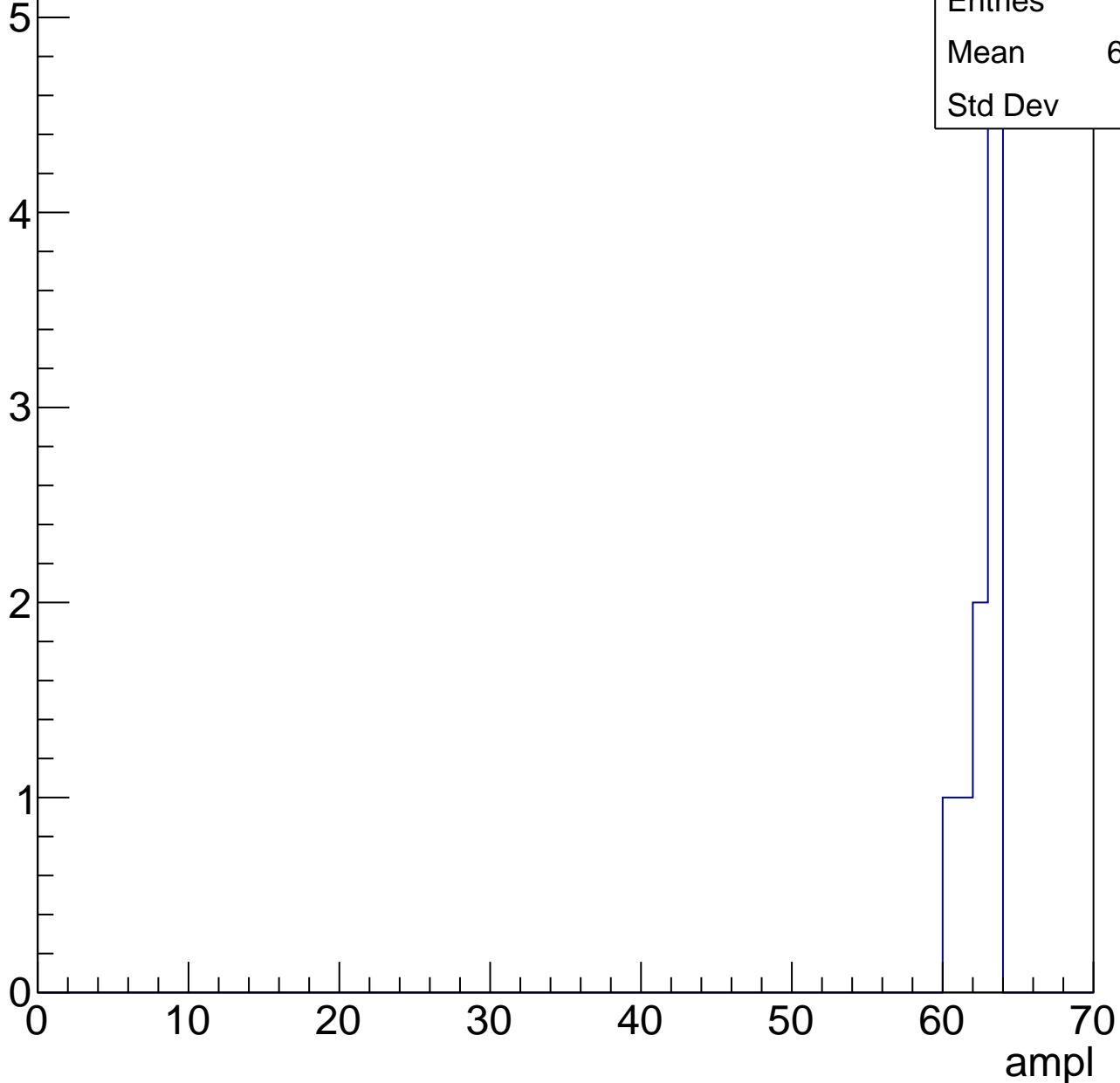


B0L101S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B0L101S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U14-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.87
Std Dev	3.654

Gaus mean : 29.2157

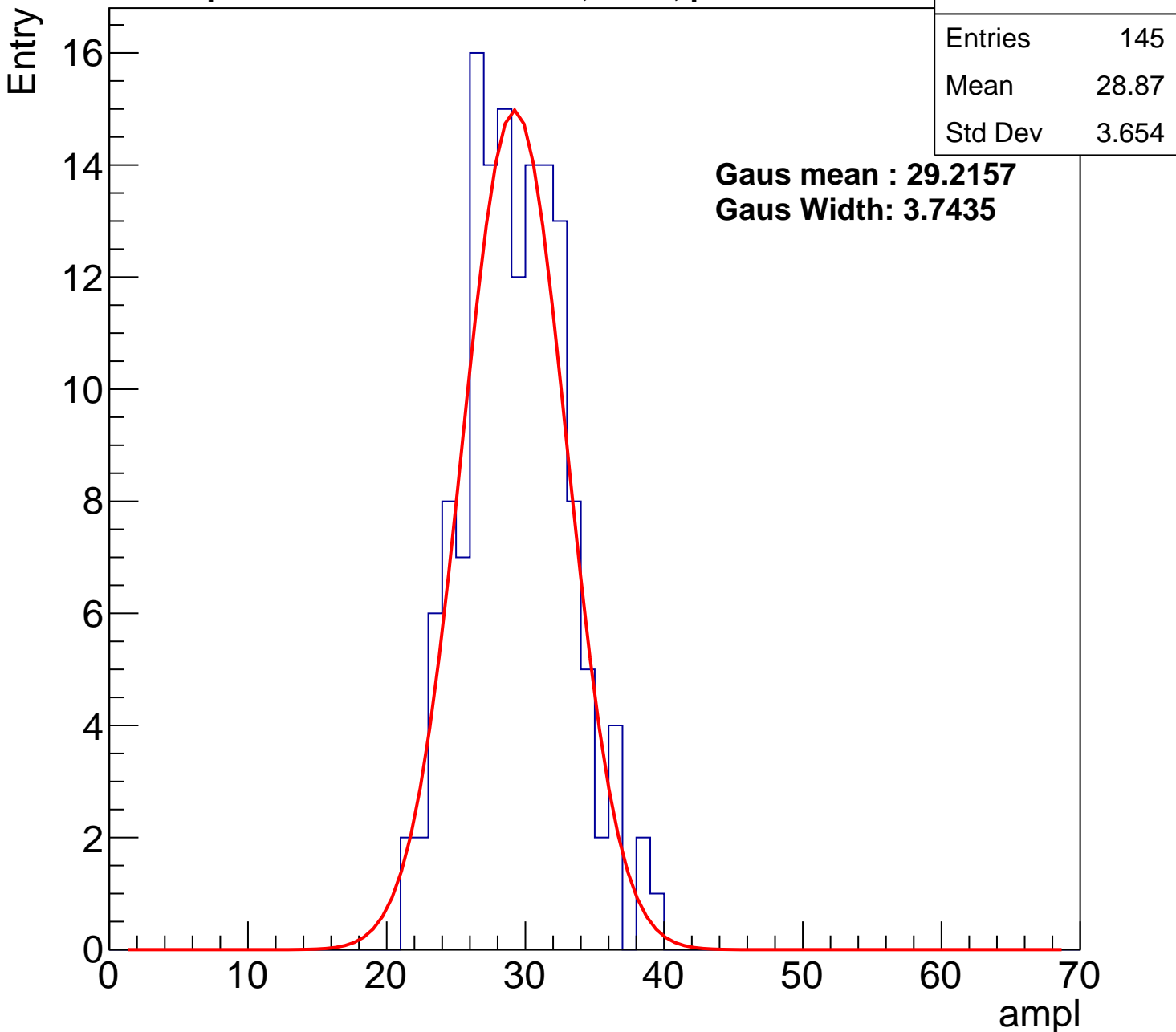
Gaus Width: 3.7435

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



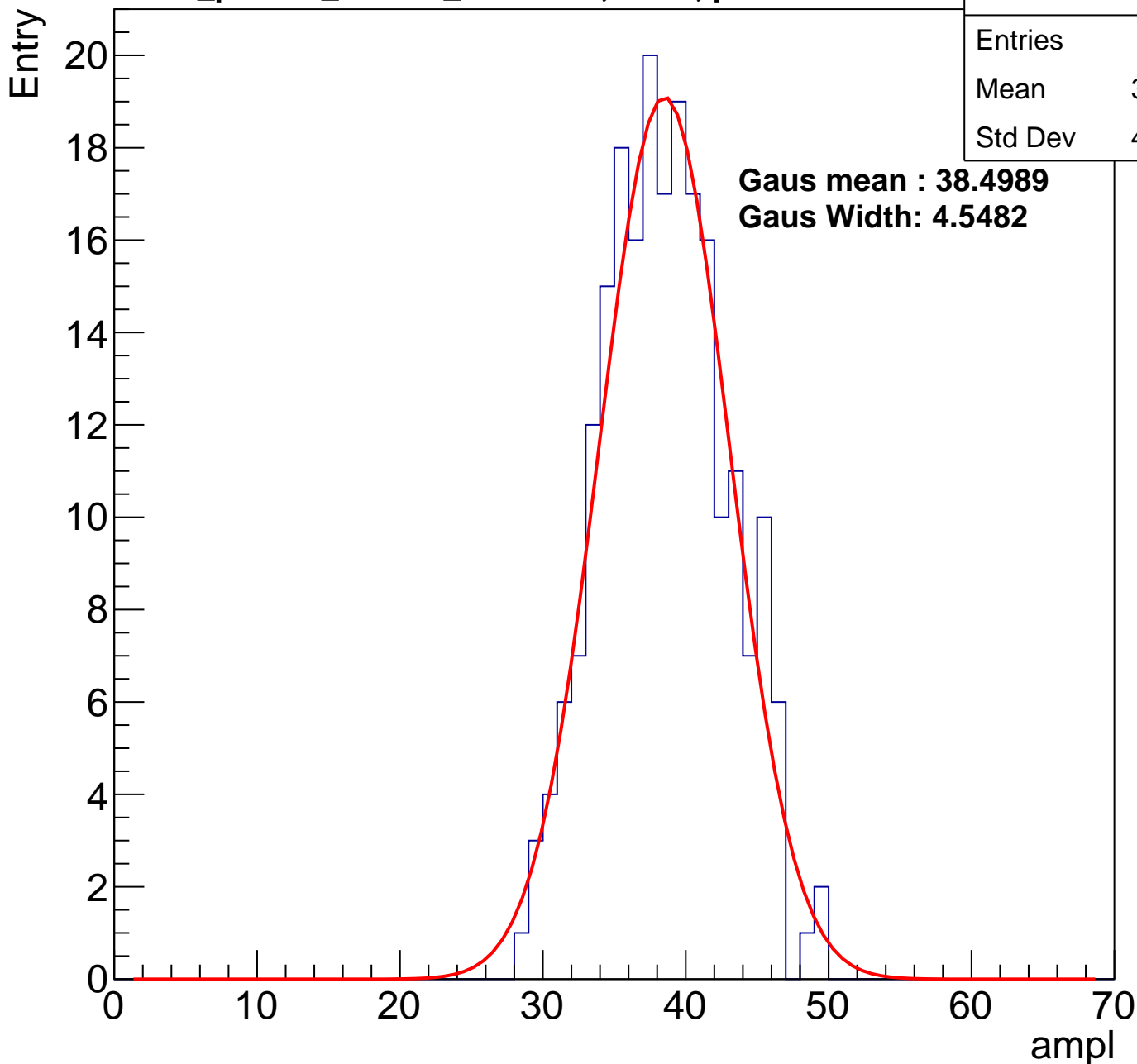
B0L101S, U14-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	218
Mean	38.04
Std Dev	4.318

Gaus mean : 38.4989

Gaus Width: 4.5482



B0L101S, U14-ch4, adc2

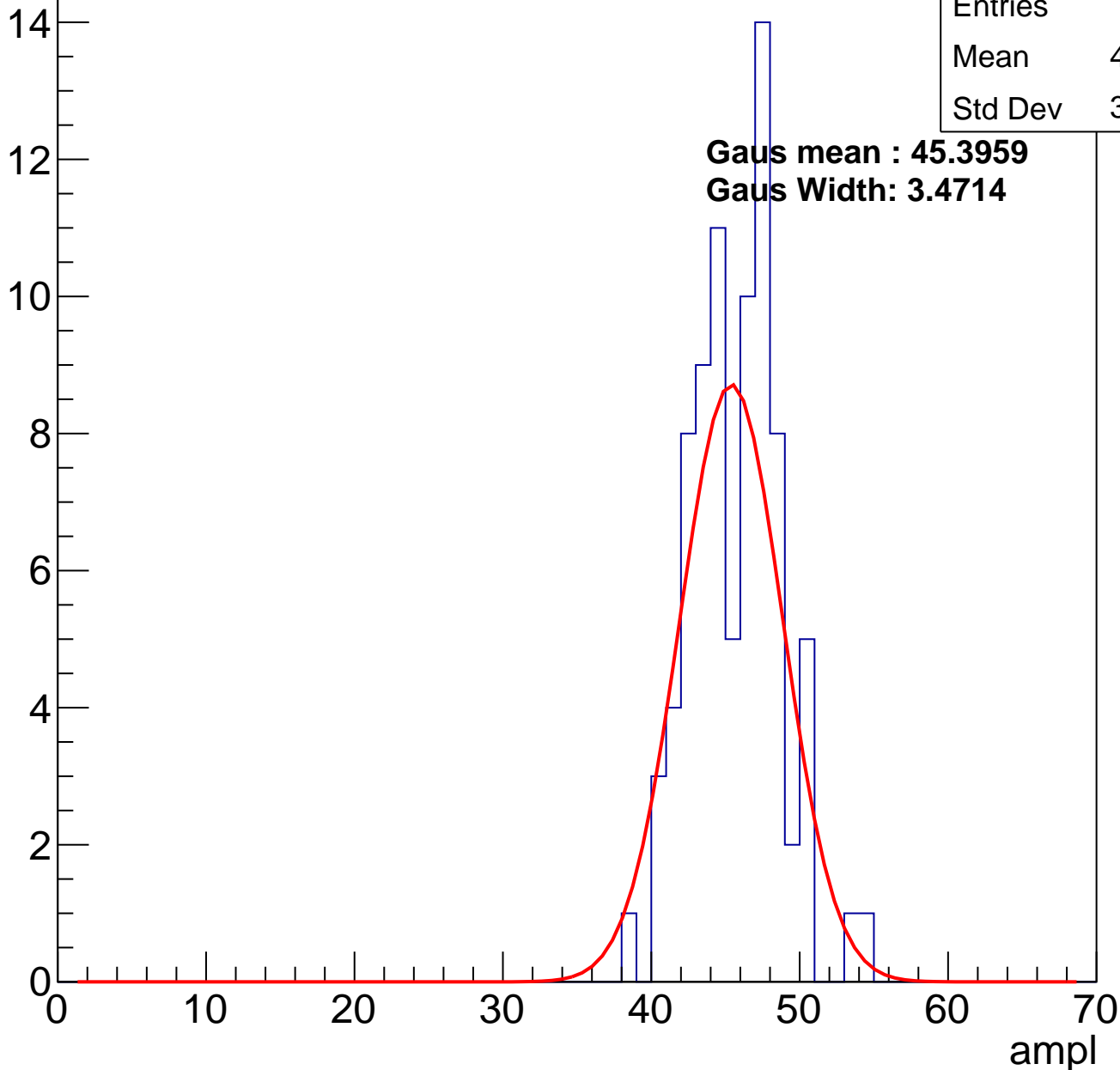
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	82
Mean	45.26
Std Dev	3.007

Gaus mean : 45.3959

Gaus Width: 3.4714



B0L101S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#1, port C1

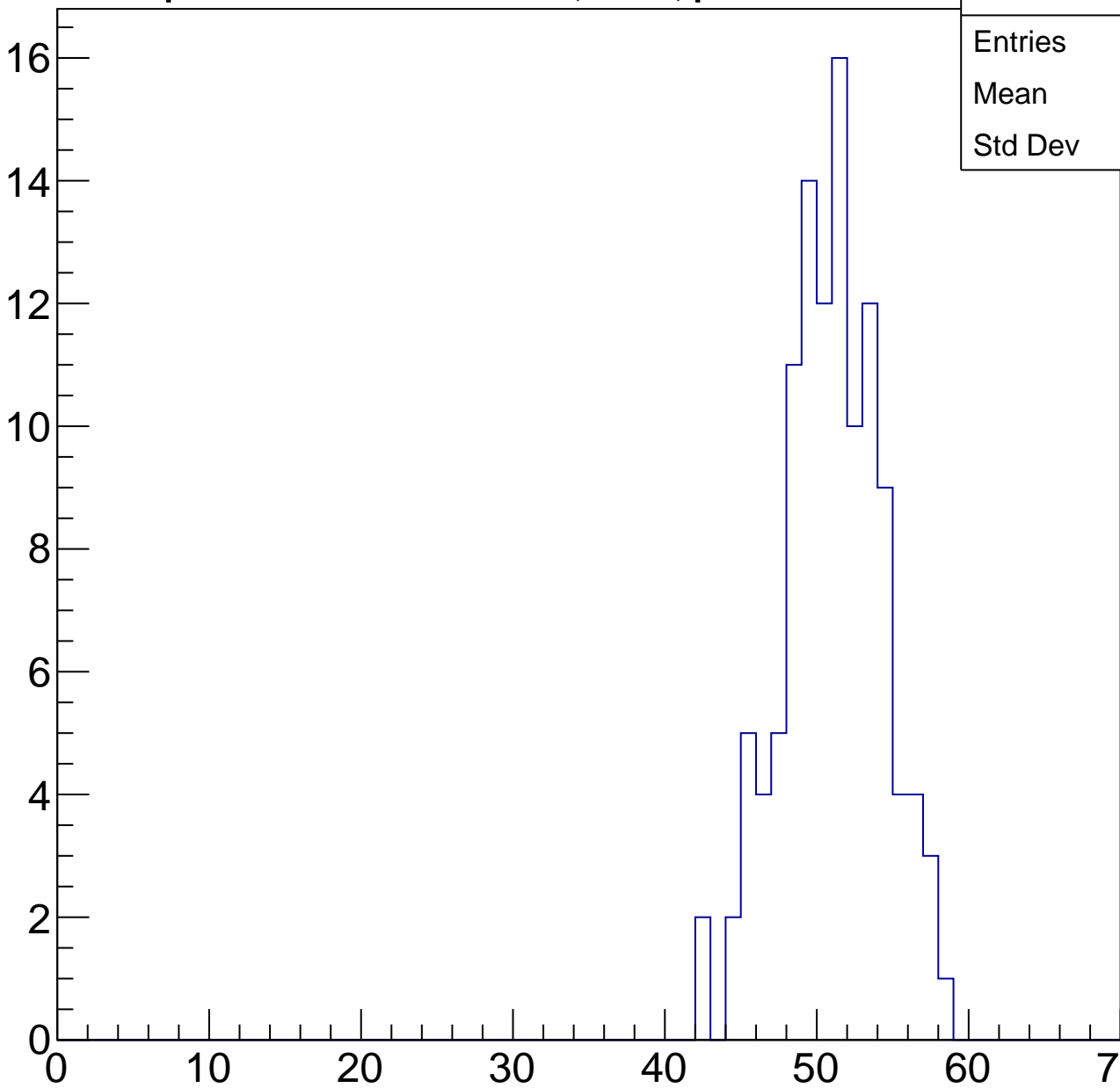
Entries	114
Mean	50.54
Std Dev	3.275

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#1, port C1

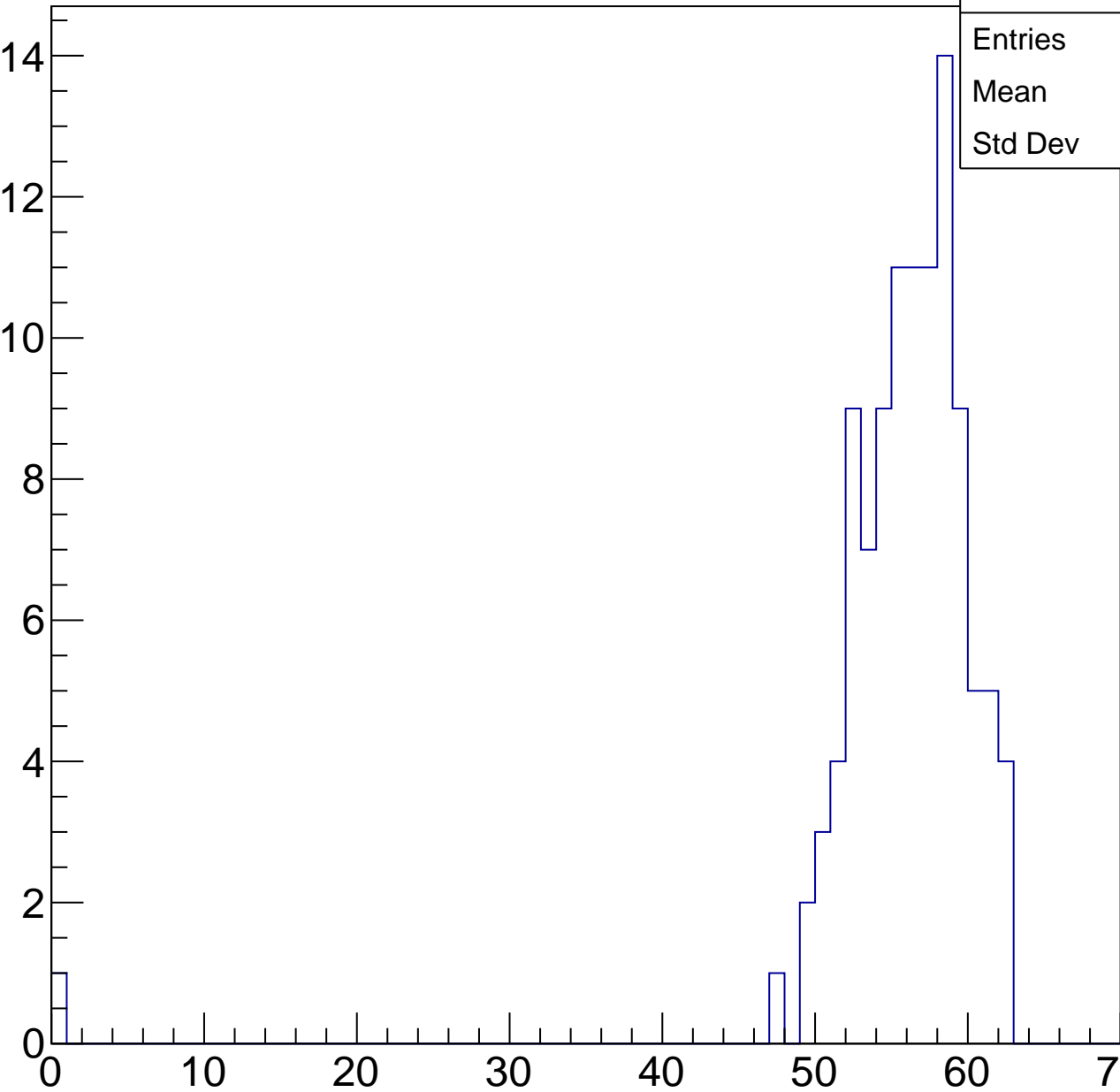
Entries	106
Mean	55.36
Std Dev	6.317

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch4, adc5

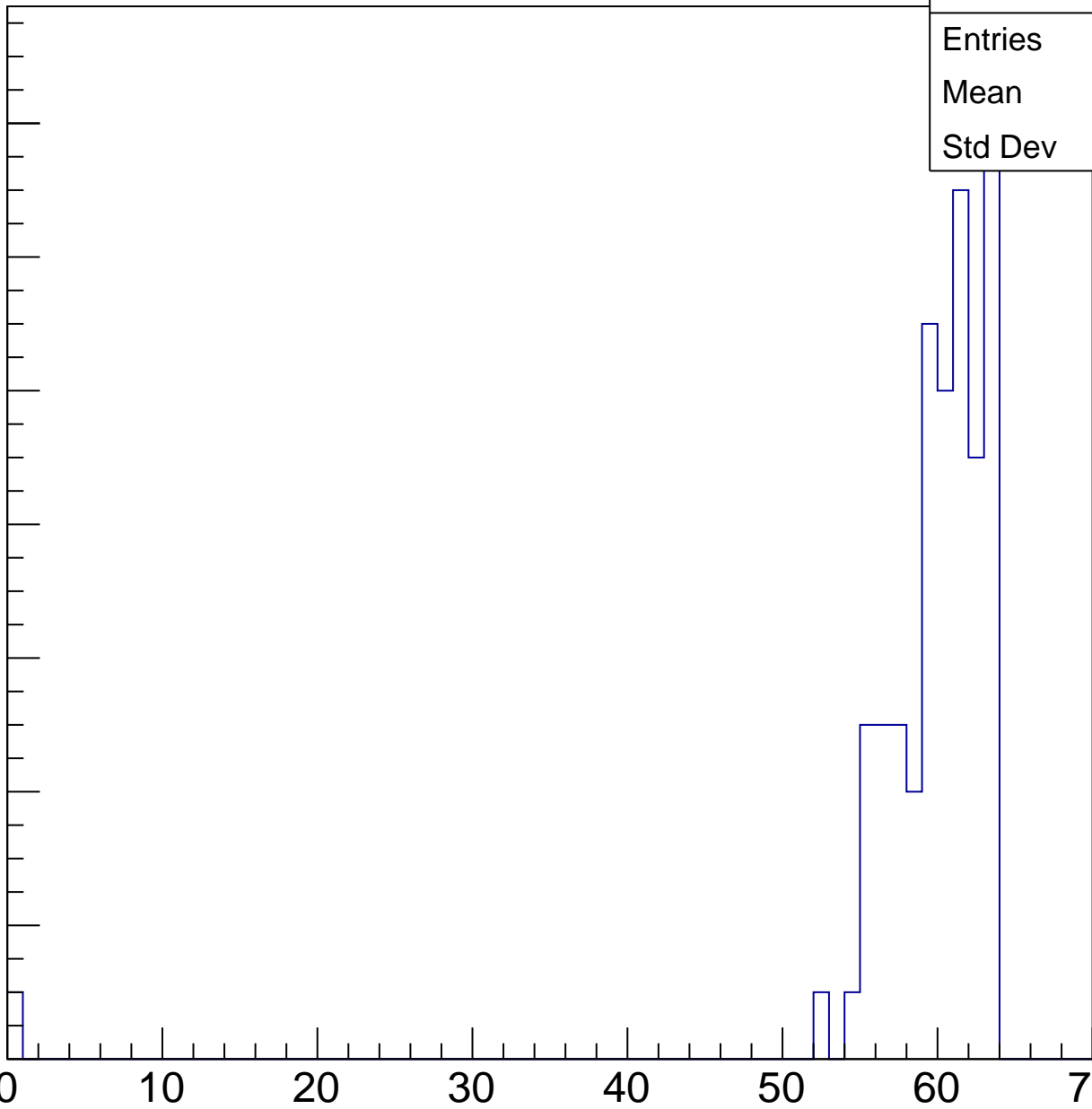
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.04
Std Dev	7.147

ampl

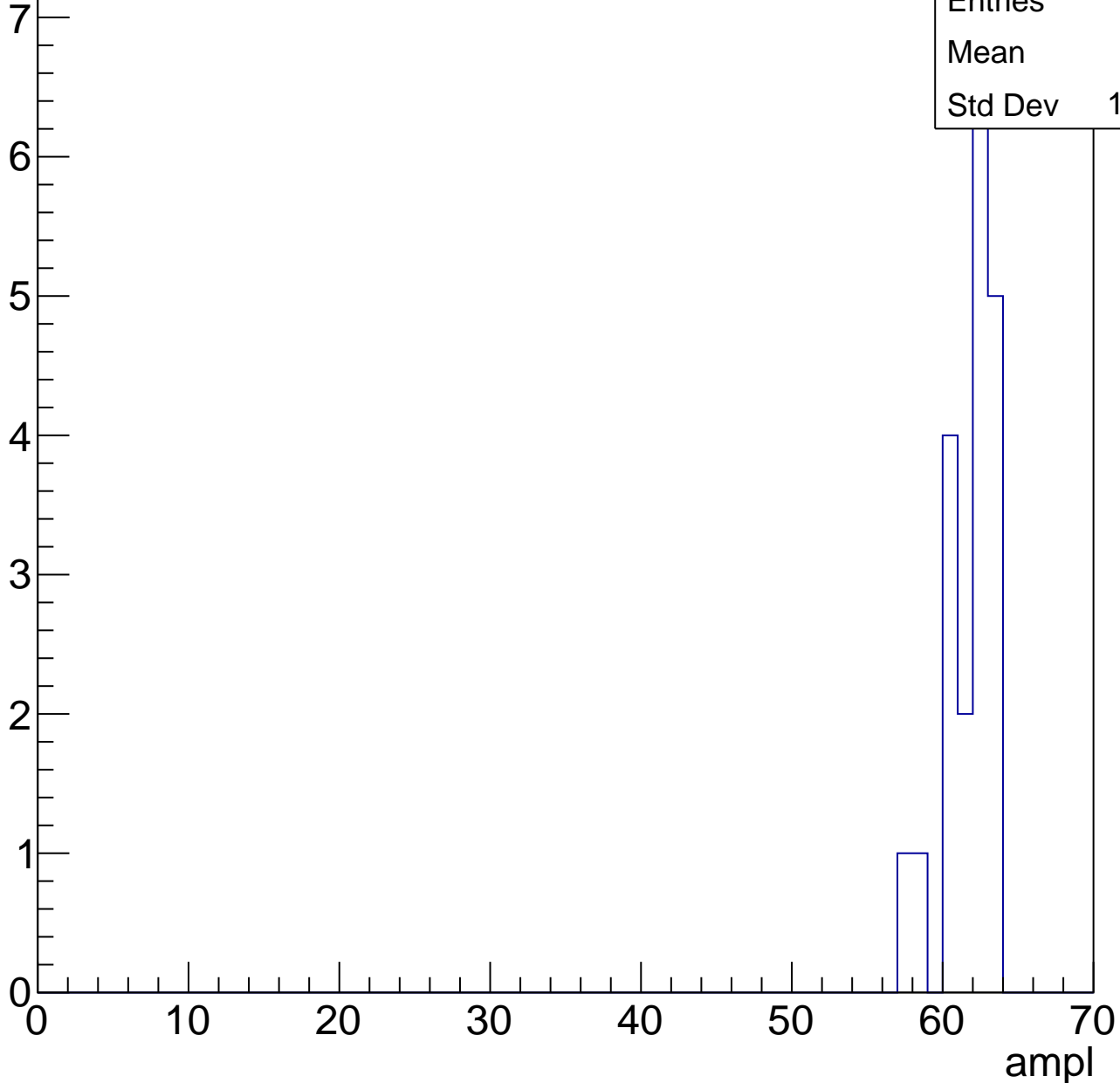


B0L101S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	61.3
Std Dev	1.646



B0L101S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch5, adc0

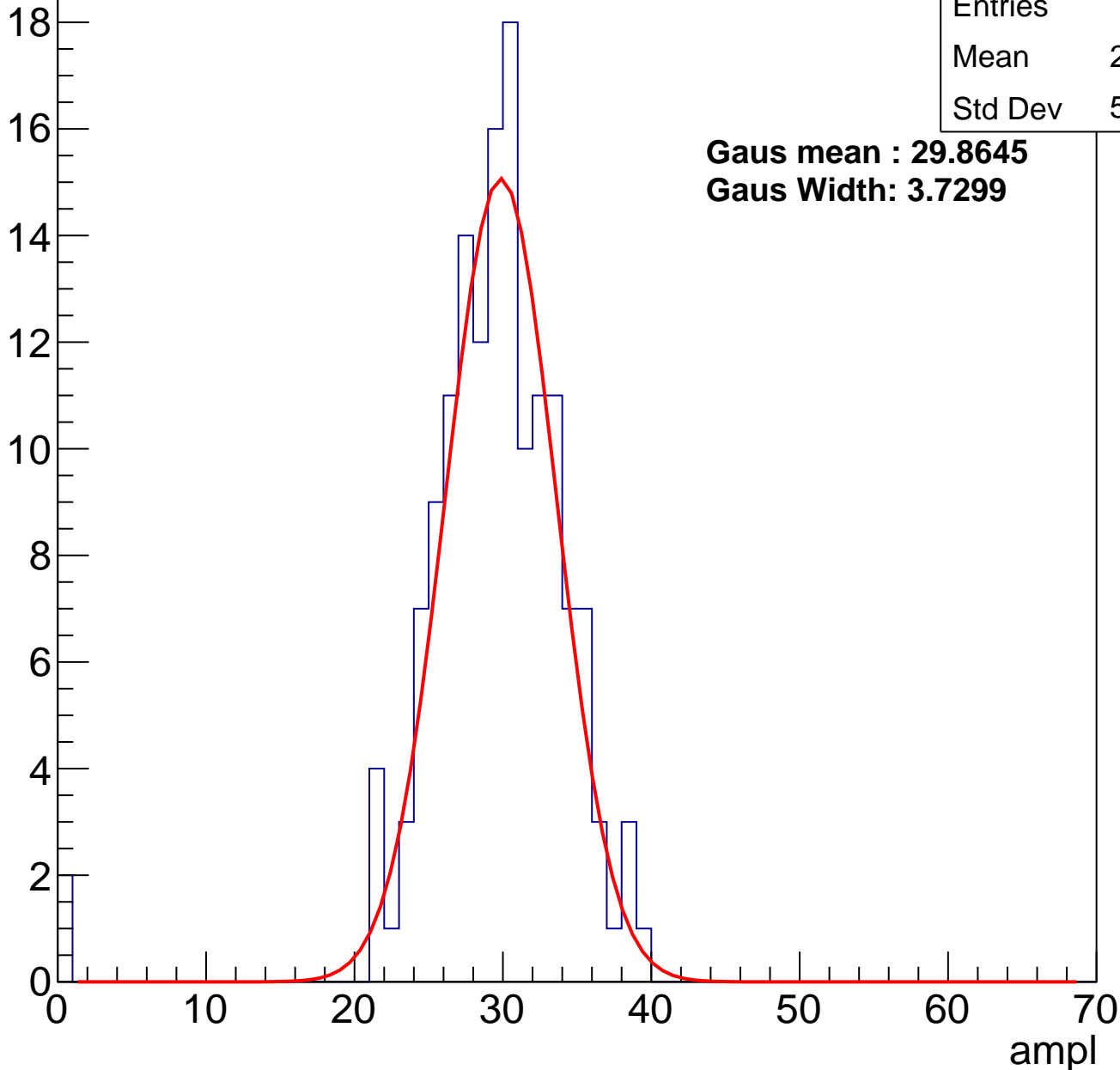
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.99
Std Dev	5.079

Gaus mean : 29.8645

Gaus Width: 3.7299

Entry



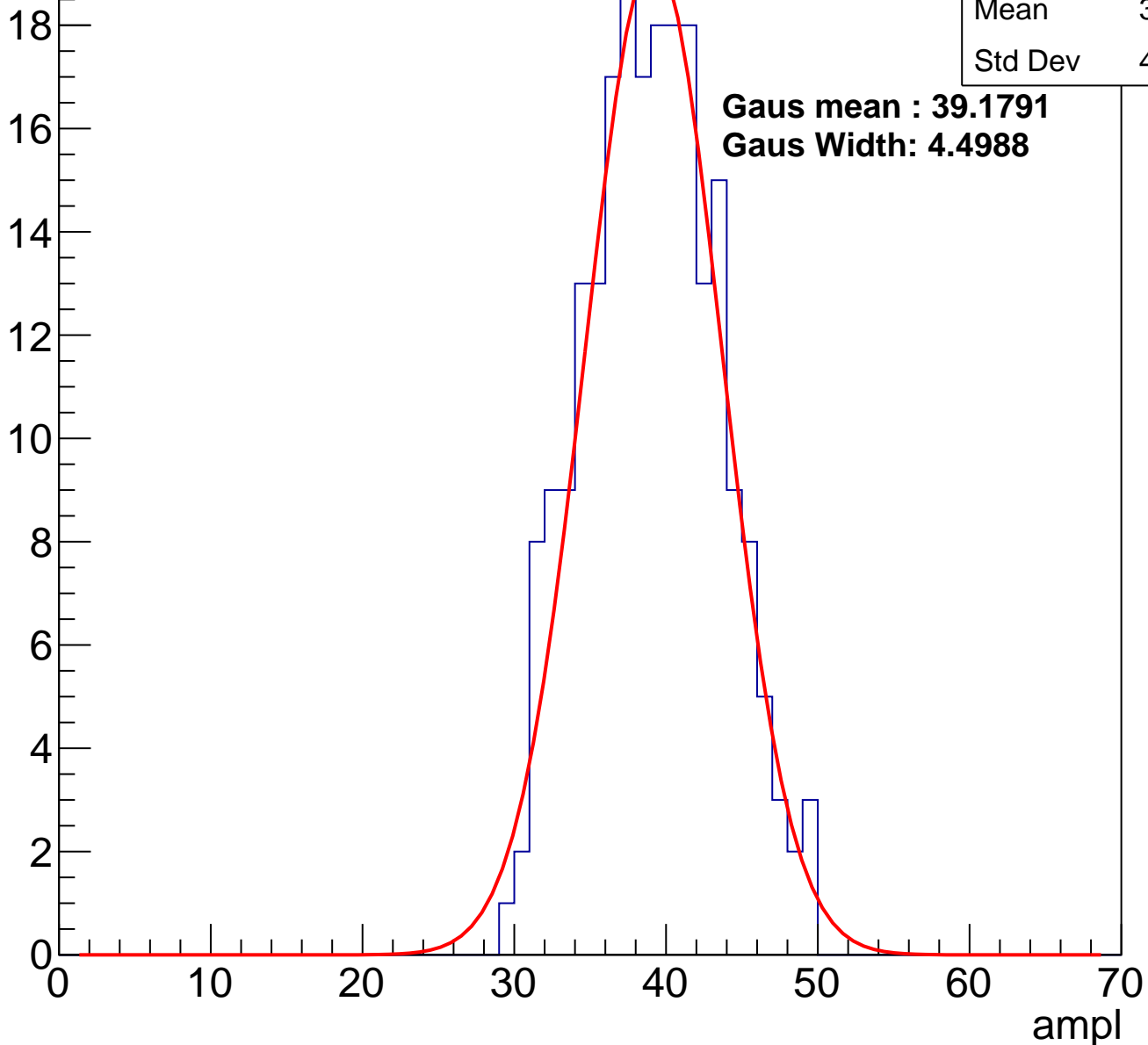
B0L101S, U14-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	220
Mean	38.64
Std Dev	4.343

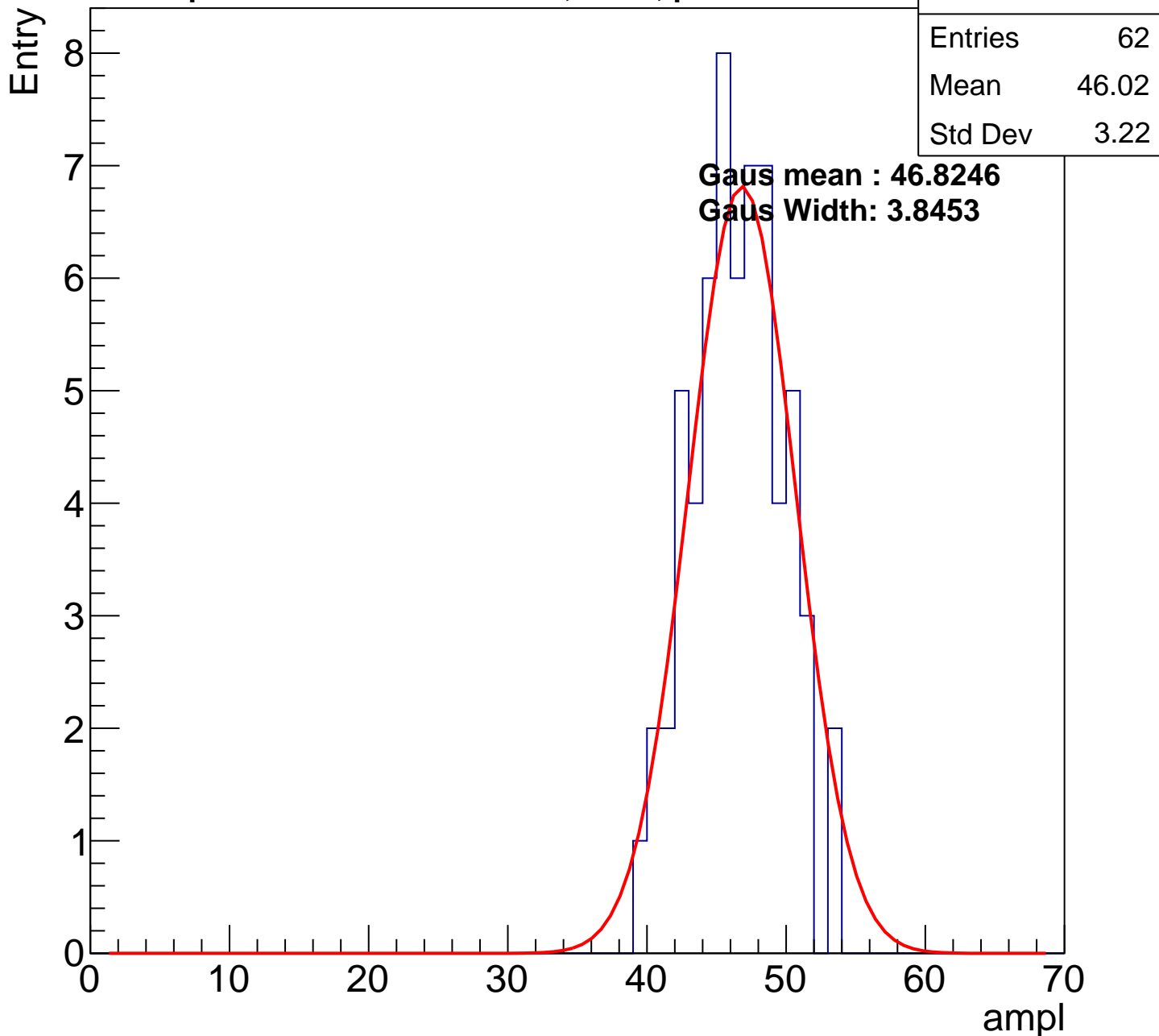
Gaus mean : 39.1791
Gaus Width: 4.4988

Entry



B0L101S, U14-ch5, adc2

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U14-ch5, adc3

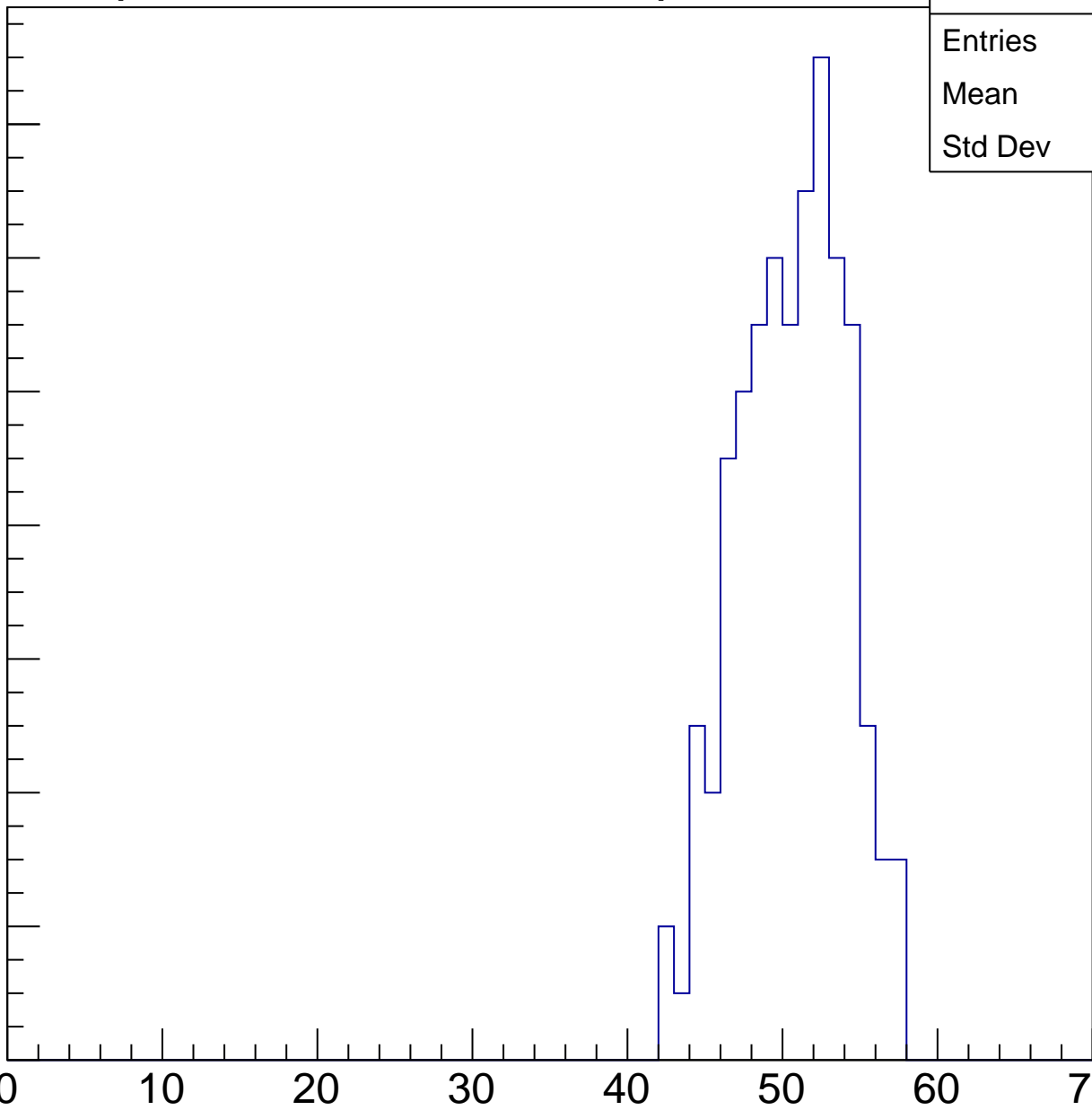
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	50.11
Std Dev	3.417

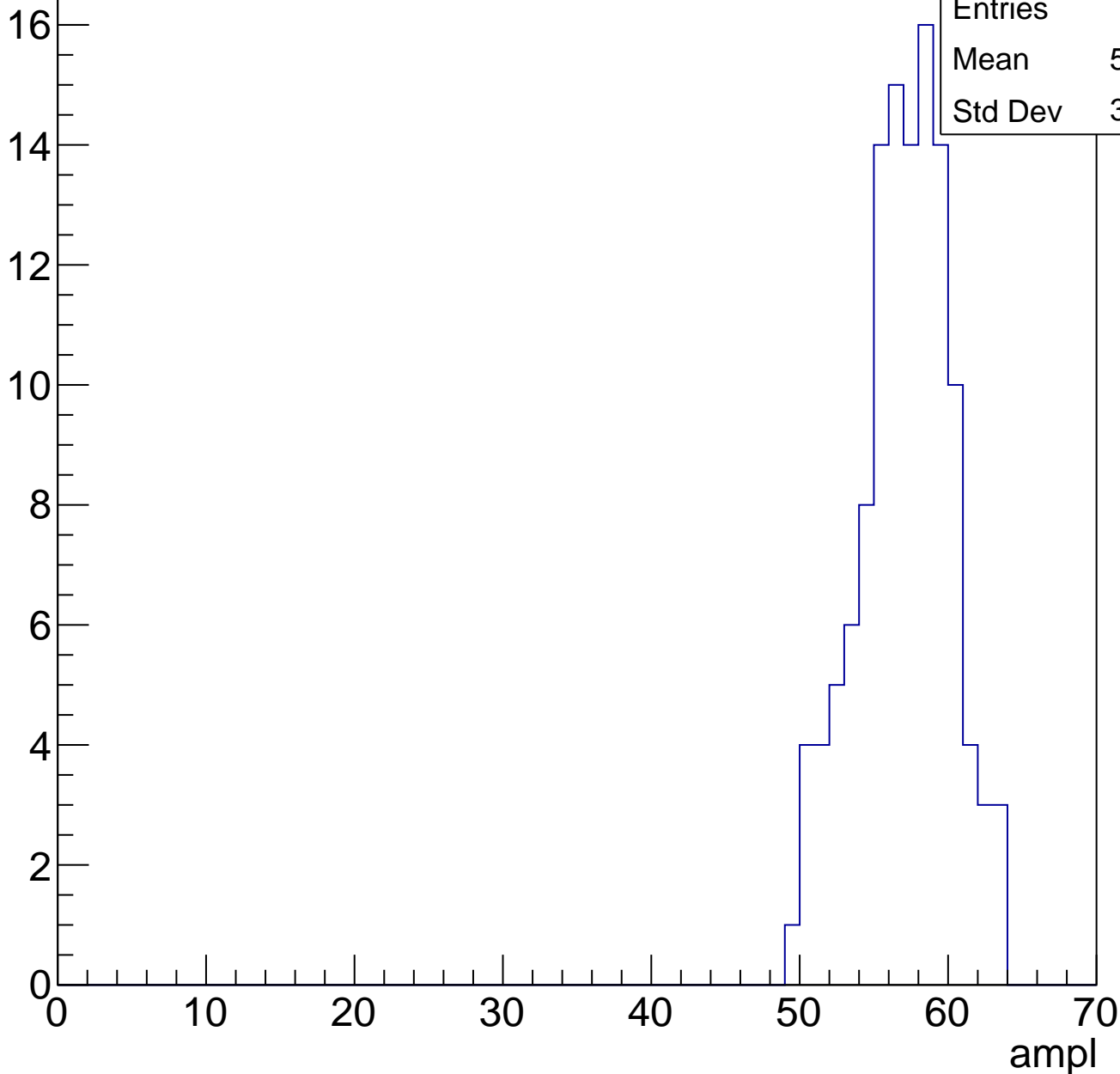
ampl



B0L101S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch5, adc5

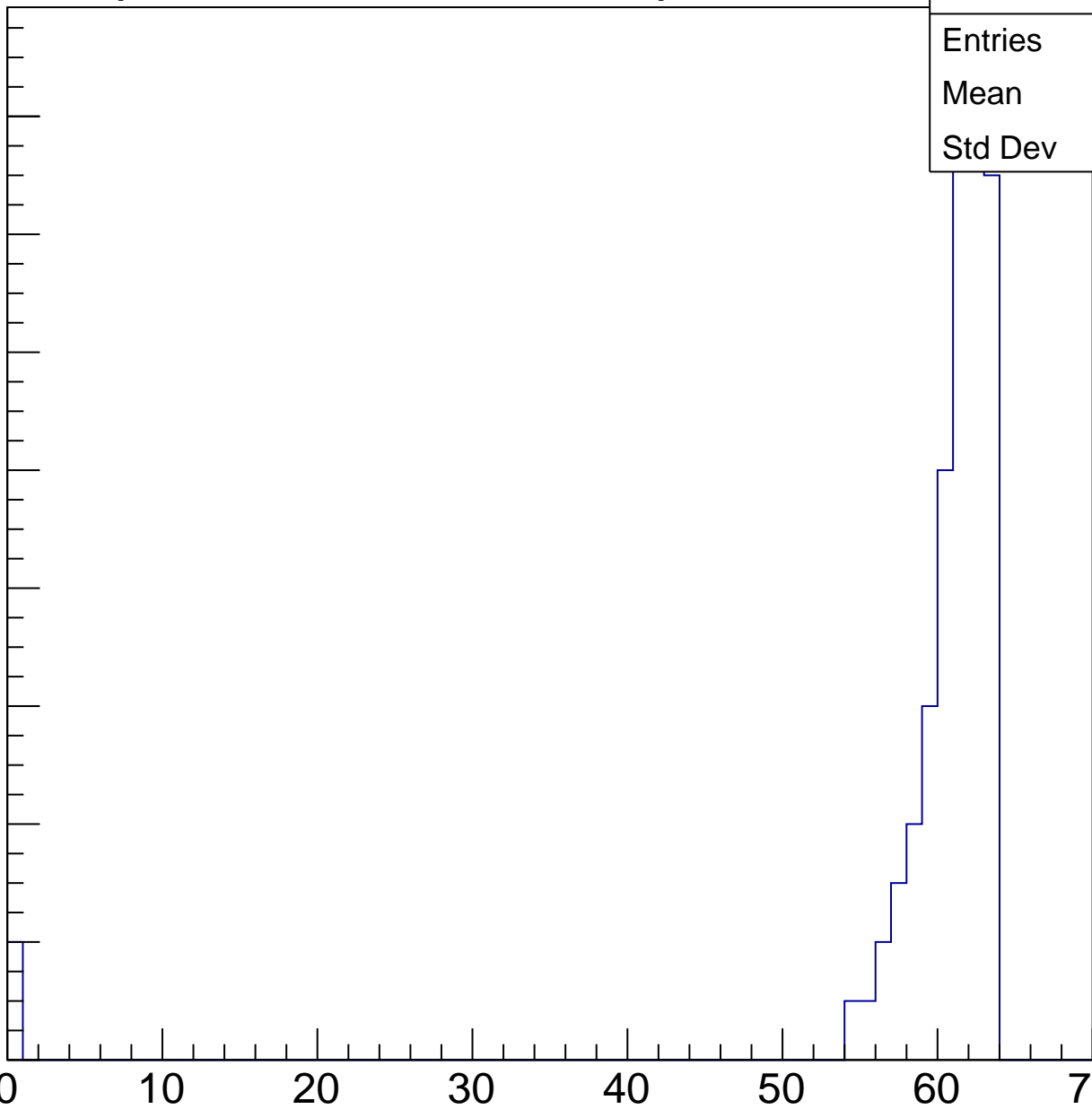
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.13
Std Dev	9.871

ampl



B0L101S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	30.42
Std Dev	3.652

Gaus mean : 30.7916

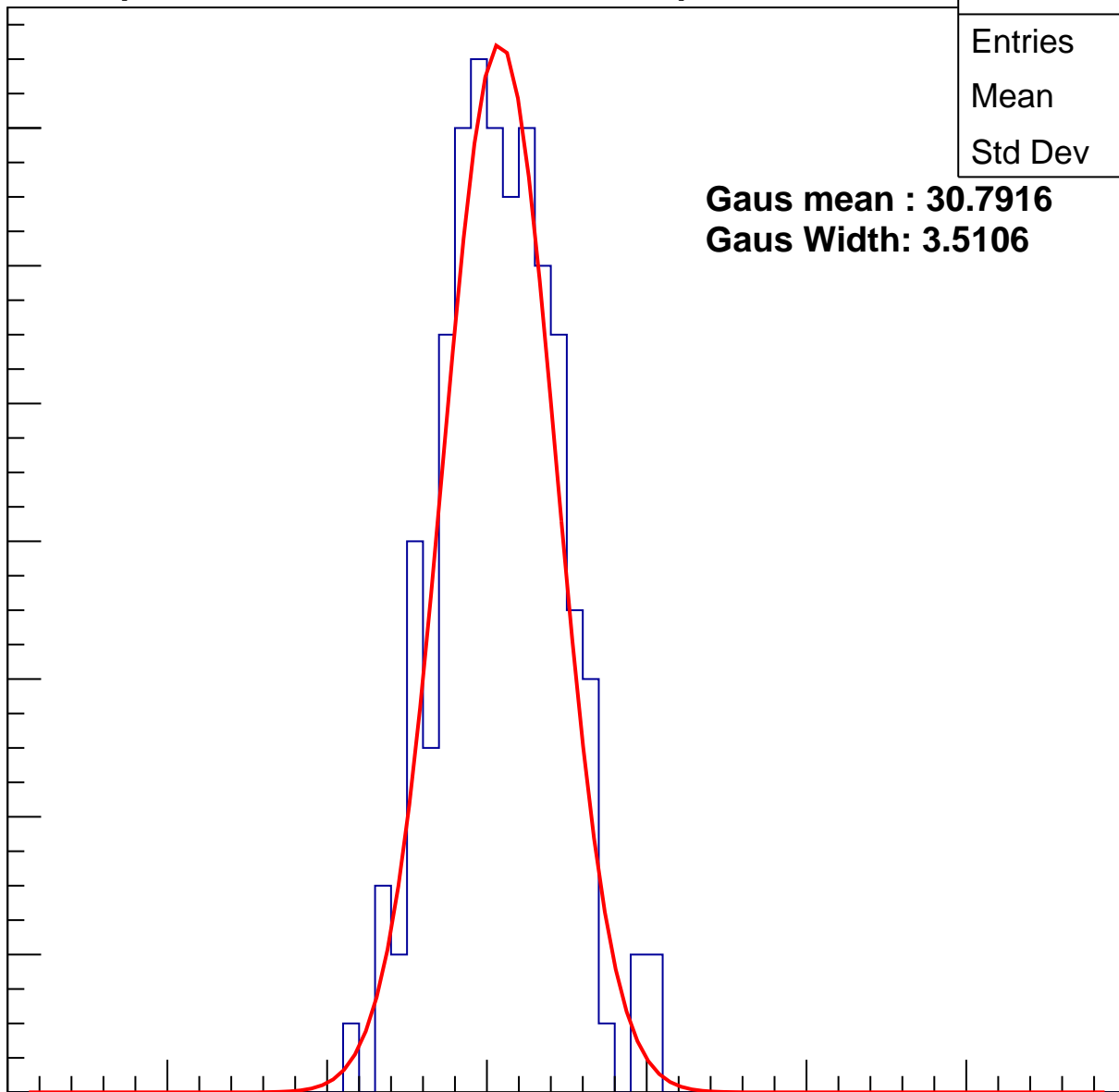
Gaus Width: 3.5106

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch6, adc1

calib_packv5_042523_0143.root, FC#1, port C1

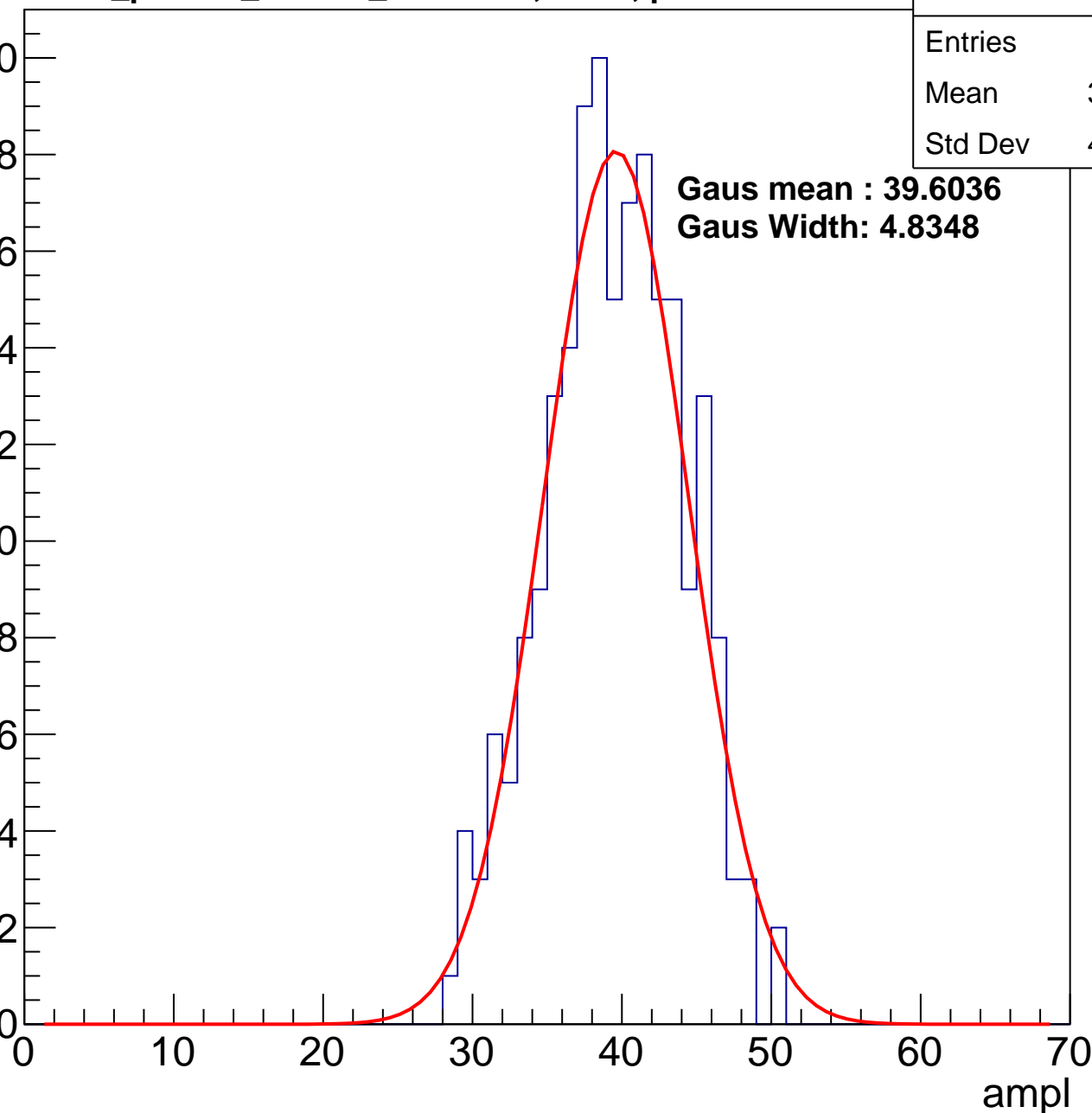
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	220
Mean	39.02
Std Dev	4.554

Gaus mean : 39.6036

Gaus Width: 4.8348



B0L101S, U14-ch6, adc2

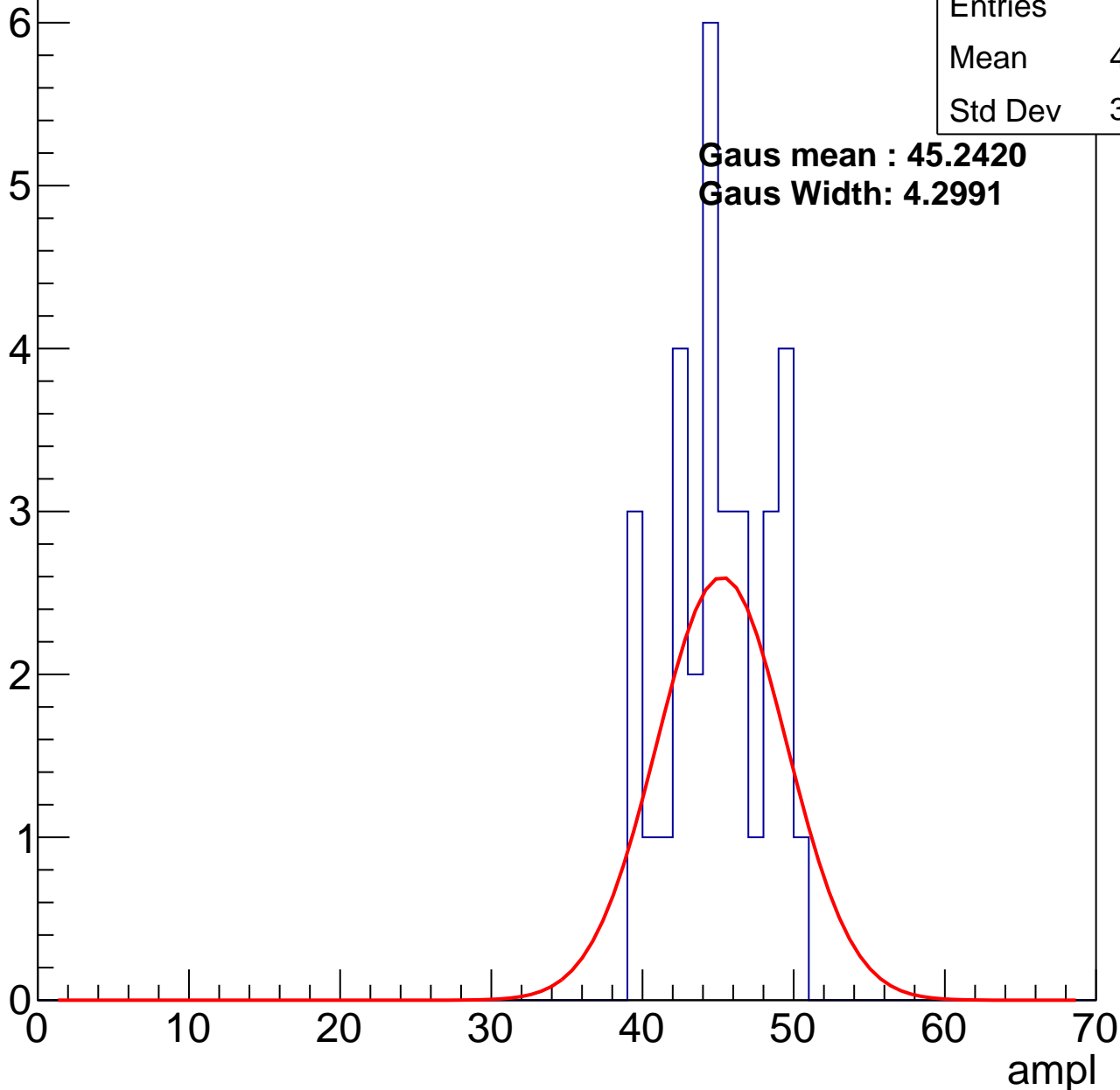
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	32
Mean	44.56
Std Dev	3.142

Gaus mean : 45.2420

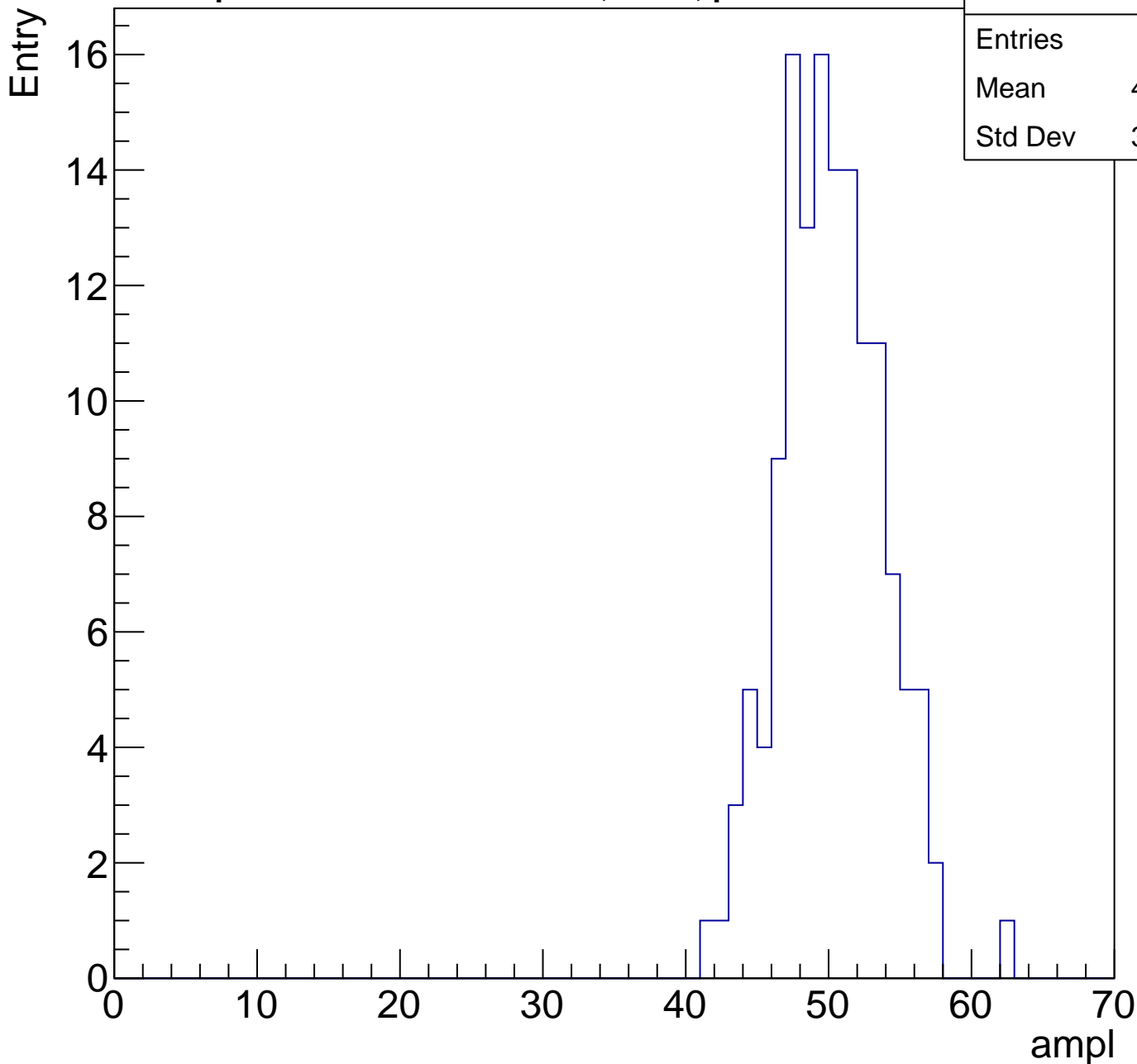
Gaus Width: 4.2991



B0L101S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	49.74
Std Dev	3.545



B0L101S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#1, port C1

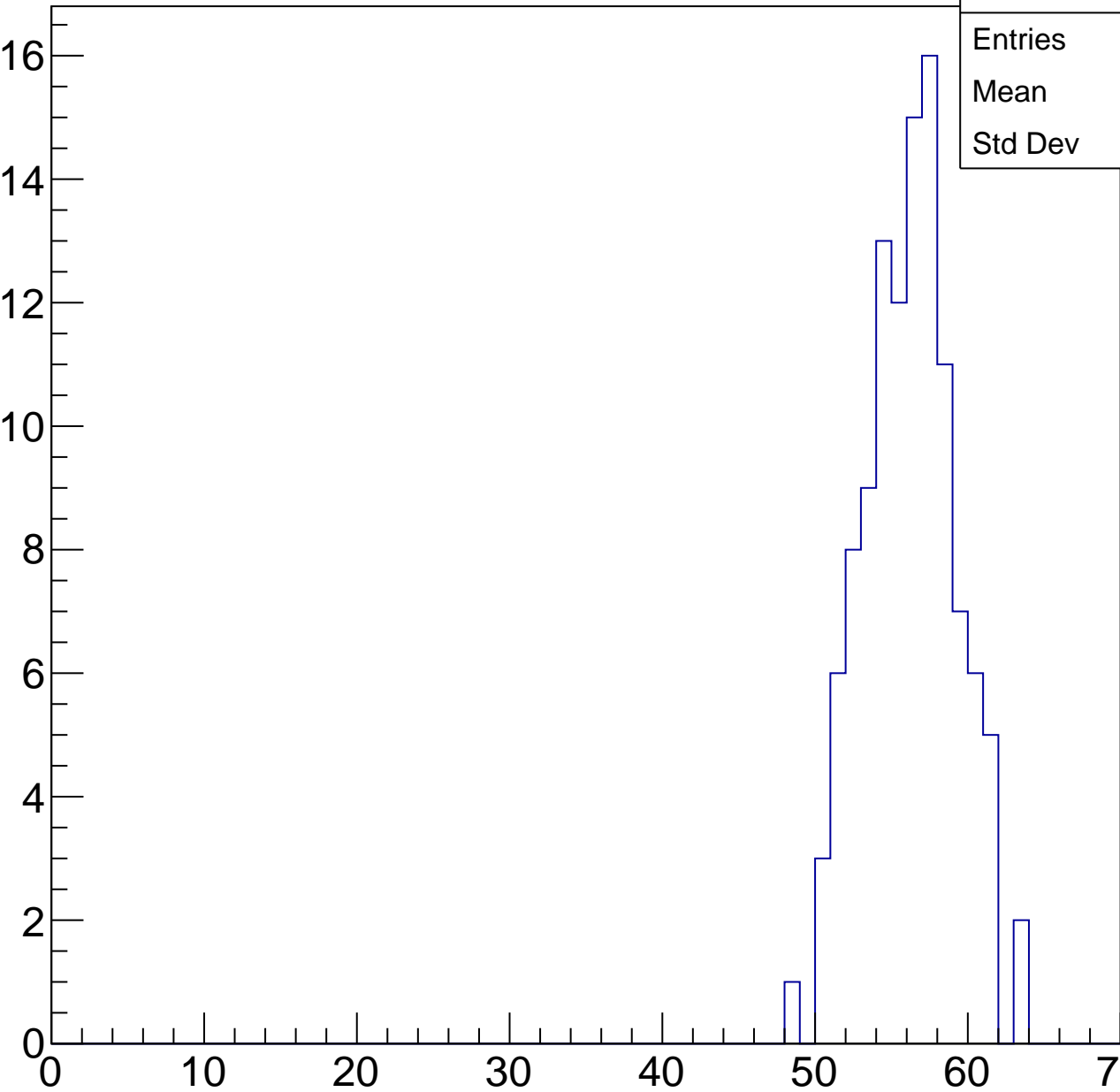
Entries	114
Mean	55.73
Std Dev	3.004

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	85
Mean	59.67
Std Dev	6.873

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

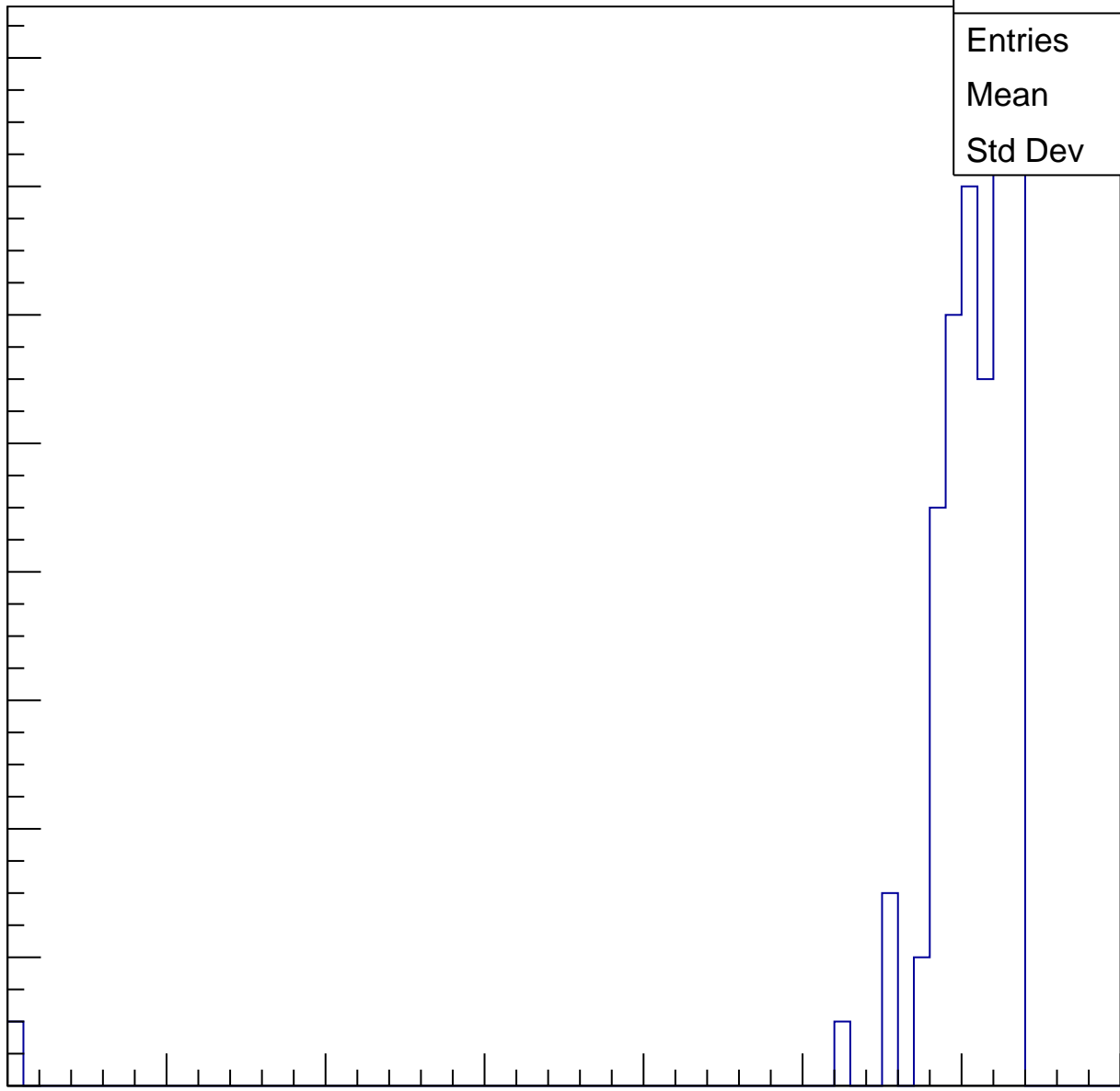
40

50

60

70

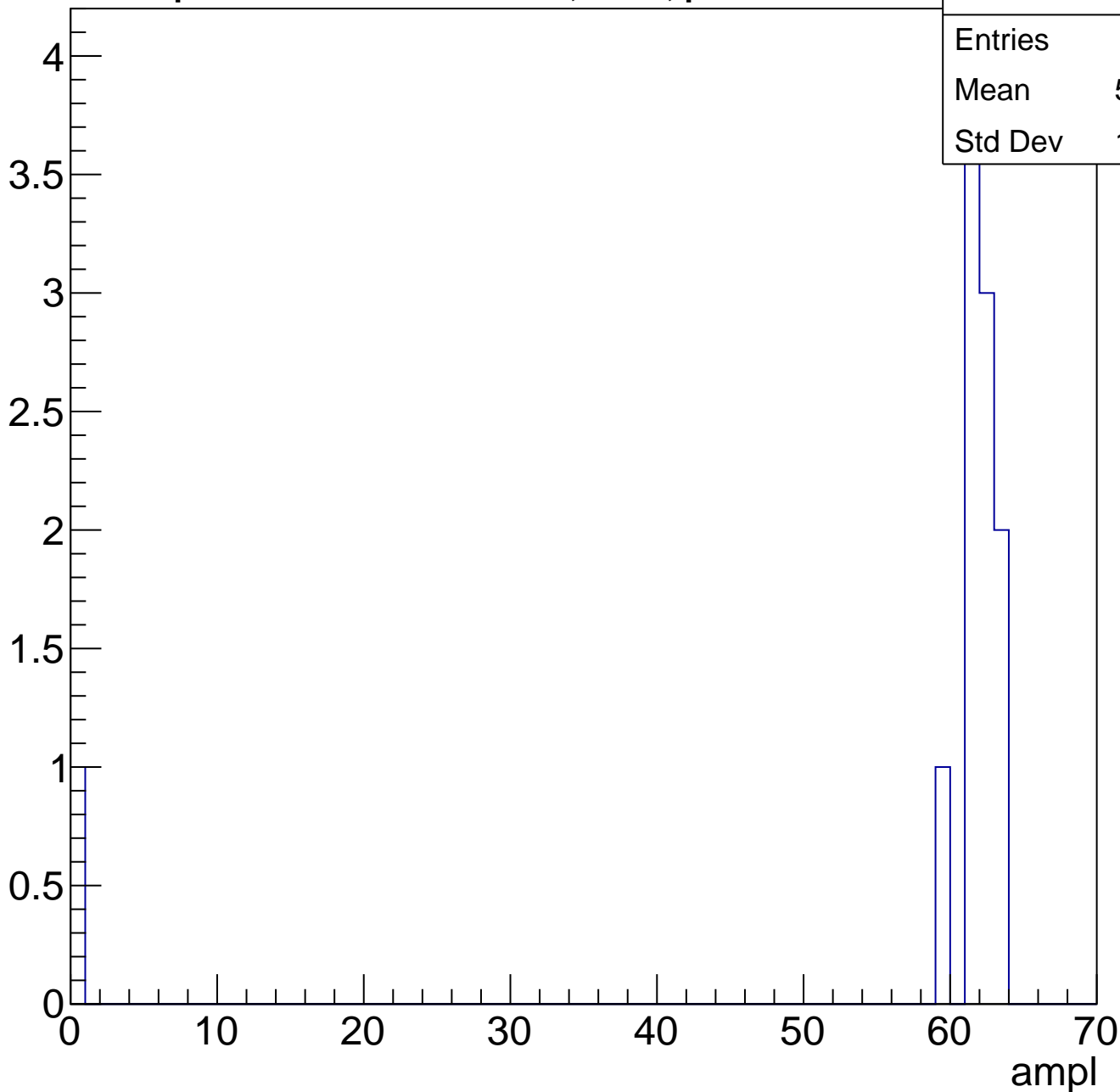
ampl



B0L101S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

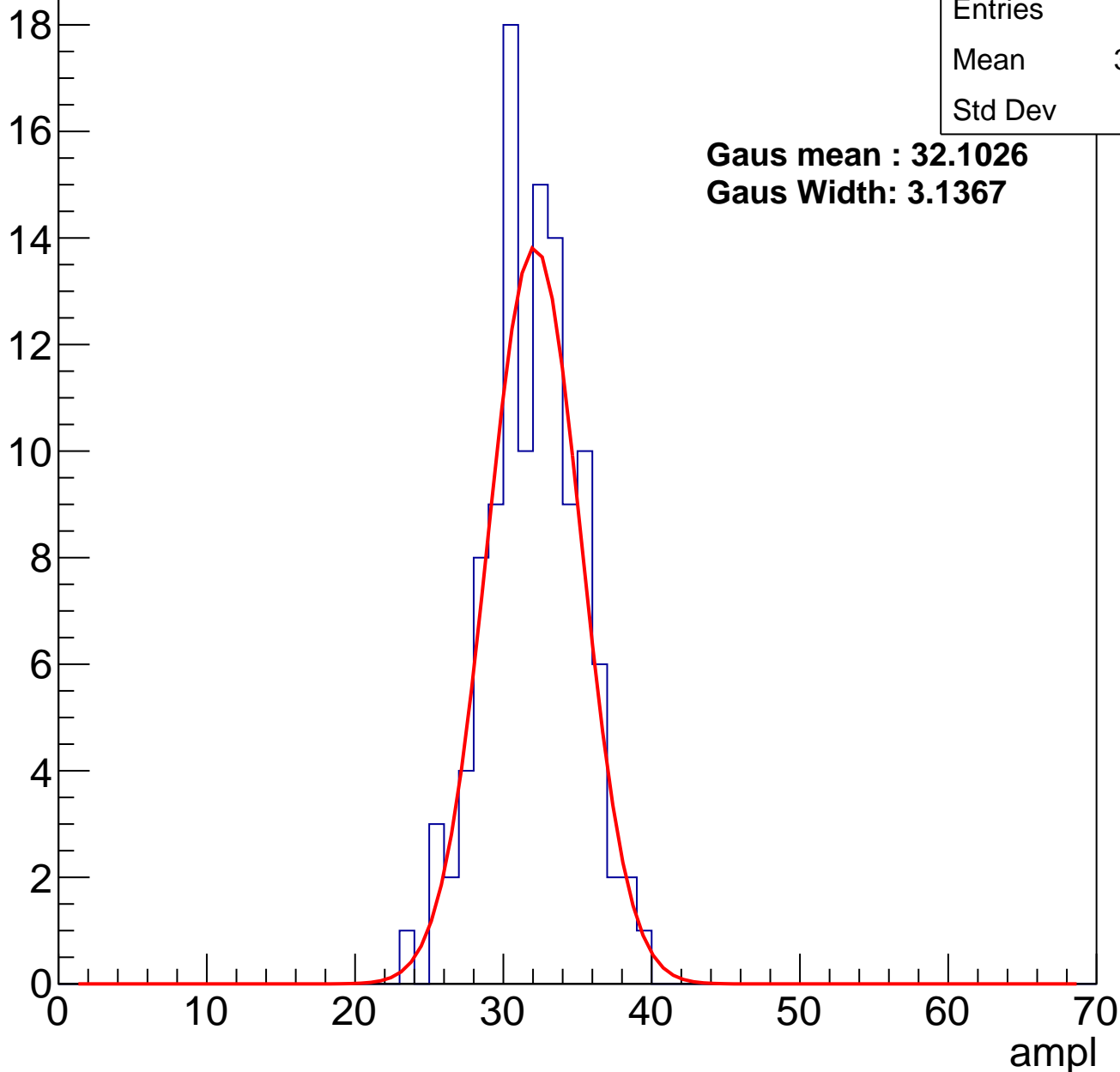


Entries	1
Mean	24
Std Dev	0

B0L101S, U14-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



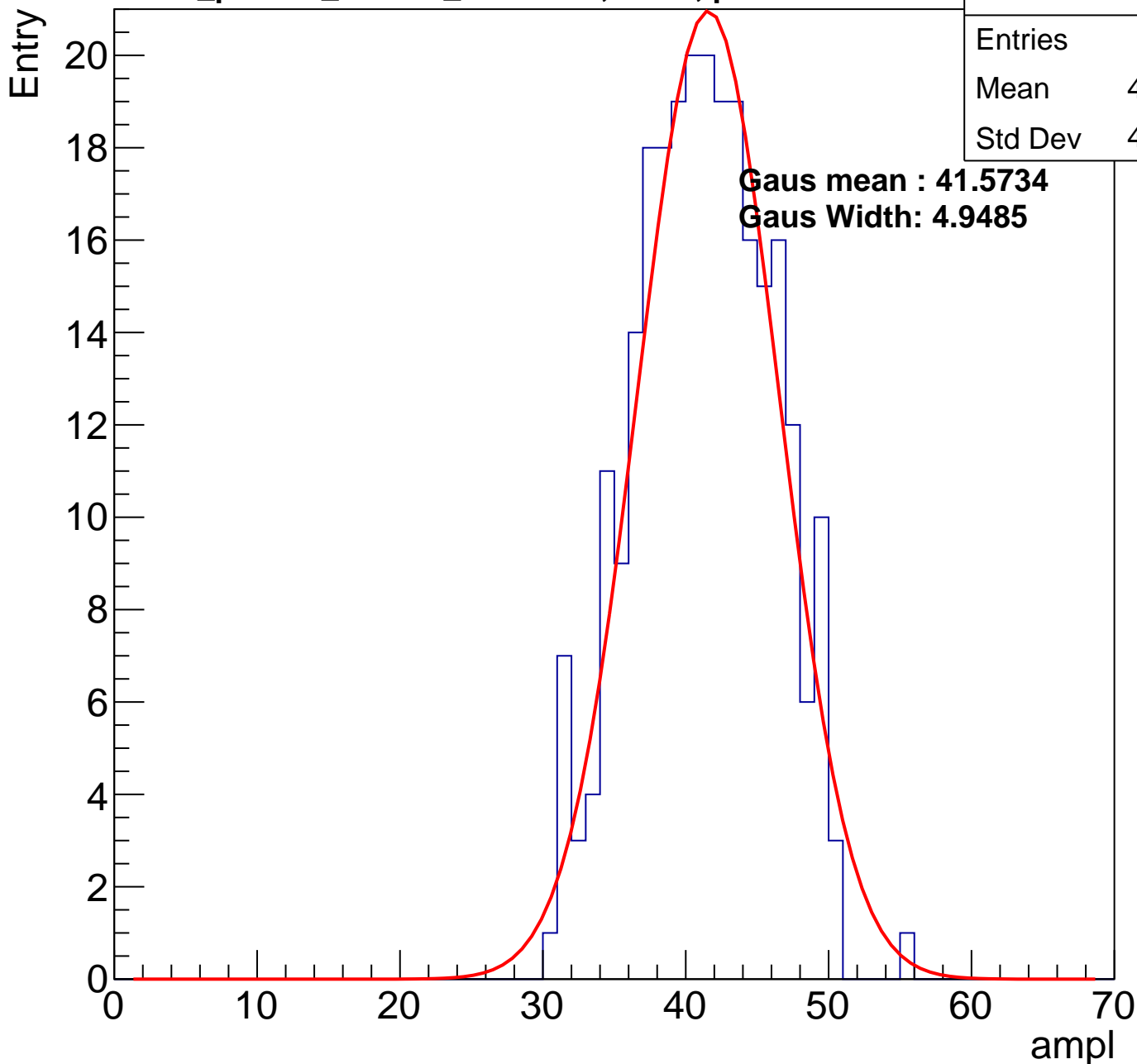
B0L101S, U14-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	261
Mean	40.83
Std Dev	4.696

Gaus mean : 41.5734

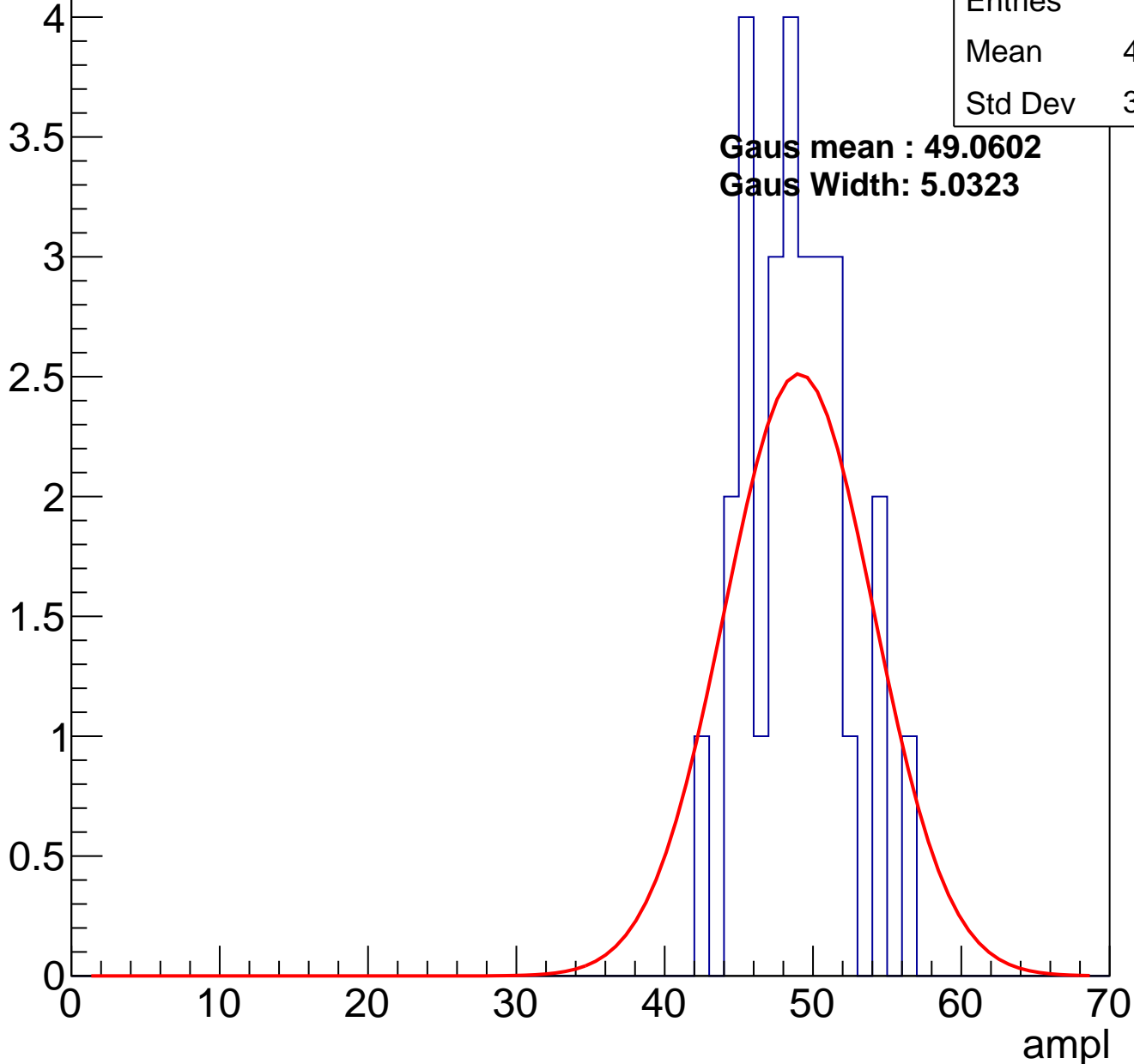
Gaus Width: 4.9485



B0L101S, U14-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch7, adc3

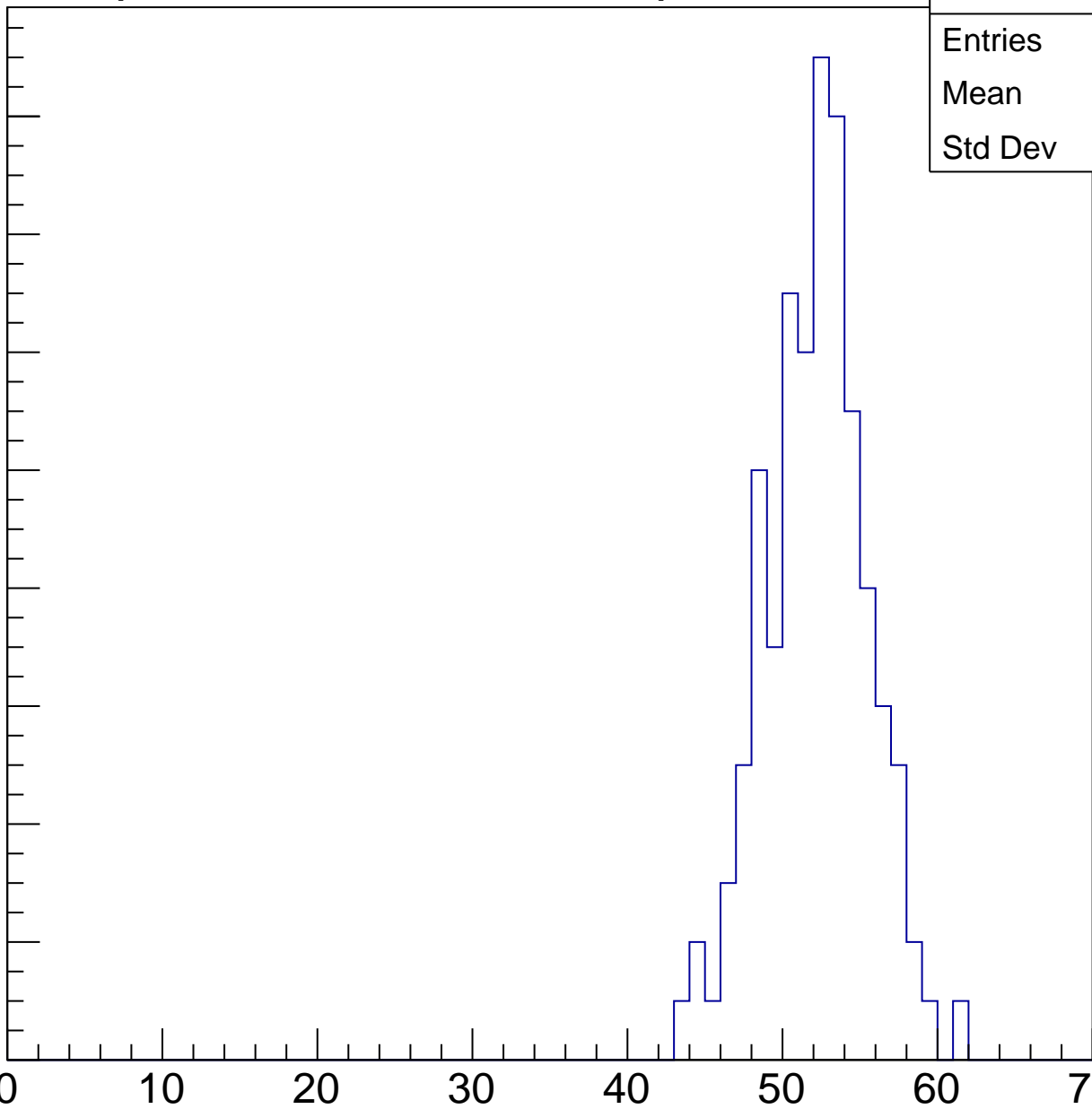
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	51.71
Std Dev	3.306

ampl



B0L101S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

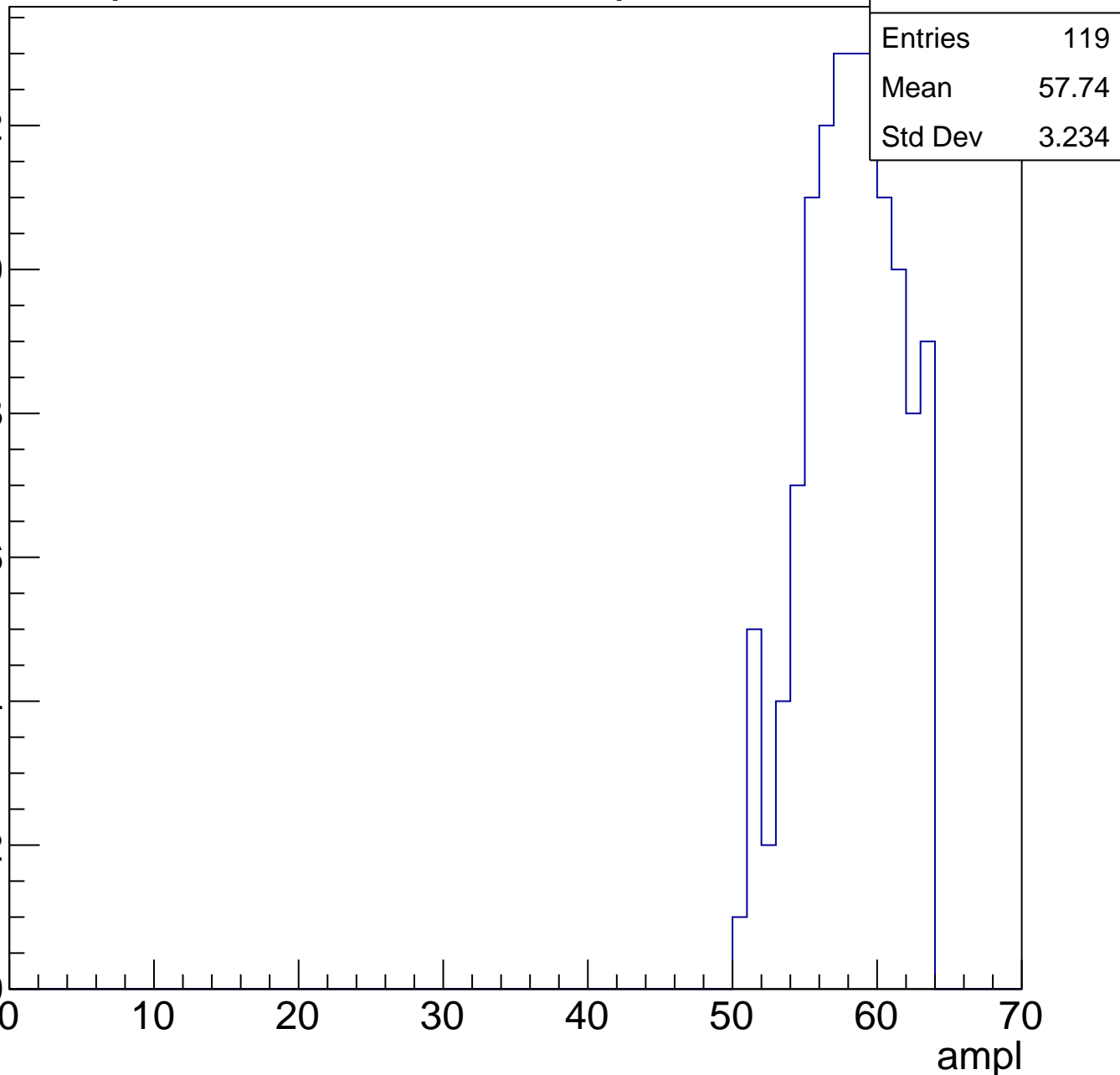
50

60

70

ampl

Entries	119
Mean	57.74
Std Dev	3.234



B0L101S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

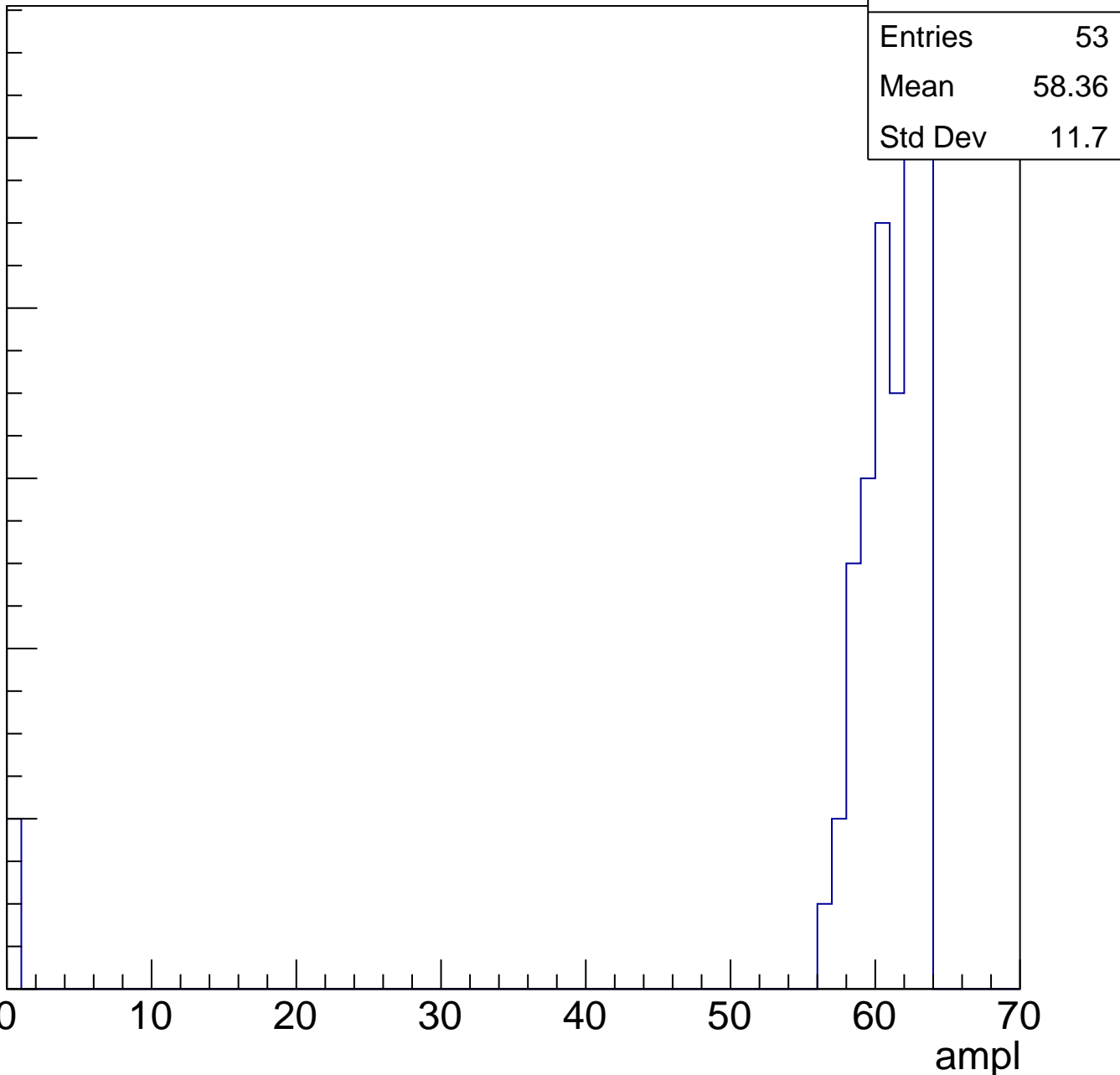
50

60

70

ampl

Entries	53
Mean	58.36
Std Dev	11.7



B0L101S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	28.92
Std Dev	3.84

Gaus mean : 29.2200

Gaus Width: 3.7686

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

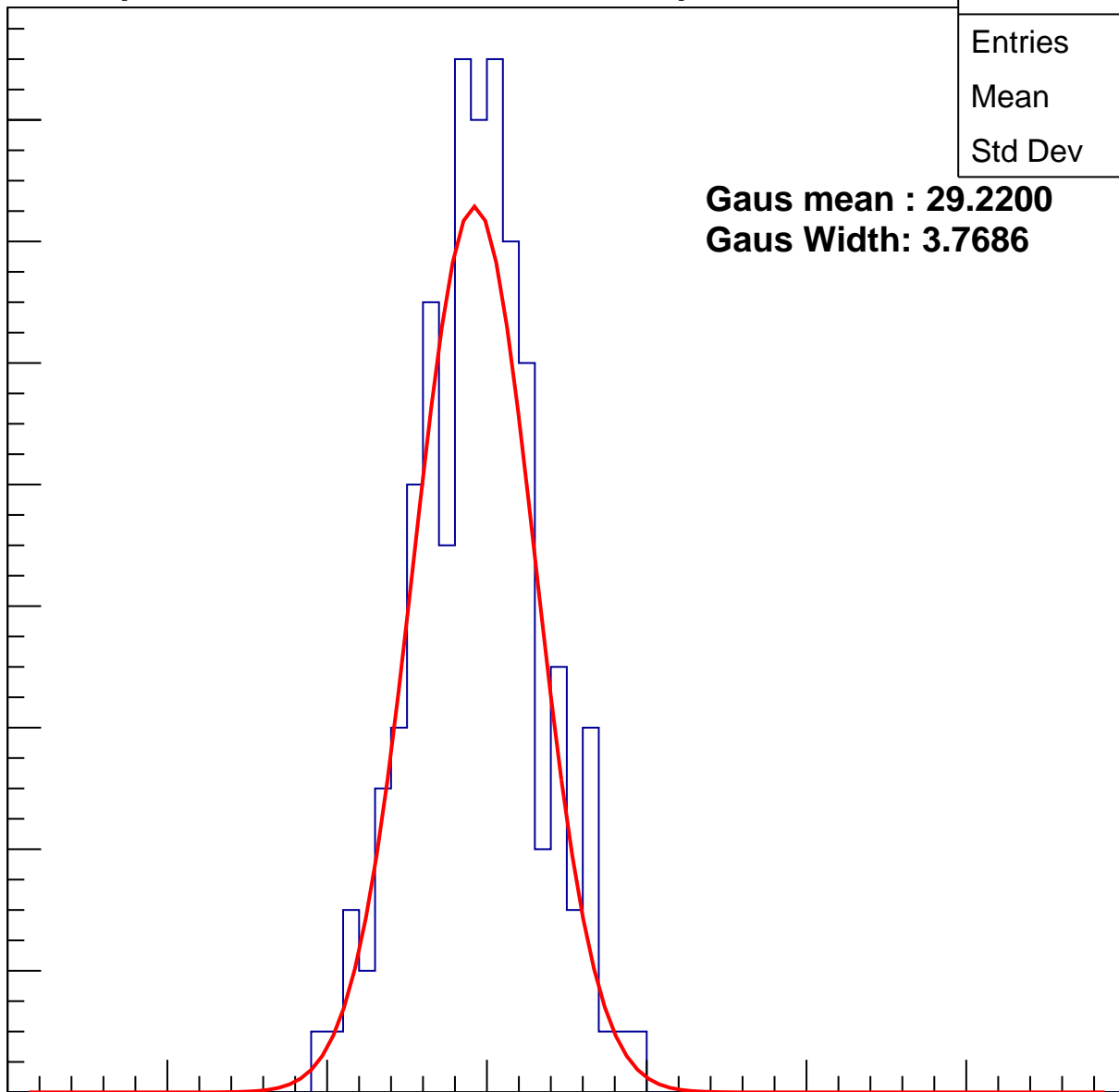
40

50

60

70

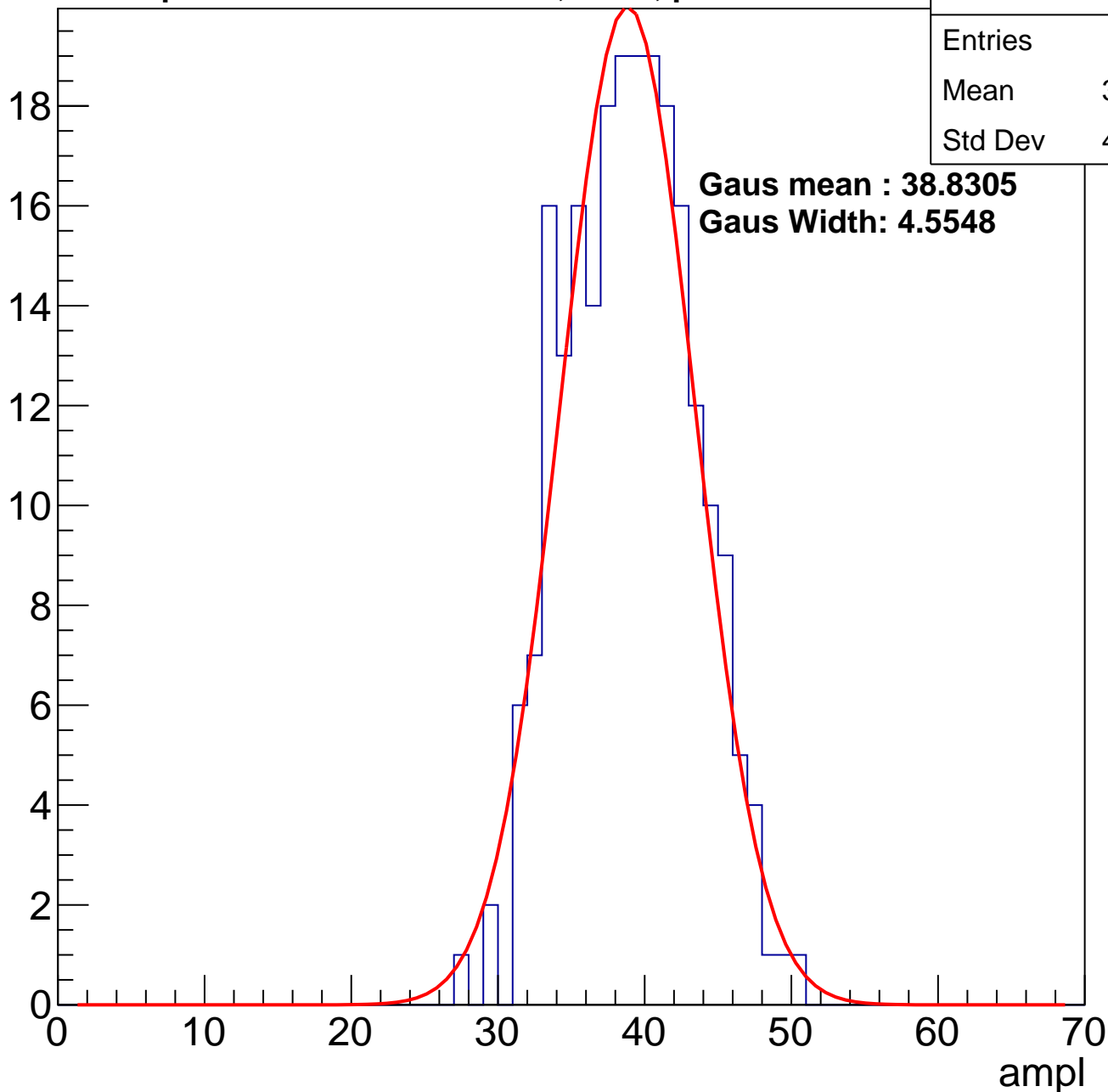
ampl



B0L101S, U14-ch8, adc1

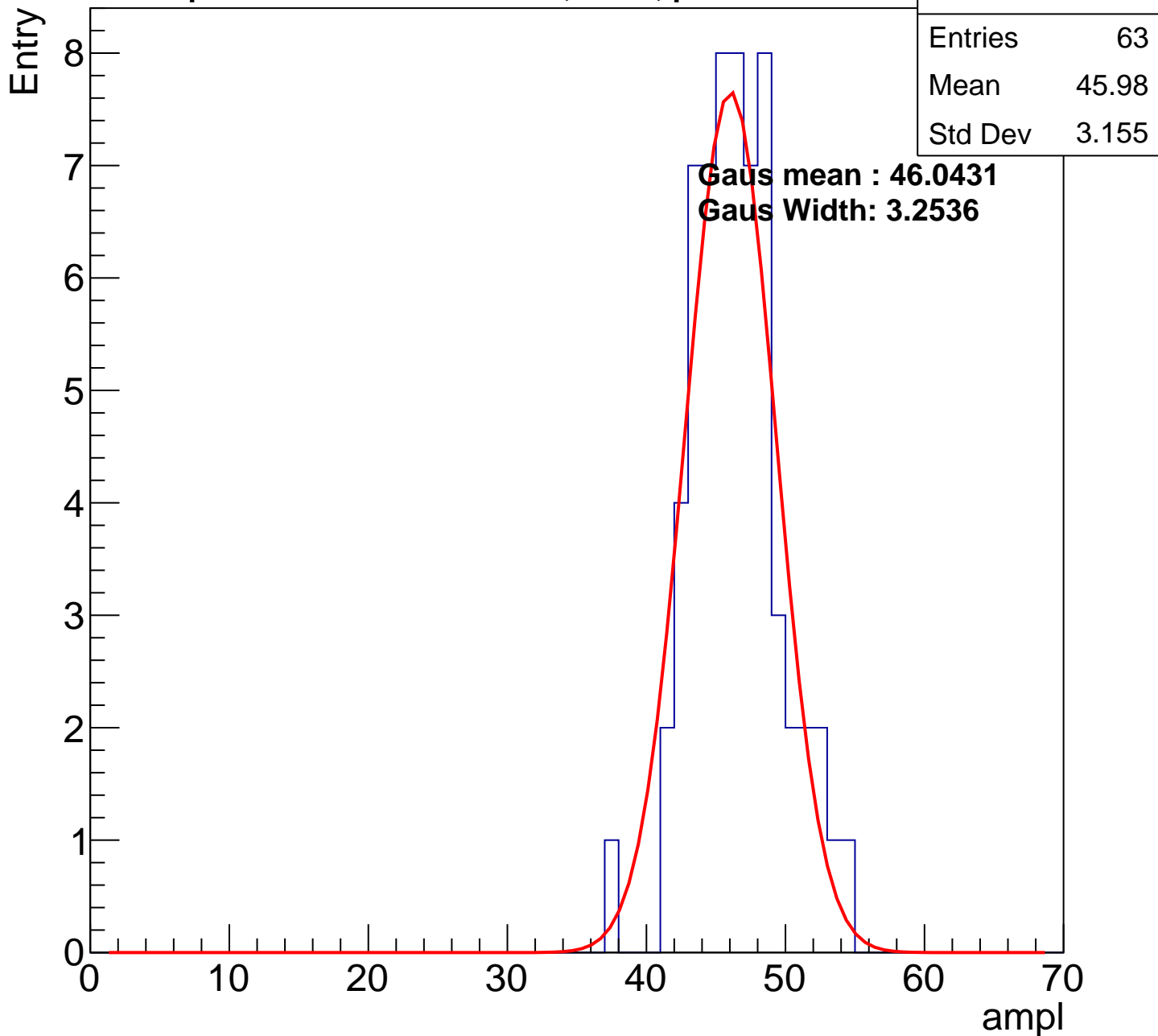
calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

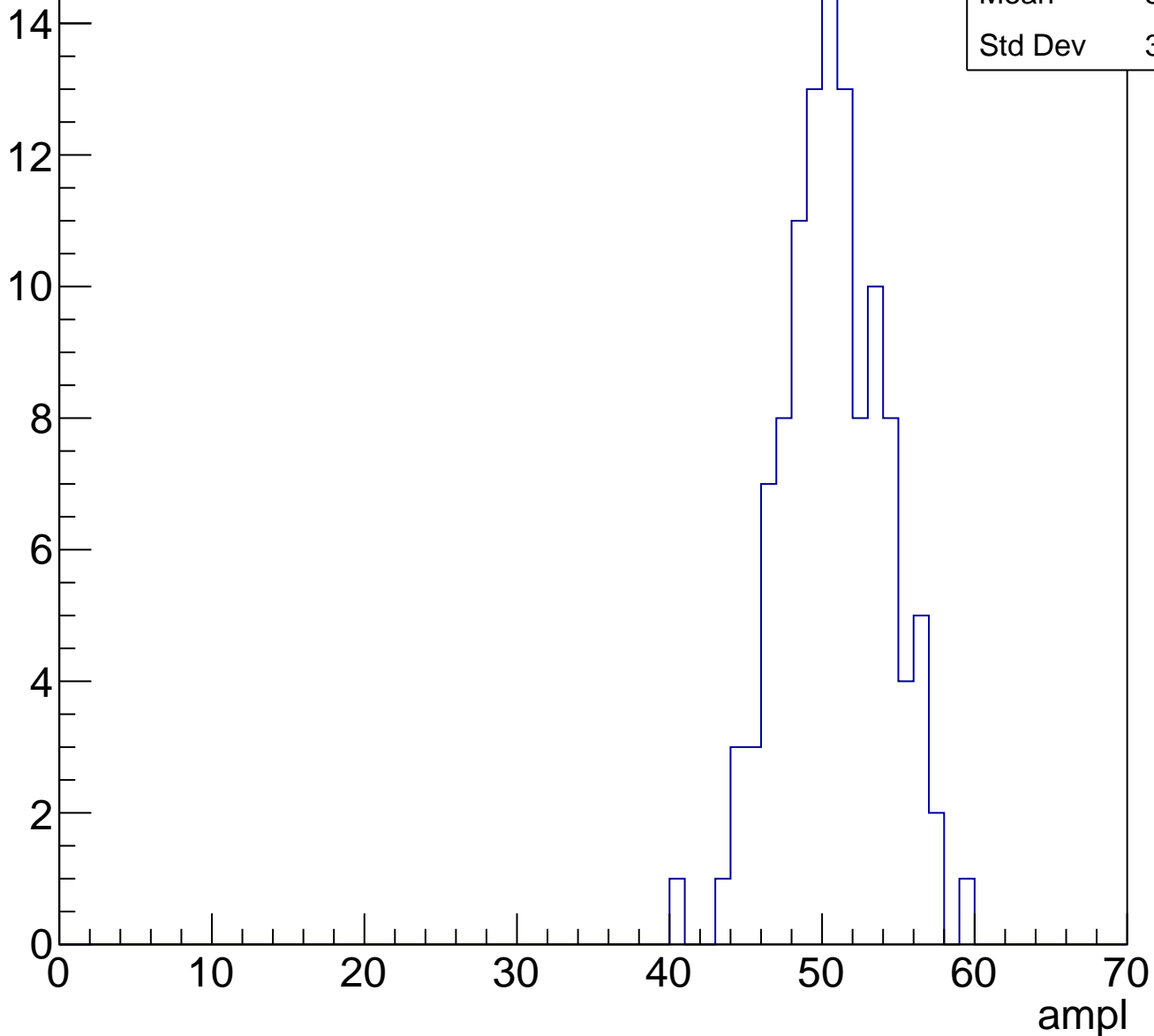


B0L101S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	50.24
Std Dev	3.377

Entry

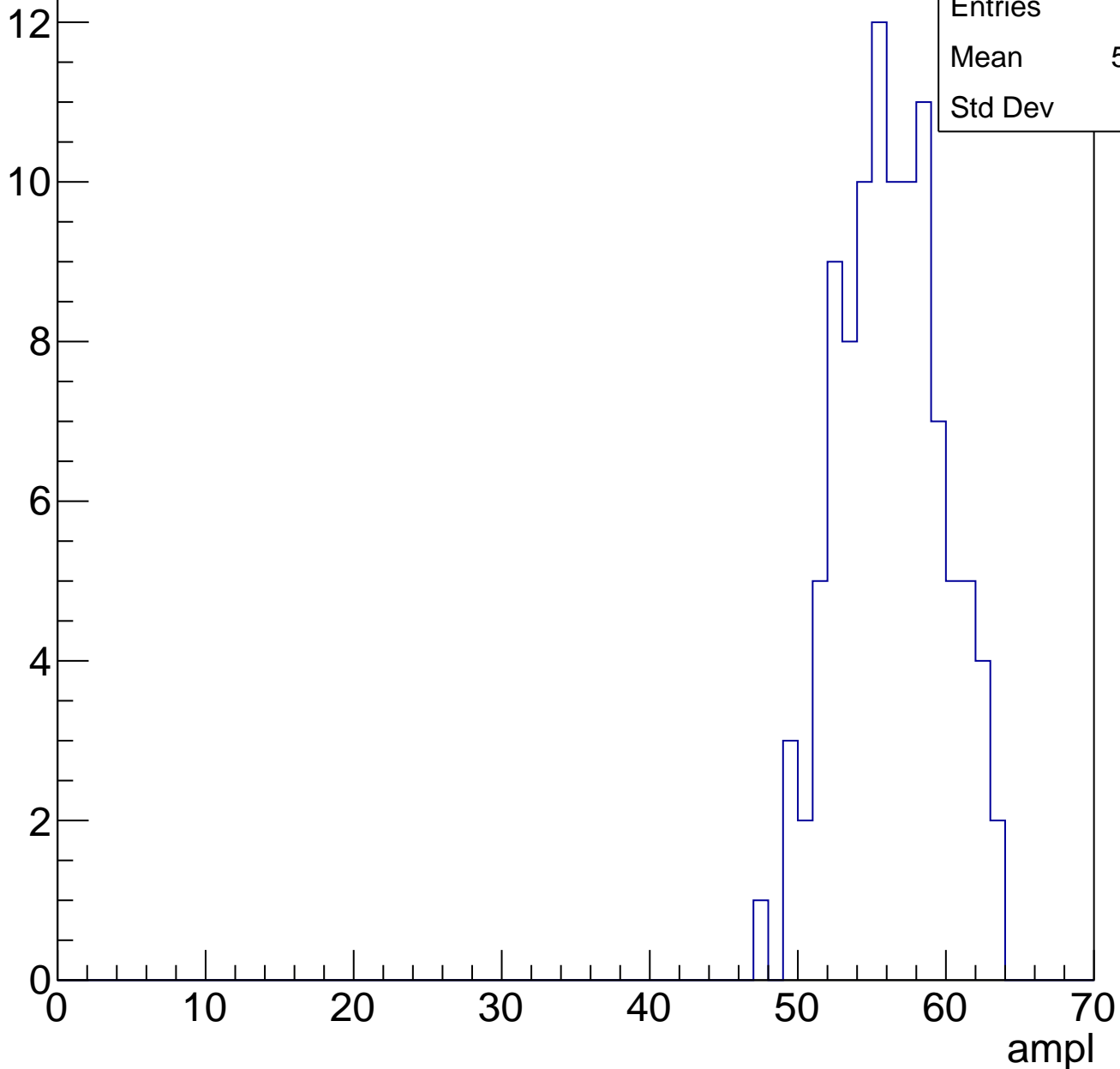


B0L101S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	55.78
Std Dev	3.47

Entry



B0L101S, U14-ch8, adc5

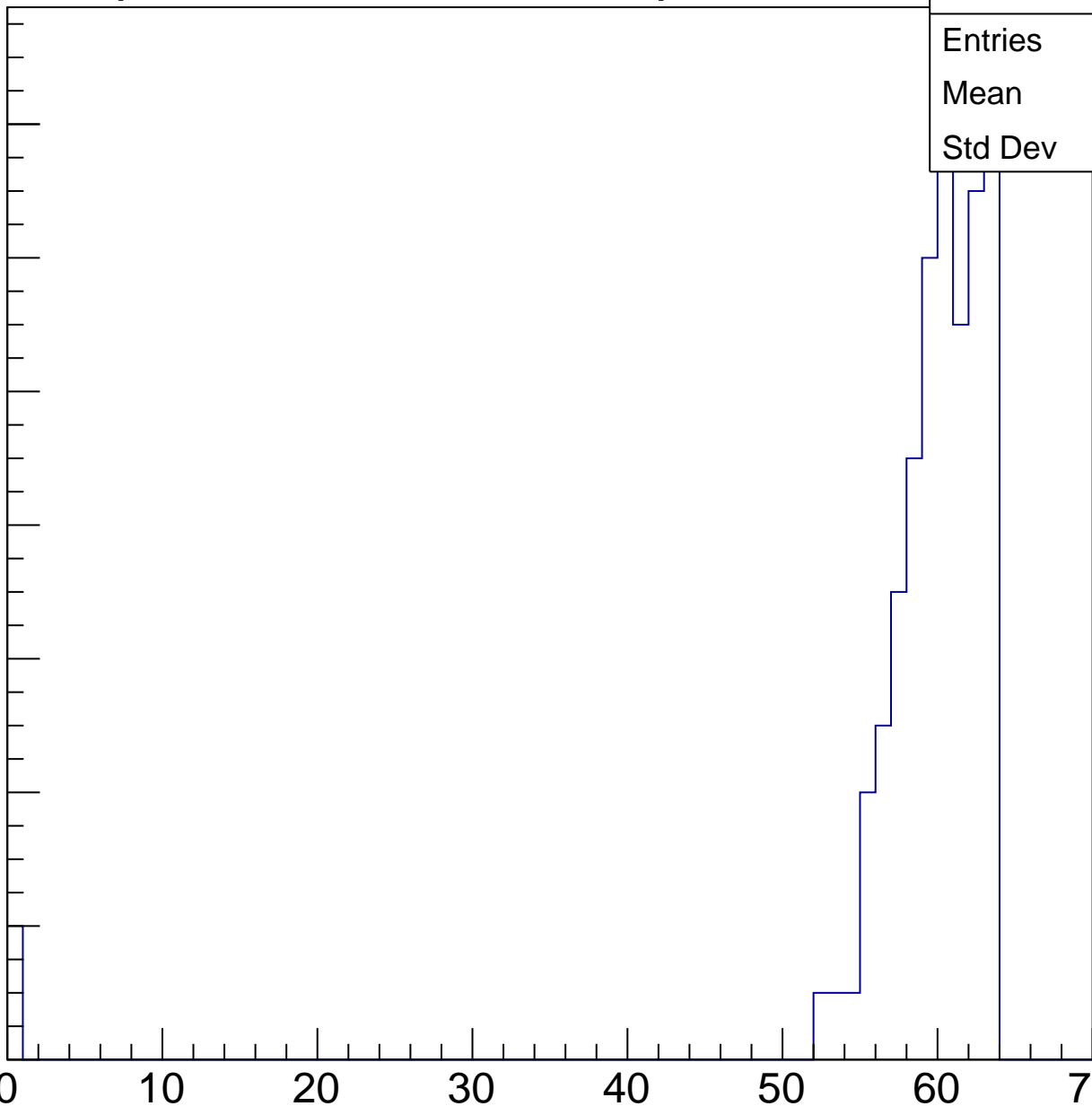
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.42
Std Dev	8.945

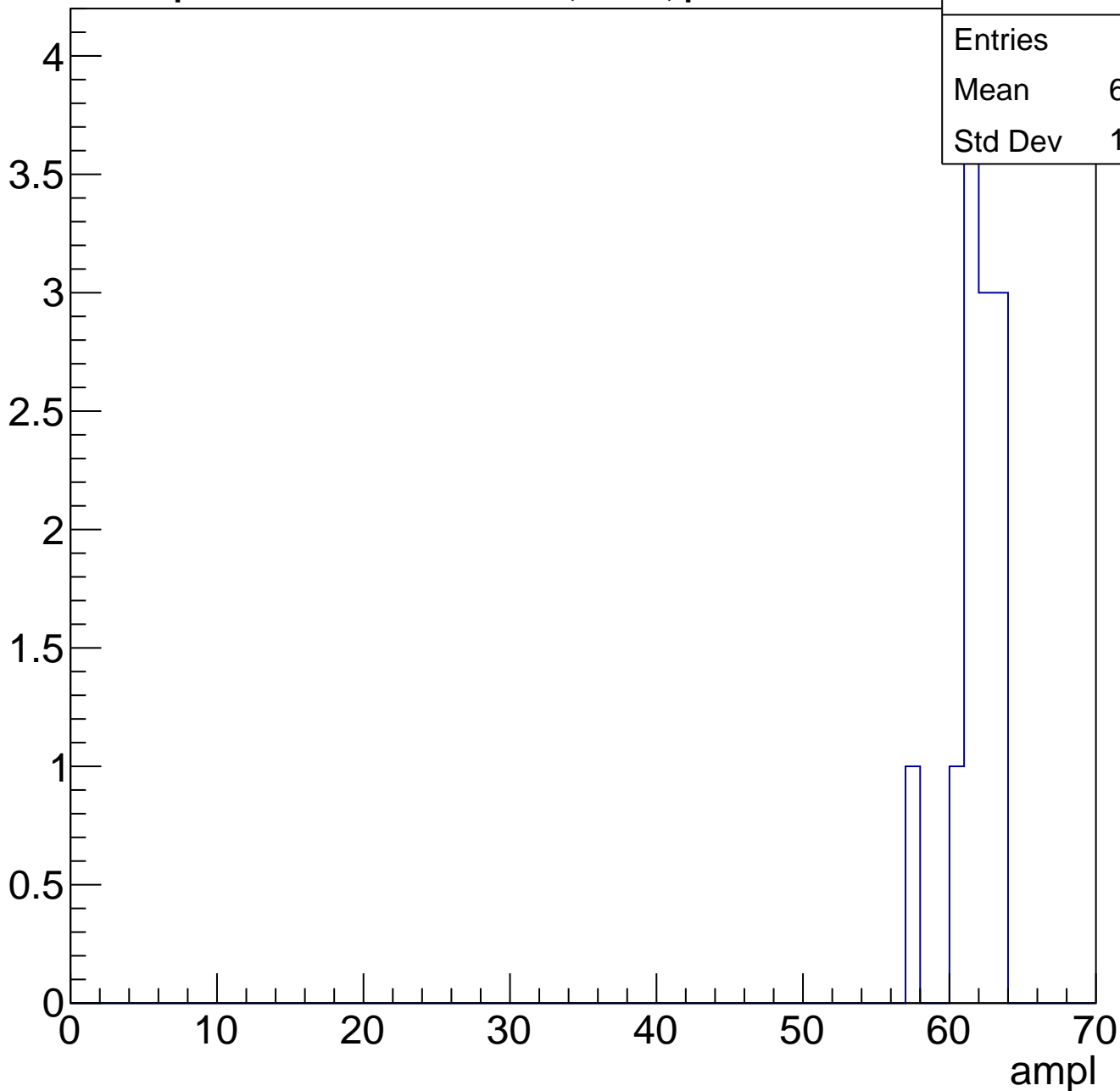
ampl



B0L101S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

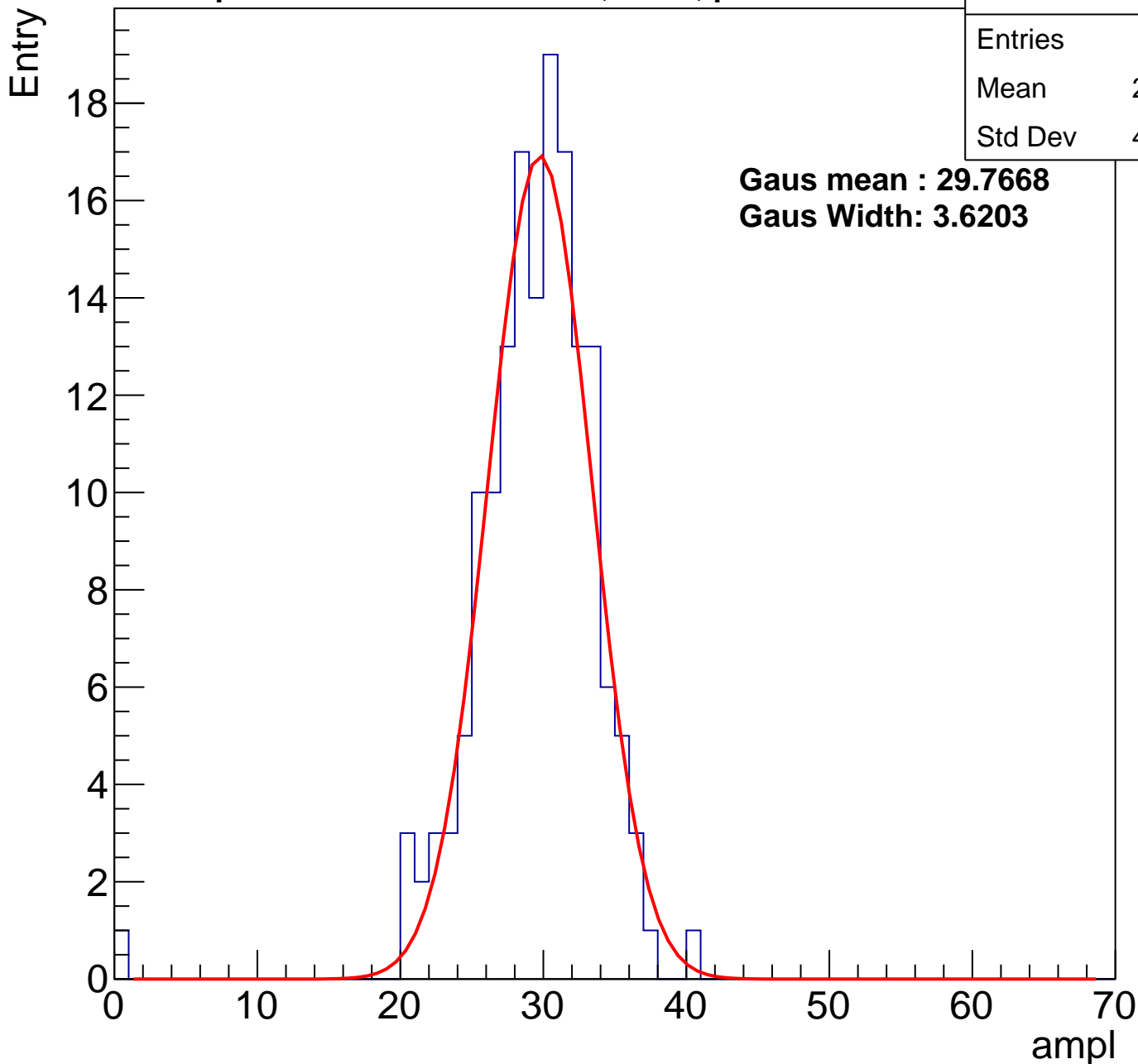
B0L101S, U14-ch9, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	28.97
Std Dev	4.333

Gaus mean : 29.7668

Gaus Width: 3.6203



B0L101S, U14-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

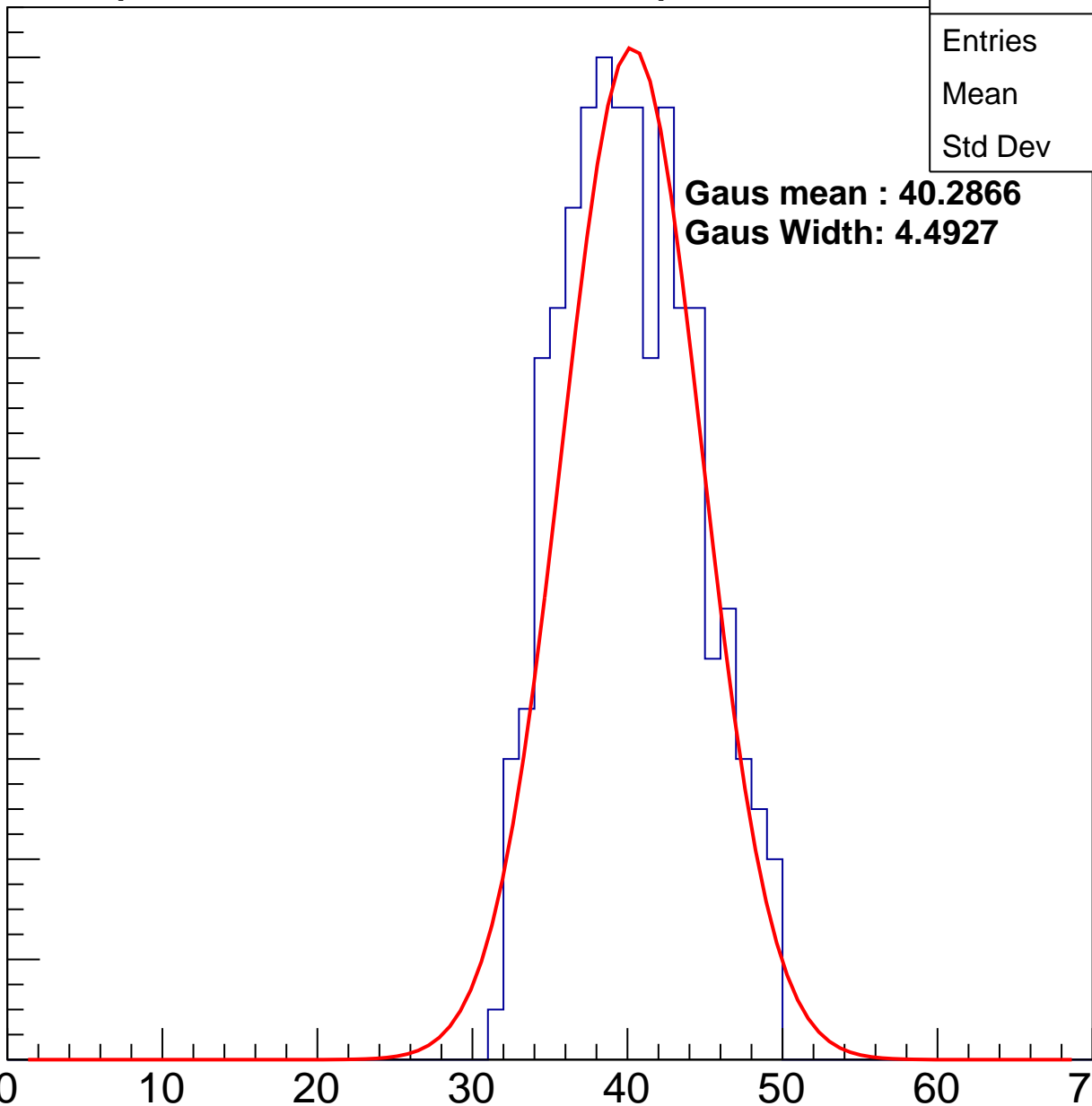
20
18
16
14
12
10
8
6
4
2
0

Entries	232
Mean	39.66
Std Dev	4.218

Gaus mean : 40.2866

Gaus Width: 4.4927

0 10 20 30 40 50 60 70
ampl



B0L101S, U14-ch9, adc2

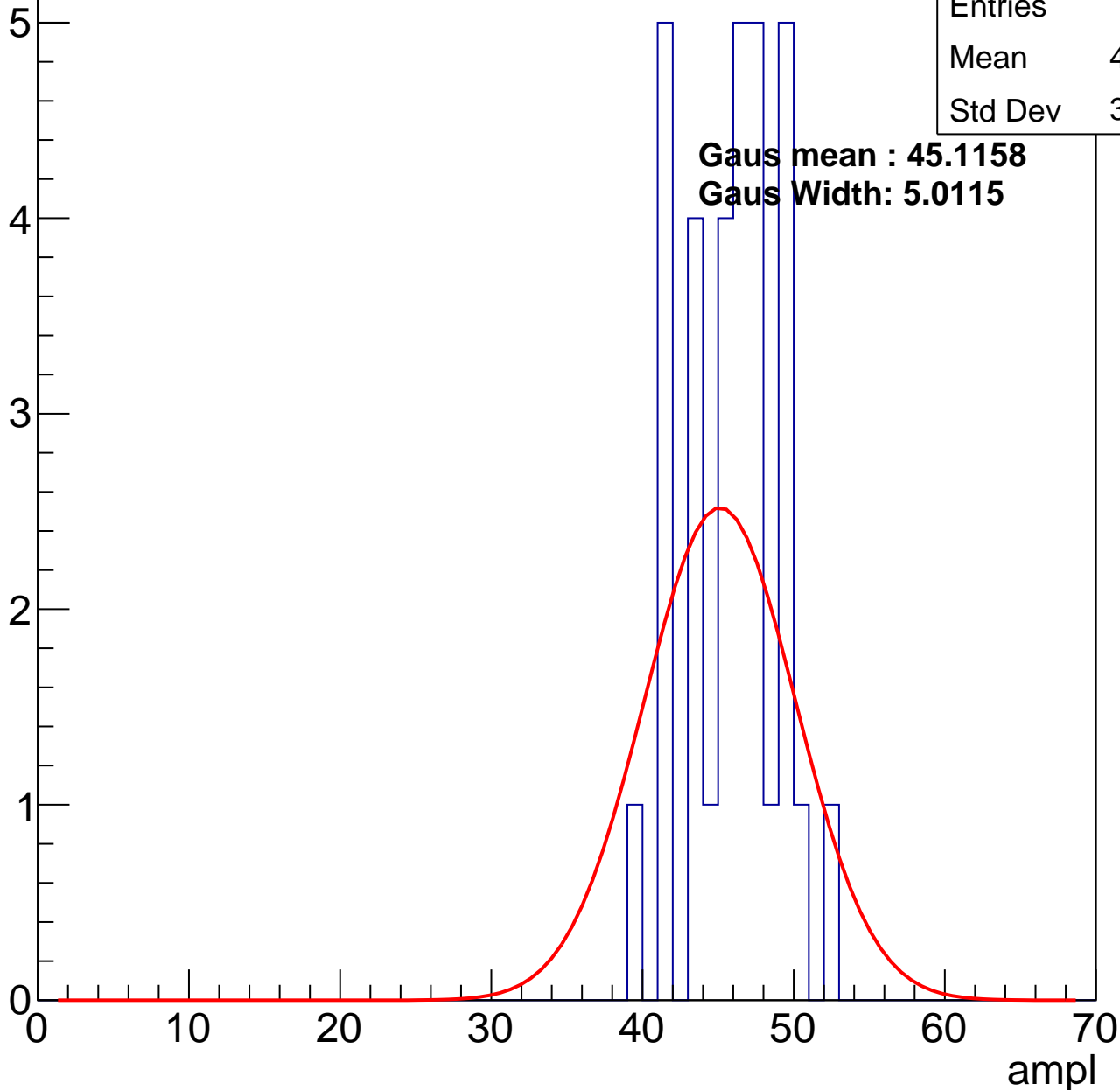
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	33
Mean	45.45
Std Dev	3.086

Gaus mean : 45.1158

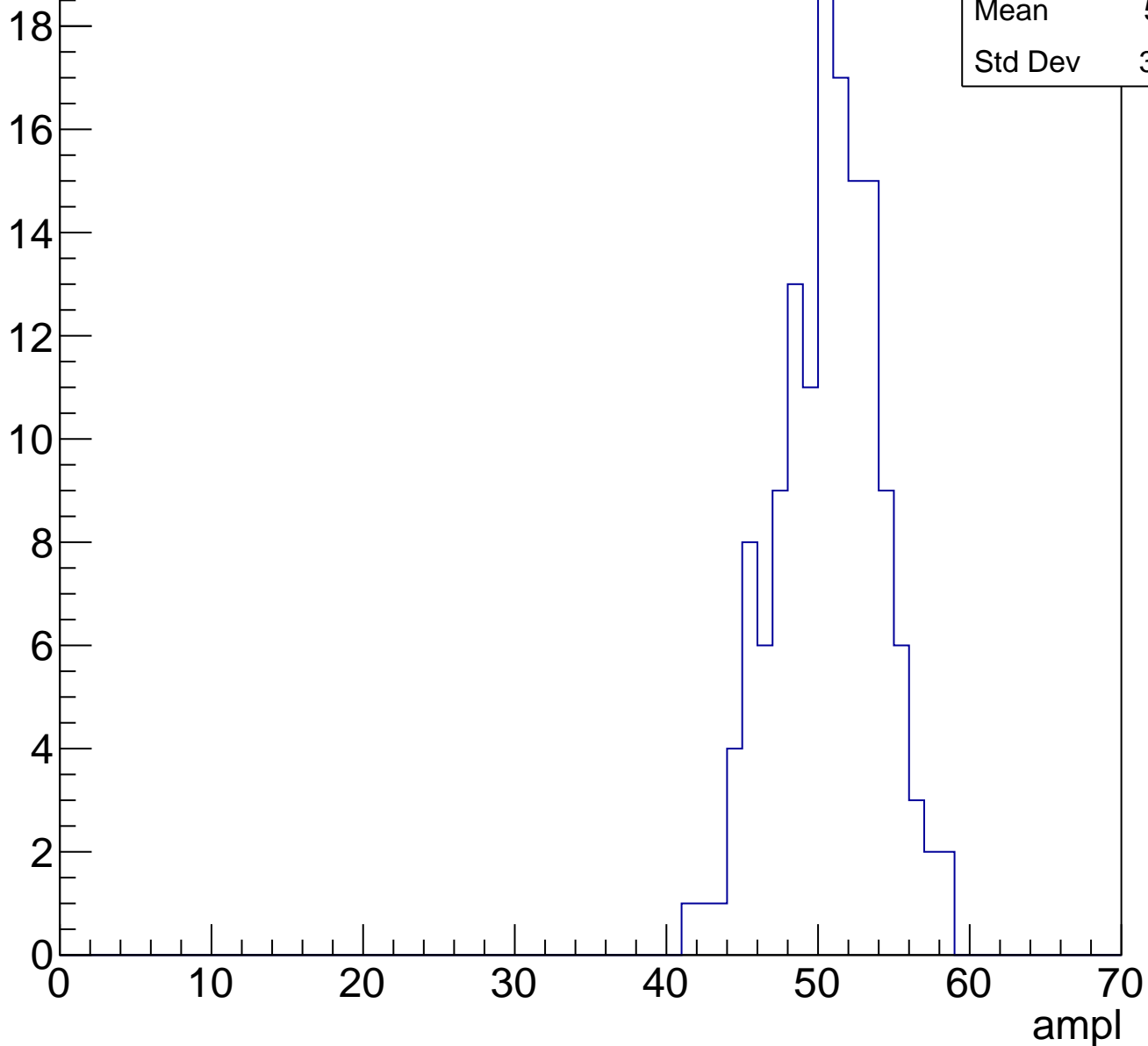
Gaus Width: 5.0115



B0L101S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

124

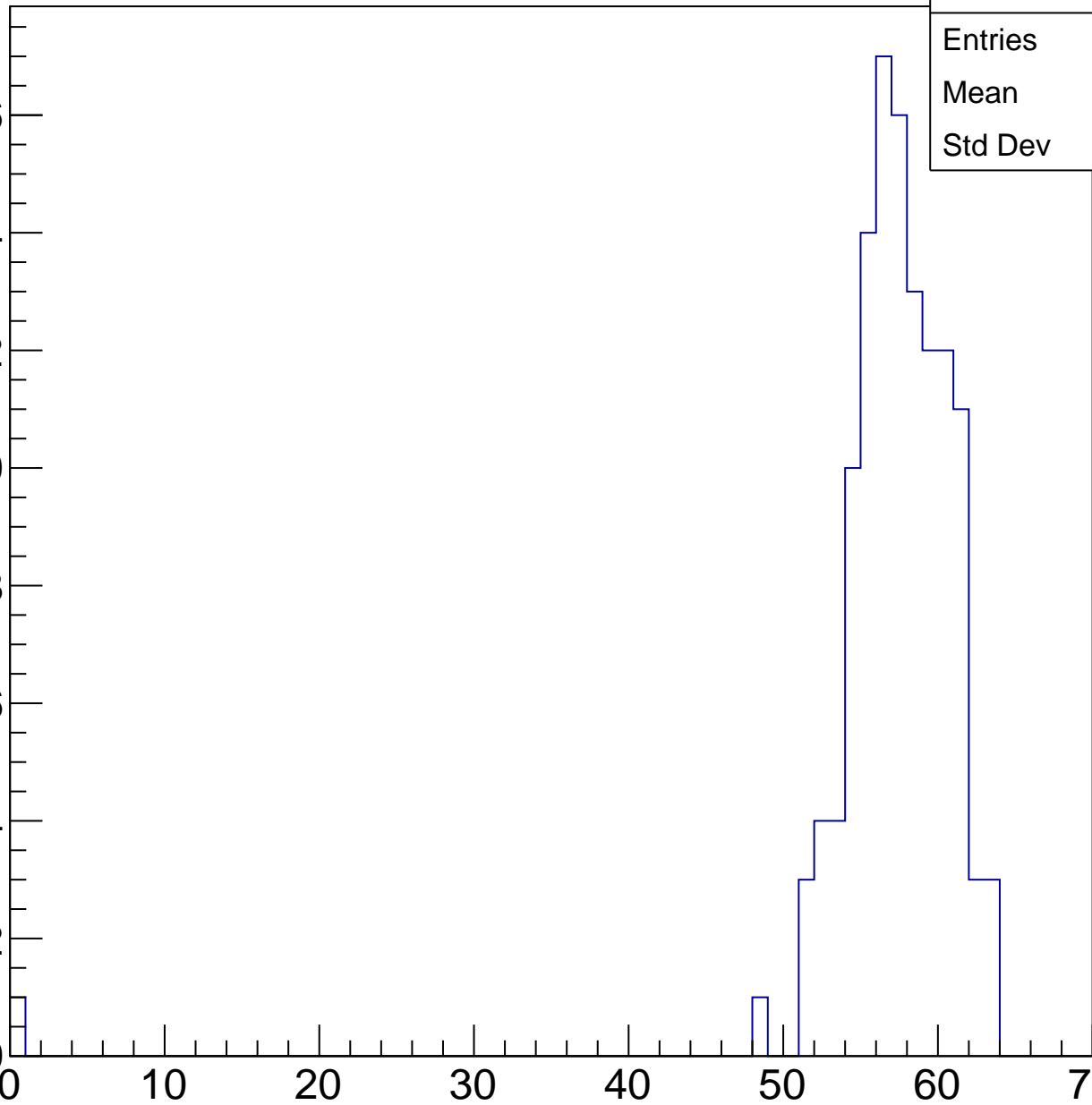
Mean

56.64

Std Dev

5.873

ampl



B0L101S, U14-ch9, adc5

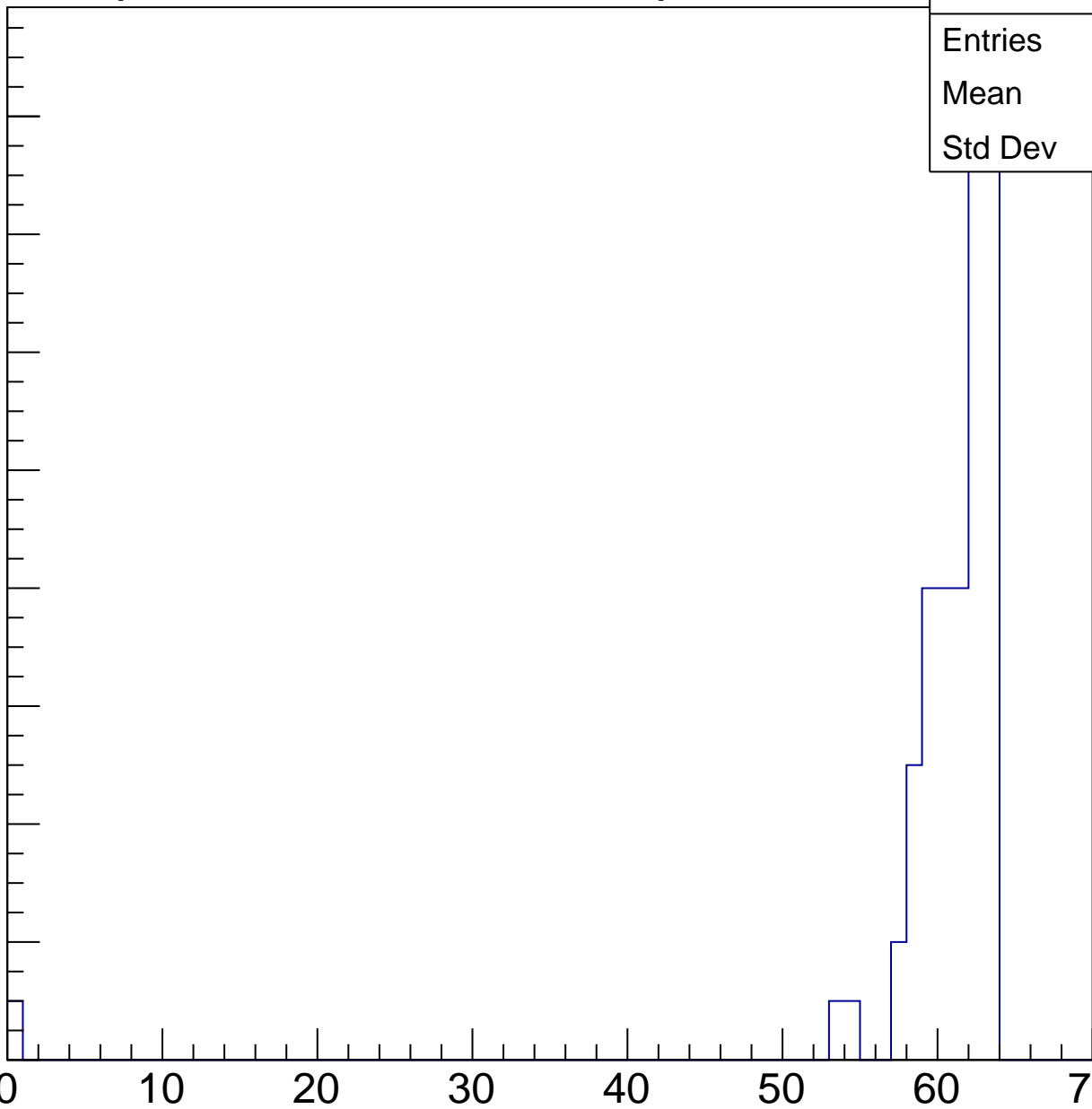
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	59.91
Std Dev	7.684

ampl



B0L101S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch10, adc0

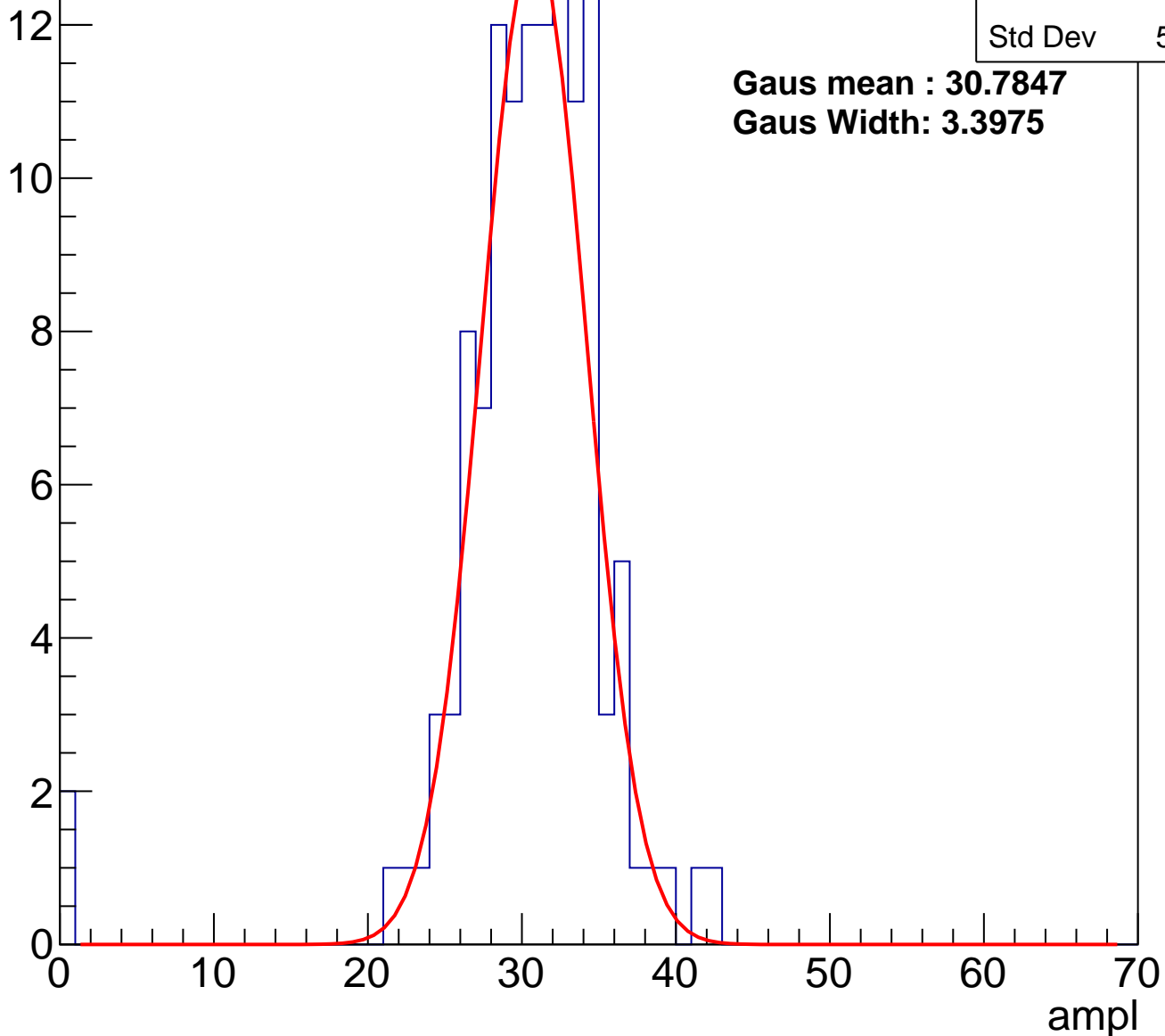
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	30.08
Std Dev	5.343

Gaus mean : 30.7847

Gaus Width: 3.3975

Entry



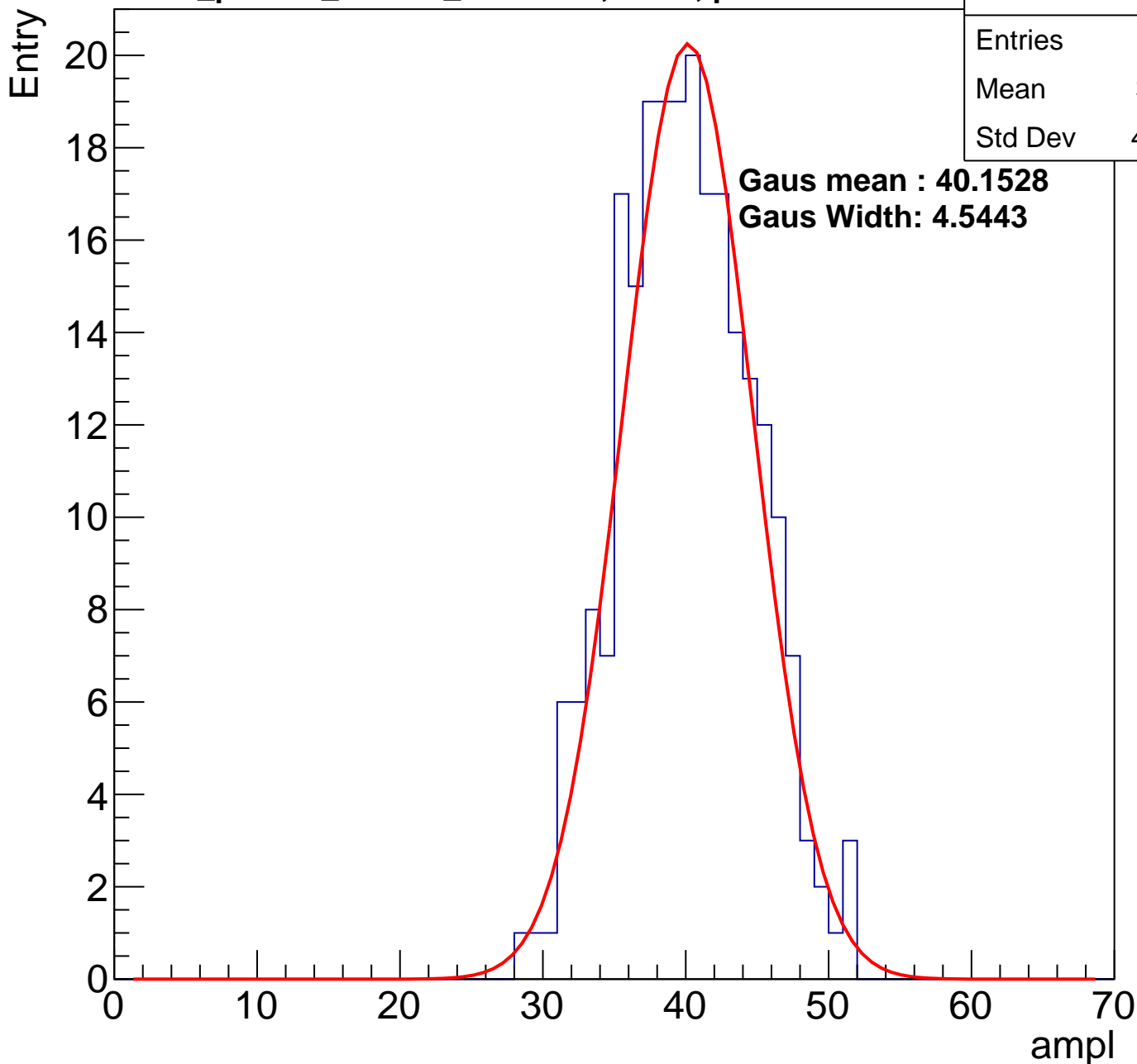
B0L101S, U14-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	238
Mean	39.61
Std Dev	4.589

Gaus mean : 40.1528

Gaus Width: 4.5443



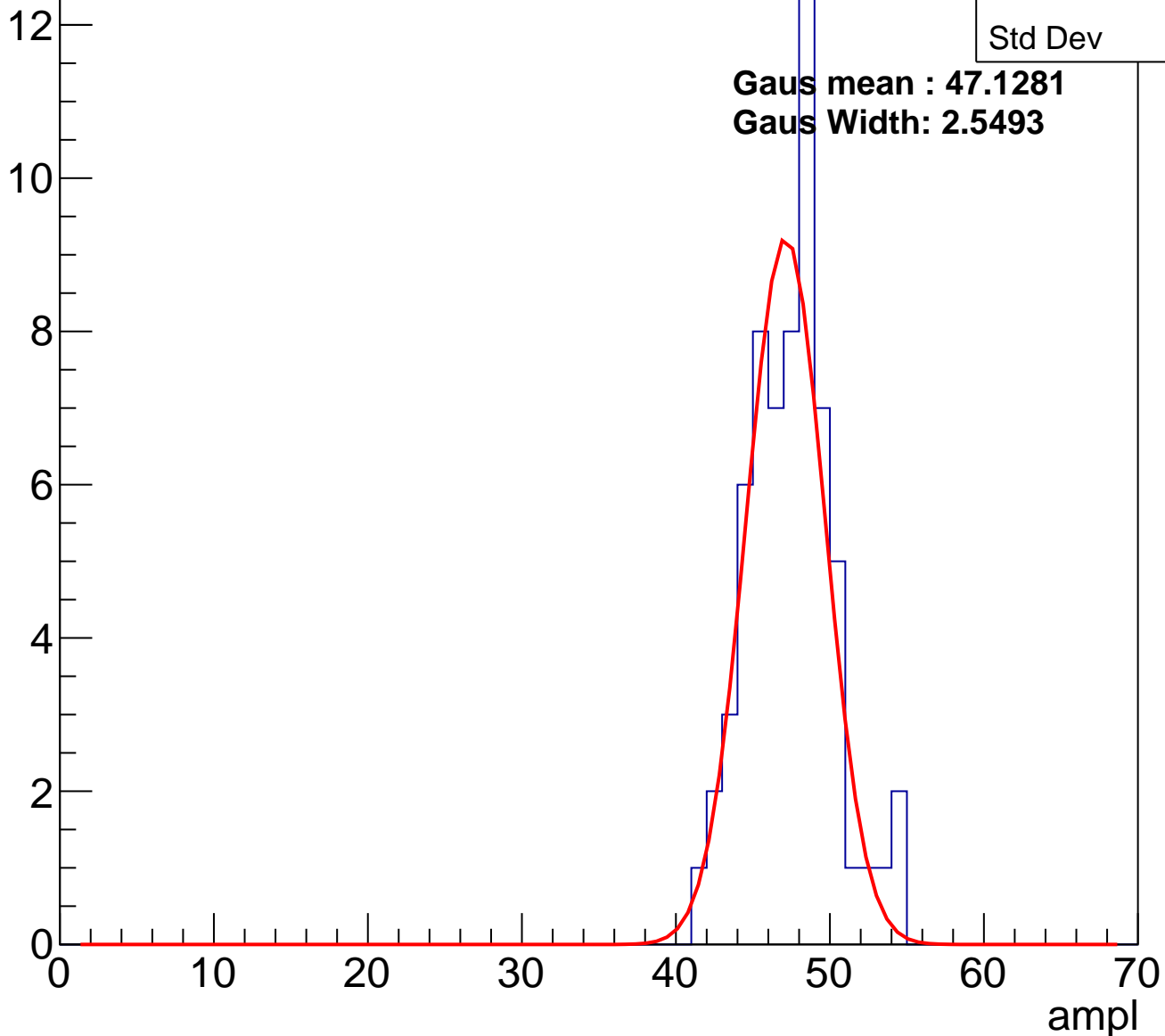
B0L101S, U14-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	65
Mean	47.03
Std Dev	2.74

Gaus mean : 47.1281
Gaus Width: 2.5493

Entry



B0L101S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

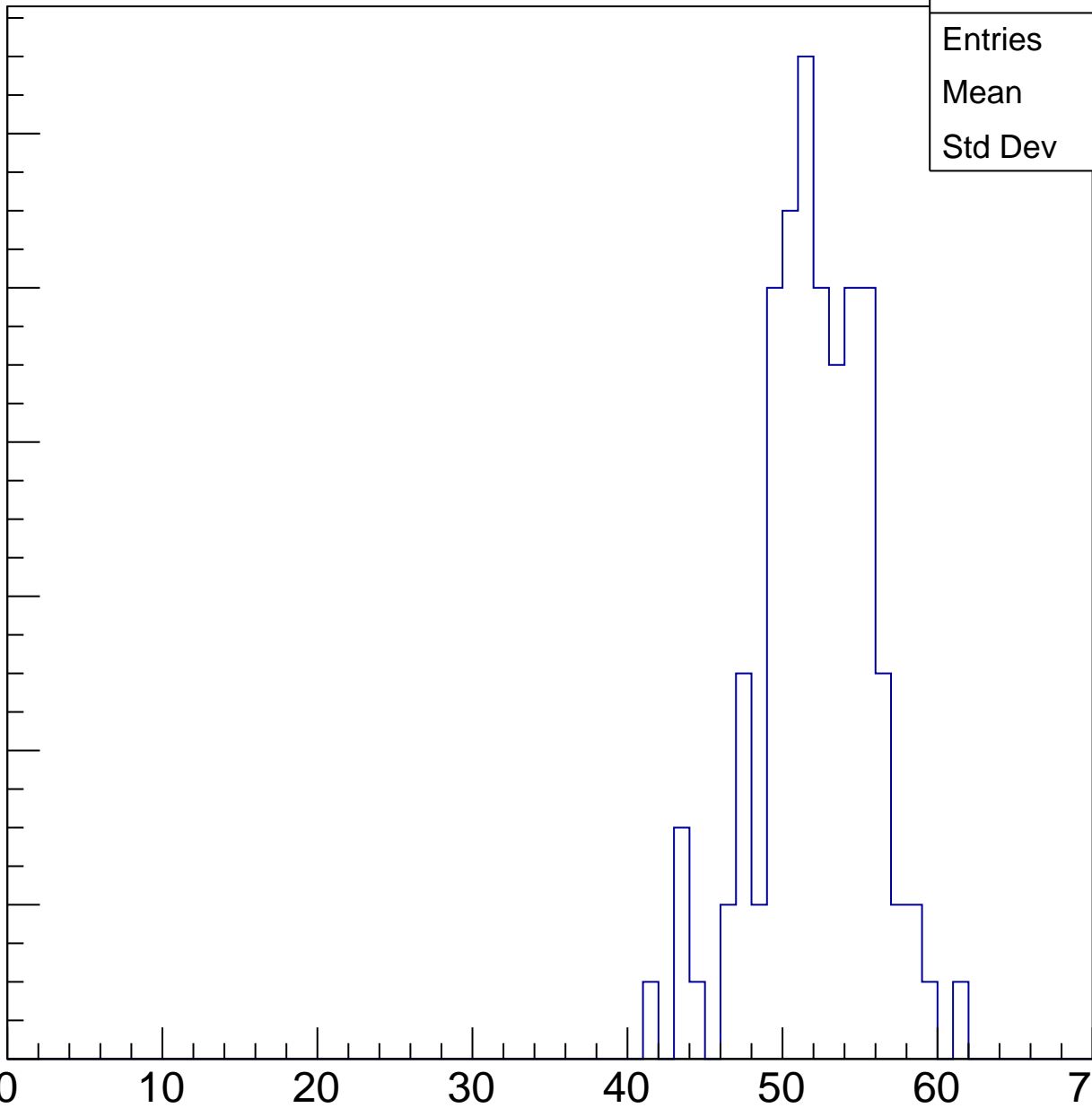
2

0

Entries	98
Mean	51.6
Std Dev	3.585

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

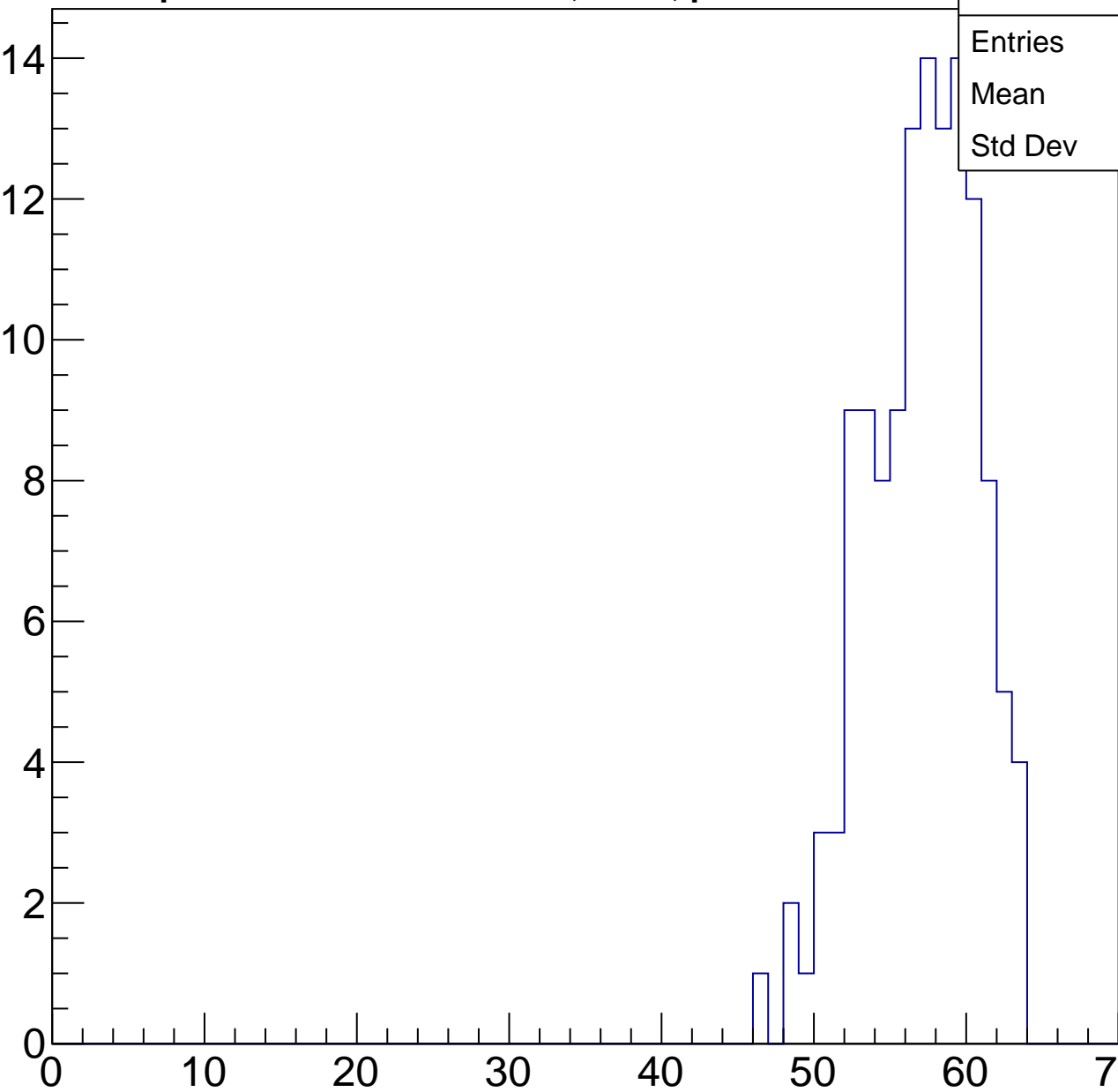
Entries	128
Mean	56.58
Std Dev	3.583

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch10, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	62
Mean	58.48
Std Dev	10.92

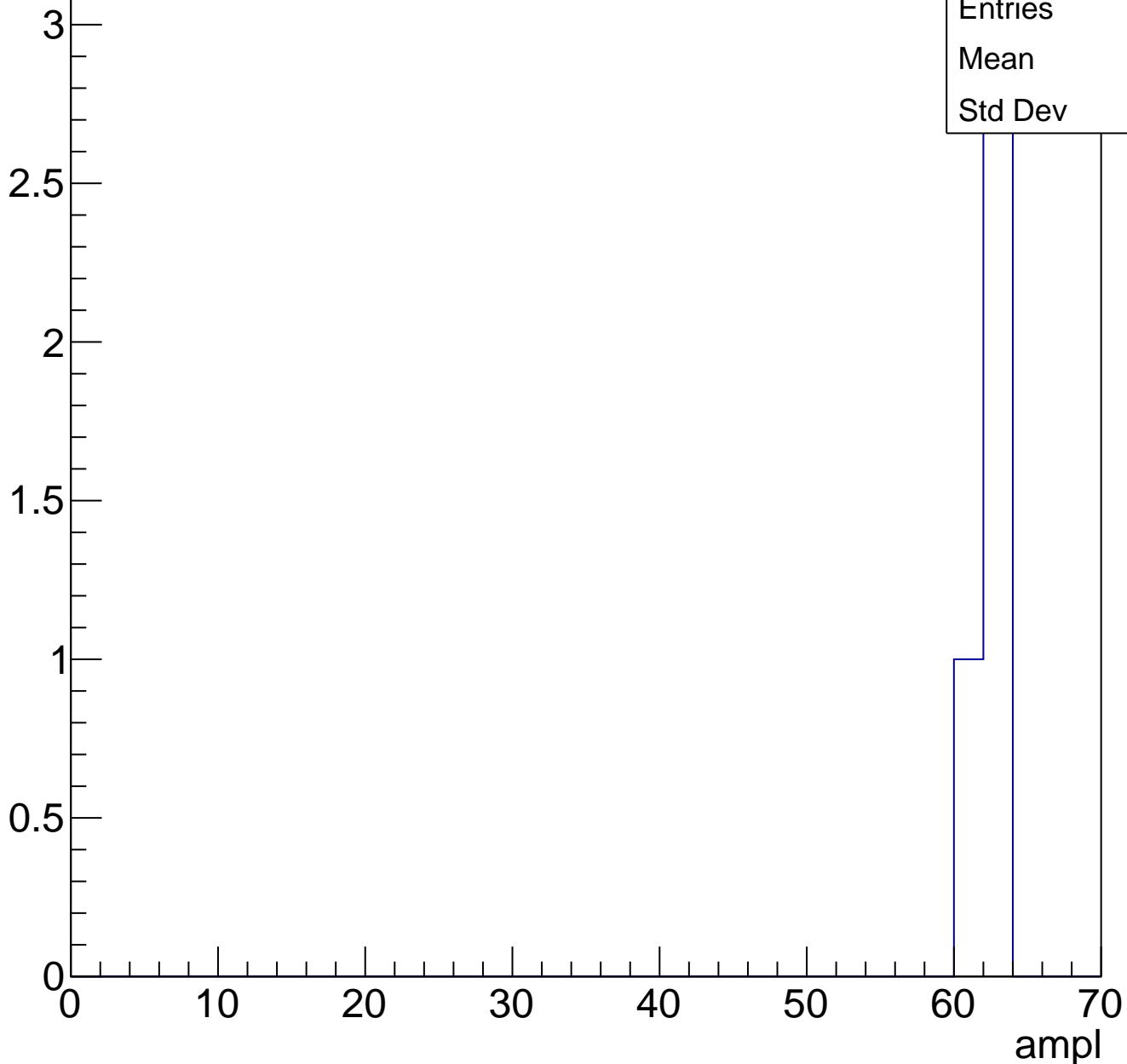
ampl

0 10 20 30 40 50 60 70

B0L101S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	30.1
Std Dev	4.147

Gaus mean : 30.6894

Gaus Width: 3.1636

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

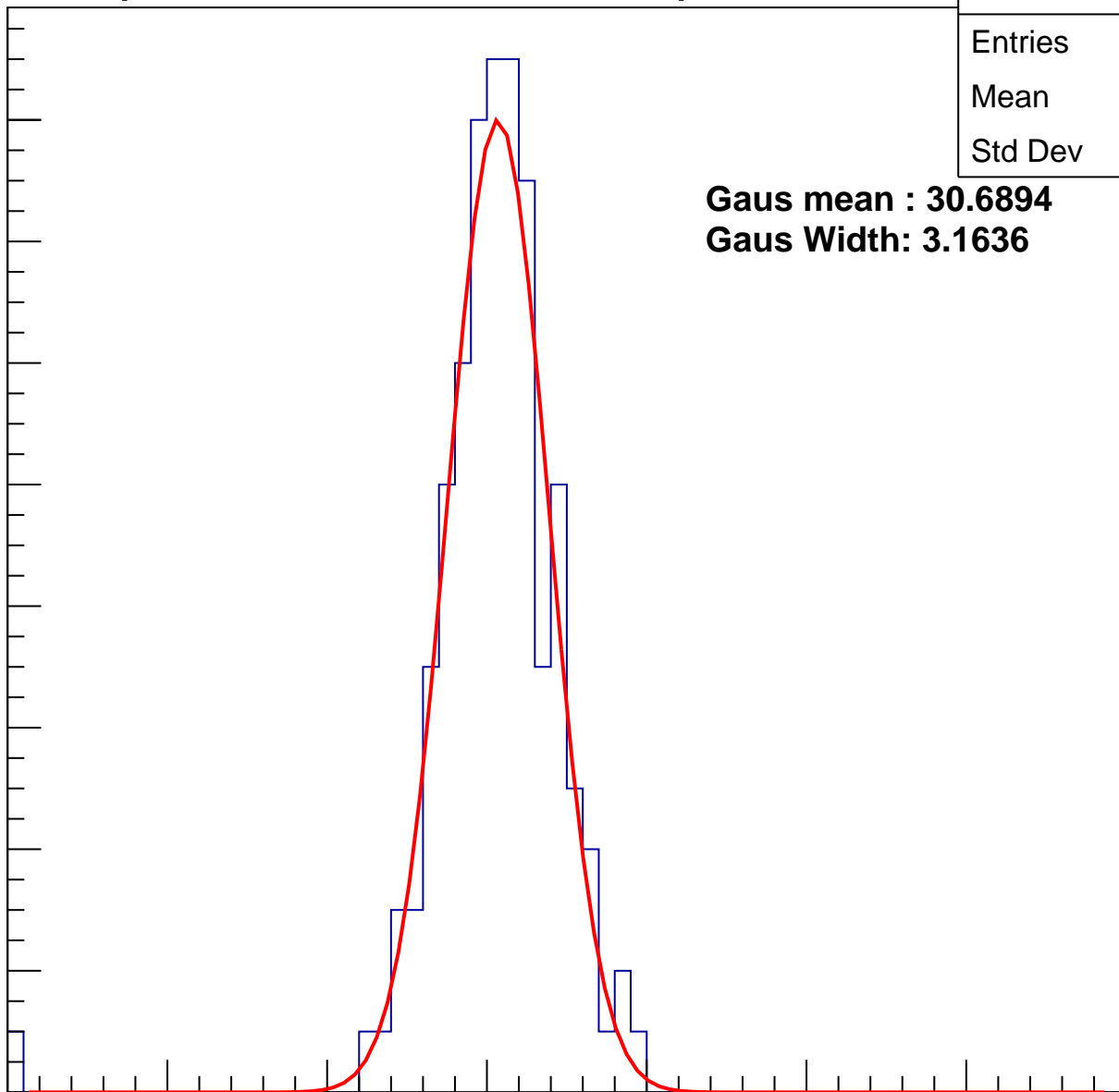
40

50

60

70

ampl



B0L101S, U14-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

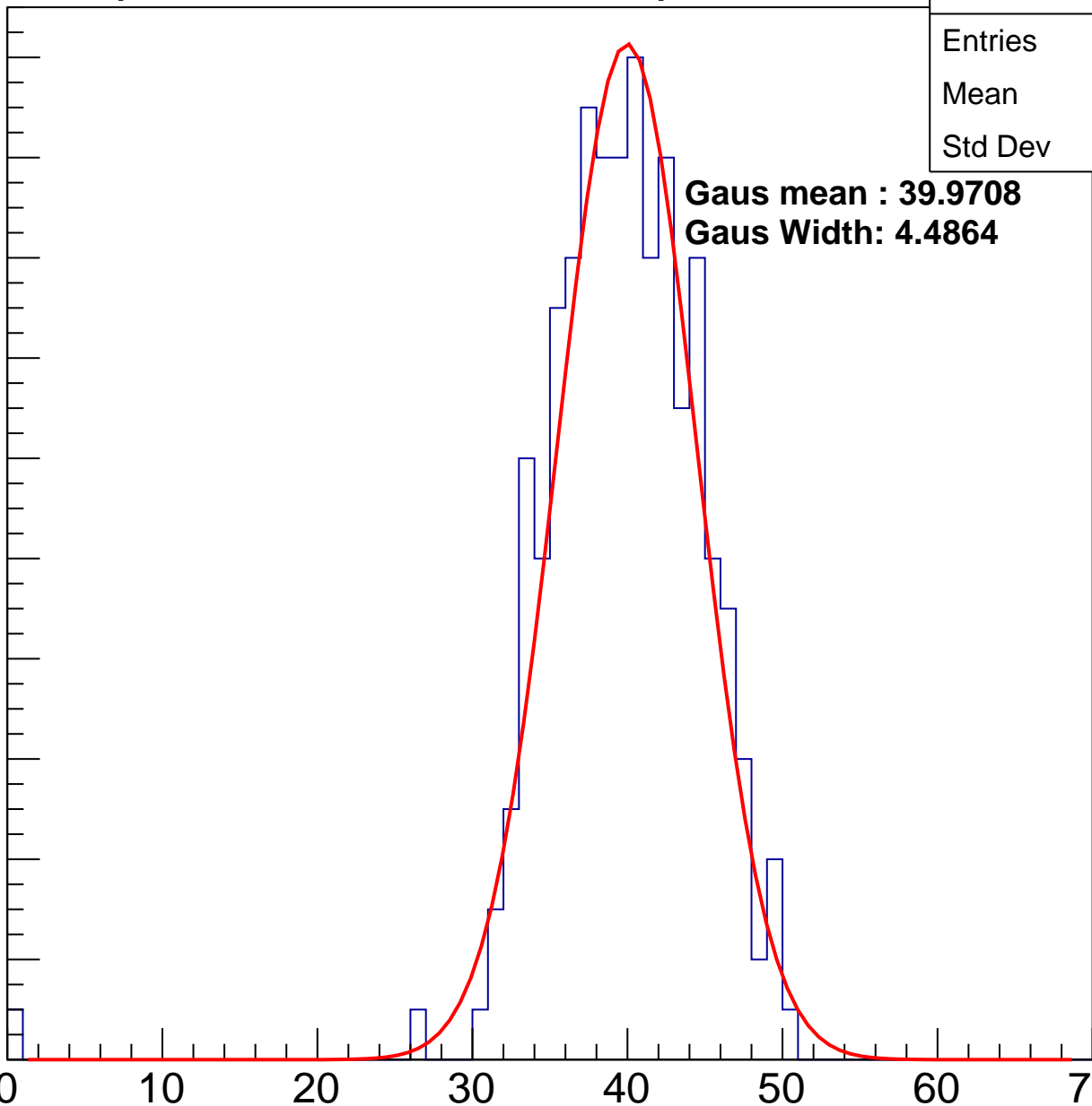
20
18
16
14
12
10
8
6
4
2
0

Entries	234
Mean	39.31
Std Dev	5.092

Gaus mean : 39.9708

Gaus Width: 4.4864

0 10 20 30 40 50 60 70
ampl



B0L101S, U14-ch11, adc2

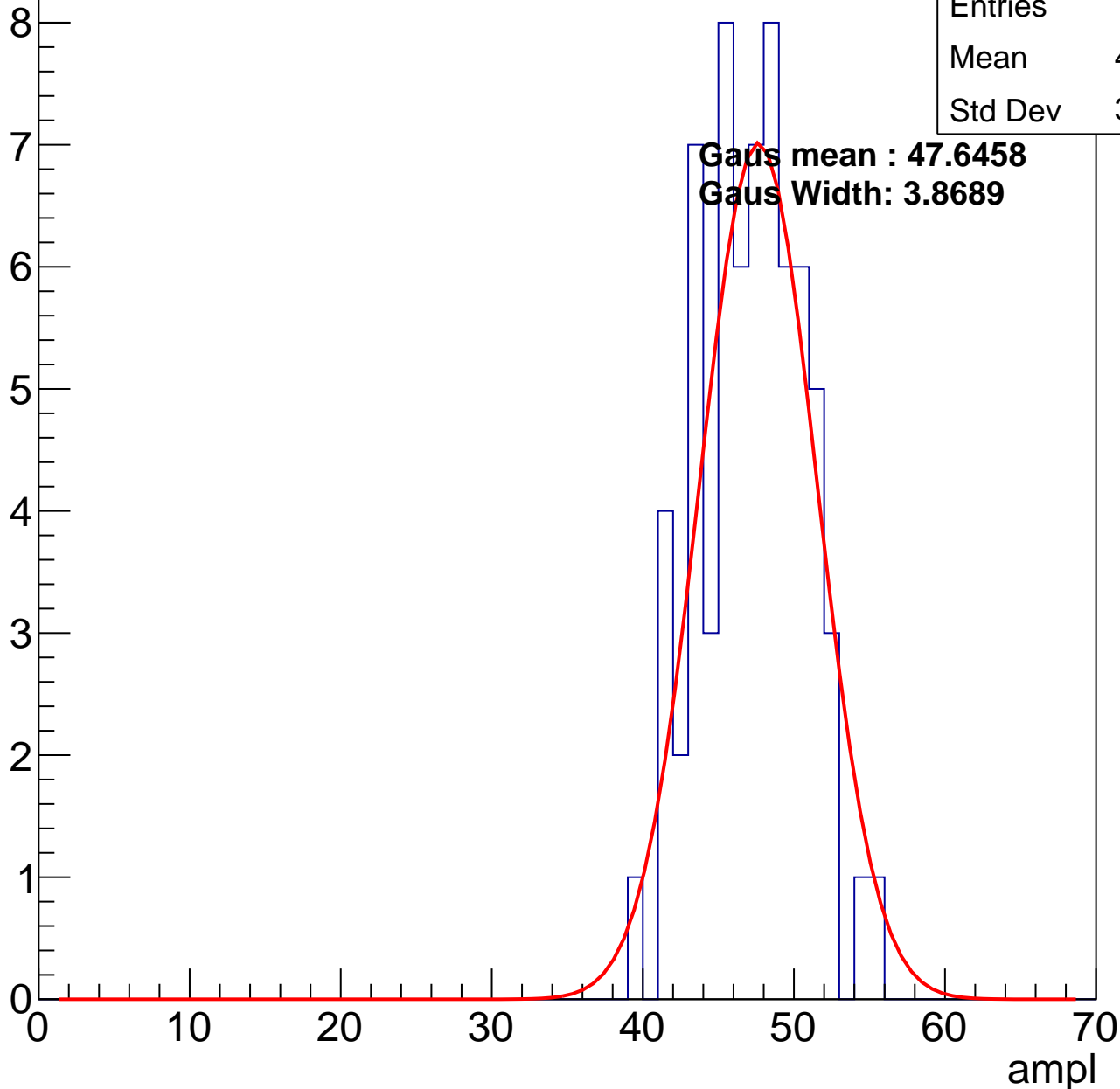
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	68
Mean	46.81
Std Dev	3.401

Gaus mean : 47.6458

Gaus Width: 3.8689



B0L101S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

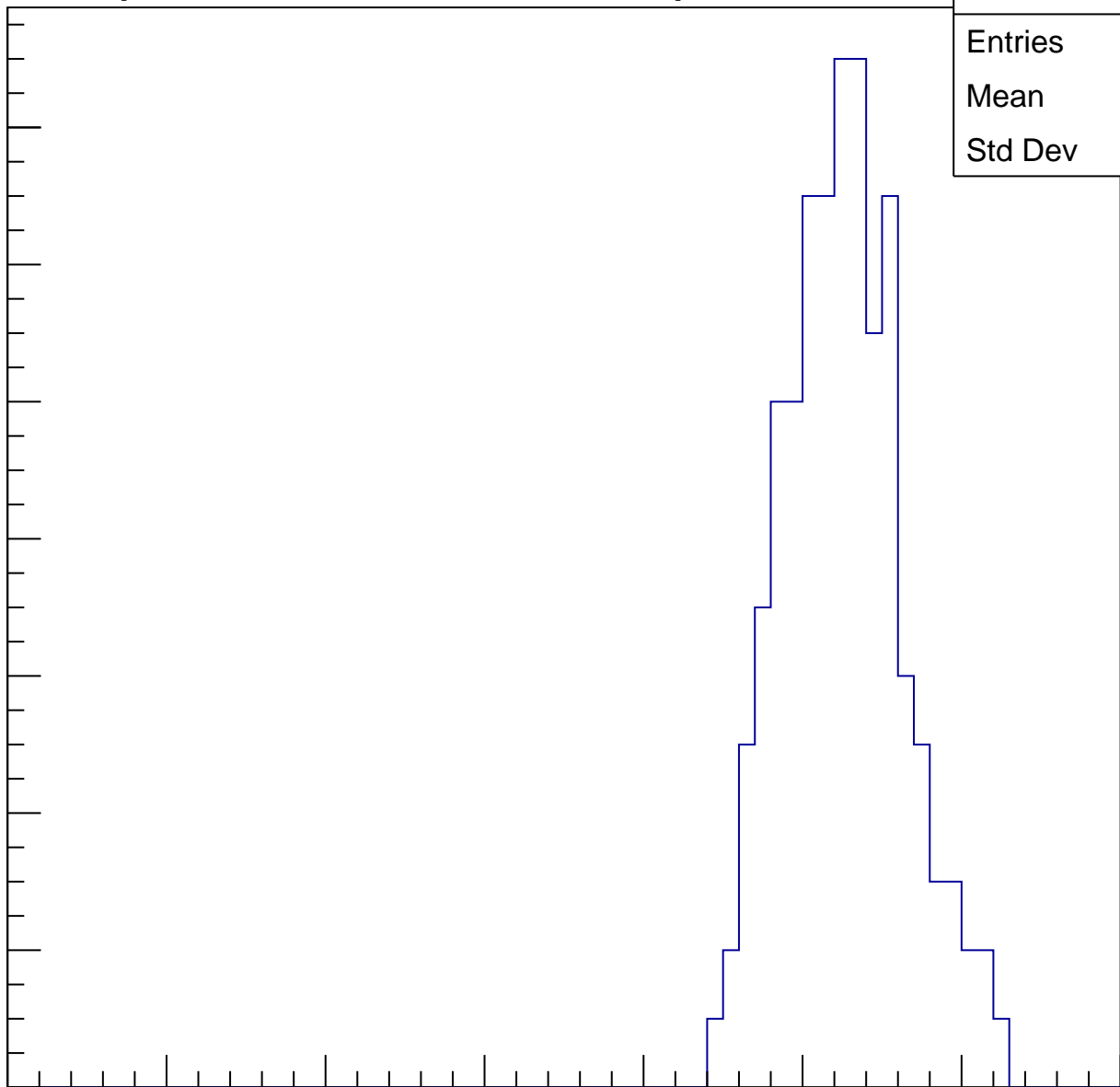
Entries	137
Mean	52.09
Std Dev	3.697

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch11, adc4

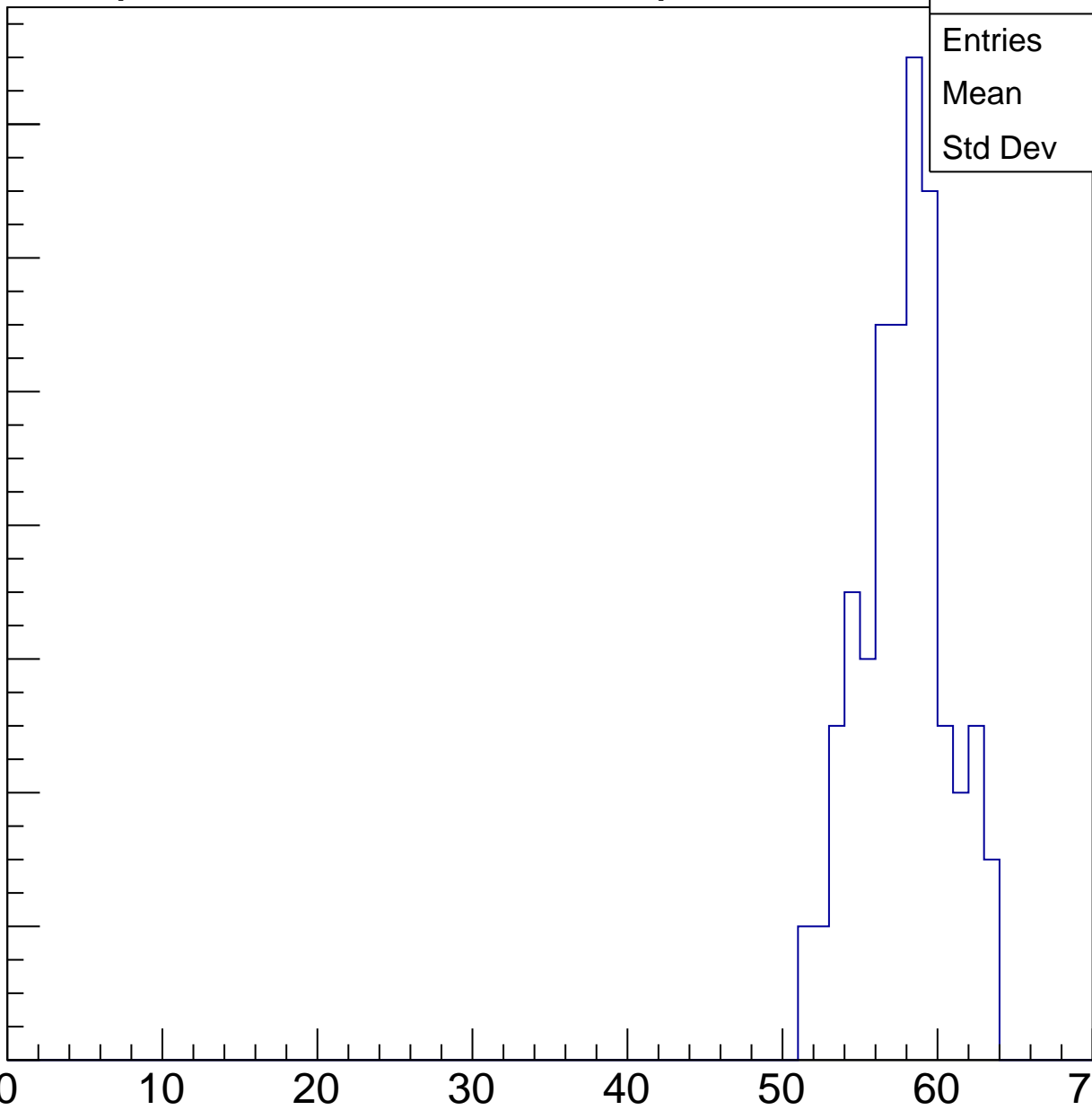
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	57.33
Std Dev	2.804

ampl



B0L101S, U14-ch11, adc5

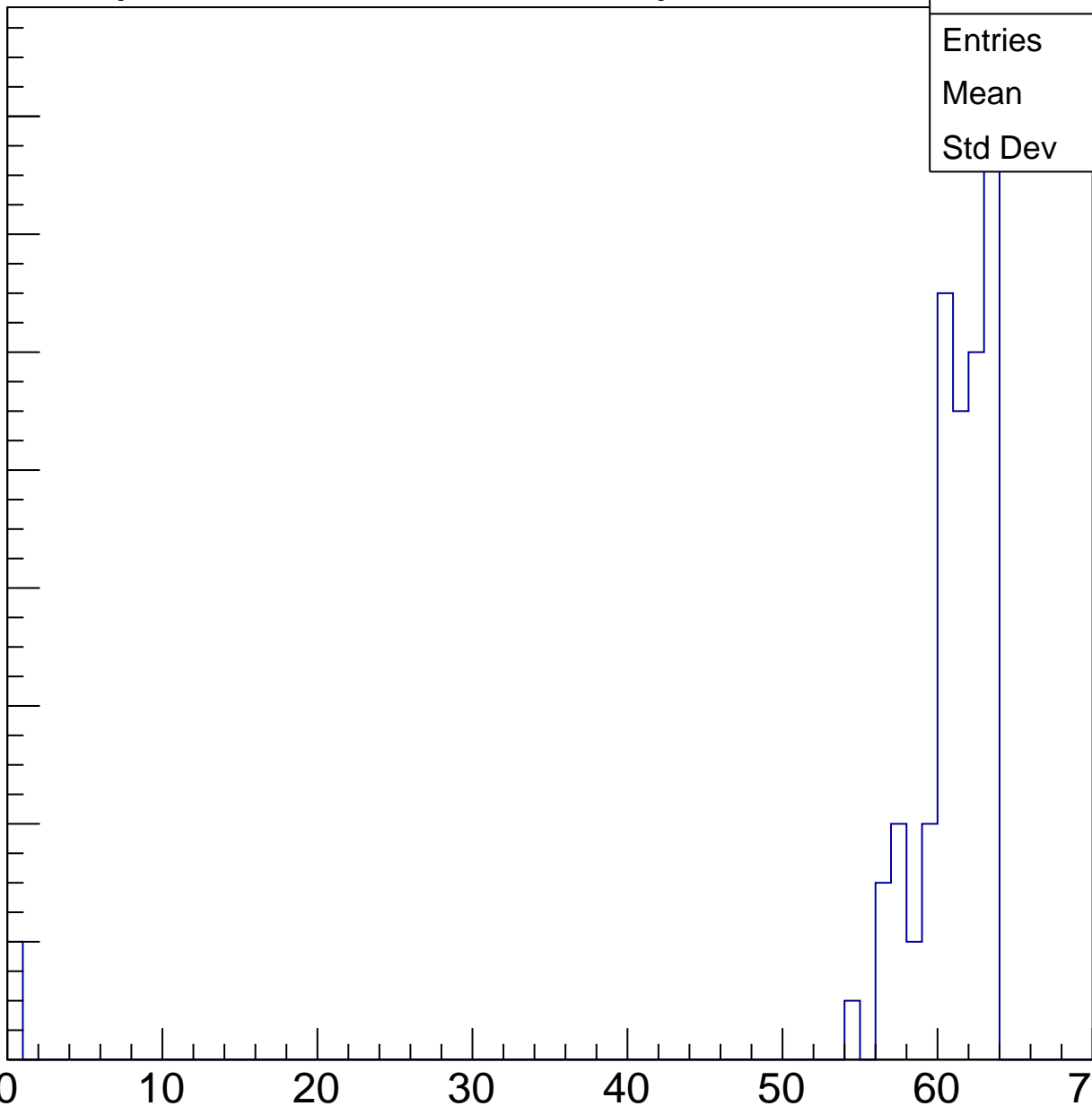
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.96
Std Dev	10.4

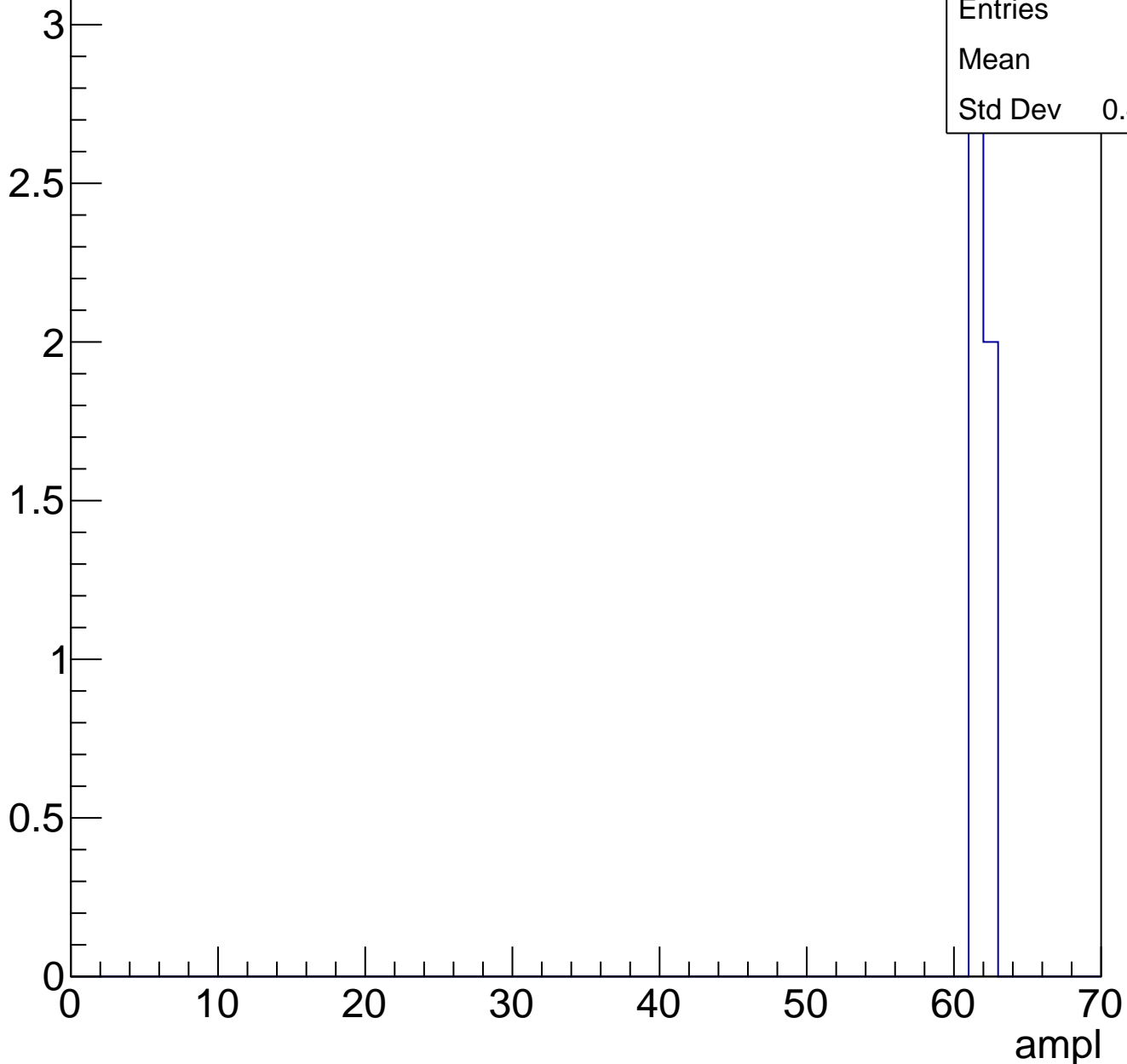
ampl



B0L101S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch12, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	28.67
Std Dev	4.357

Gaus mean : 29.3482

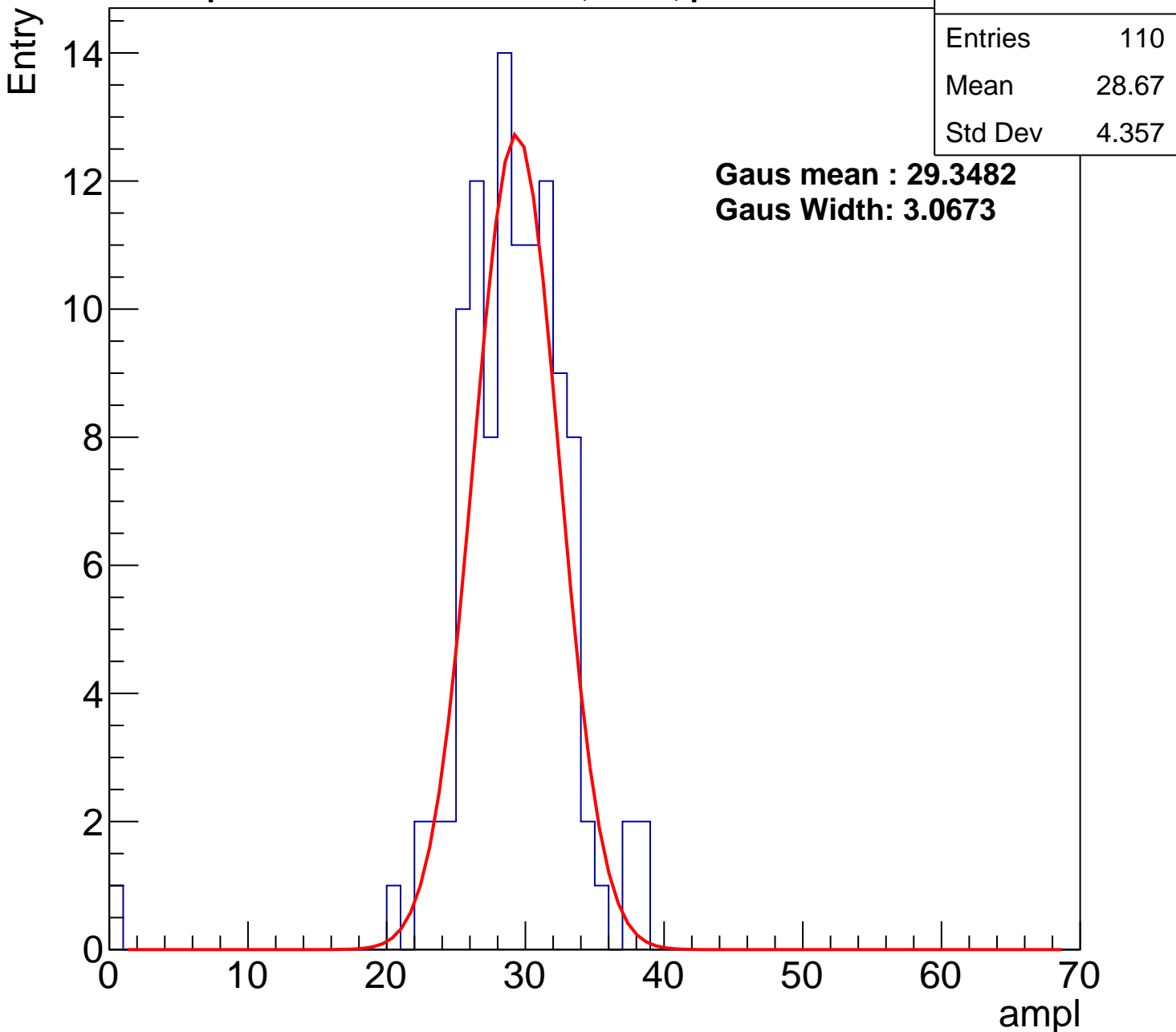
Gaus Width: 3.0673

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch12, adc1

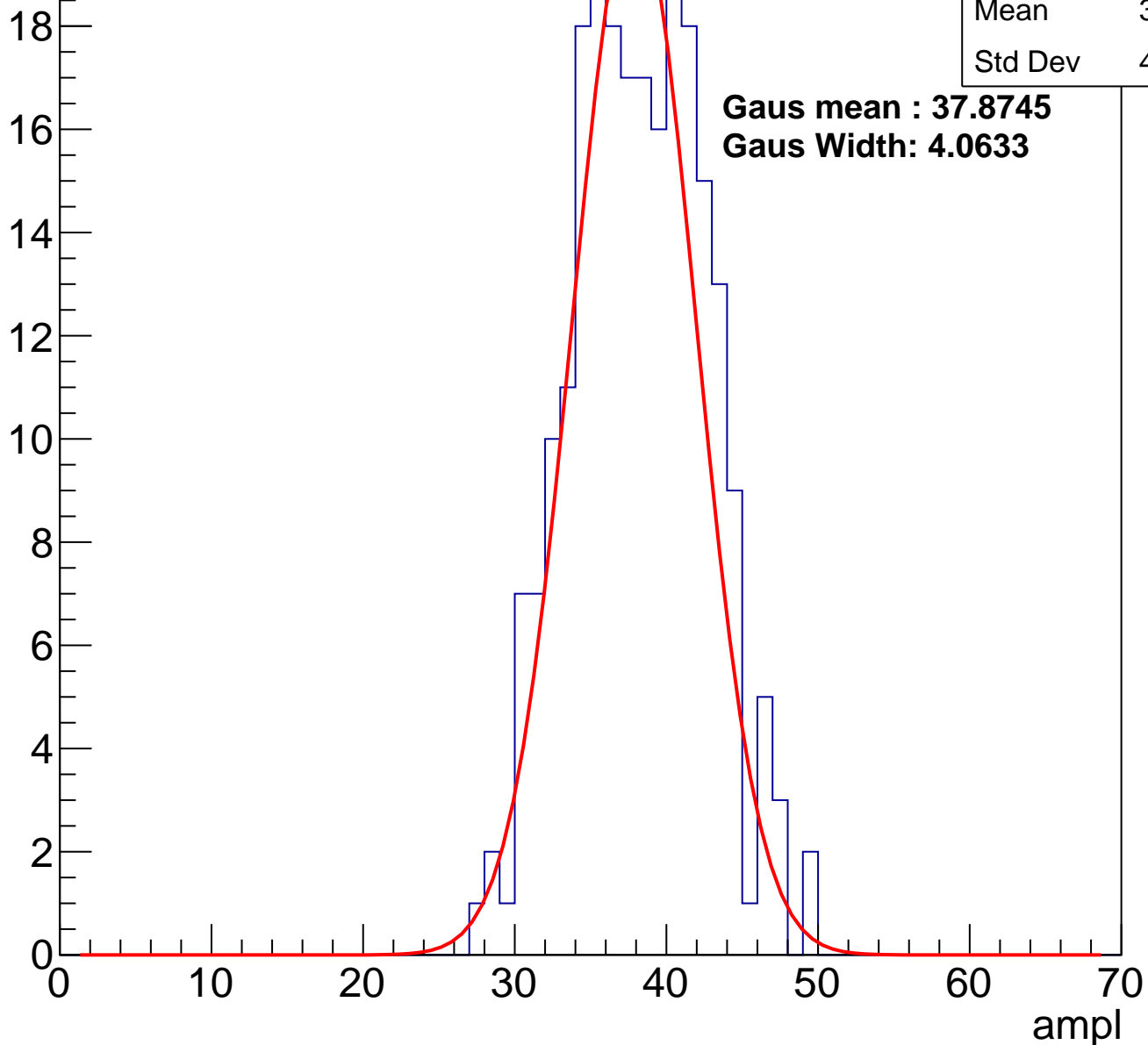
calib_packv5_042523_0143.root, FC#1, port C1

Entries	229
Mean	37.74
Std Dev	4.362

Gaus mean : 37.8745

Gaus Width: 4.0633

Entry



B0L101S, U14-ch12, adc2

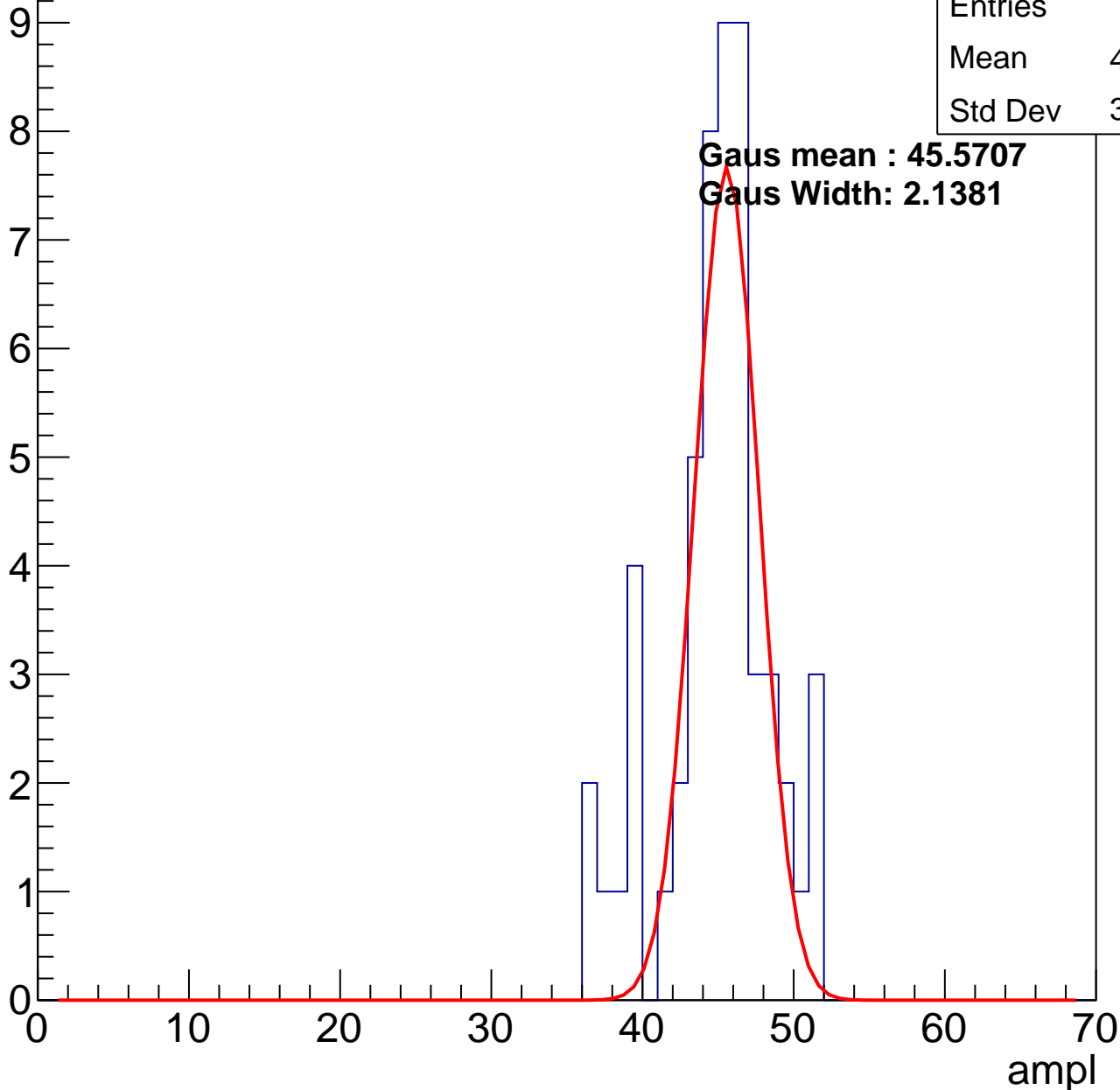
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	44.44
Std Dev	3.542

Gaus mean : 45.5707

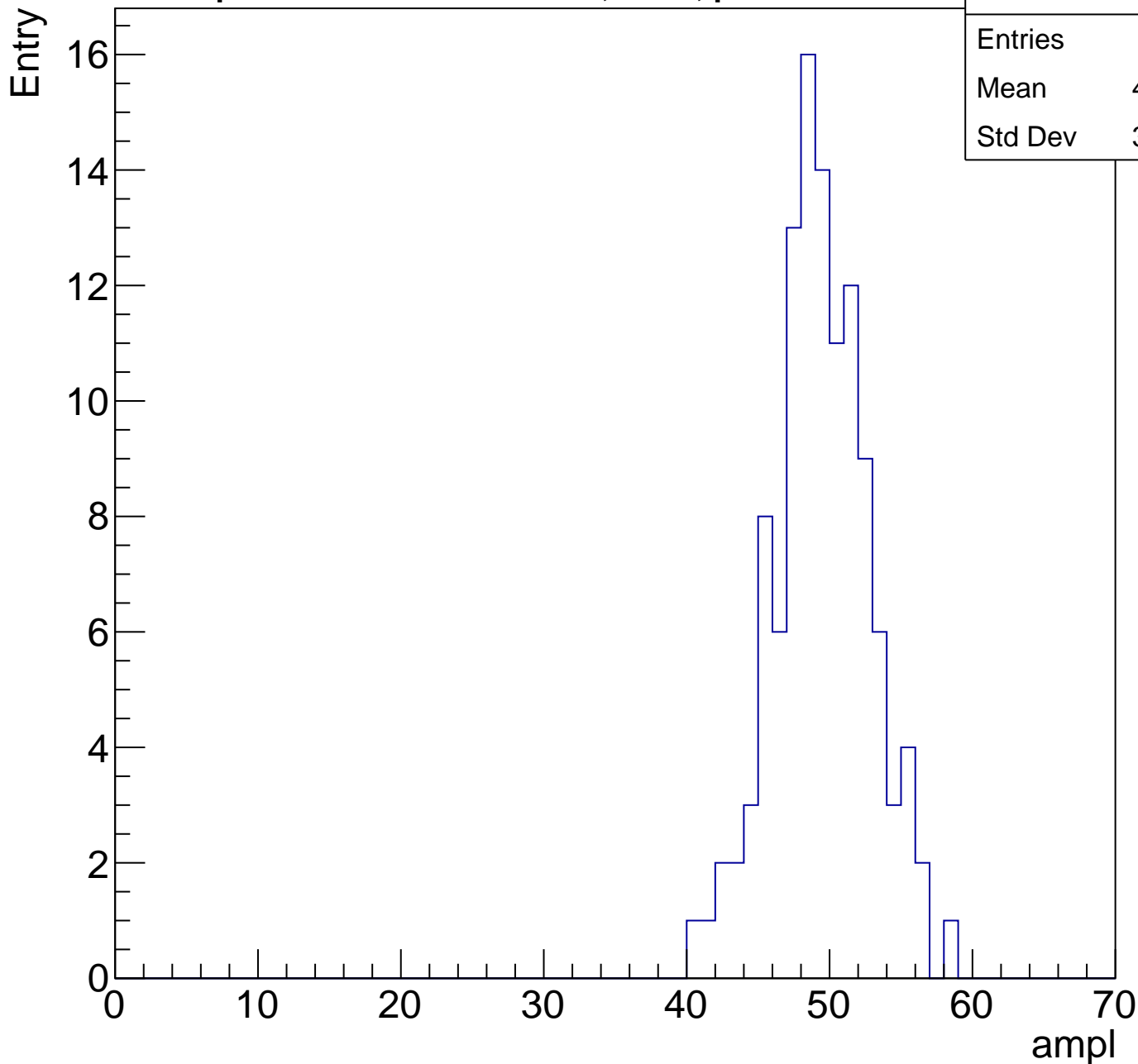
Gaus Width: 2.1381



B0L101S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

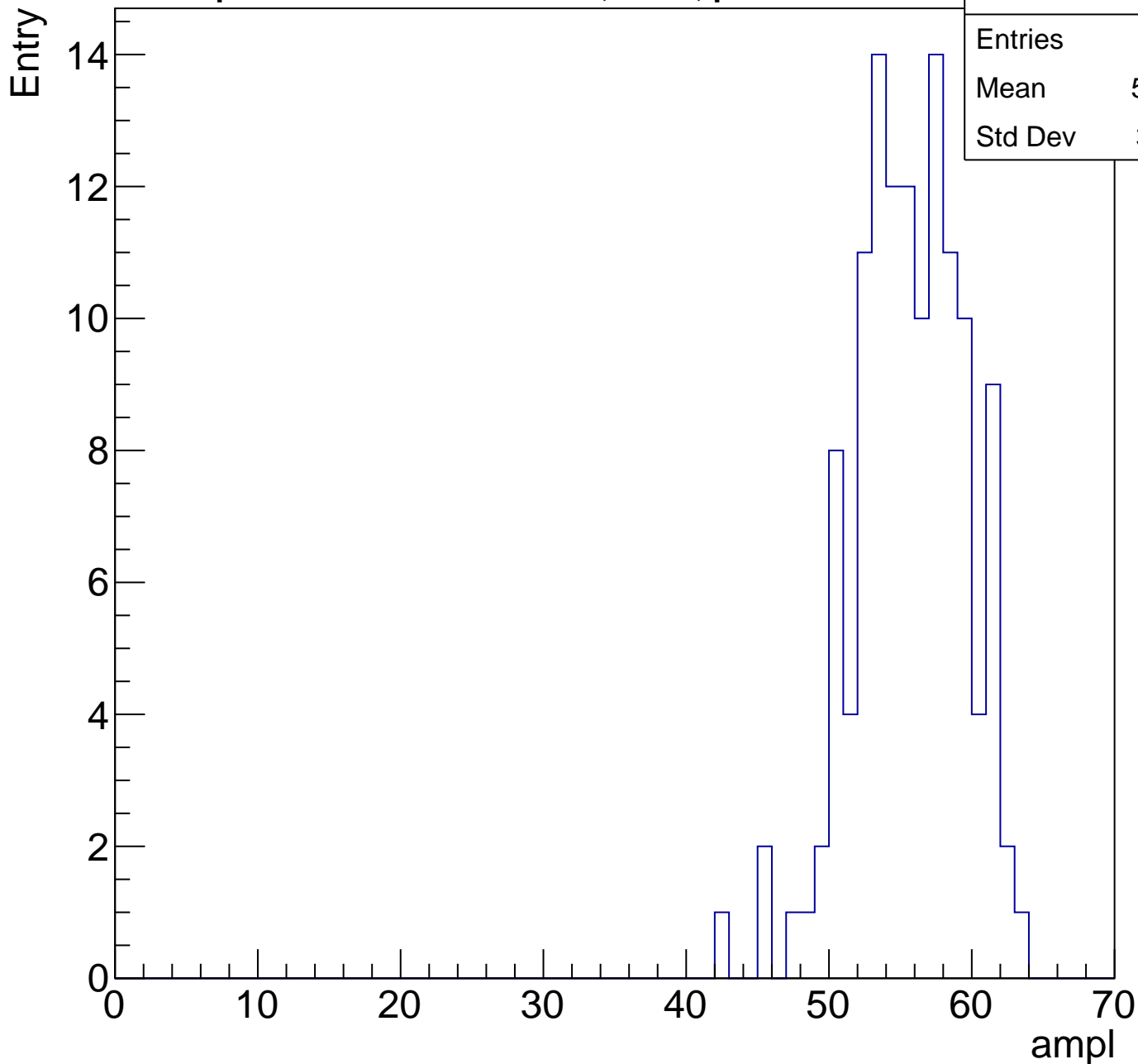
Entries	114
Mean	48.98
Std Dev	3.343



B0L101S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	55.14
Std Dev	3.831



B0L101S, U14-ch12, adc5

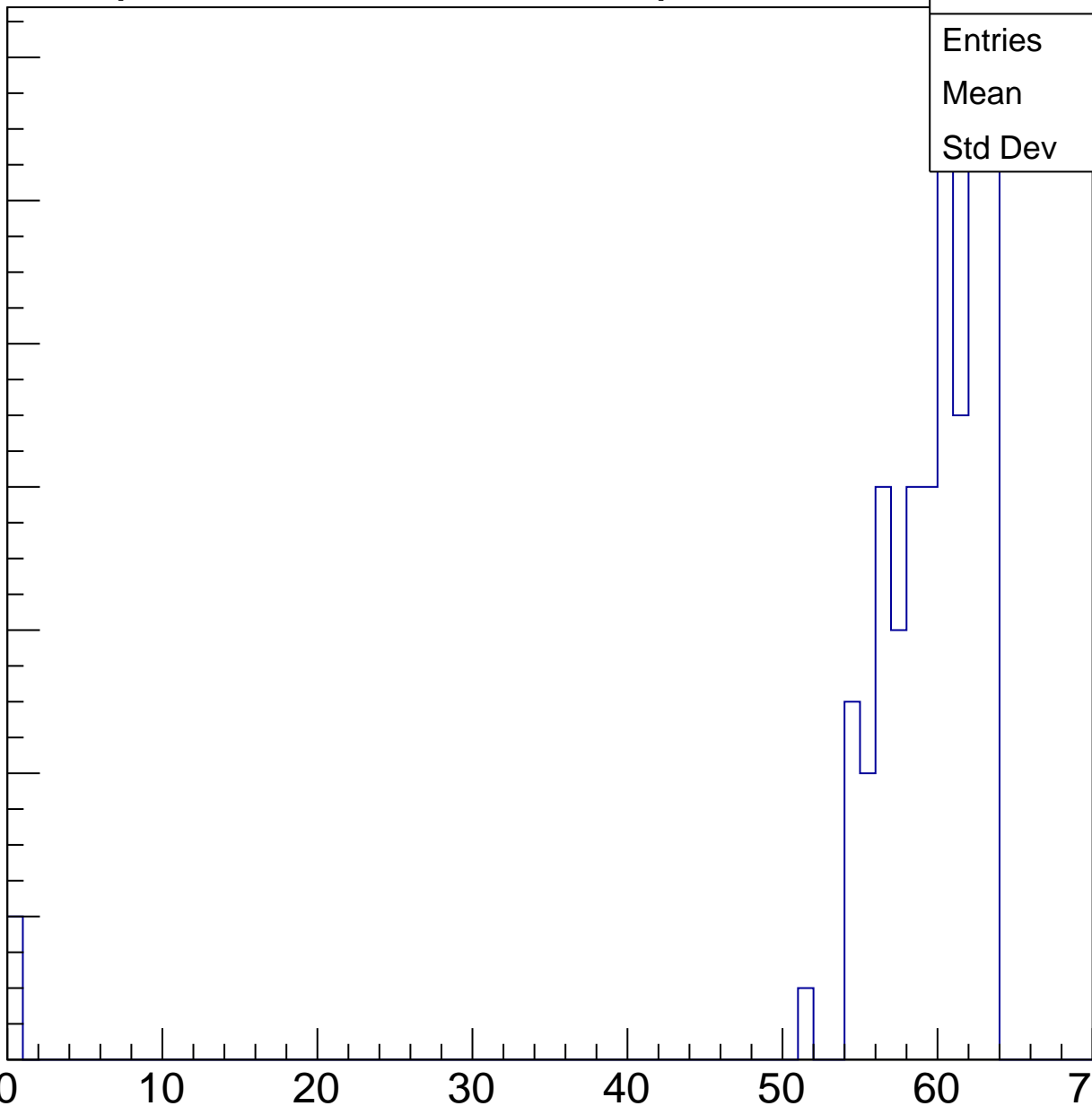
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.08
Std Dev	9.104

ampl

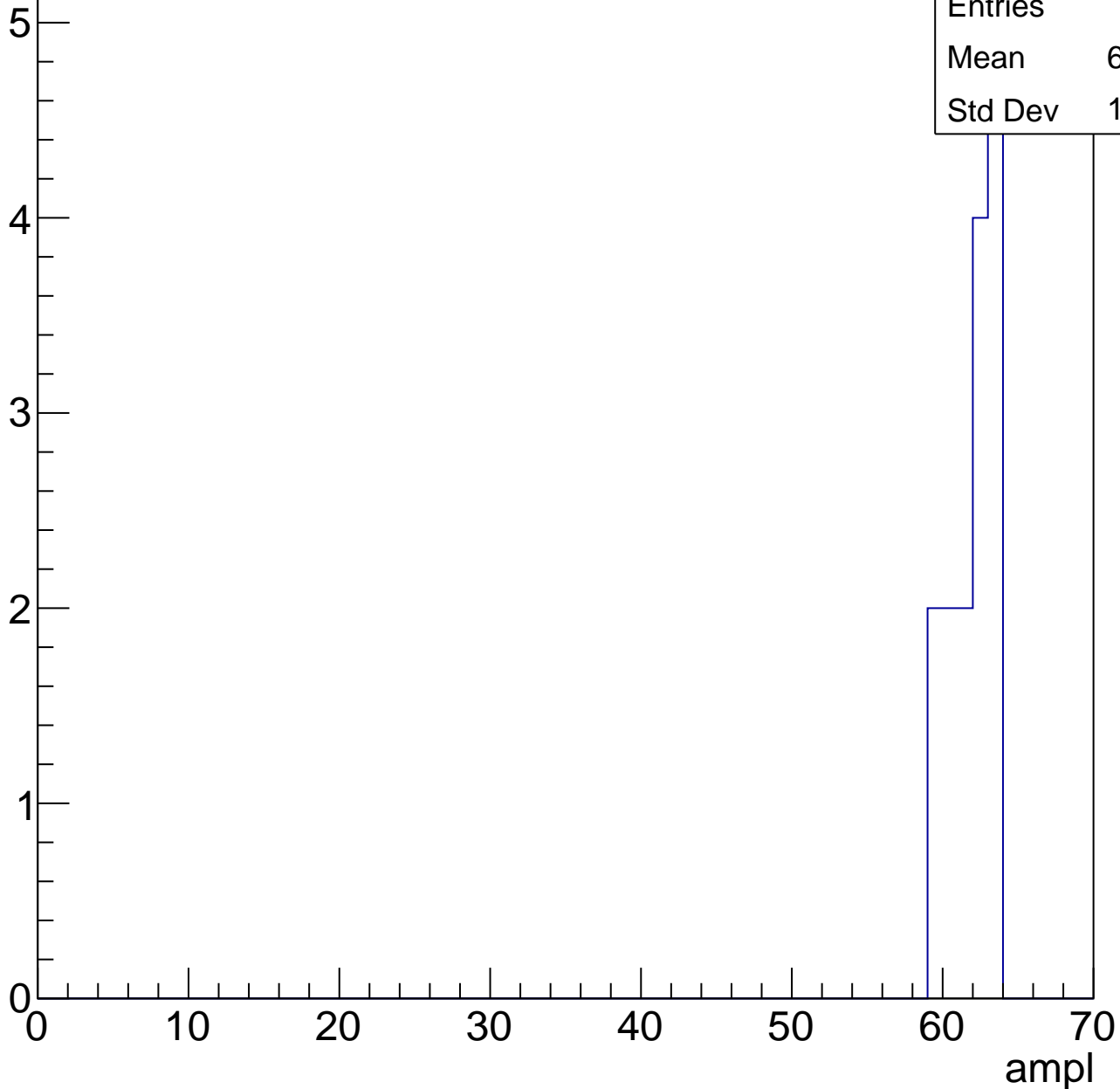


B0L101S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.53
Std Dev	1.408



B0L101S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch13, adc0

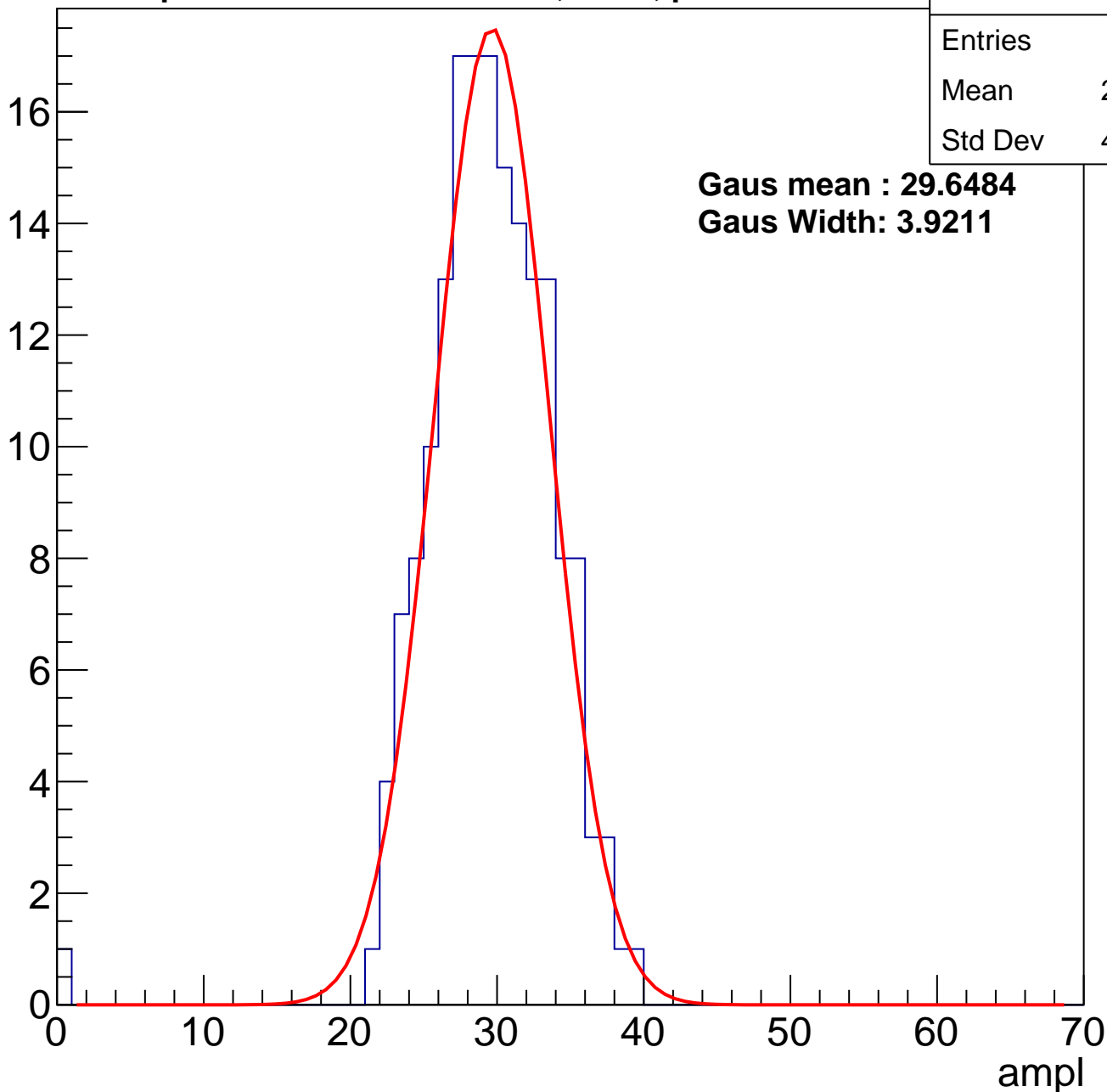
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	29.05
Std Dev	4.367

Gaus mean : 29.6484

Gaus Width: 3.9211

Entry



B0L101S, U14-ch13, adc1

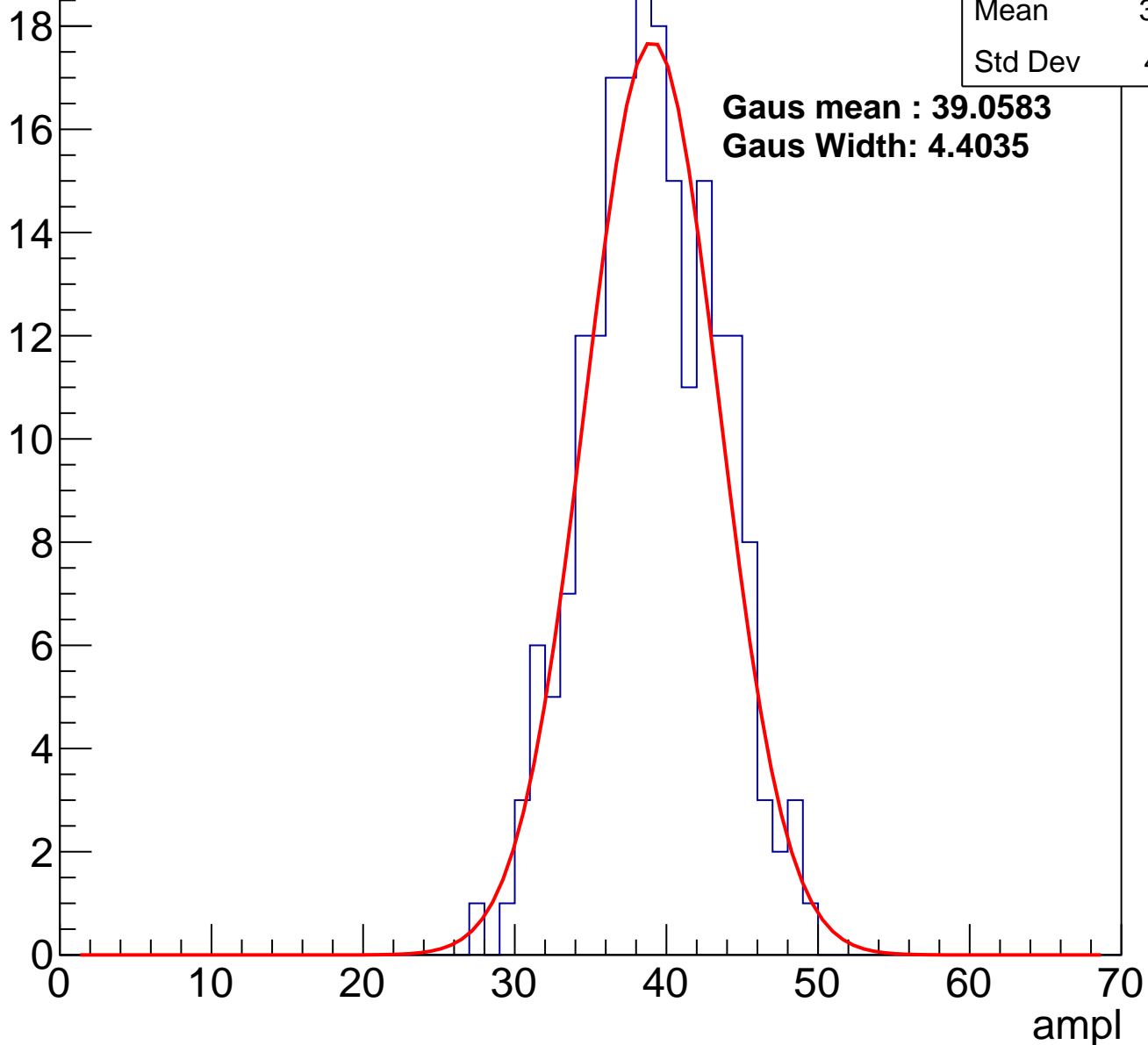
calib_packv5_042523_0143.root, FC#1, port C1

Entries	200
Mean	38.63
Std Dev	4.281

Gaus mean : 39.0583

Gaus Width: 4.4035

Entry



B0L101S, U14-ch13, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	75
Mean	45.37
Std Dev	3.314

Gaus mean : 46.3791

Gaus Width: 3.4544

Entry

12

10

8

6

4

2

0

0

10

20

30

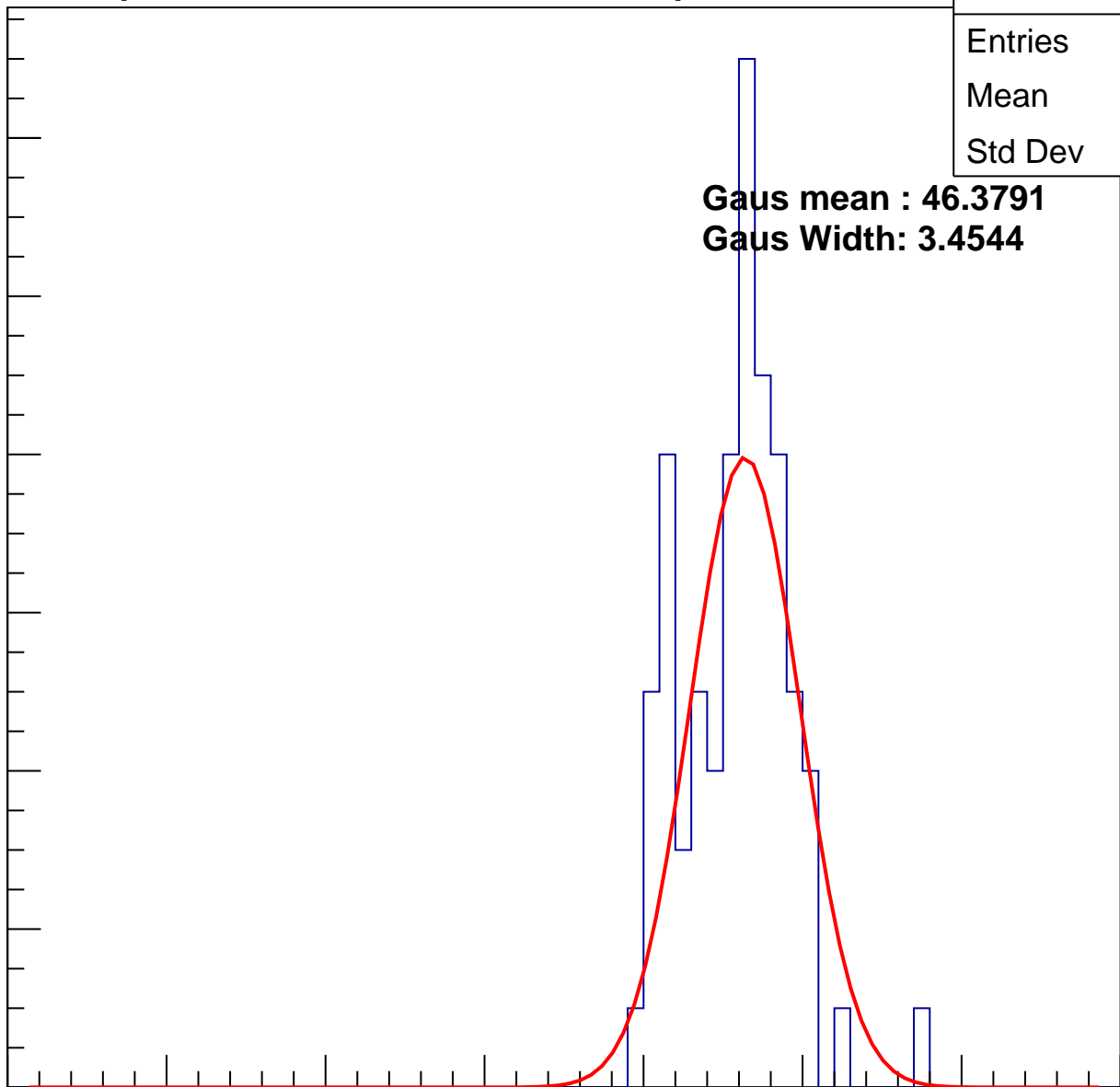
40

50

60

70

ampl

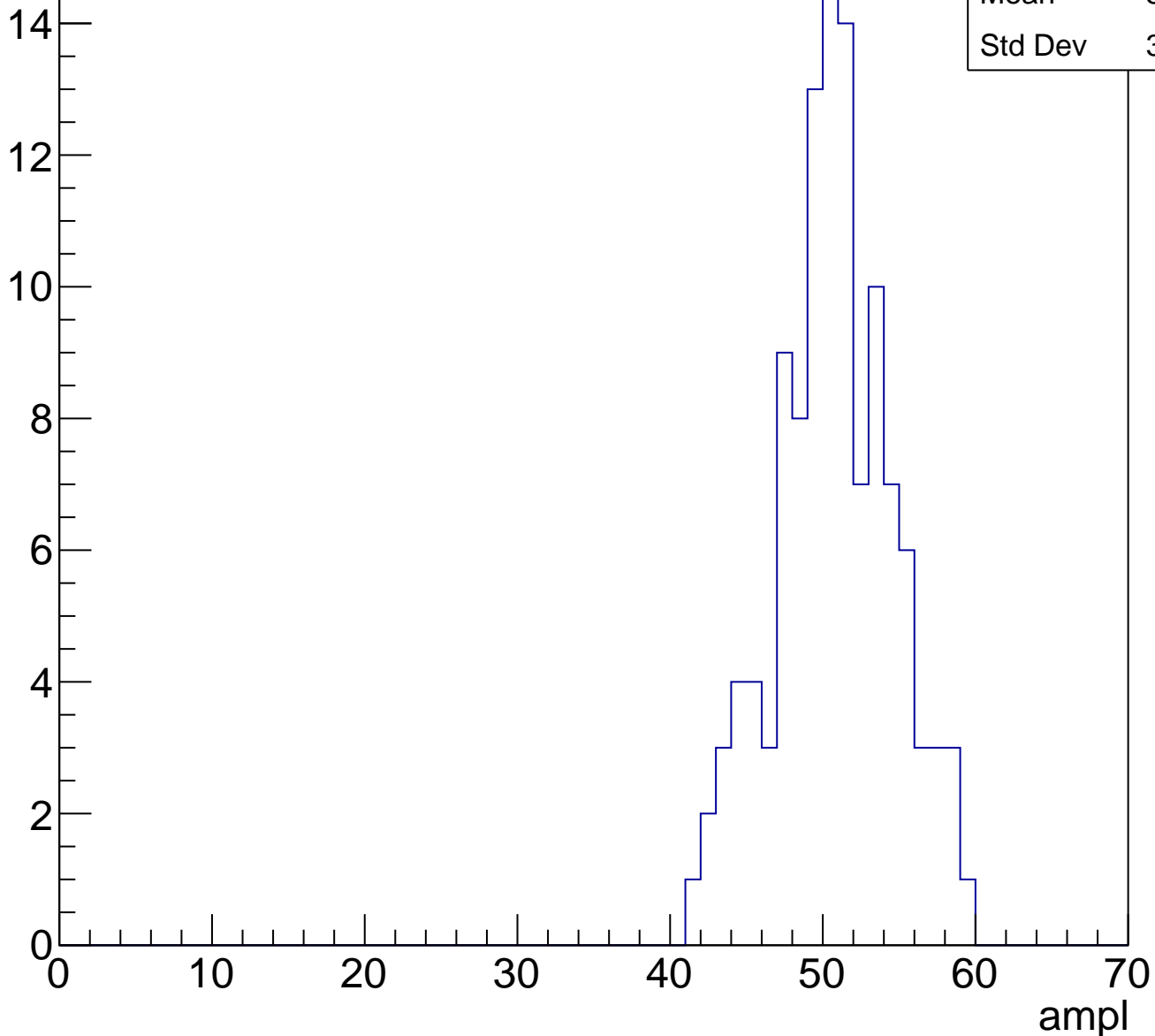


B0L101S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	50.26
Std Dev	3.815

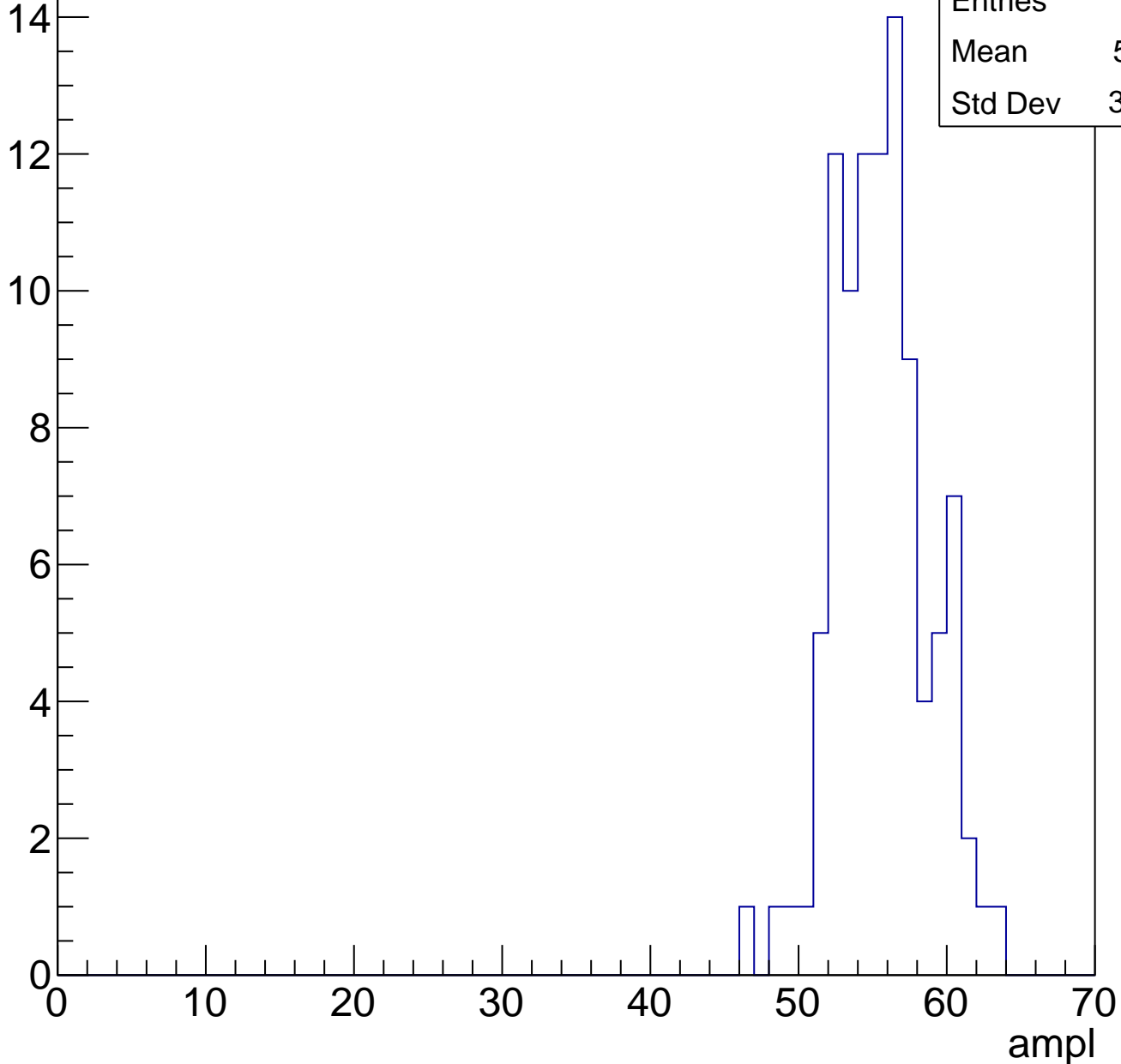
Entry



B0L101S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch13, adc5

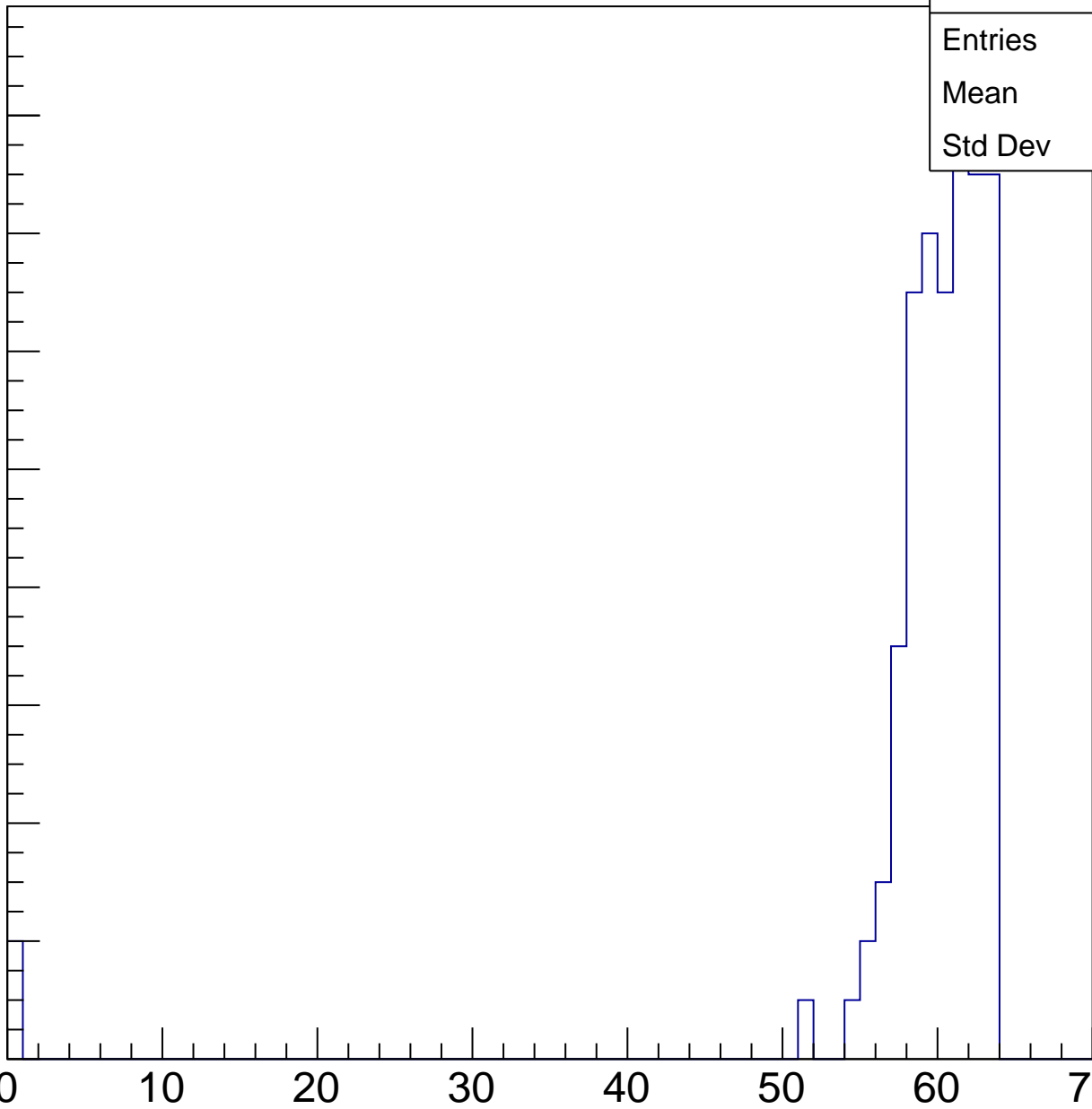
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.78
Std Dev	8.591

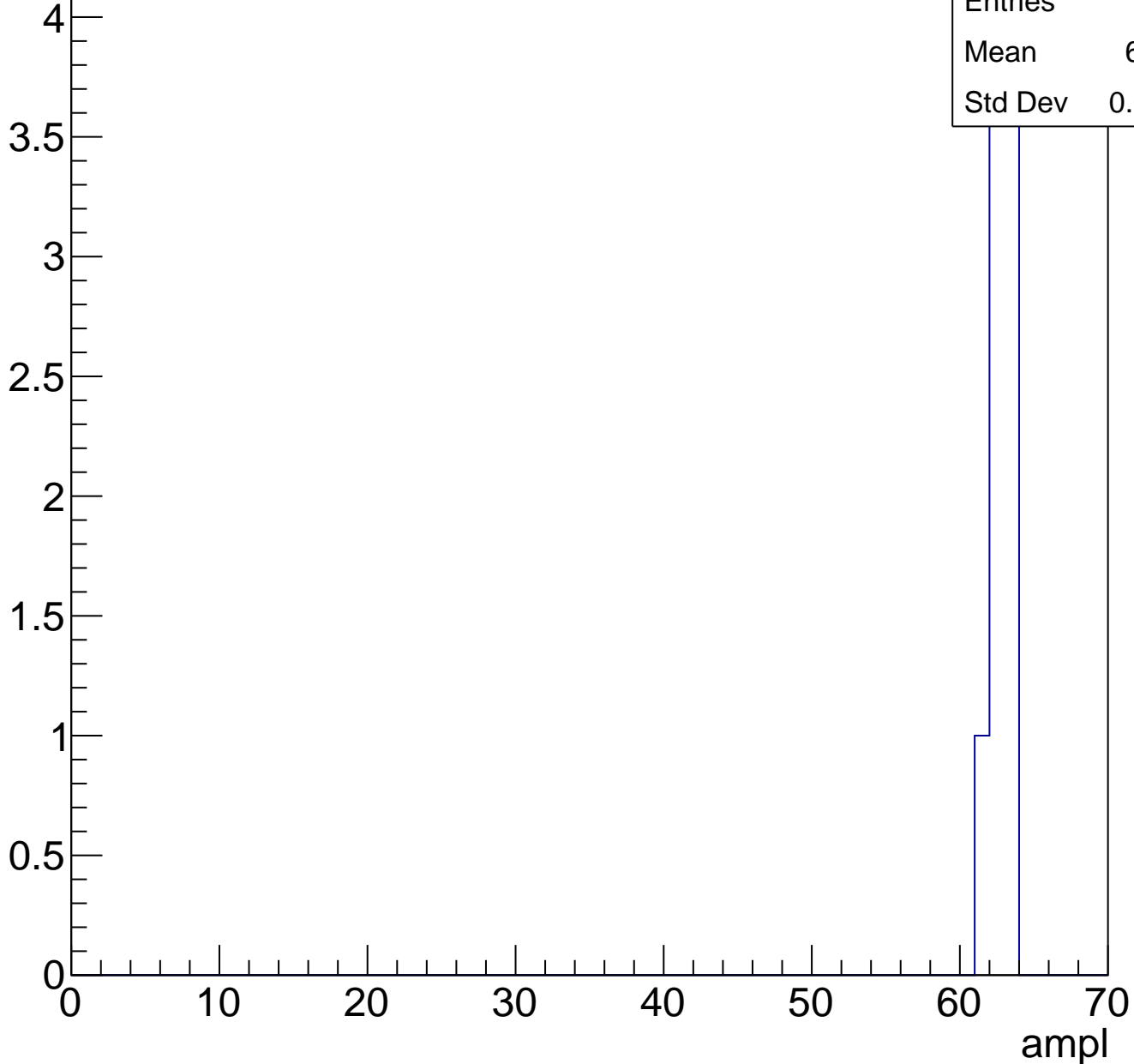
ampl



B0L101S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	30.66
Std Dev	3.723

Gaus mean : 31.1464

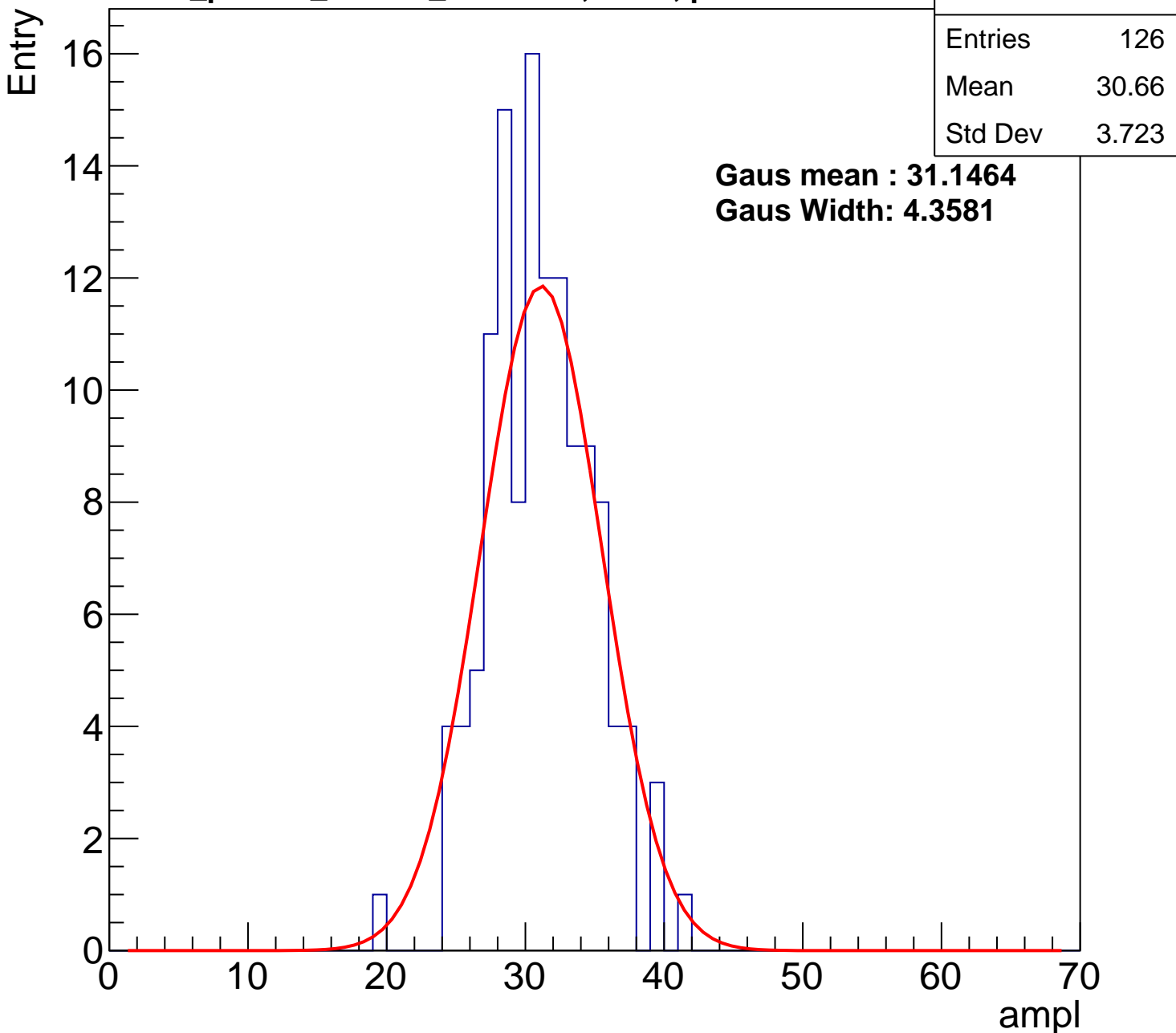
Gaus Width: 4.3581

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



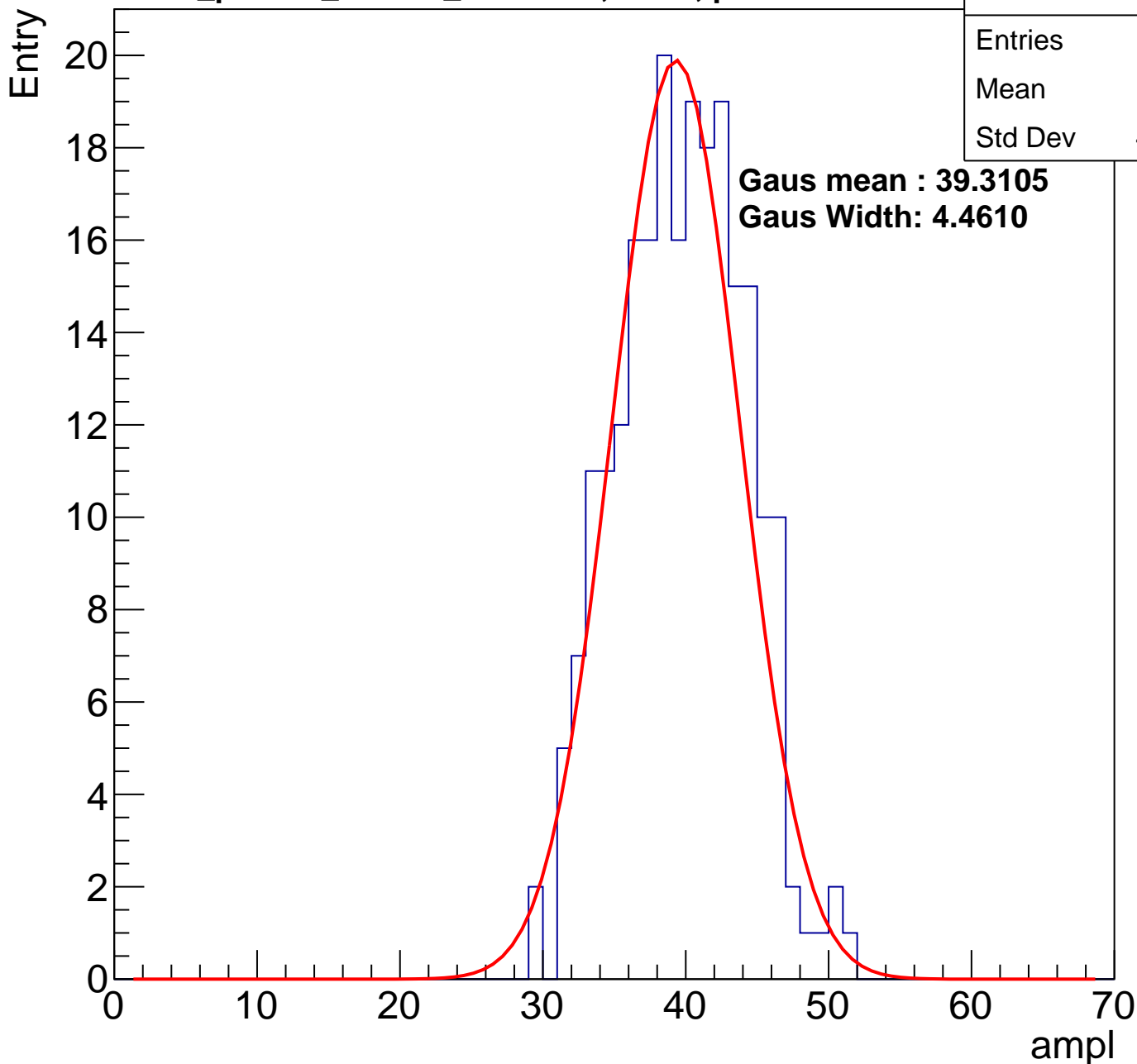
B0L101S, U14-ch14, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	229
Mean	39.3
Std Dev	4.361

Gaus mean : 39.3105

Gaus Width: 4.4610



B0L101S, U14-ch14, adc2

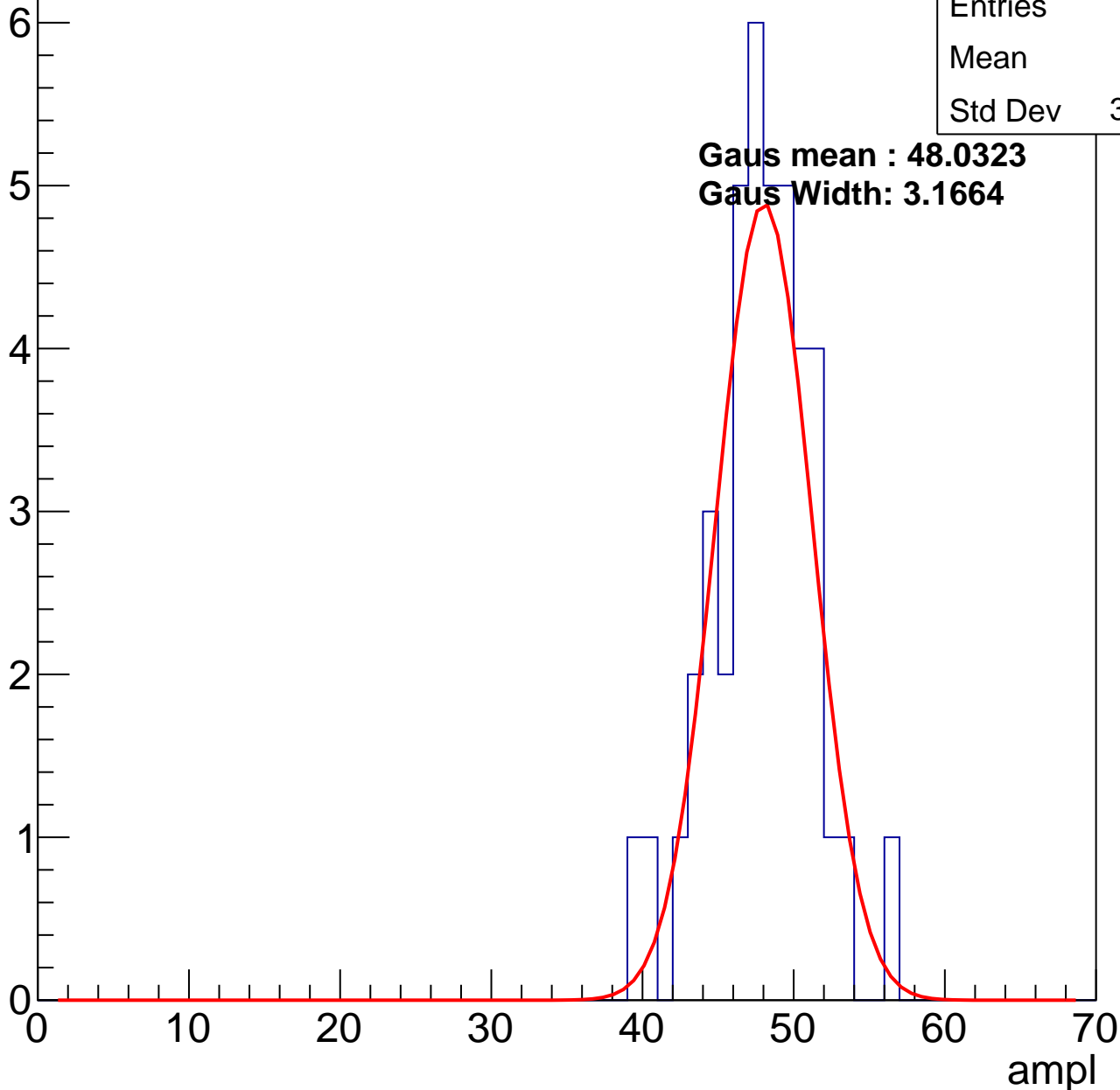
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	42
Mean	47.4
Std Dev	3.346

Gaus mean : 48.0323

Gaus Width: 3.1664



B0L101S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

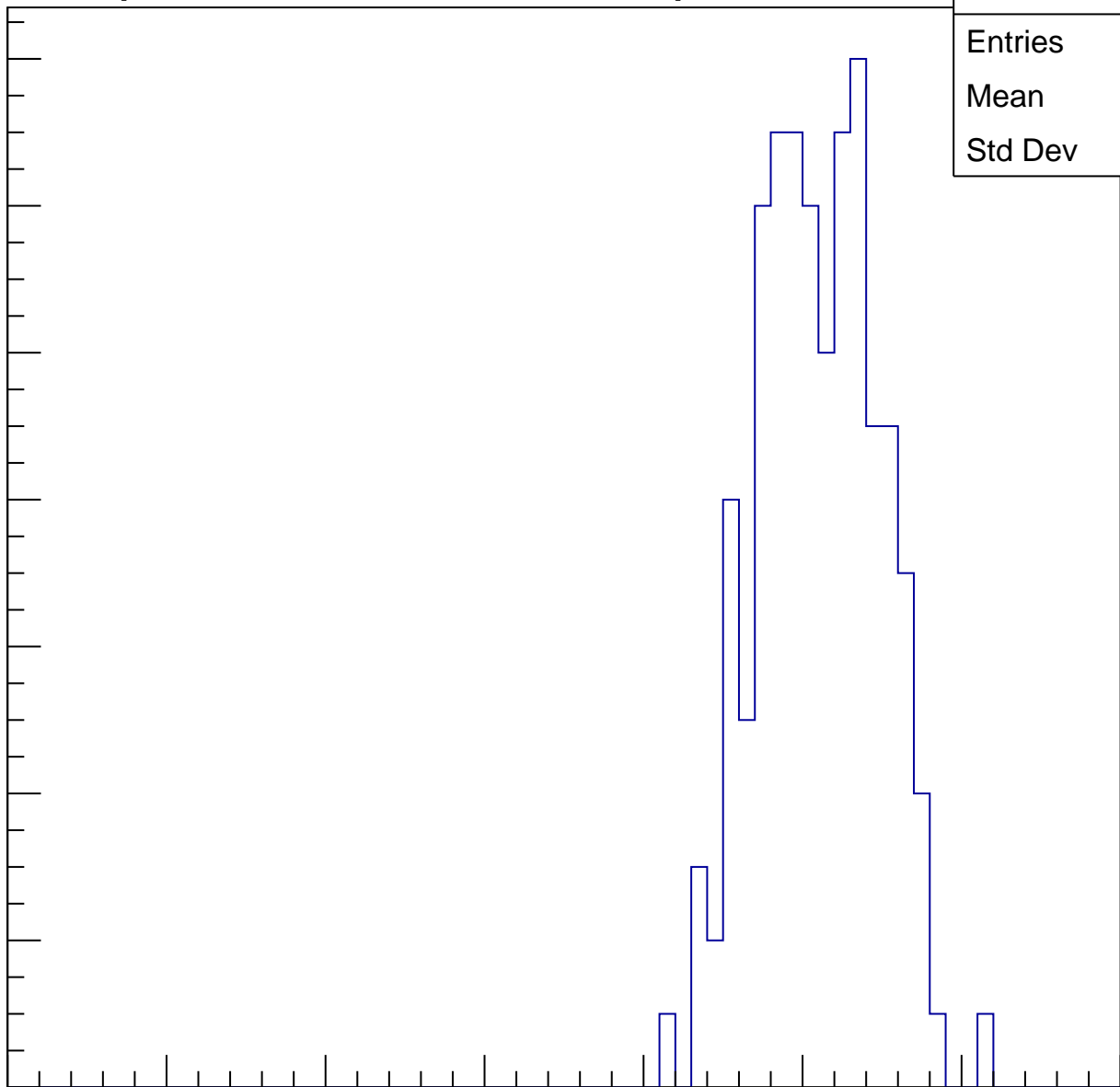
Entries	137
Mean	50.52
Std Dev	3.73

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch14, adc4

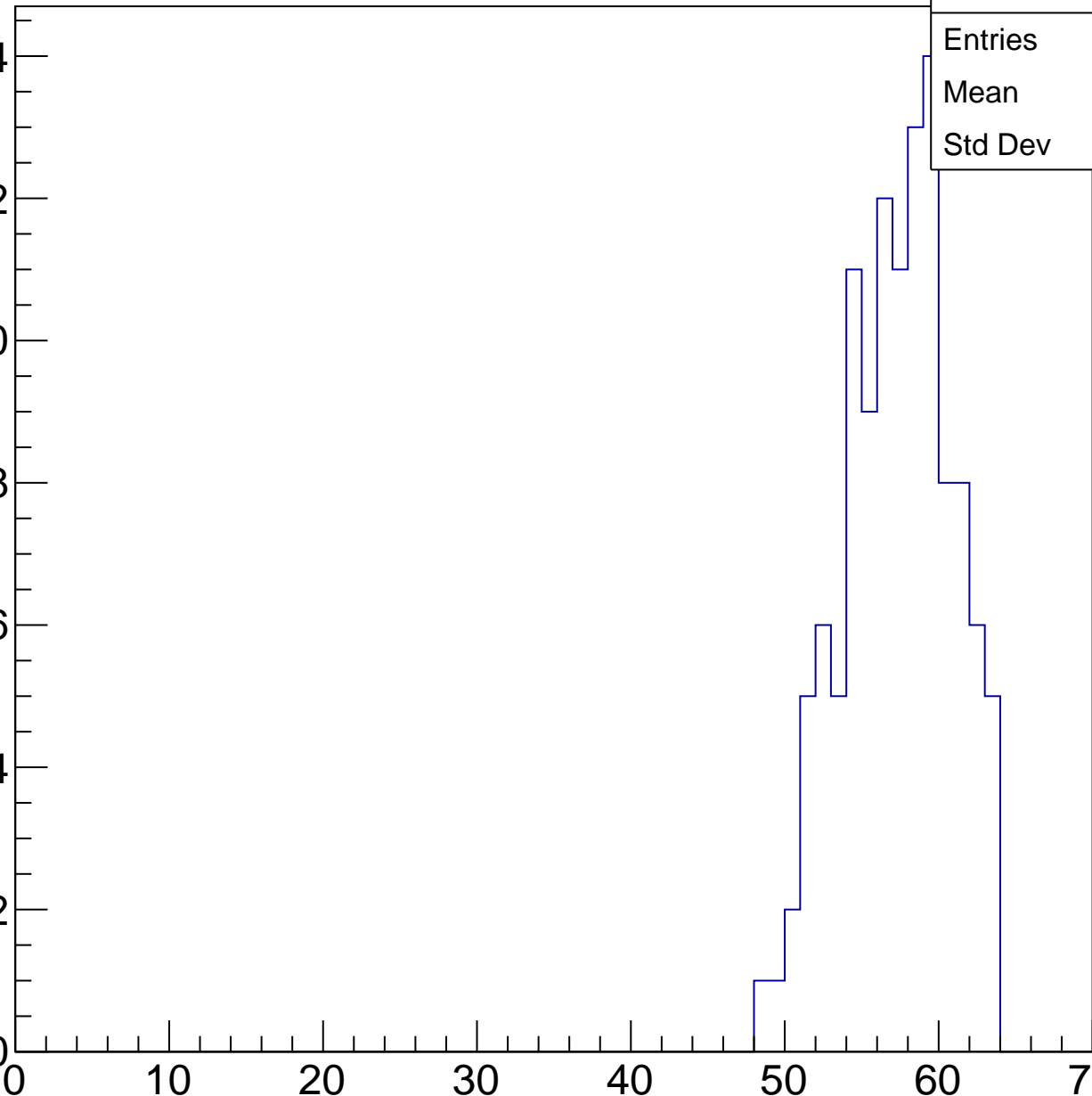
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	56.85
Std Dev	3.455

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch14, adc5

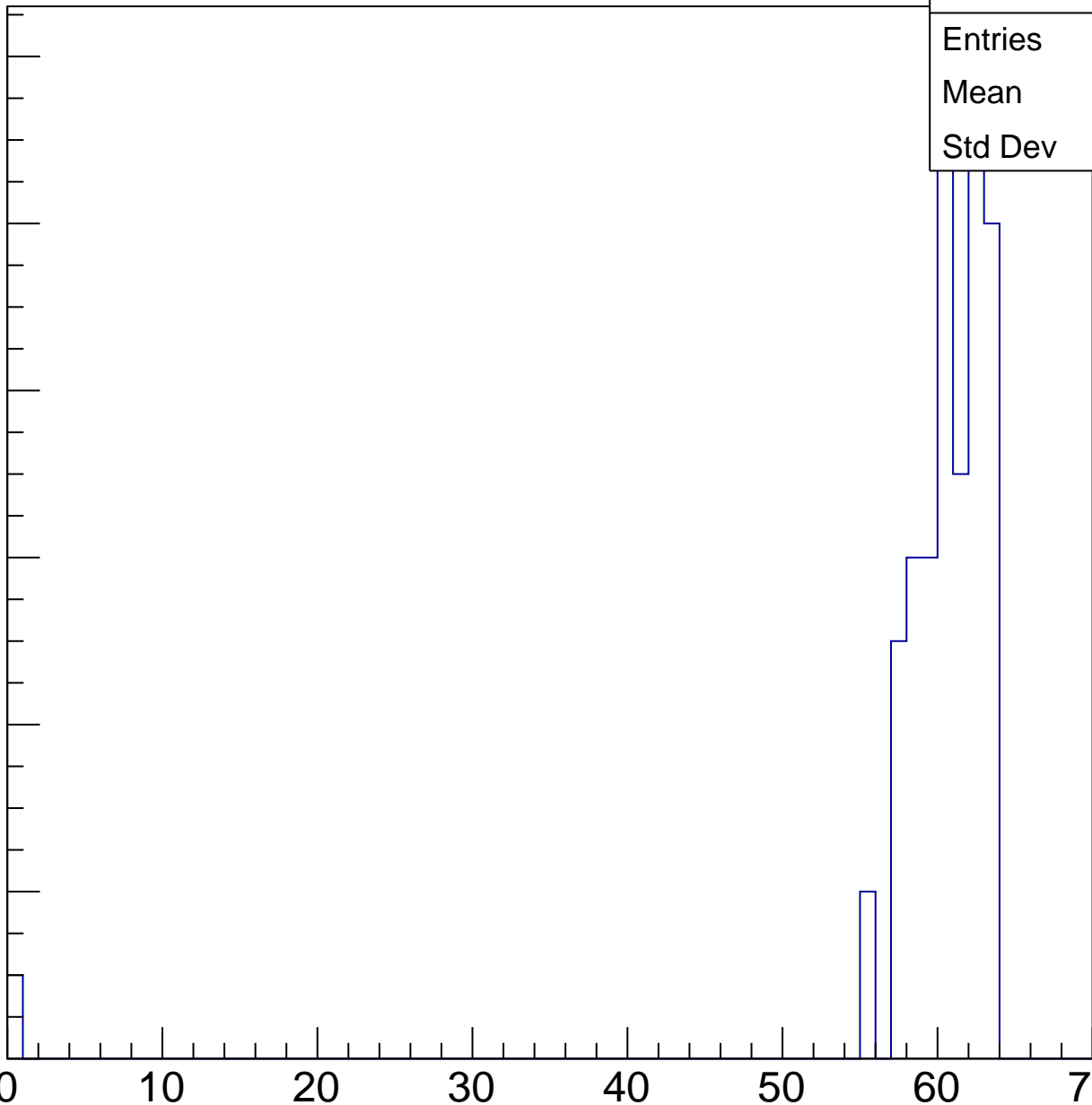
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	60
Mean	59.27
Std Dev	7.993

ampl

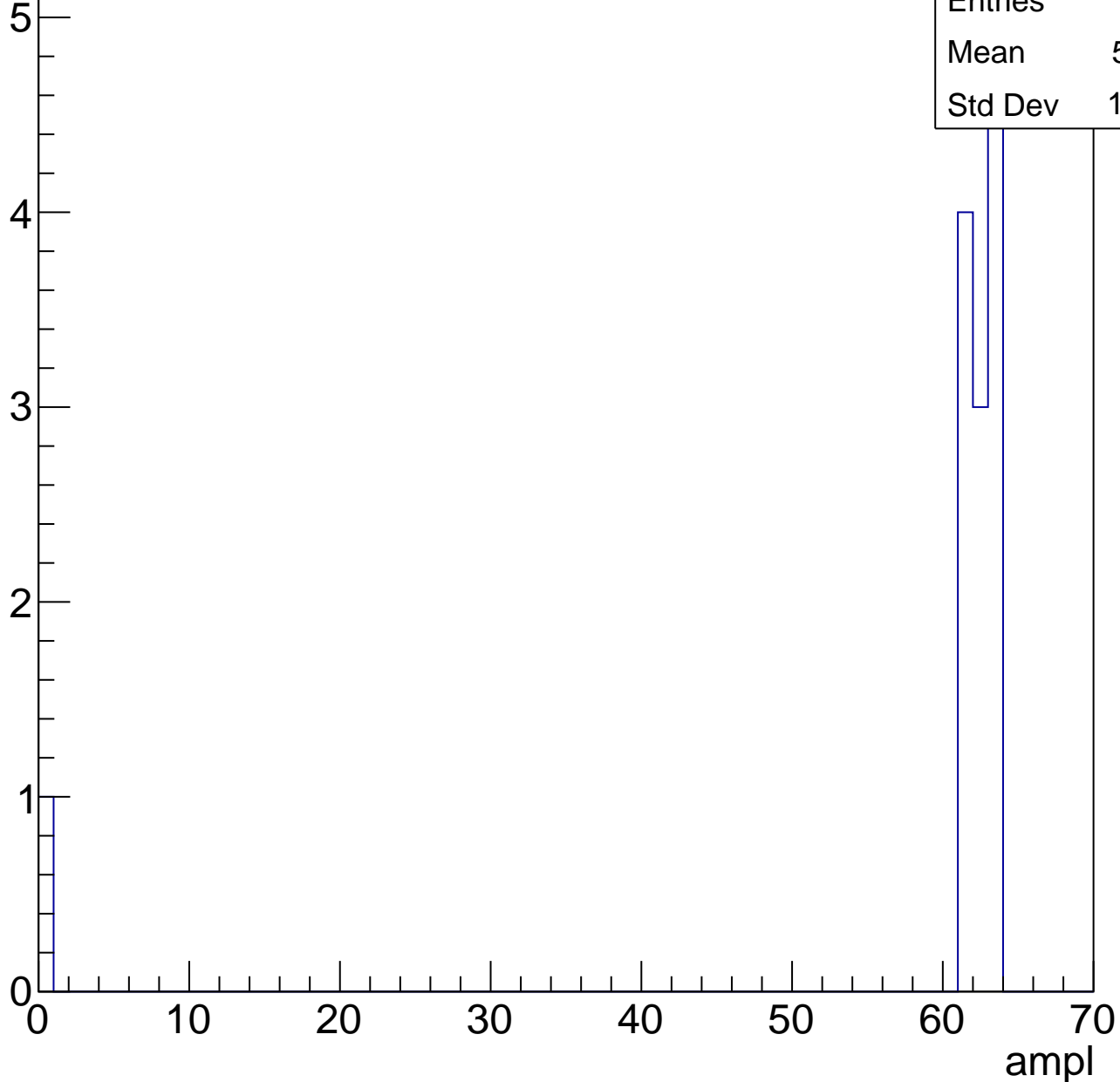


B0L101S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.31
Std Dev	16.56



B0L101S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

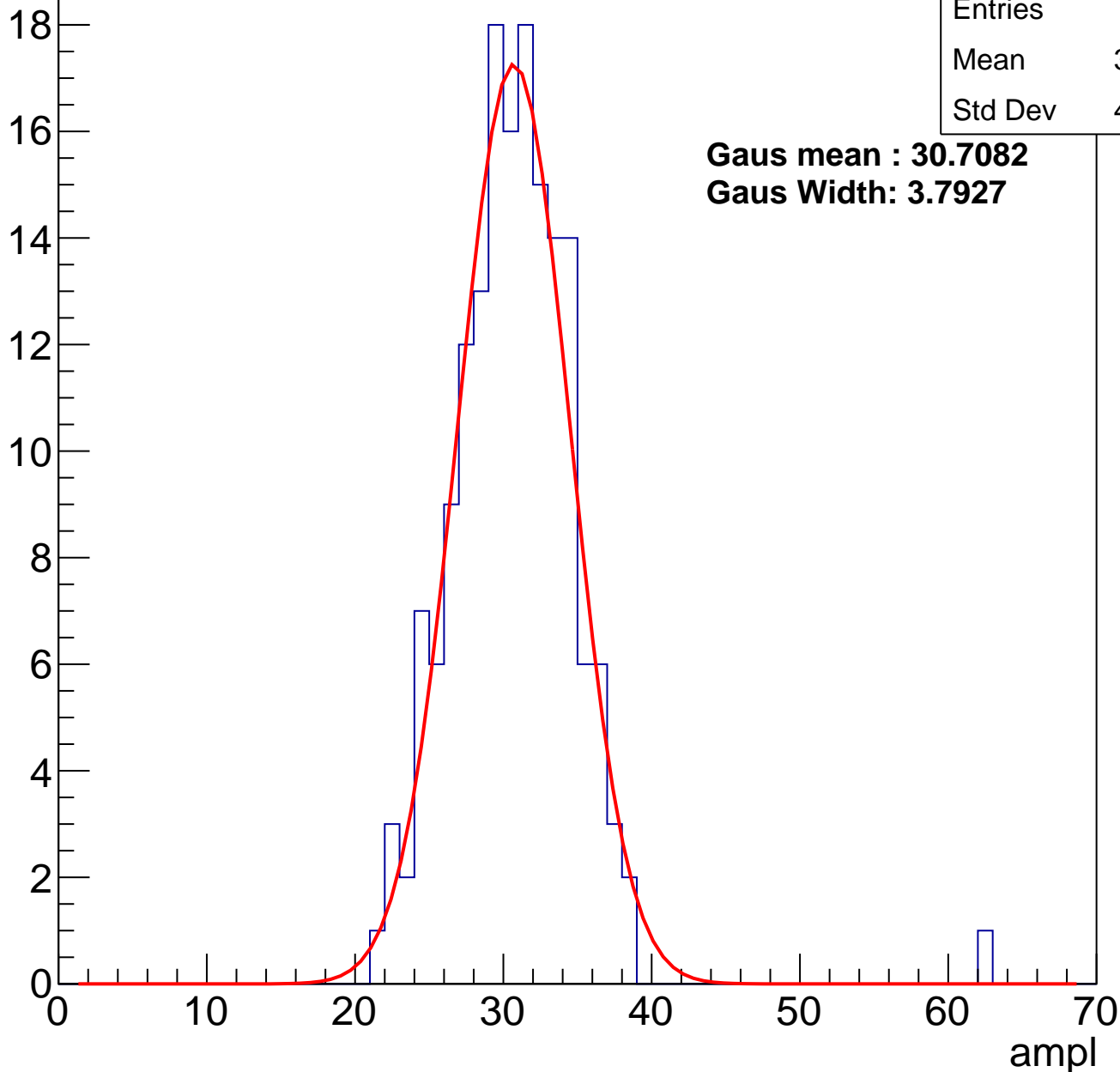
Entries	1
Mean	24
Std Dev	0

ampl

B0L101S, U14-ch15, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



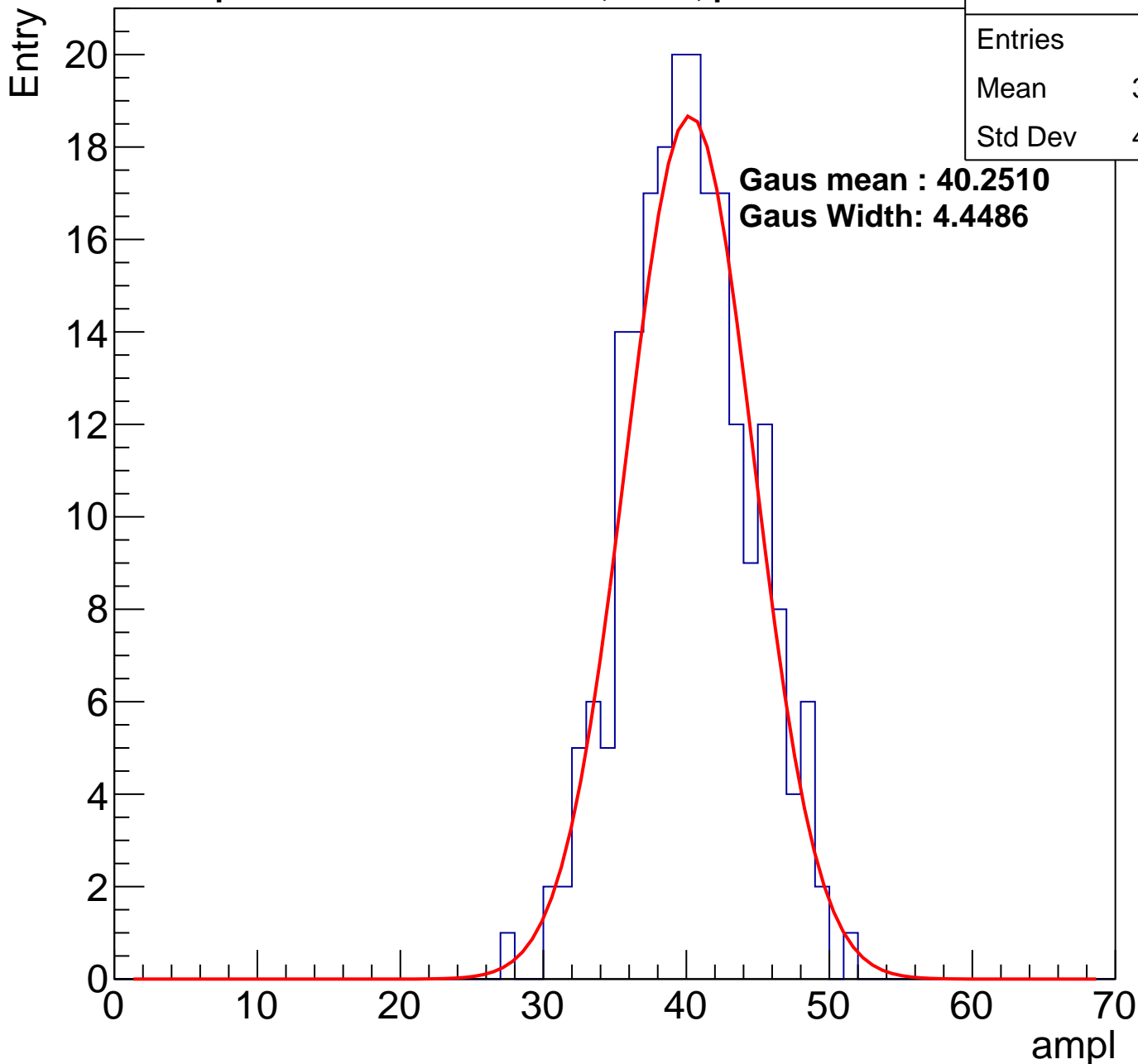
B0L101S, U14-ch15, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	212
Mean	39.72
Std Dev	4.297

Gaus mean : 40.2510

Gaus Width: 4.4486



B0L101S, U14-ch15, adc2

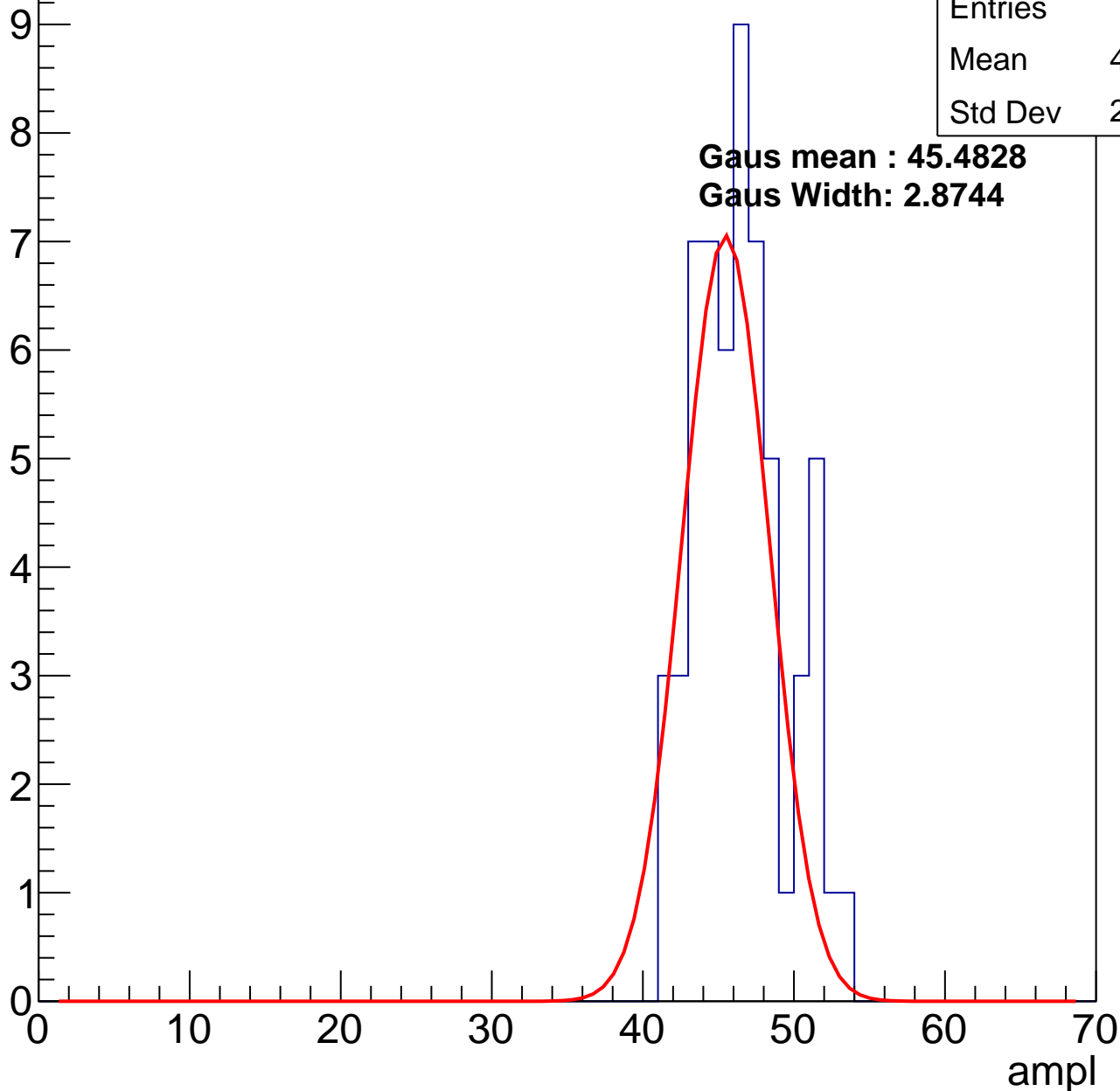
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	46.03
Std Dev	2.977

Gaus mean : 45.4828

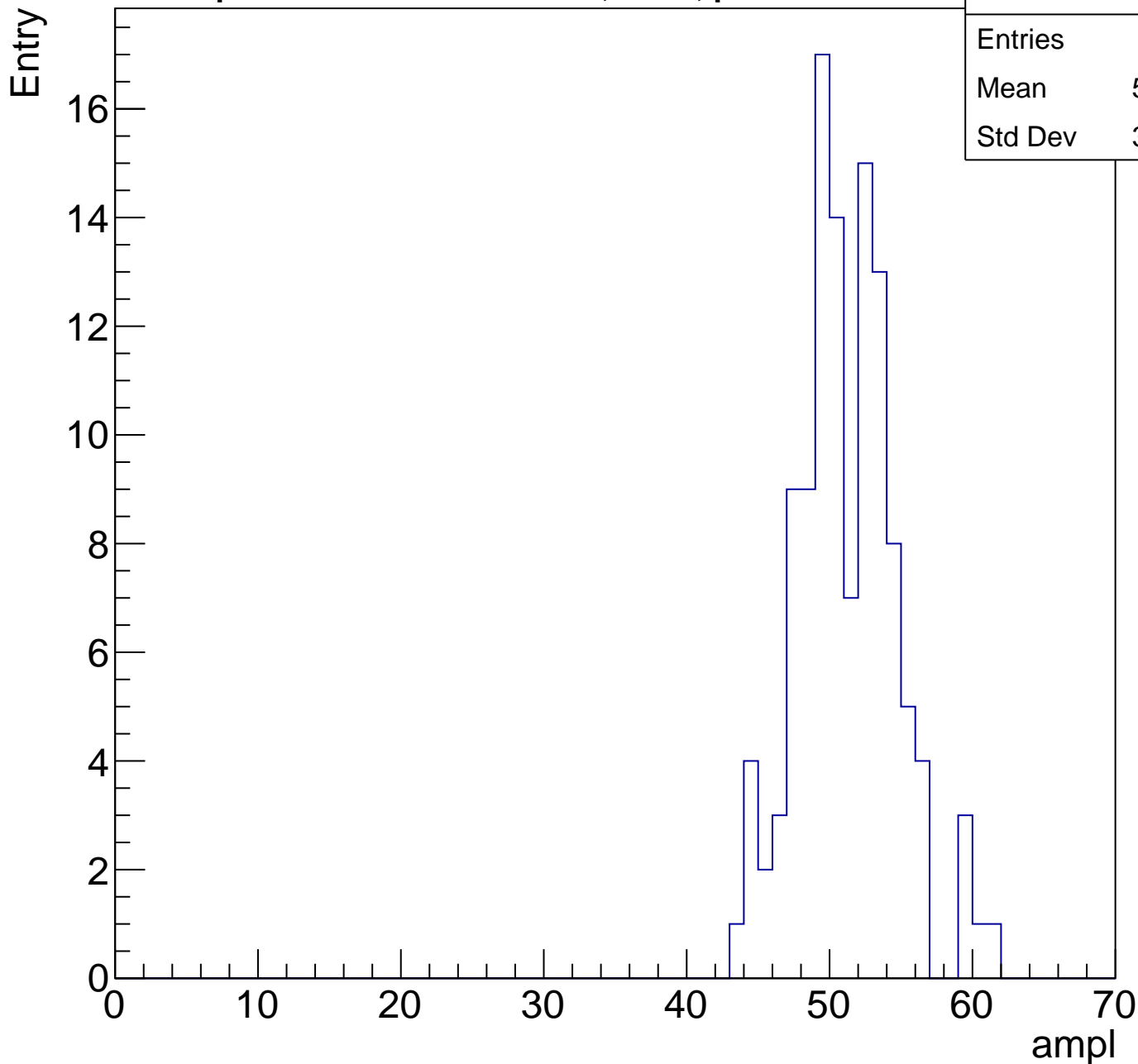
Gaus Width: 2.8744



B0L101S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	50.78
Std Dev	3.474



B0L101S, U14-ch15, adc4

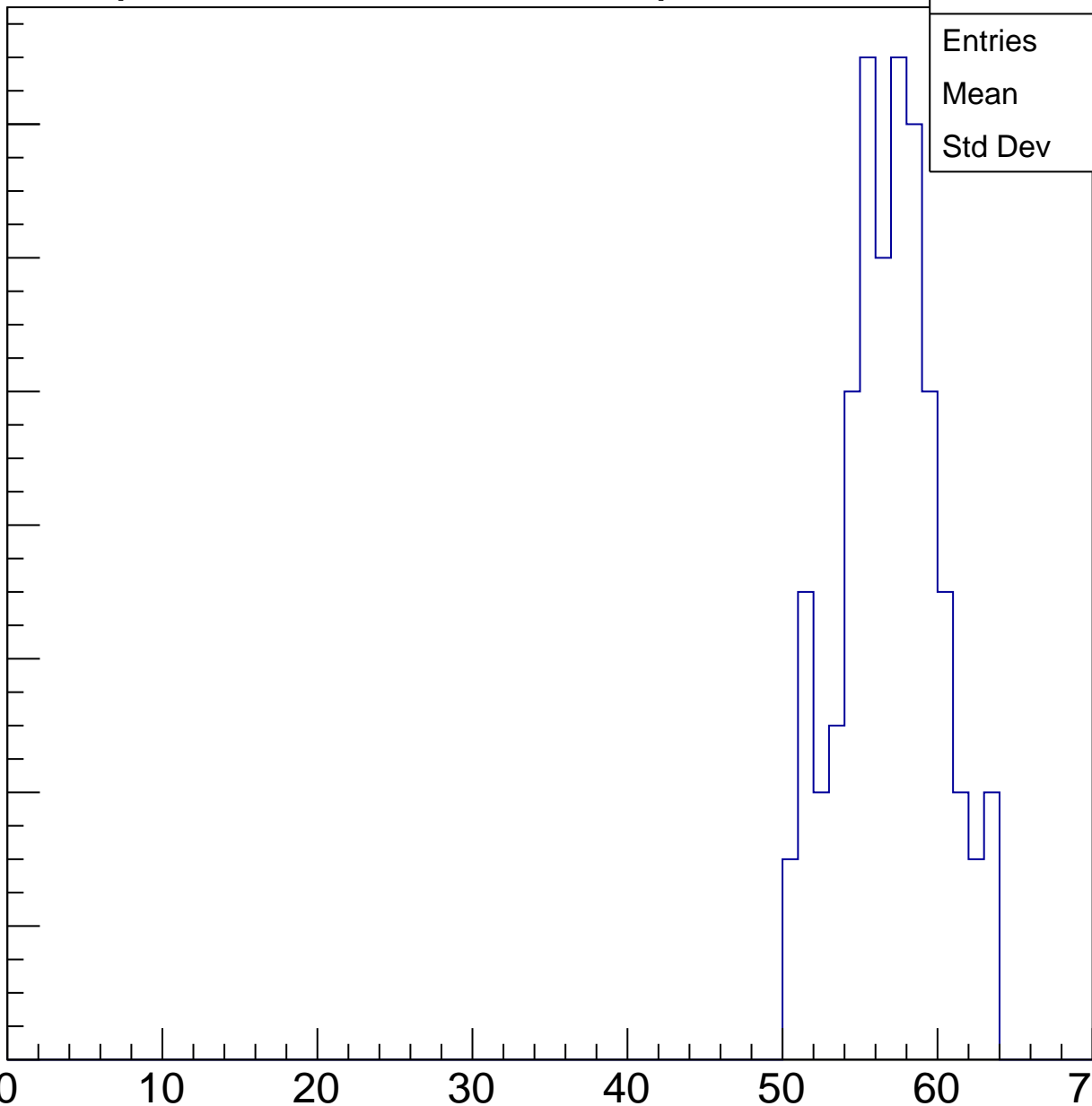
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	56.42
Std Dev	3.125

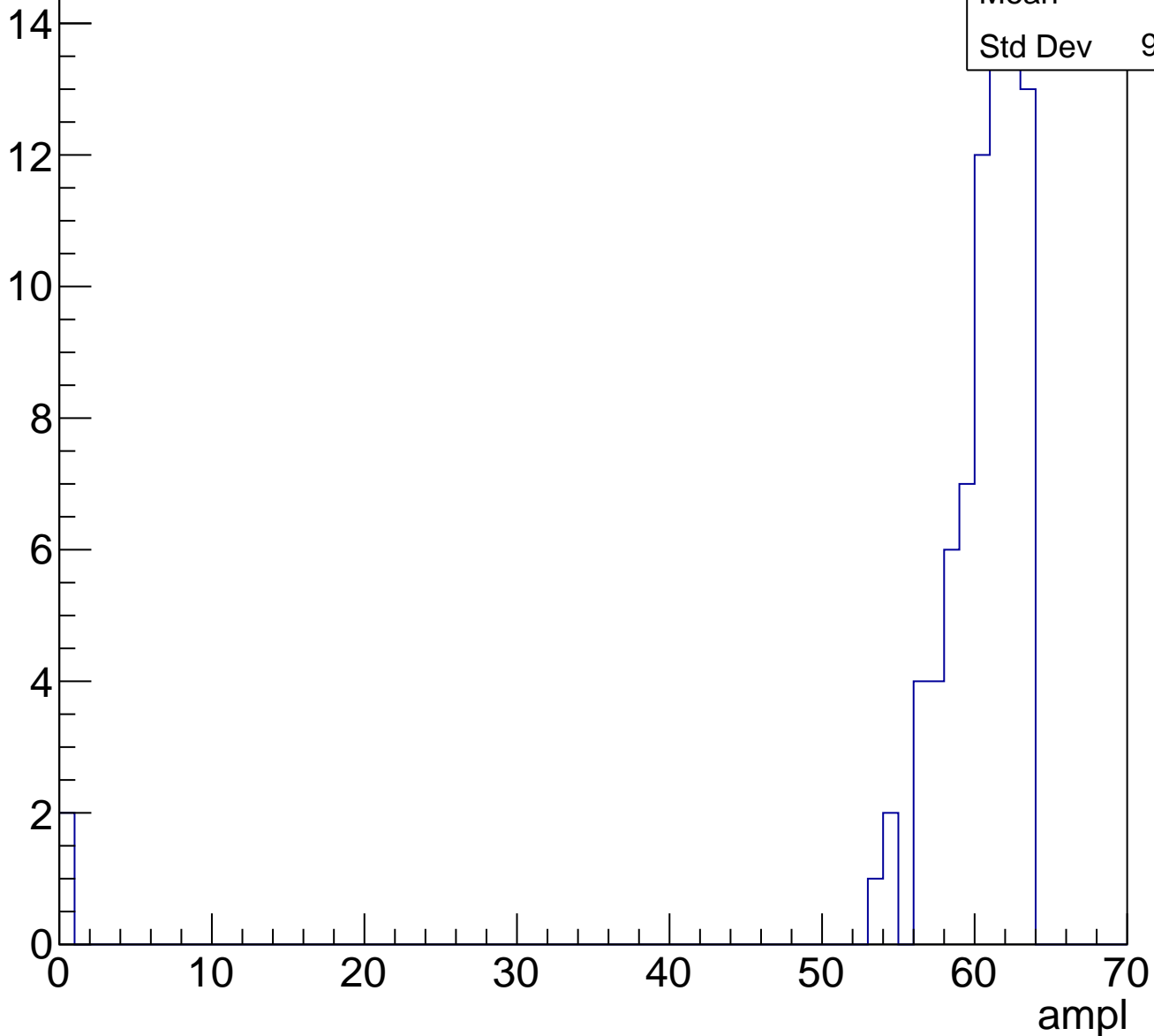
ampl



B0L101S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

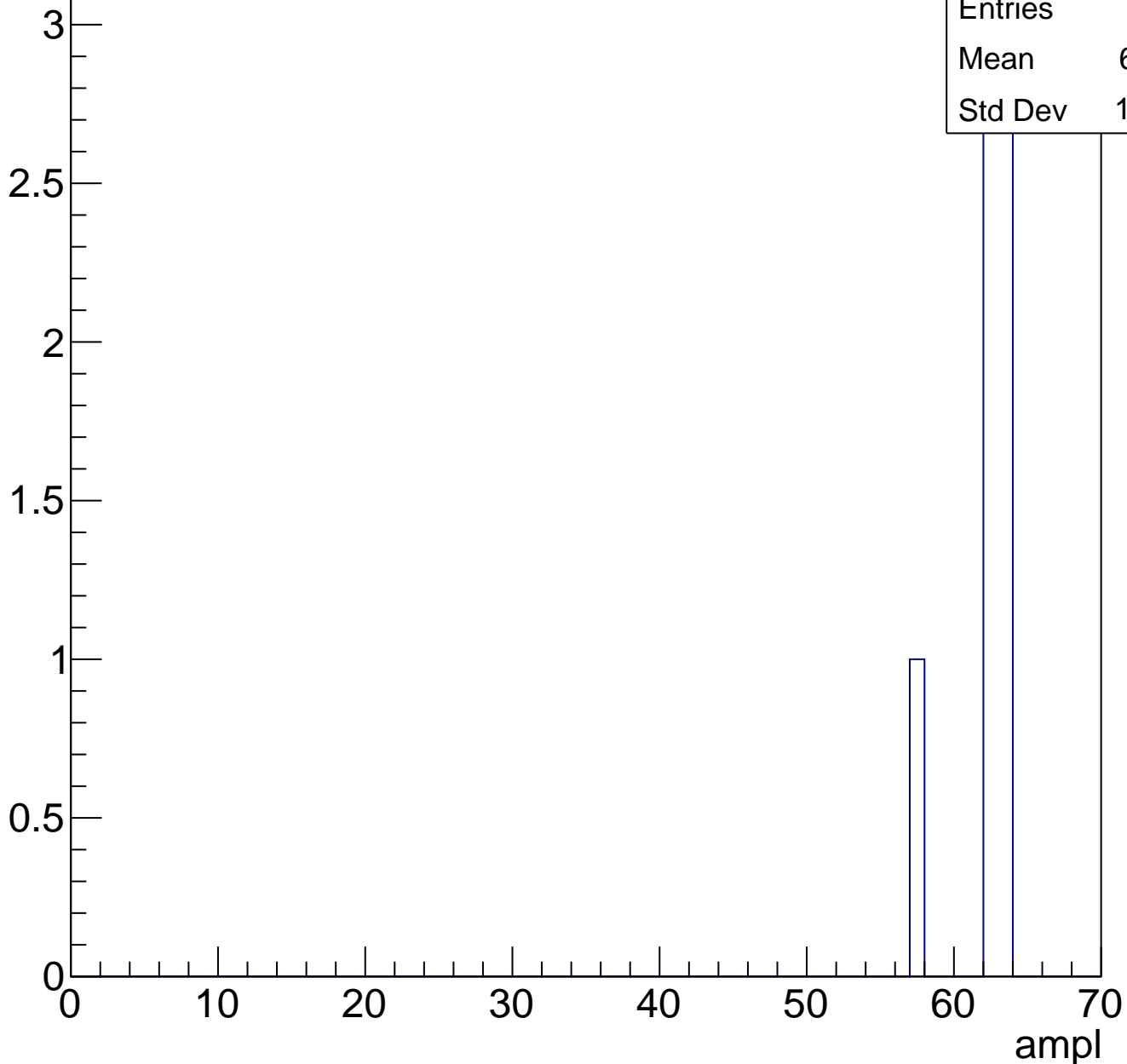


Entries	80
Mean	58.7
Std Dev	9.686

B0L101S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

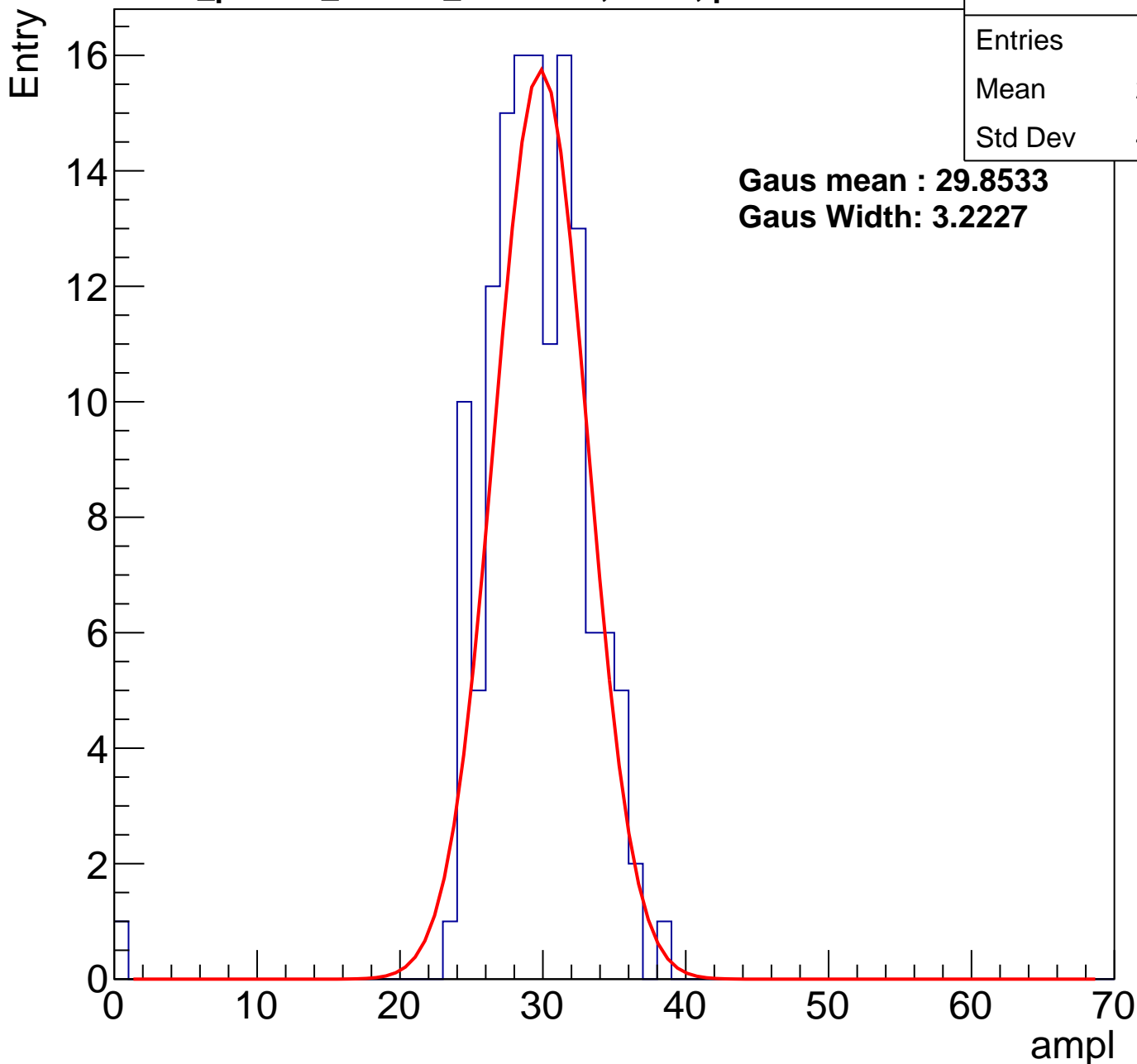
B0L101S, U14-ch16, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	29.01
Std Dev	4.011

Gaus mean : 29.8533

Gaus Width: 3.2227



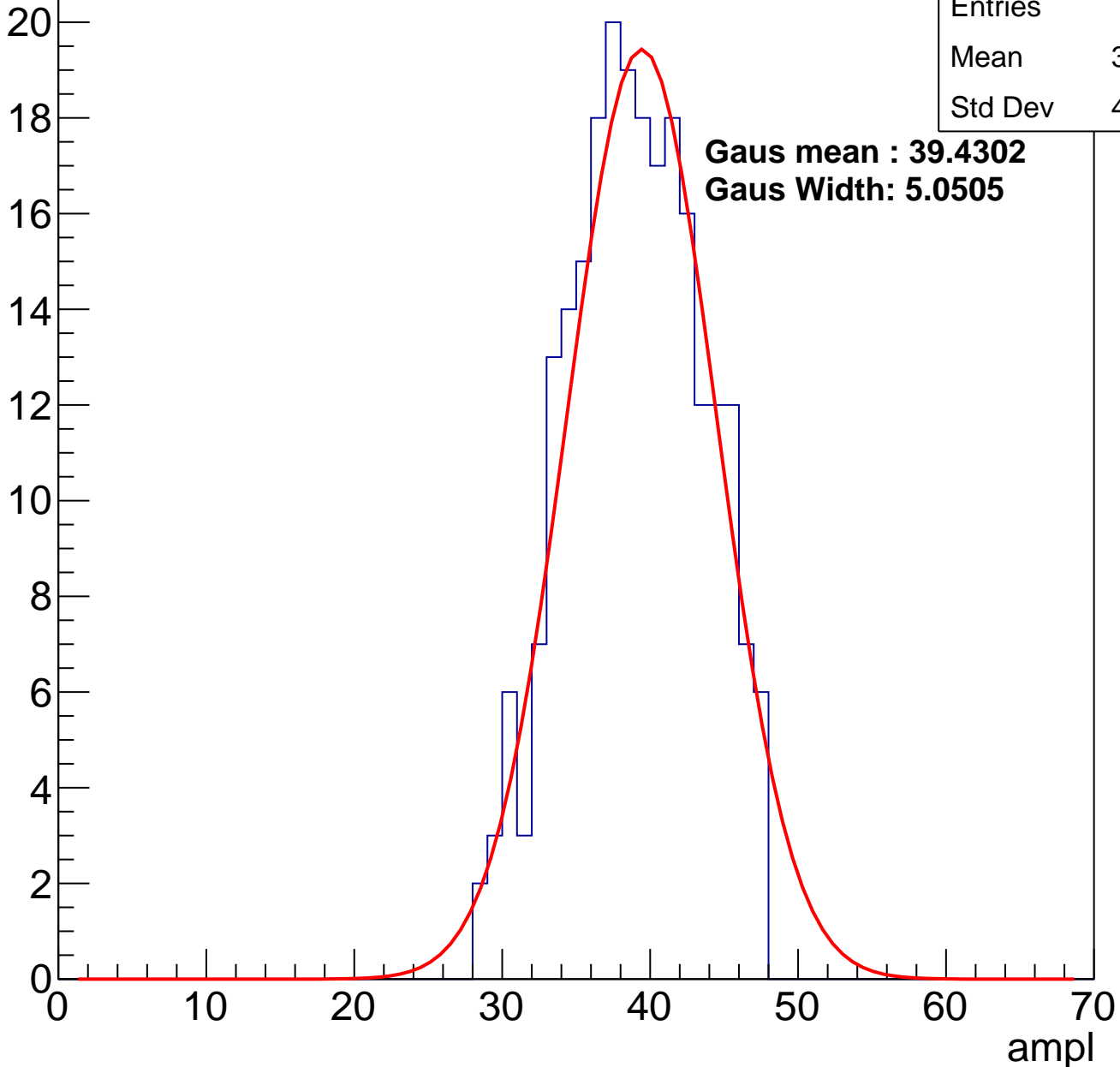
B0L101S, U14-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	238
Mean	38.49
Std Dev	4.445

Gaus mean : 39.4302
Gaus Width: 5.0505

Entry



B0L101S, U14-ch16, adc2

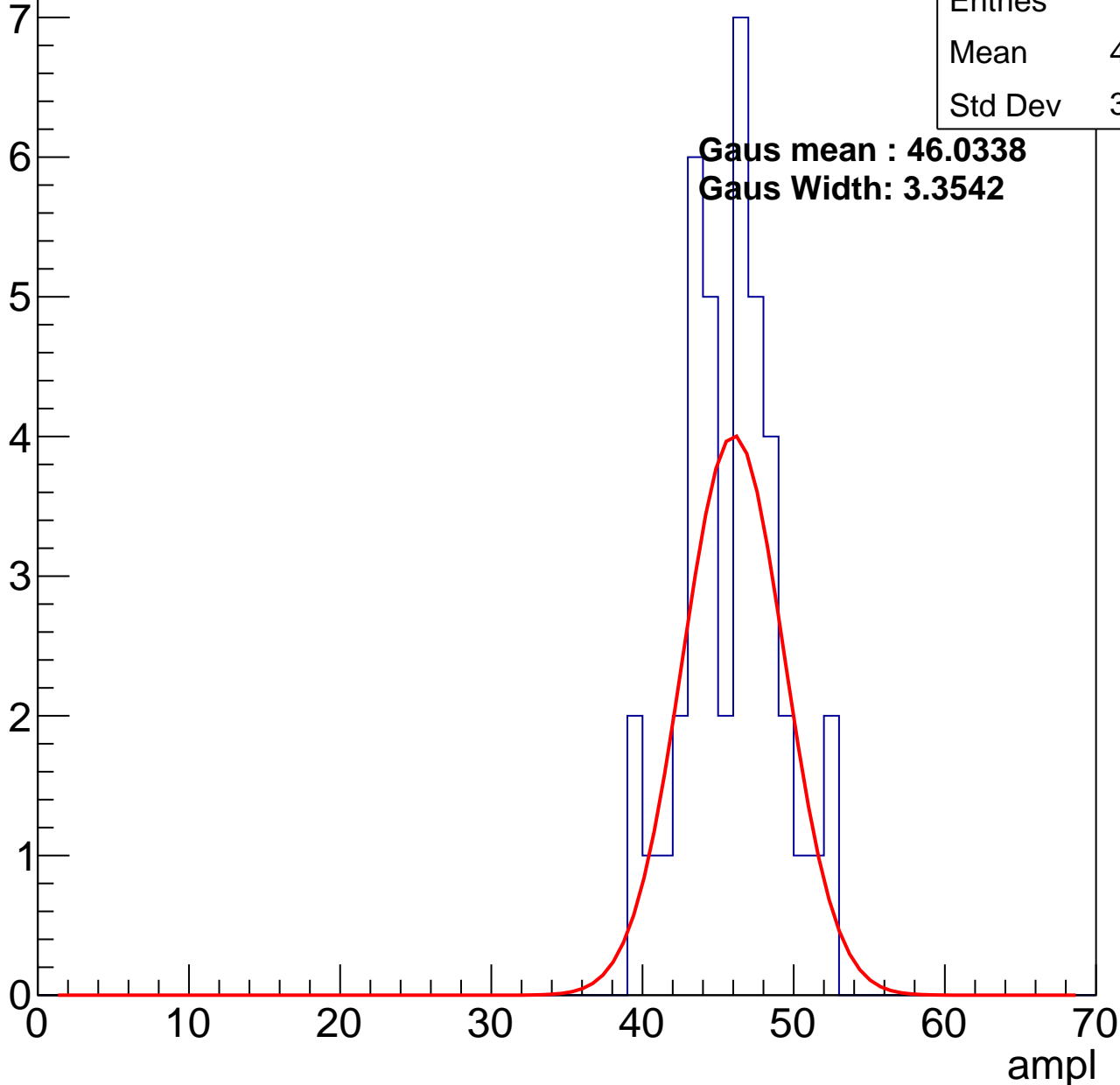
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	41
Mean	45.44
Std Dev	3.147

Gaus mean : 46.0338

Gaus Width: 3.3542

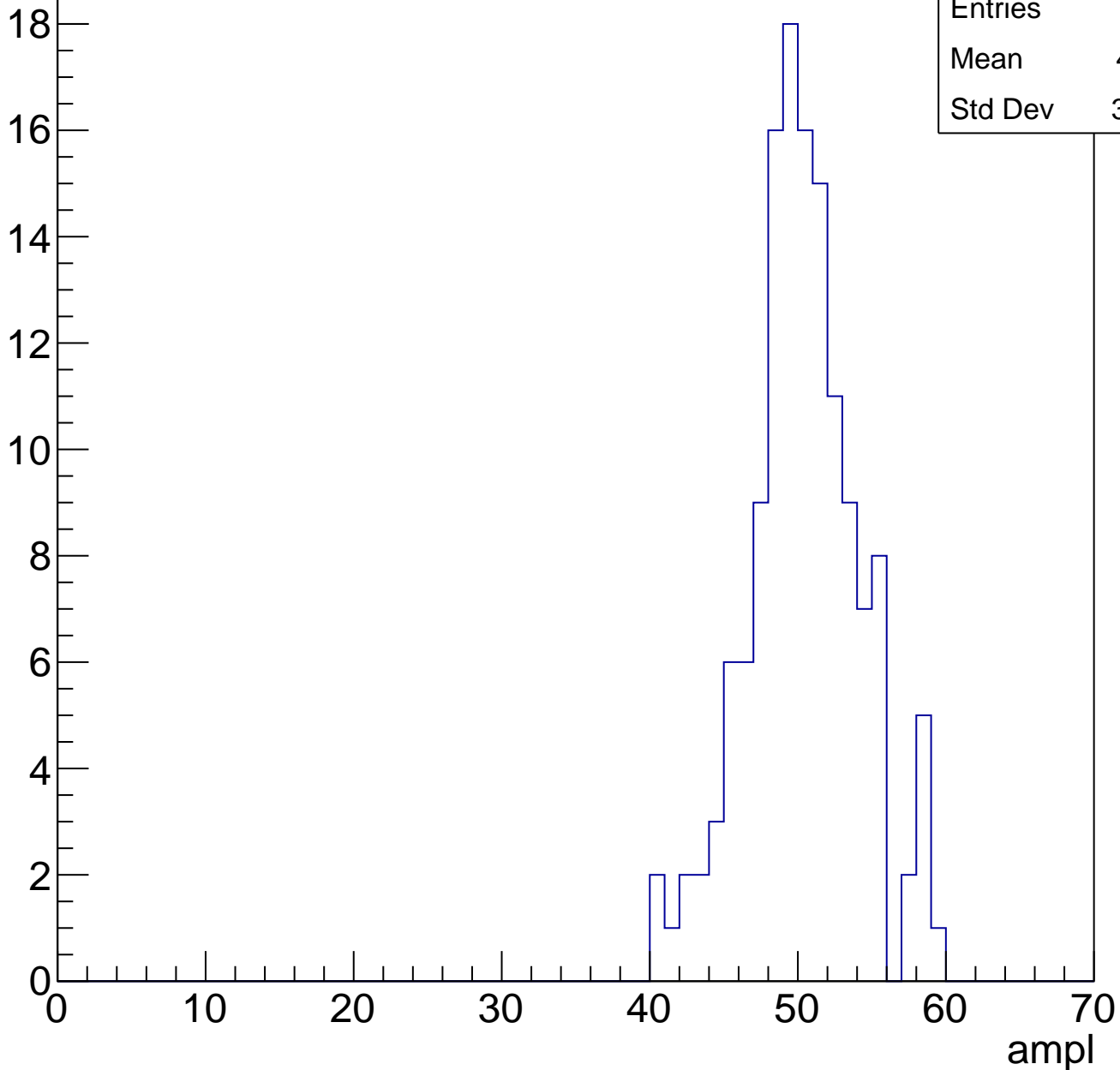


B0L101S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	49.91
Std Dev	3.768

Entry



B0L101S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#1, port C1

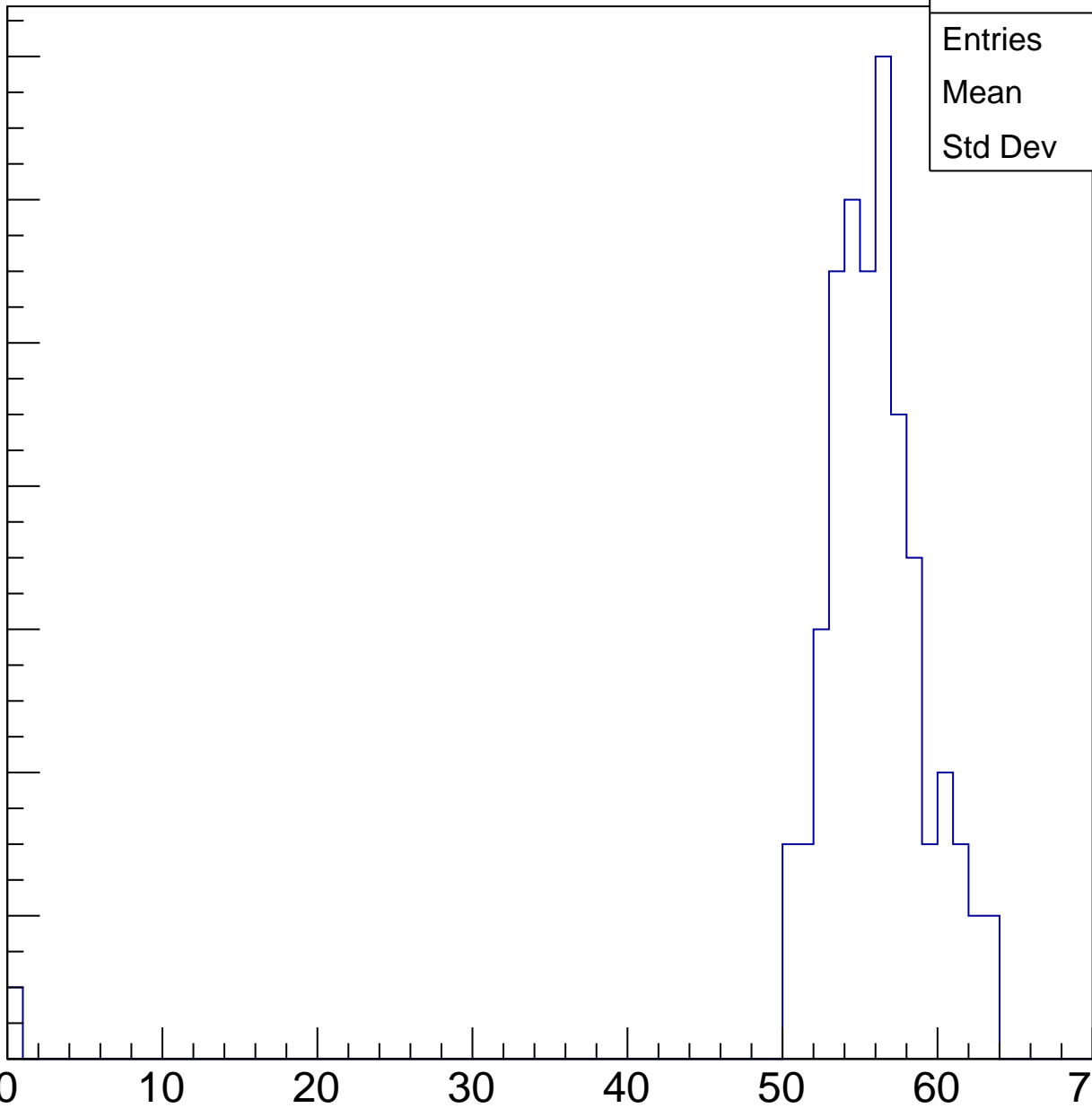
Entries	91
Mean	54.99
Std Dev	6.507

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch16, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	59.14
Std Dev	6.401

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

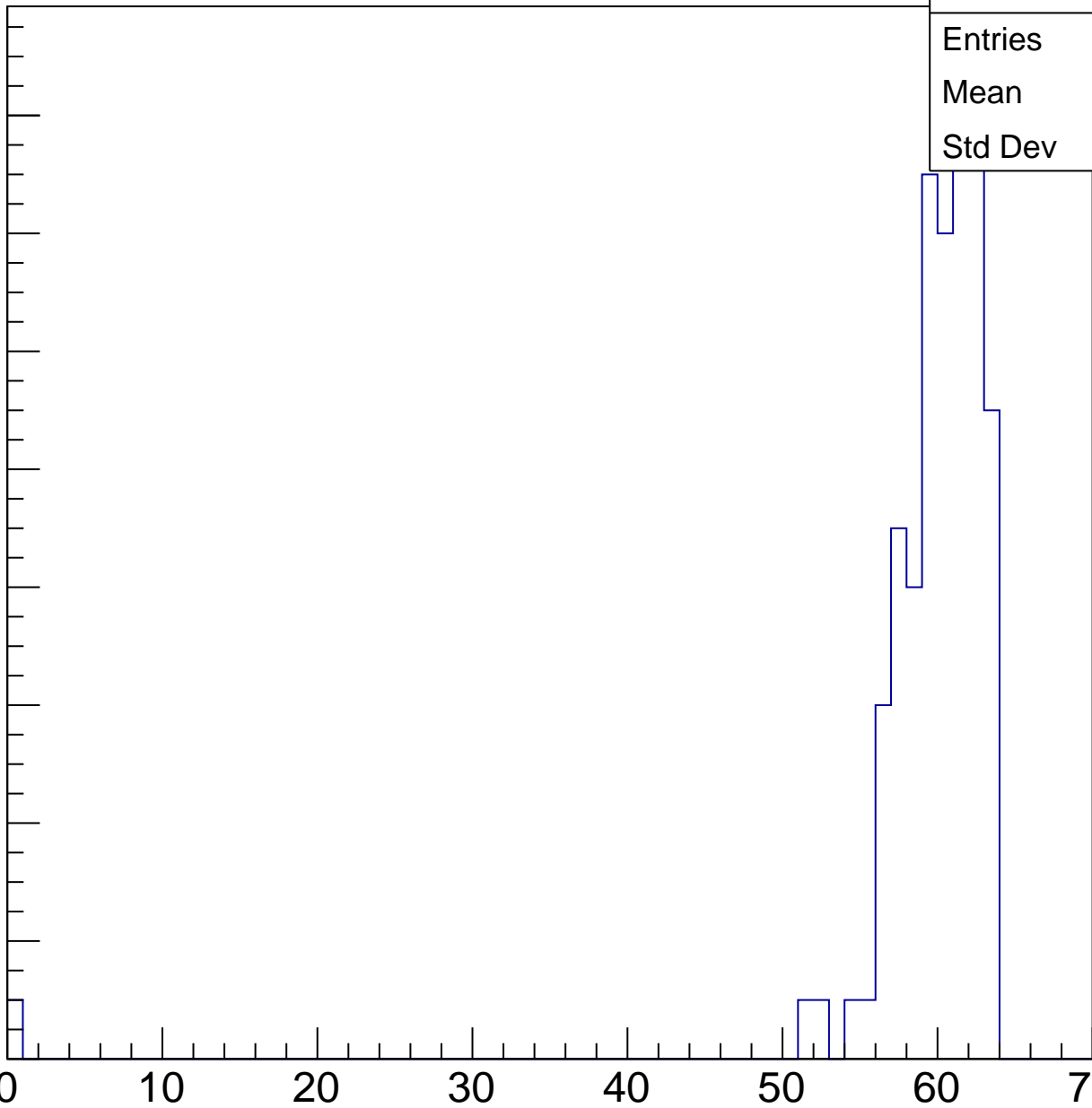
40

50

60

70

ampl

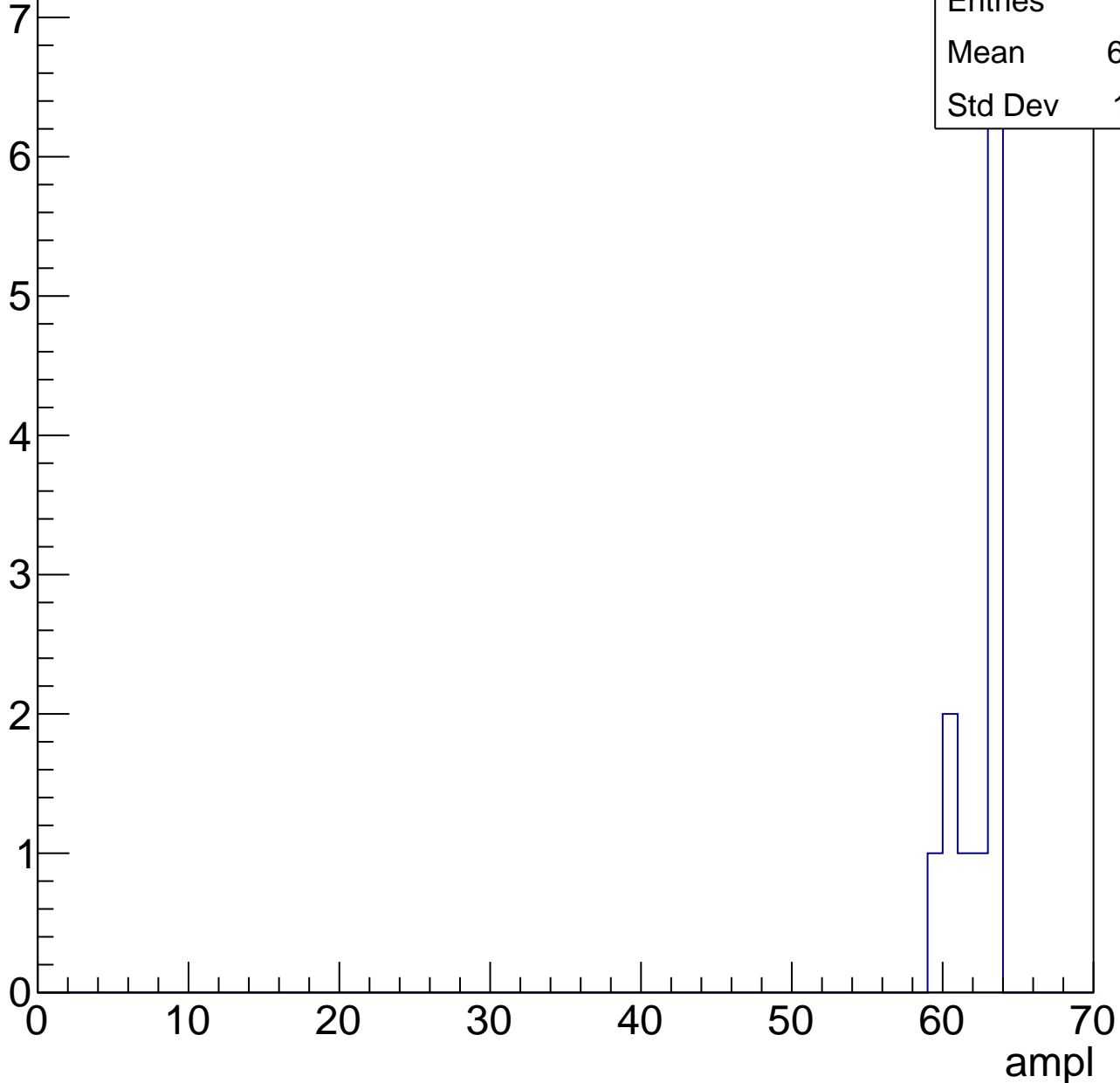


B0L101S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.92
Std Dev	1.441



B0L101S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch17, adc0

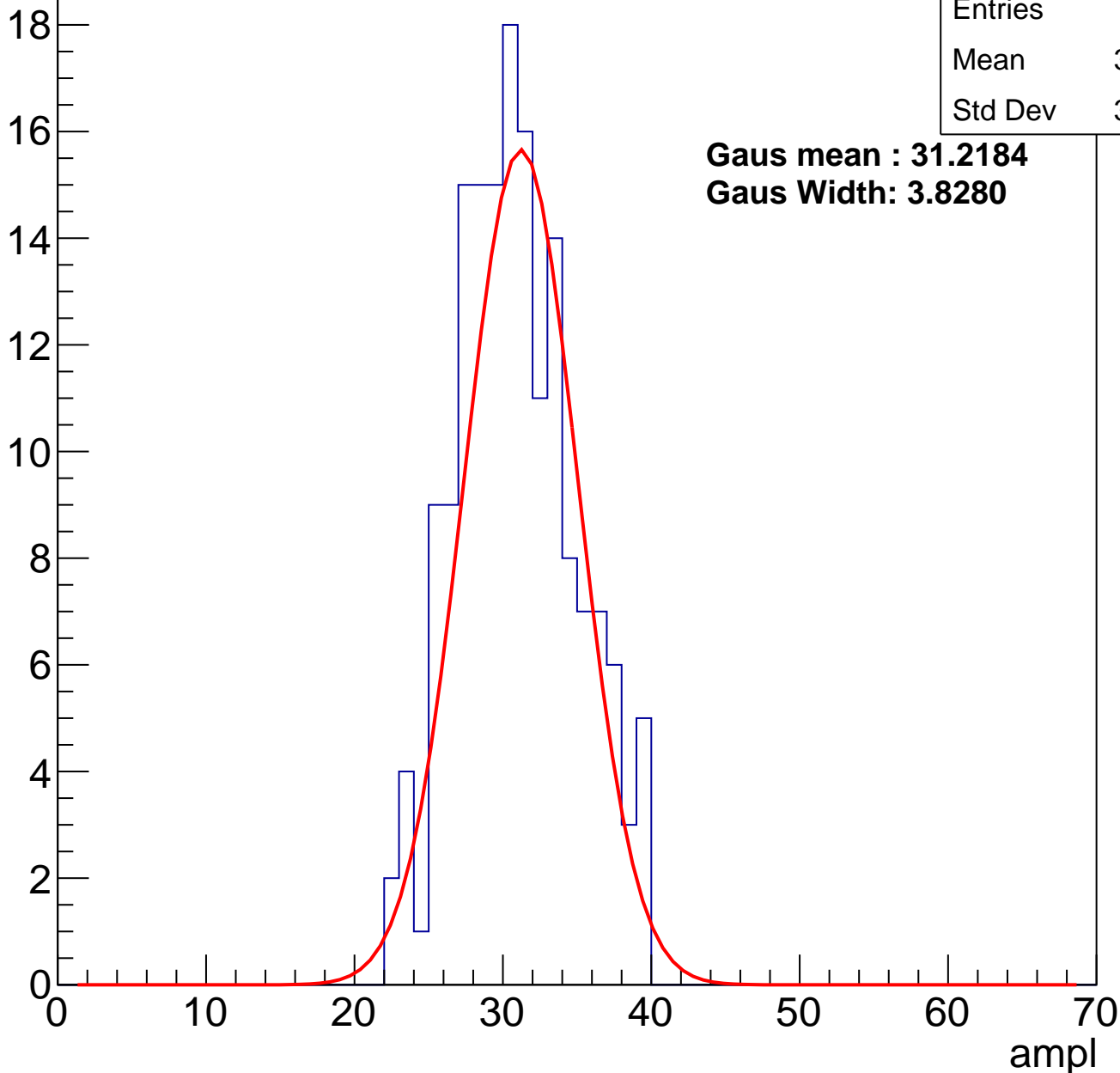
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	30.48
Std Dev	3.918

Gaus mean : 31.2184

Gaus Width: 3.8280

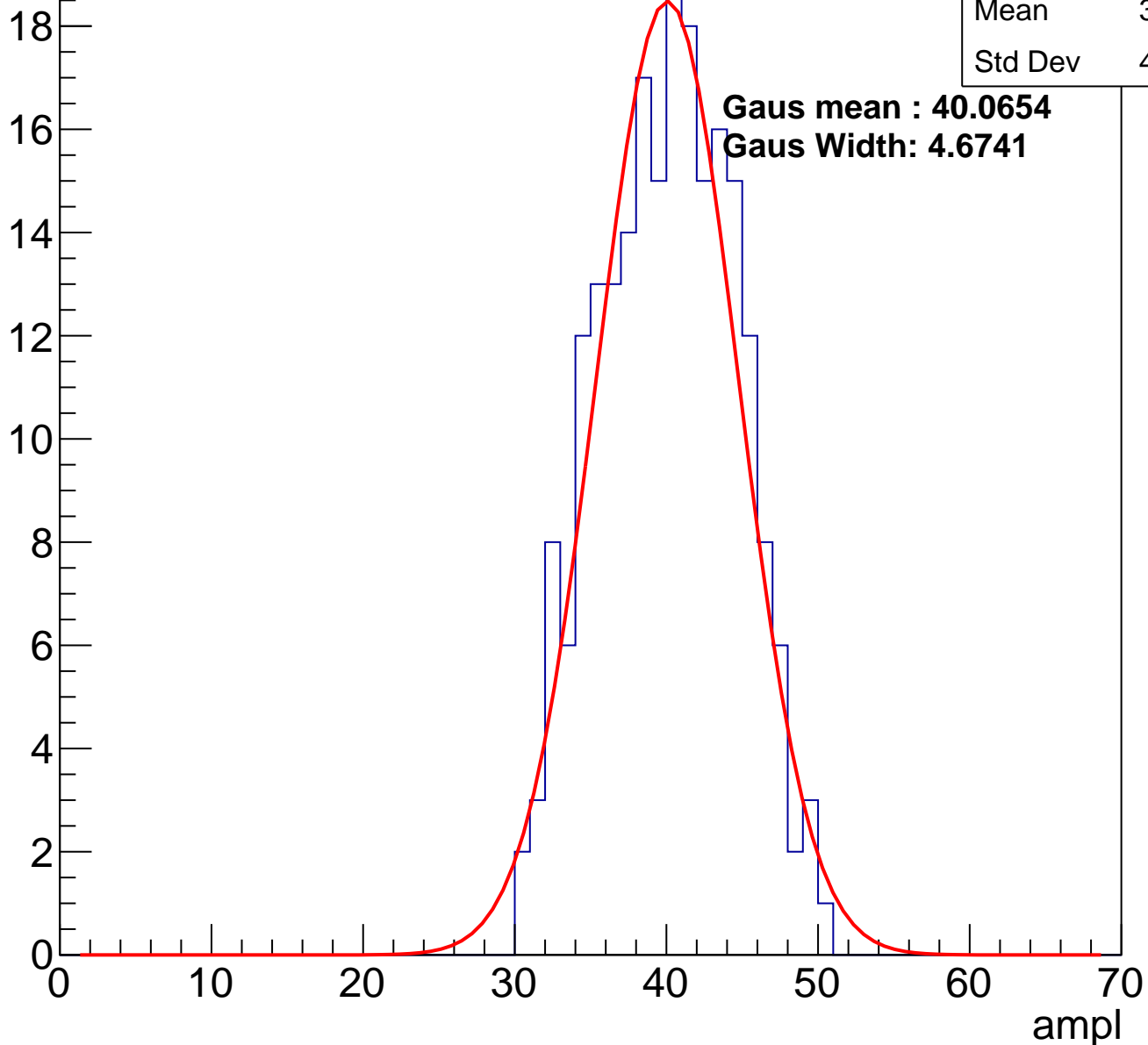
Entry



B0L101S, U14-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch17, adc2

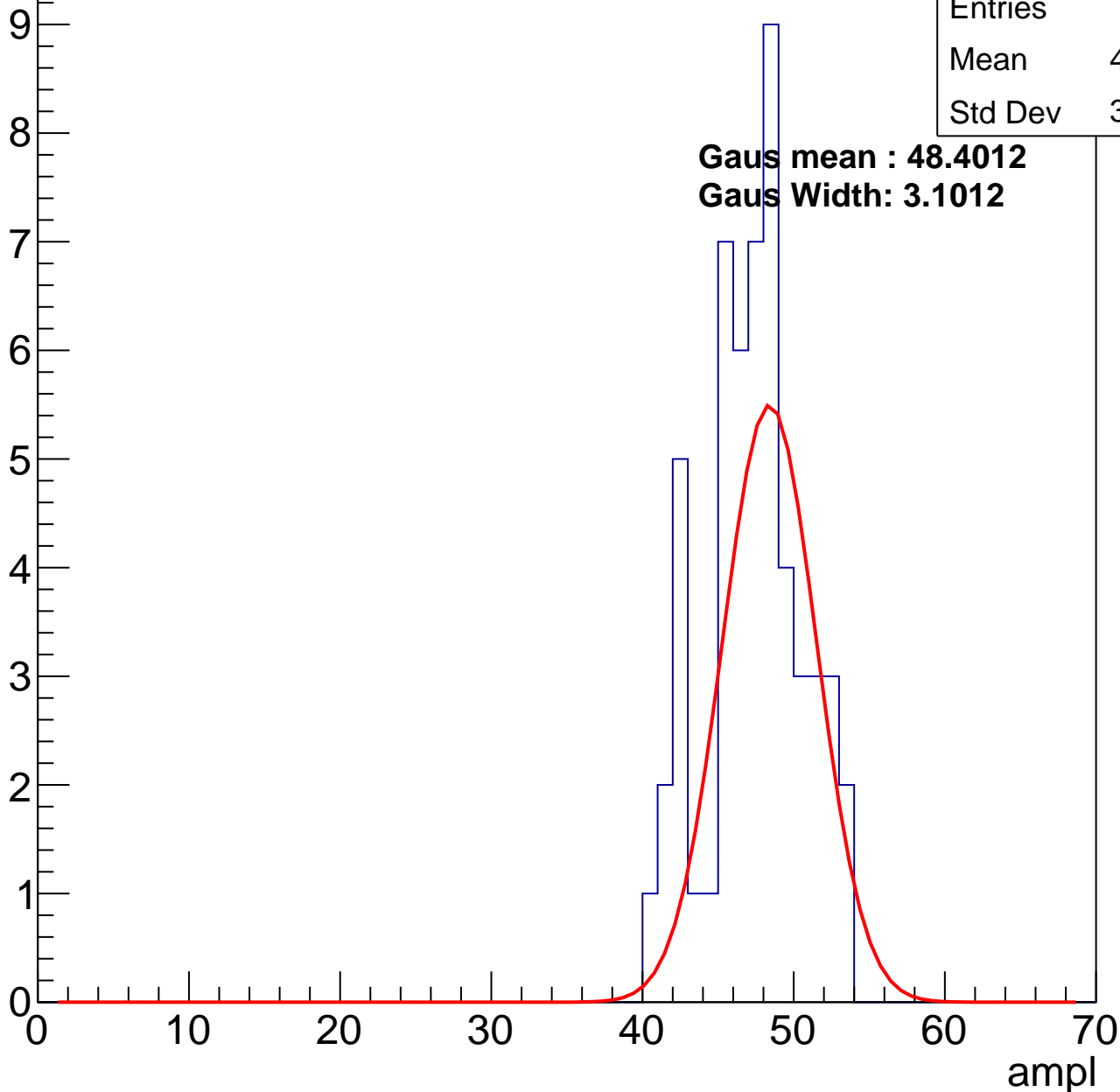
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	46.89
Std Dev	3.195

Gaus mean : 48.4012

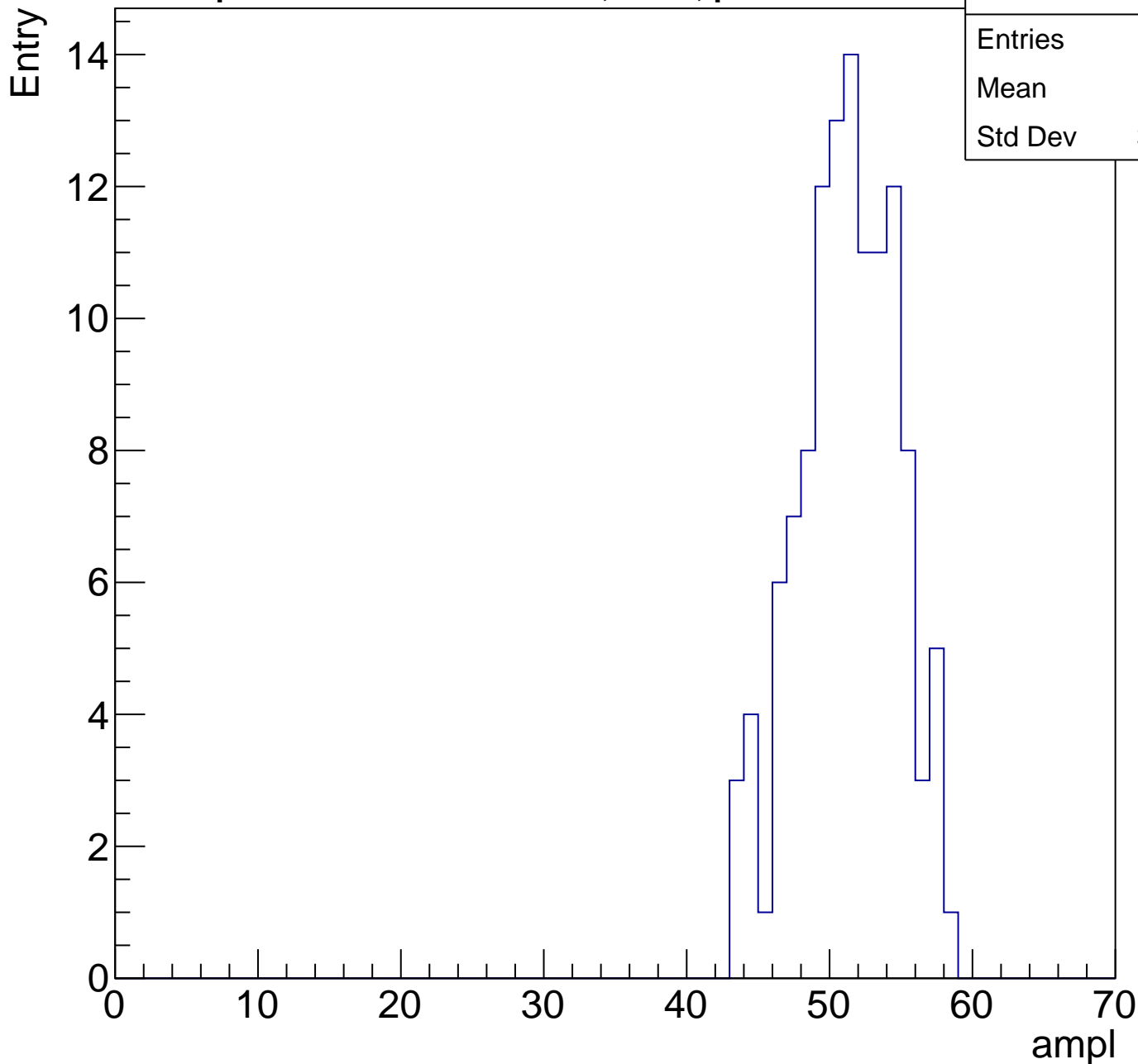
Gaus Width: 3.1012



B0L101S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	50.8
Std Dev	3.441



B0L101S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

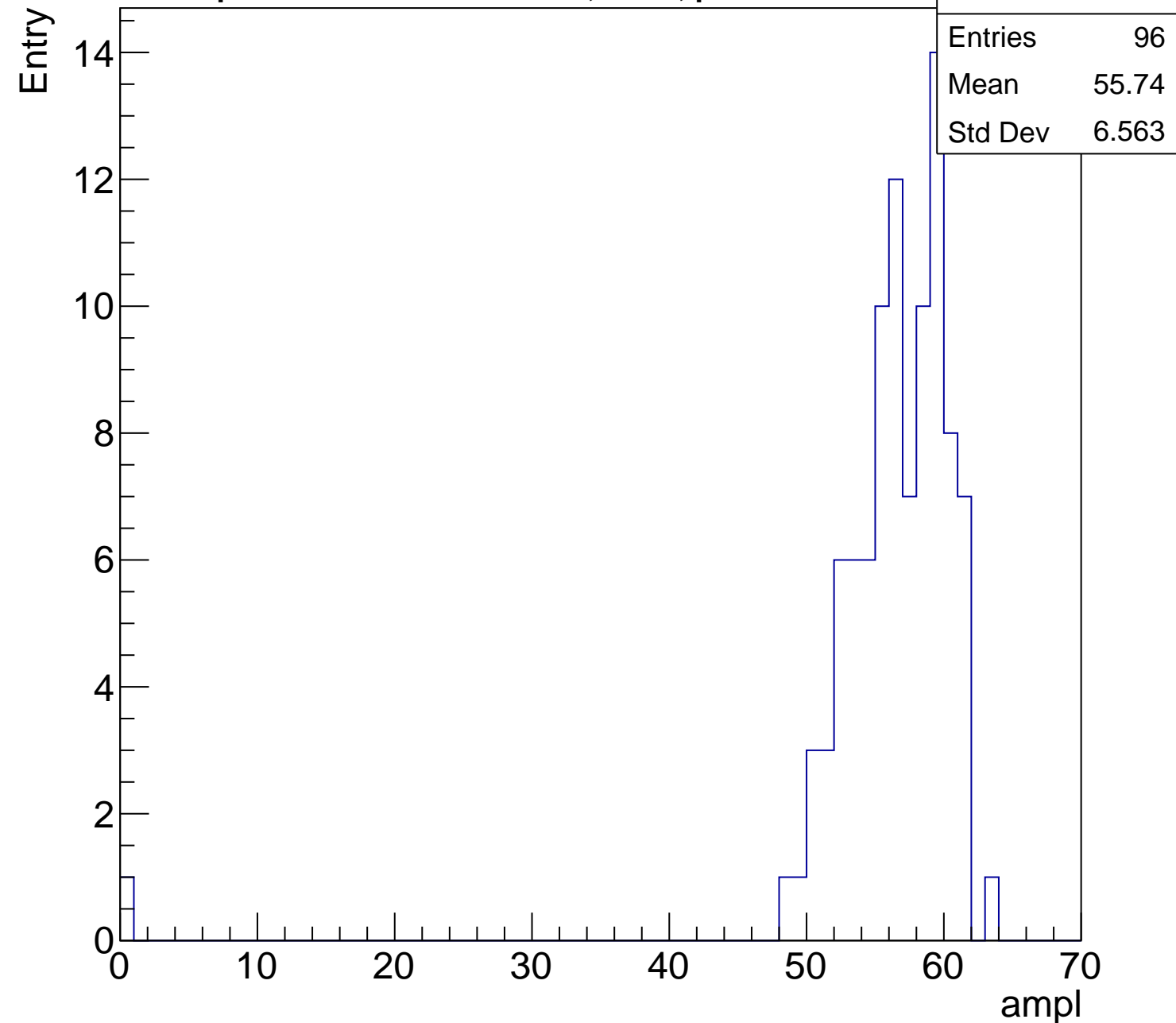
Entries	96
Mean	55.74
Std Dev	6.563

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch17, adc5

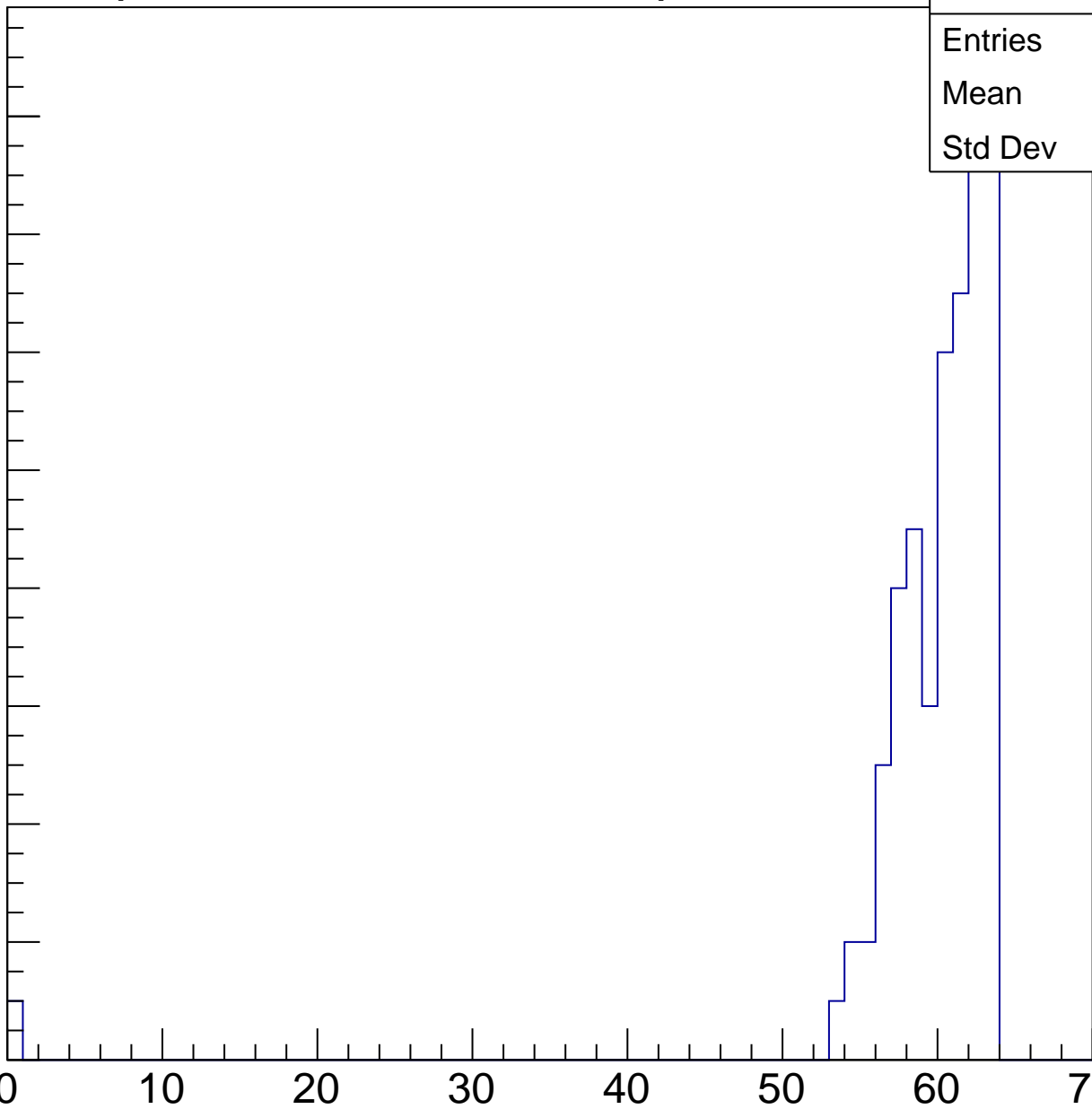
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.37
Std Dev	6.689

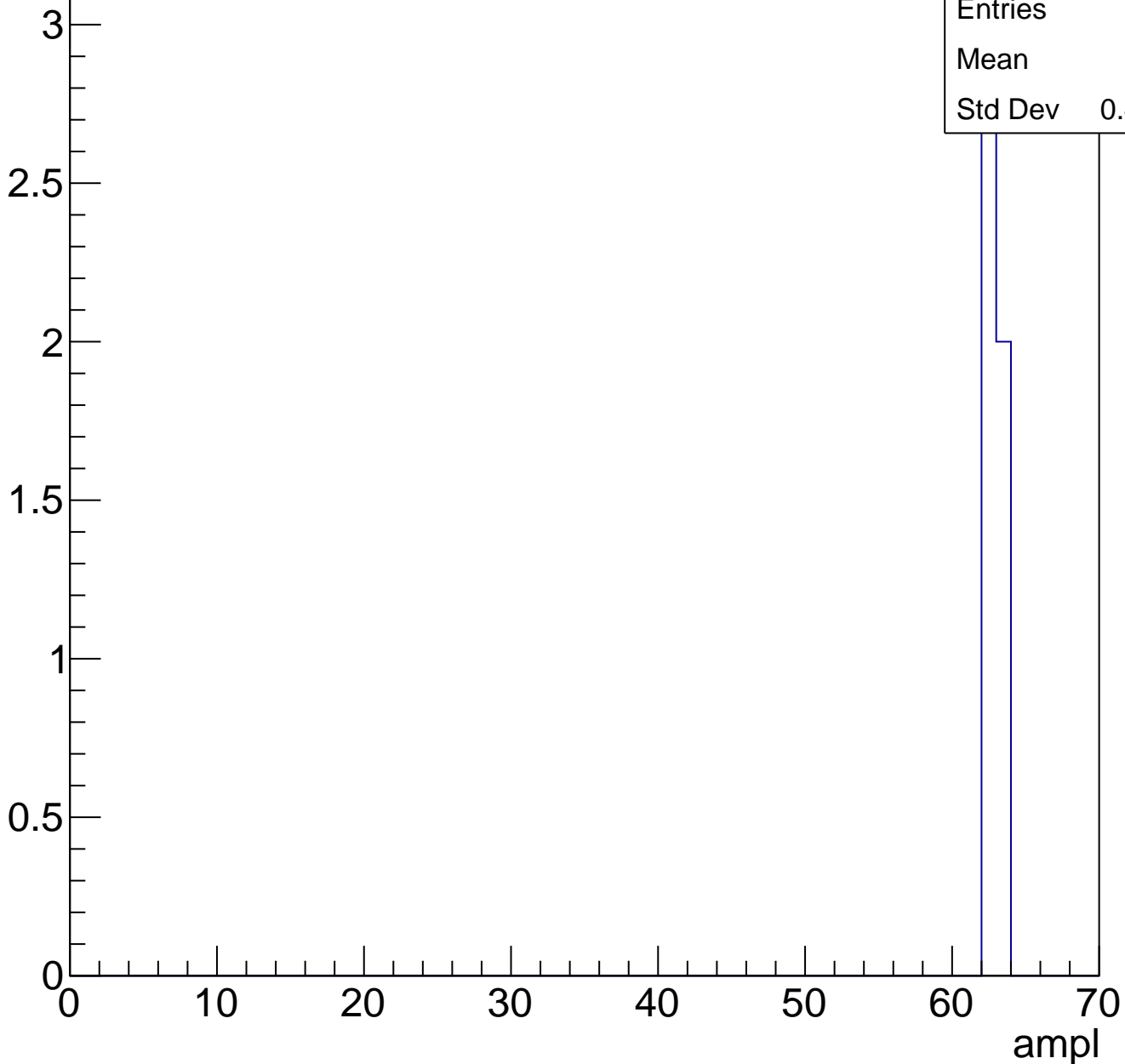
ampl



B0L101S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L101S, U14-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

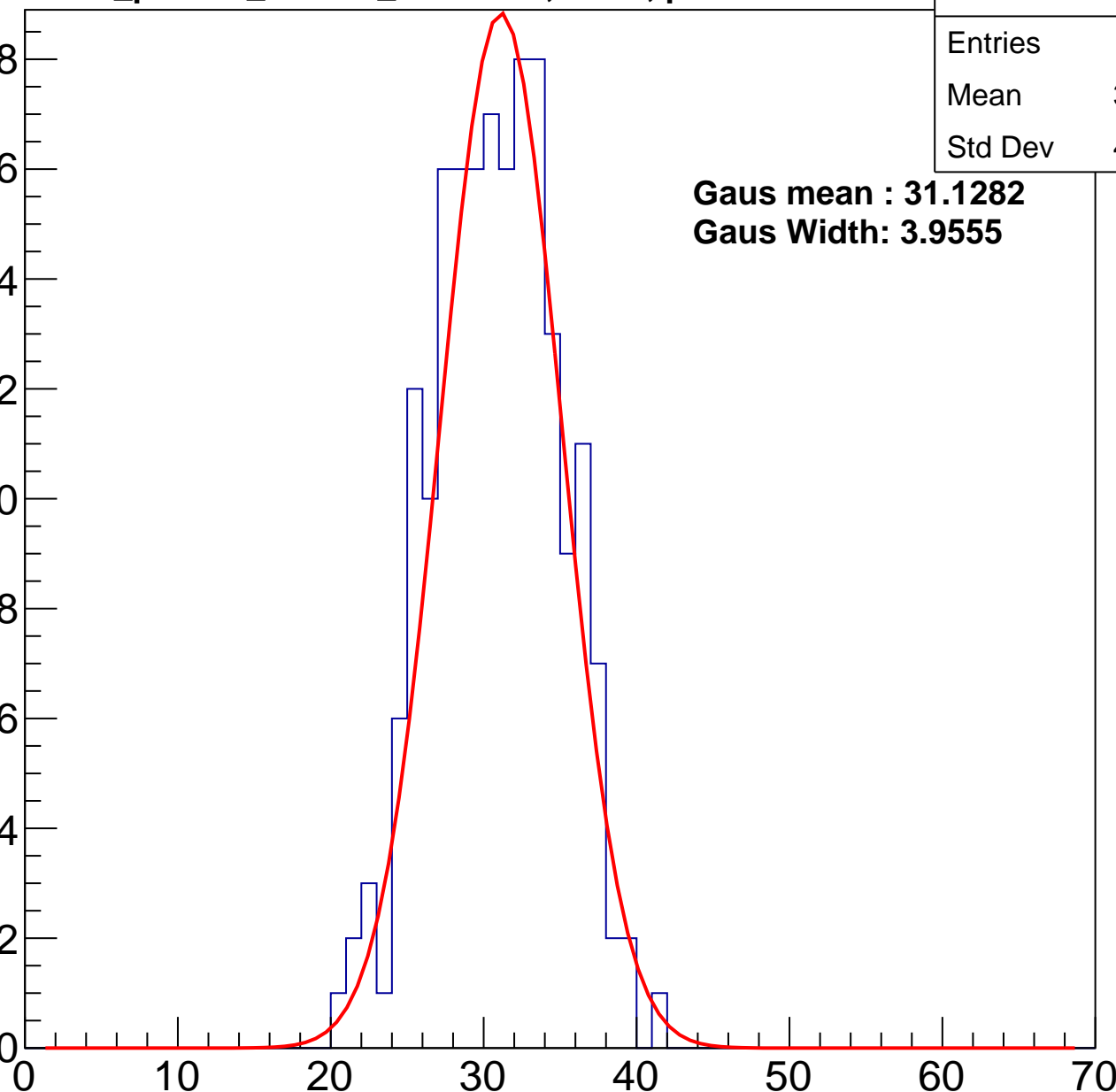
Entry

18
16
14
12
10
8
6
4
2
0

Entries	197
Mean	30.37
Std Dev	4.036

Gaus mean : 31.1282

Gaus Width: 3.9555



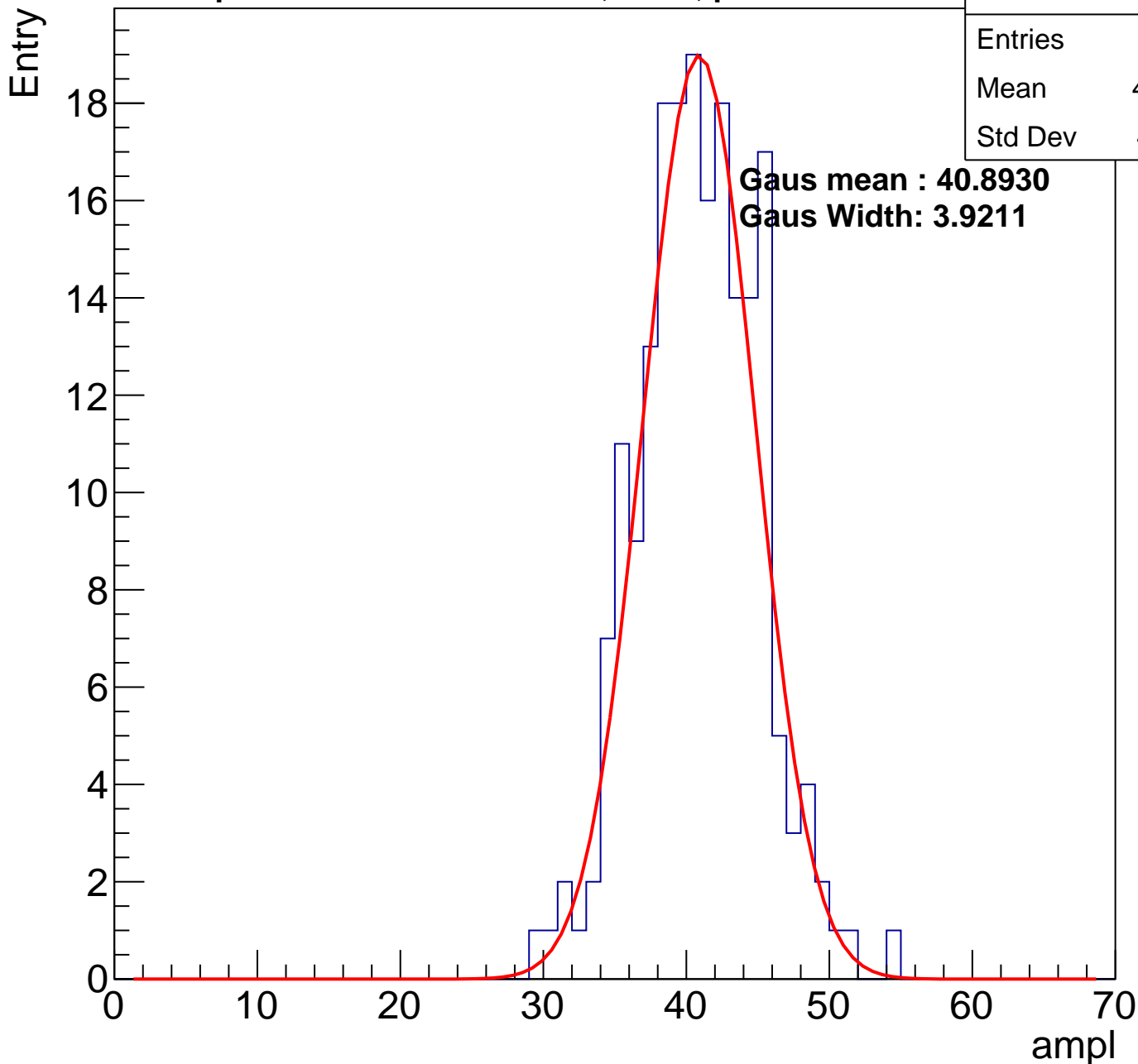
ampl

B0L101S, U14-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	198
Mean	40.42
Std Dev	4.151

Gaus mean : 40.8930
Gaus Width: 3.9211



B0L101S, U14-ch18, adc2

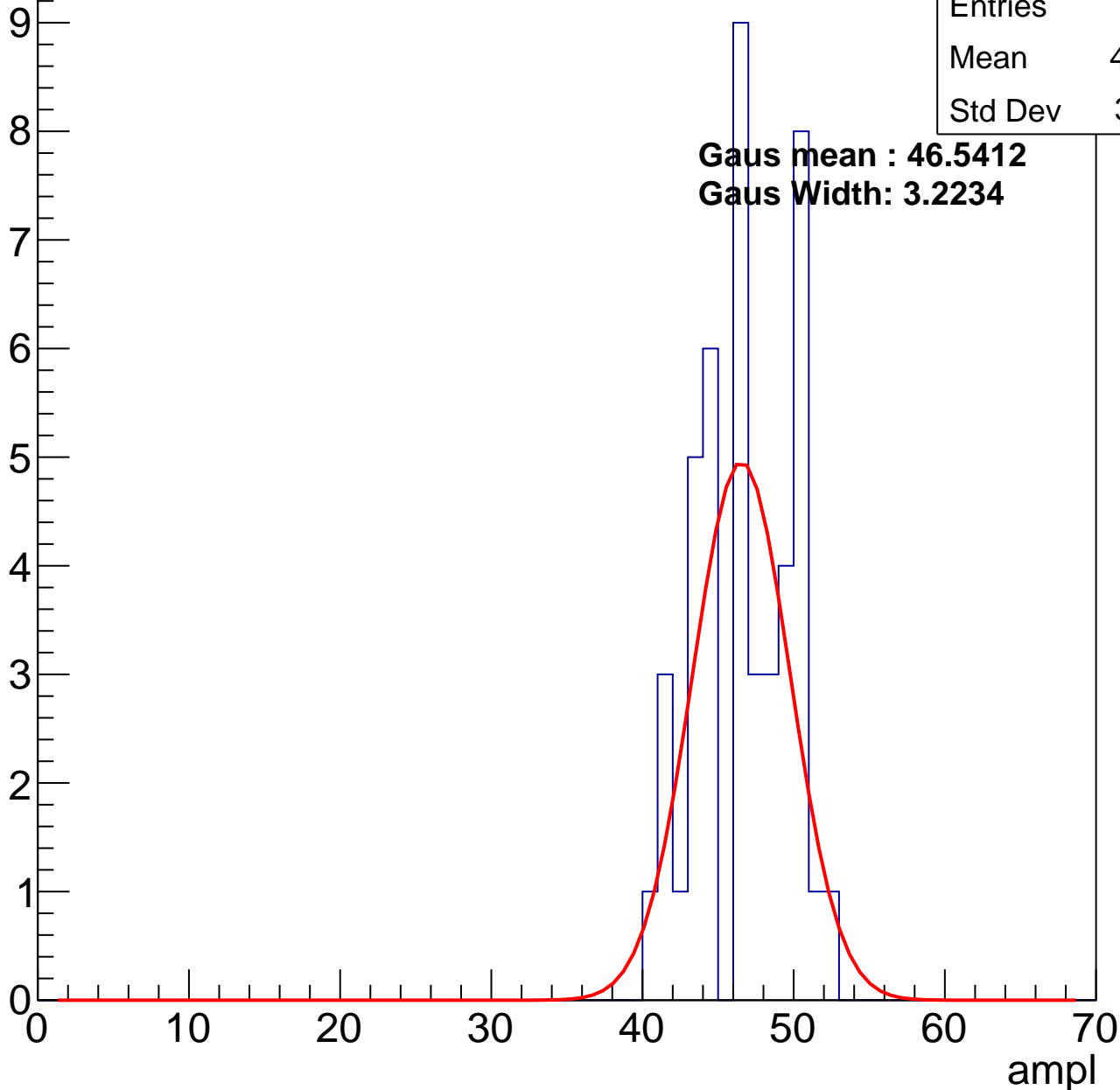
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	45
Mean	46.27
Std Dev	3.101

Gaus mean : 46.5412

Gaus Width: 3.2234

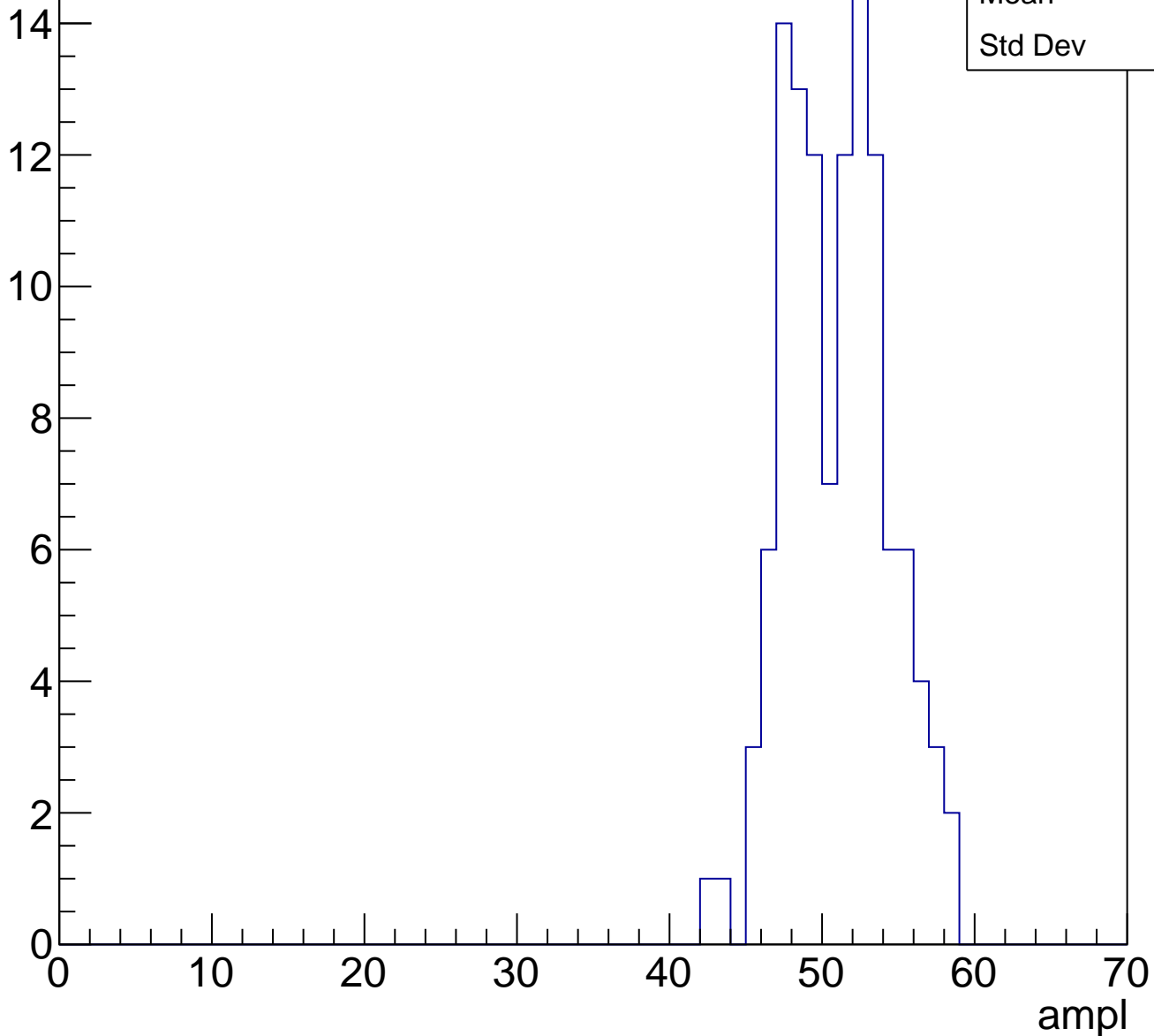


B0L101S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	50.5
Std Dev	3.32

Entry



B0L101S, U14-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	55.82
Std Dev	6.242

Entry

12

10

8

6

4

2

0

0

10

20

30

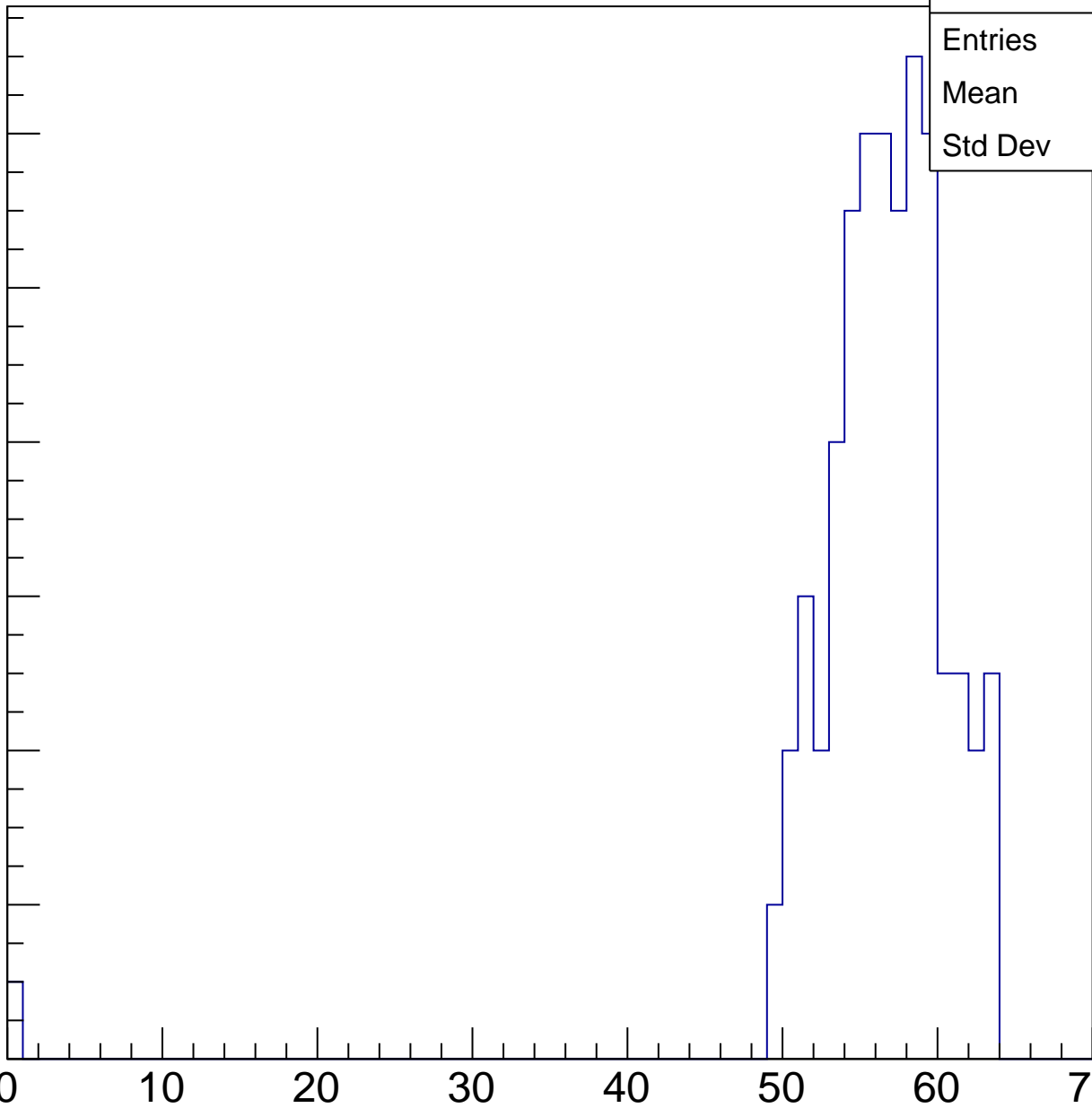
40

50

60

70

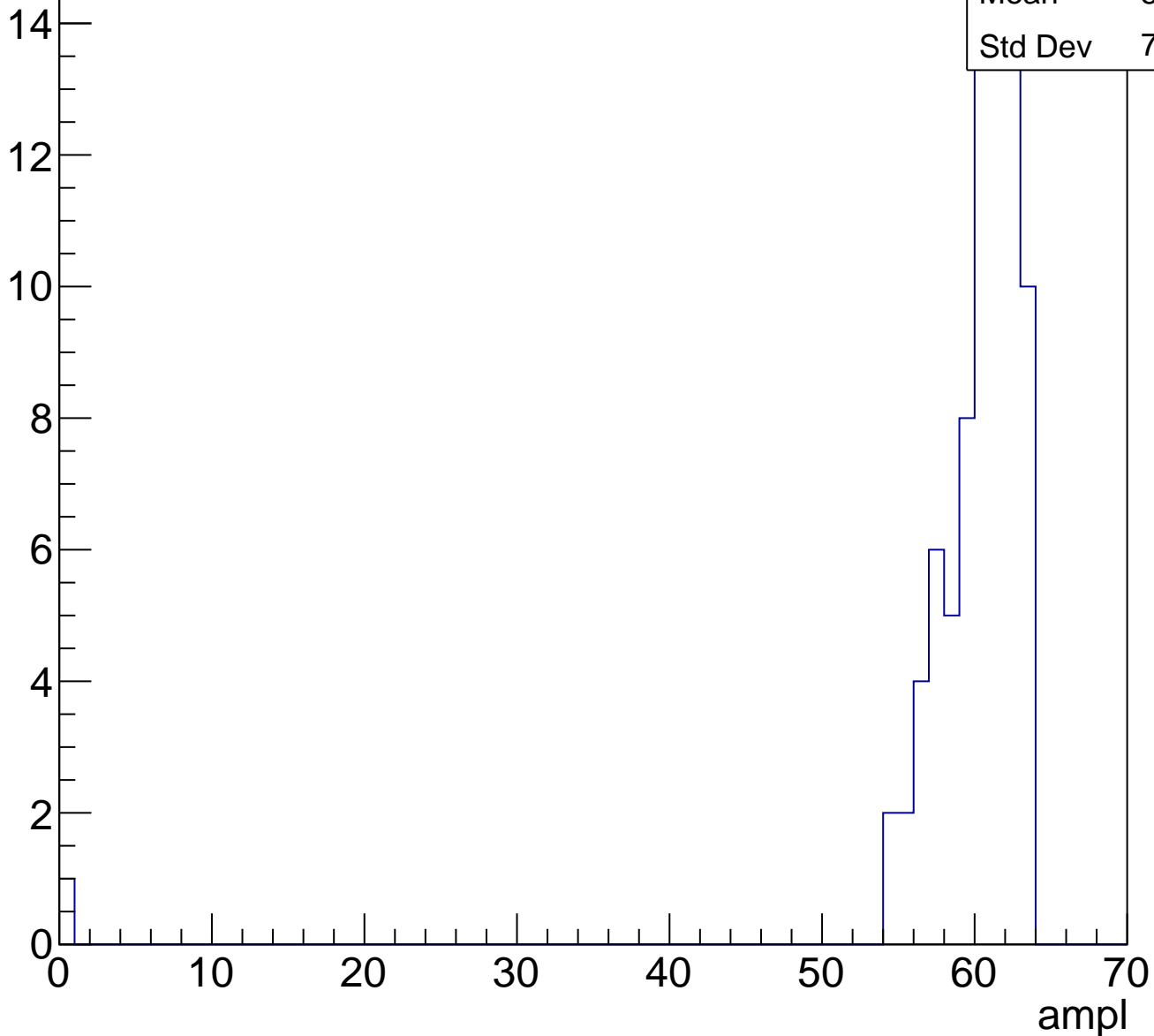
ampl



B0L101S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

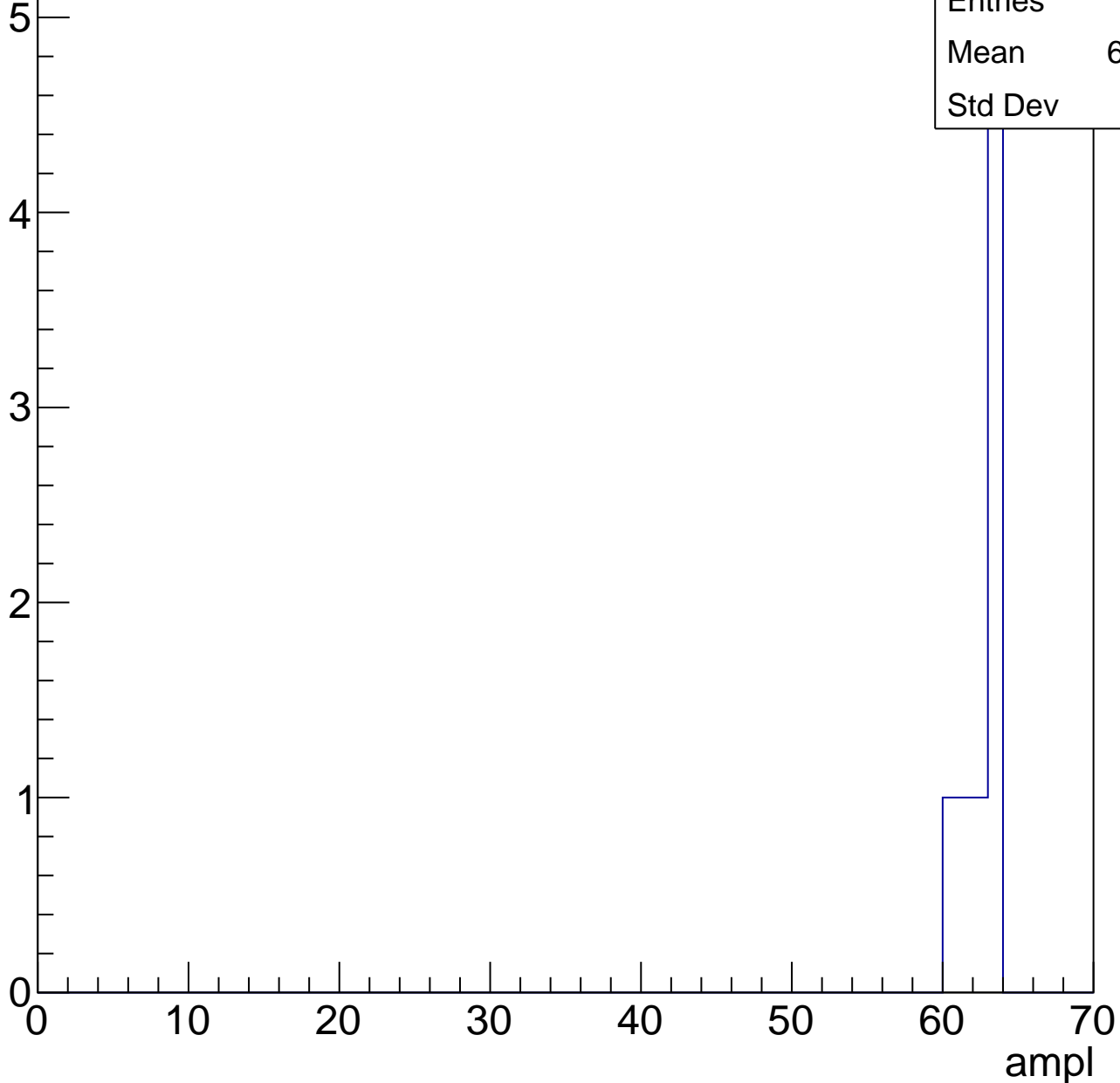


B0L101S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

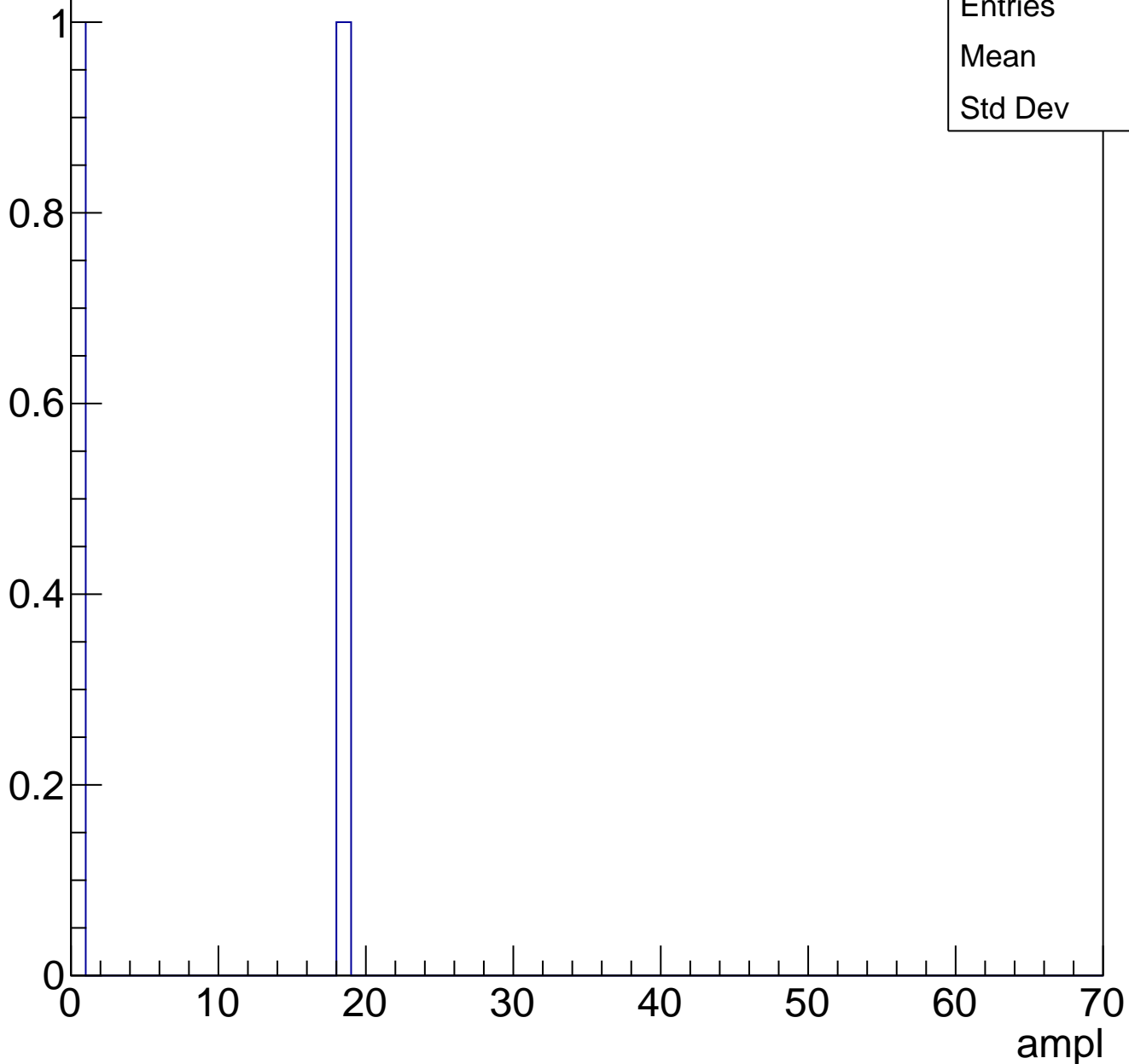
Entries	8
Mean	62.25
Std Dev	1.09



B0L101S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9
Std Dev	9

B0L101S, U14-ch19, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	29.68
Std Dev	4.203

Gaus mean : 30.5492

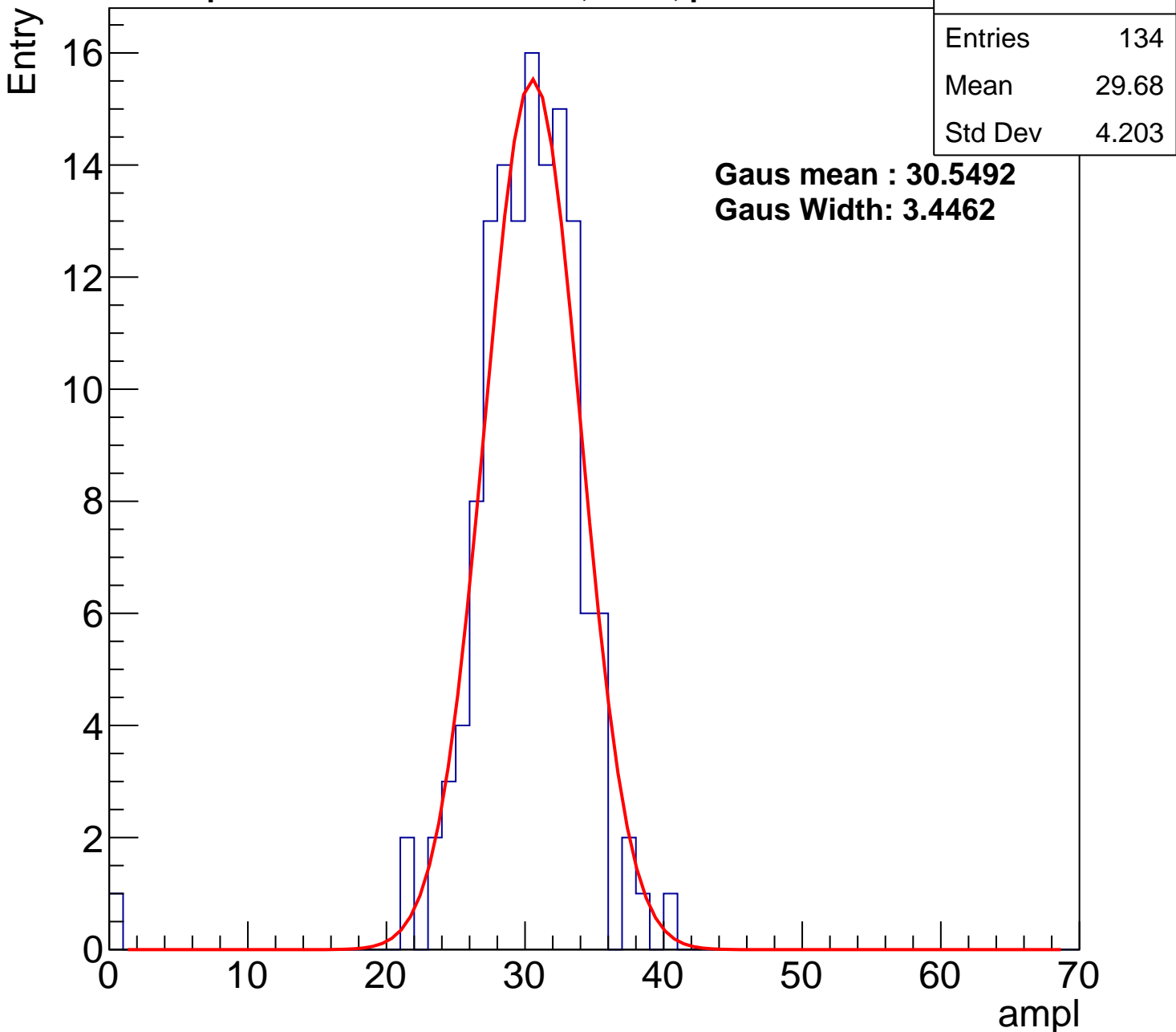
Gaus Width: 3.4462

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch19, adc1

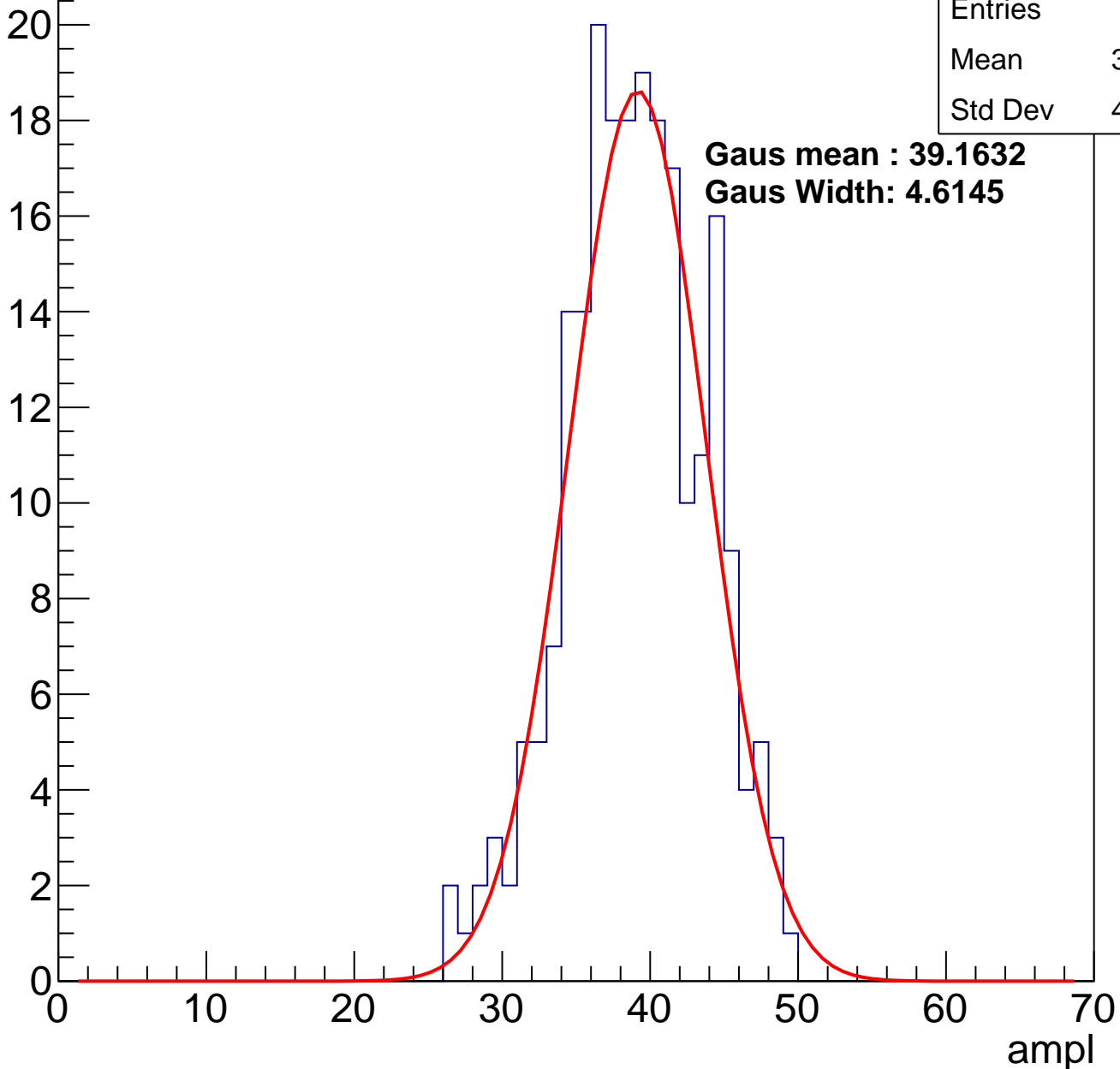
calib_packv5_042523_0143.root, FC#1, port C1

Entries	224
Mean	38.55
Std Dev	4.599

Gaus mean : 39.1632

Gaus Width: 4.6145

Entry



B0L101S, U14-ch19, adc2

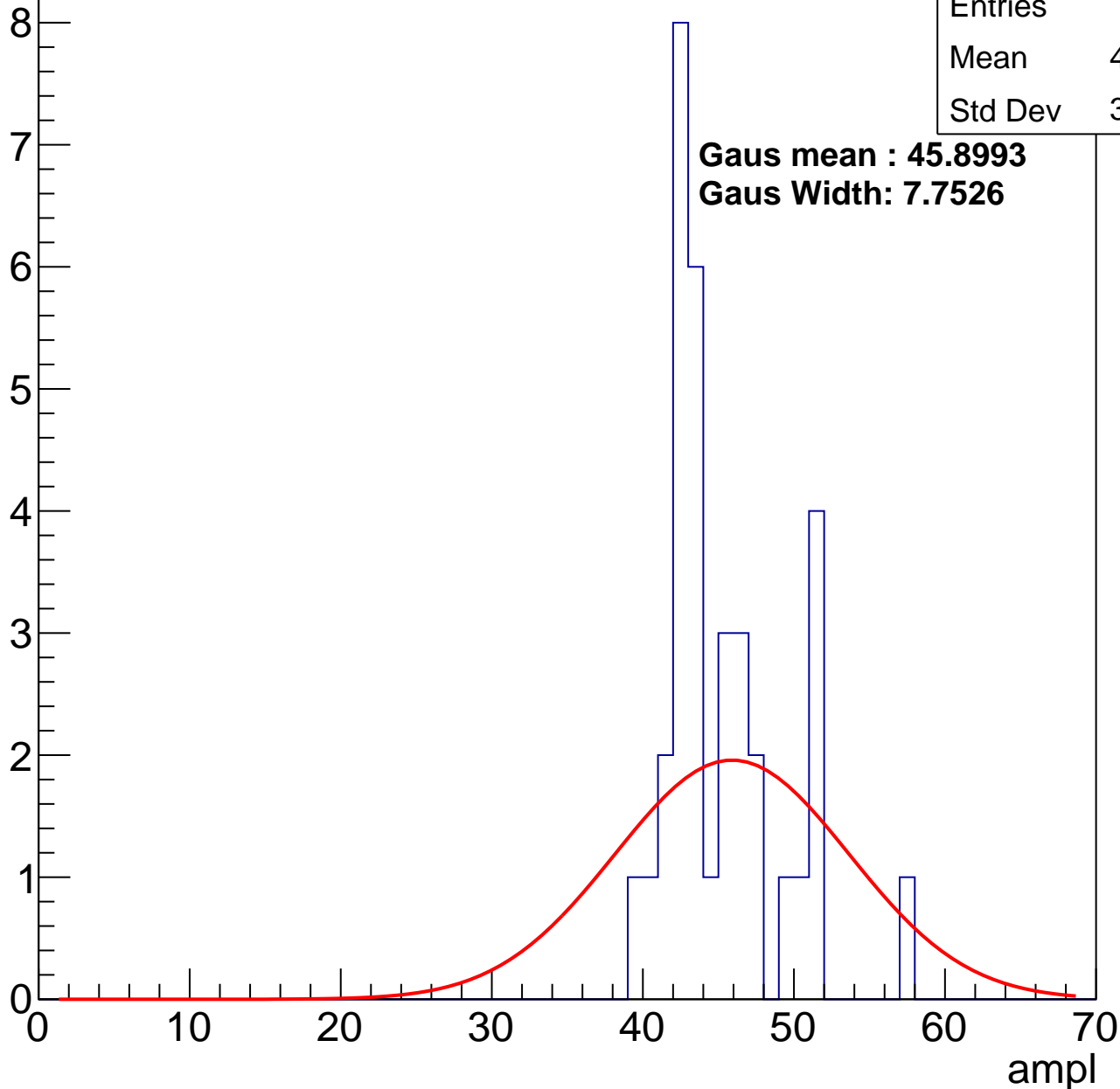
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	34
Mean	44.88
Std Dev	3.946

Gaus mean : 45.8993

Gaus Width: 7.7526



B0L101S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

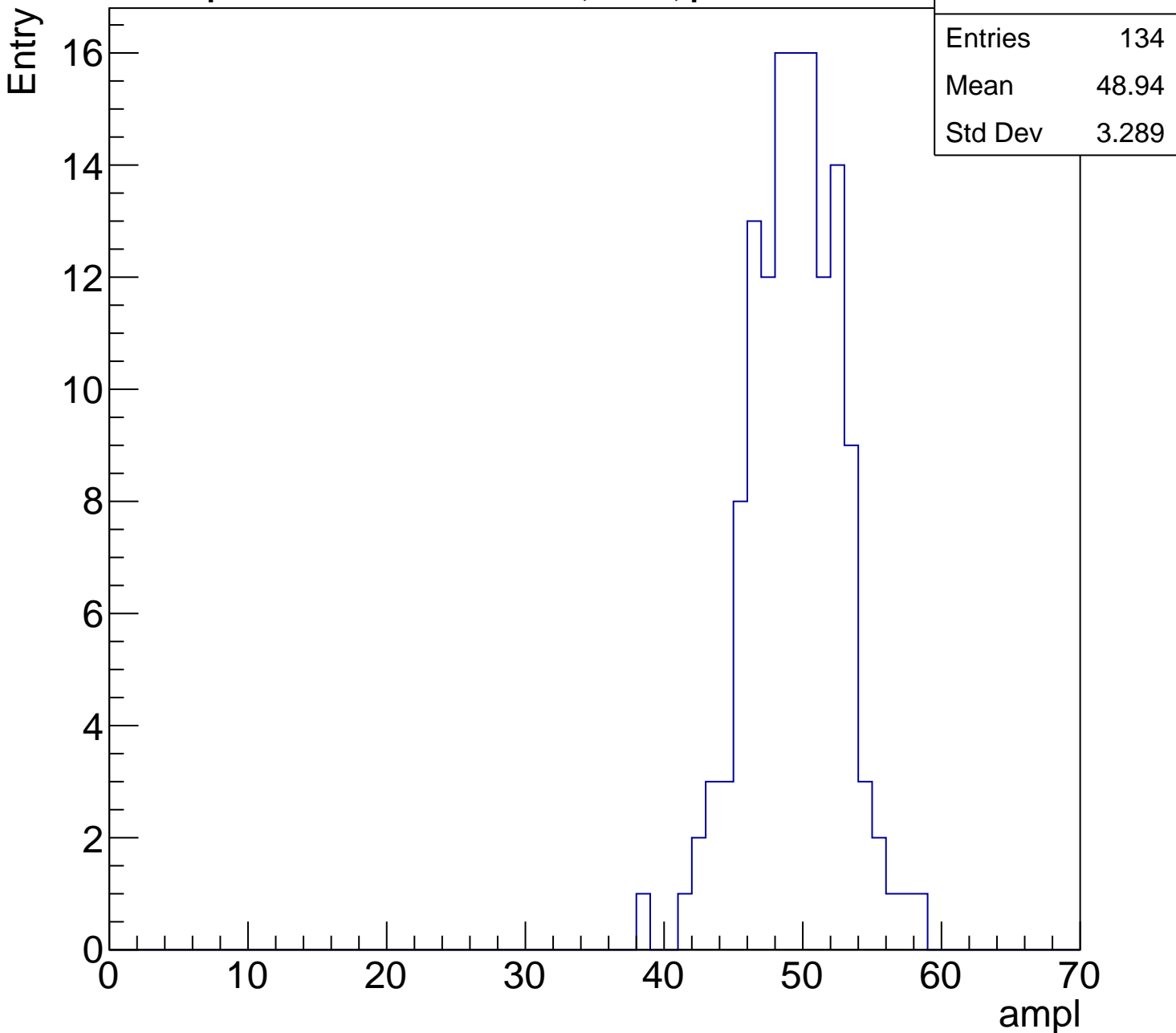
Entries	134
Mean	48.94
Std Dev	3.289

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

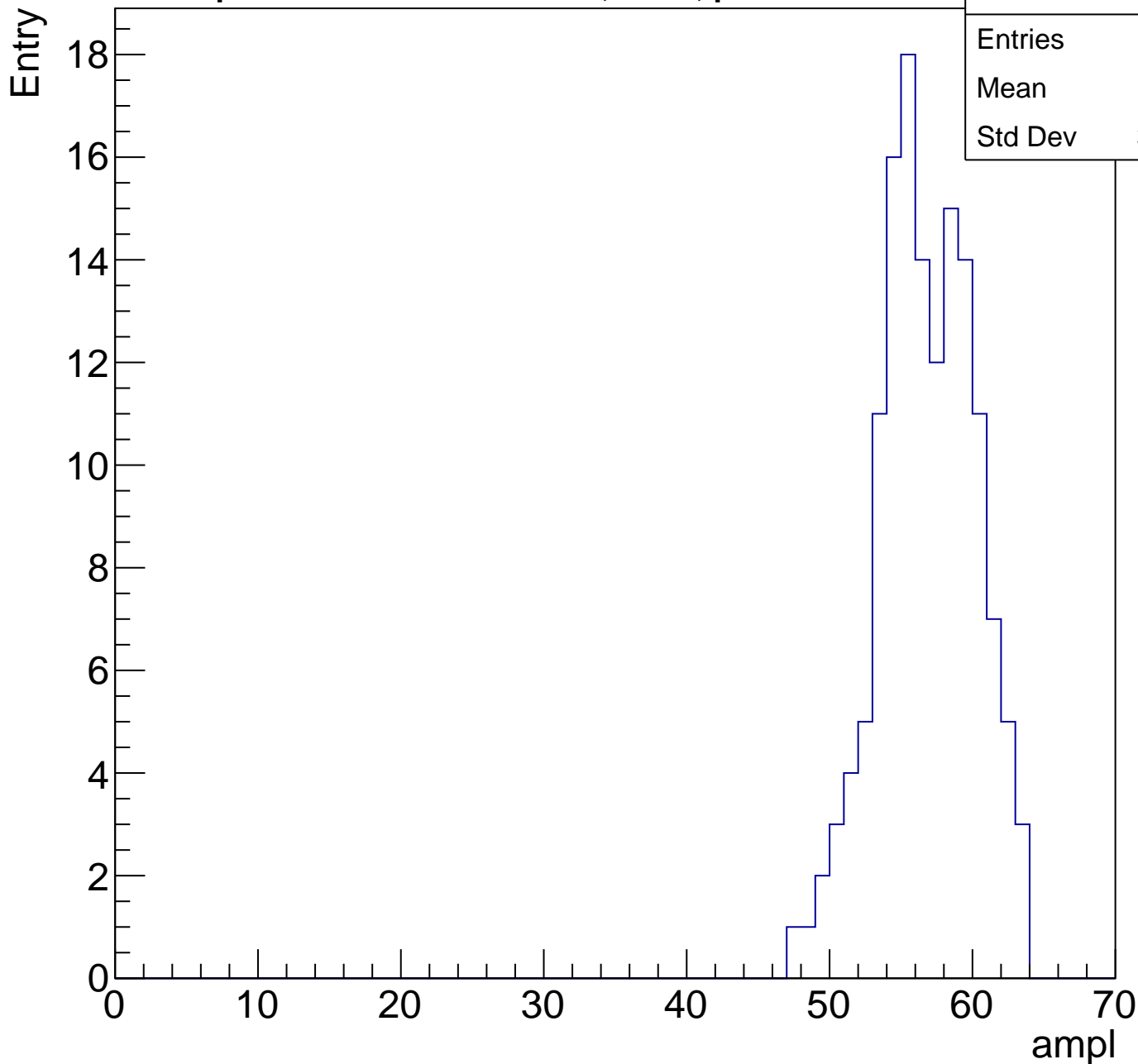
ampl



B0L101S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	56.3
Std Dev	3.331



B0L101S, U14-ch19, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

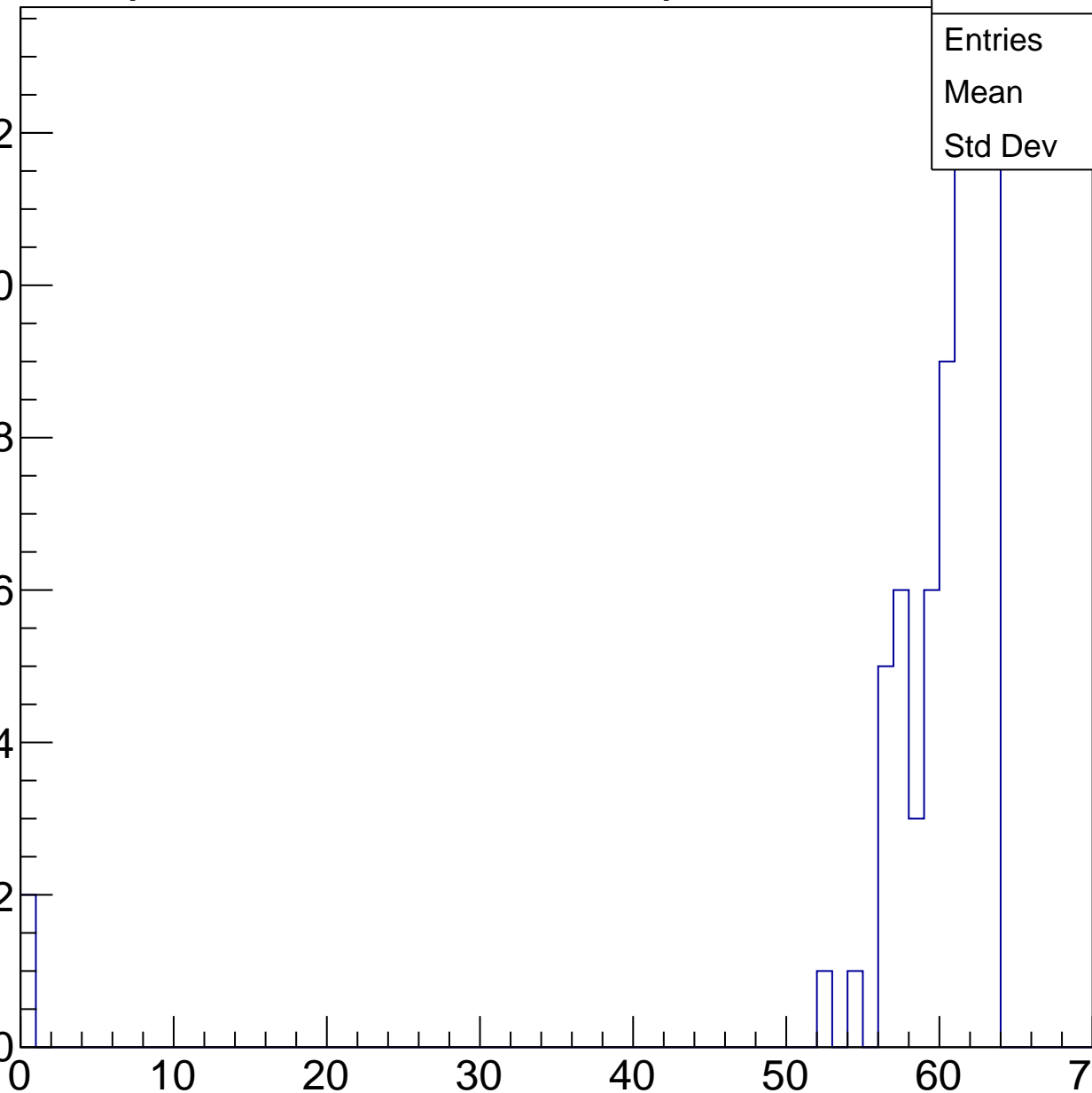
4

2

0

Entries	70
Mean	58.44
Std Dev	10.32

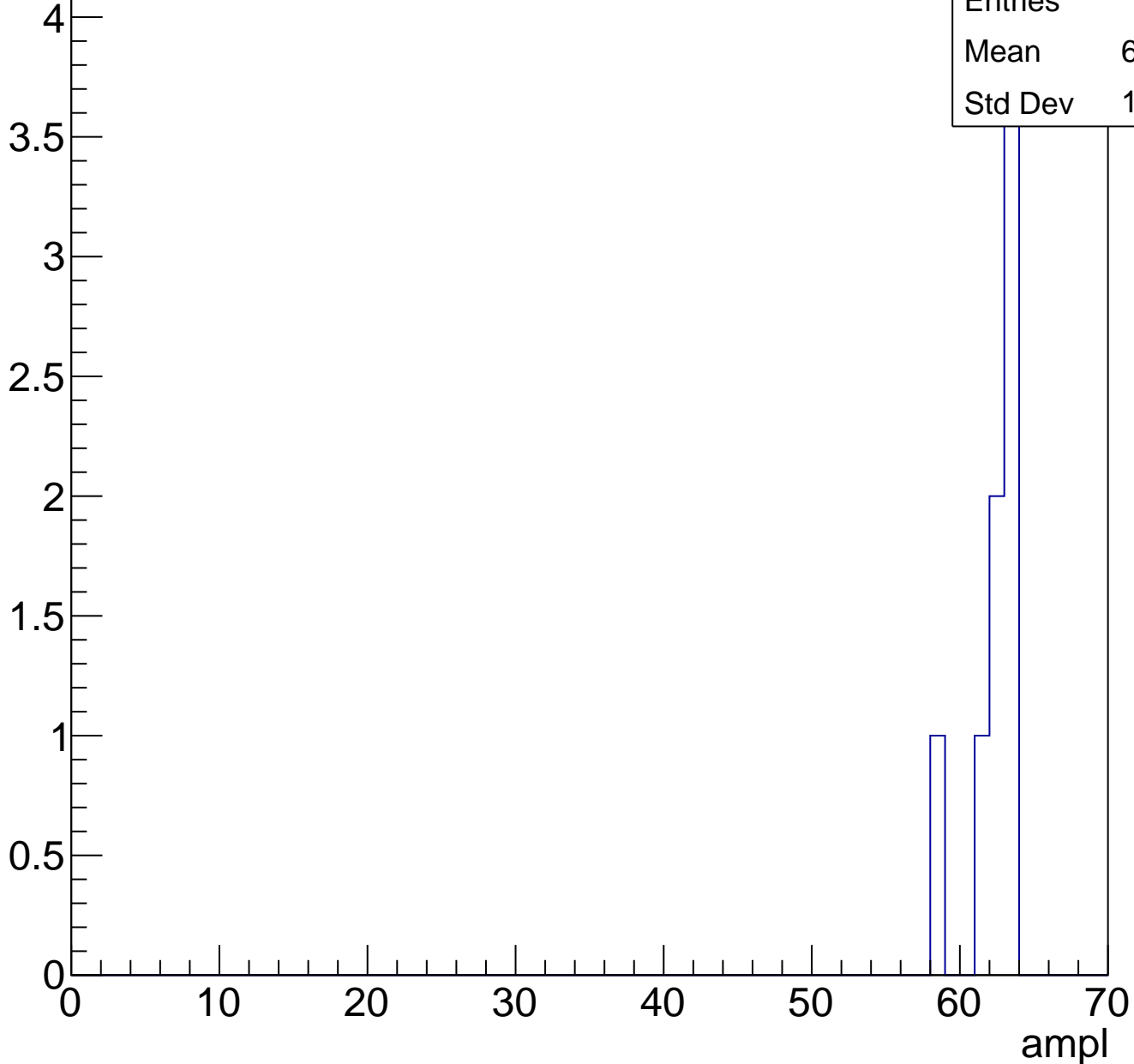
ampl



B0L101S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

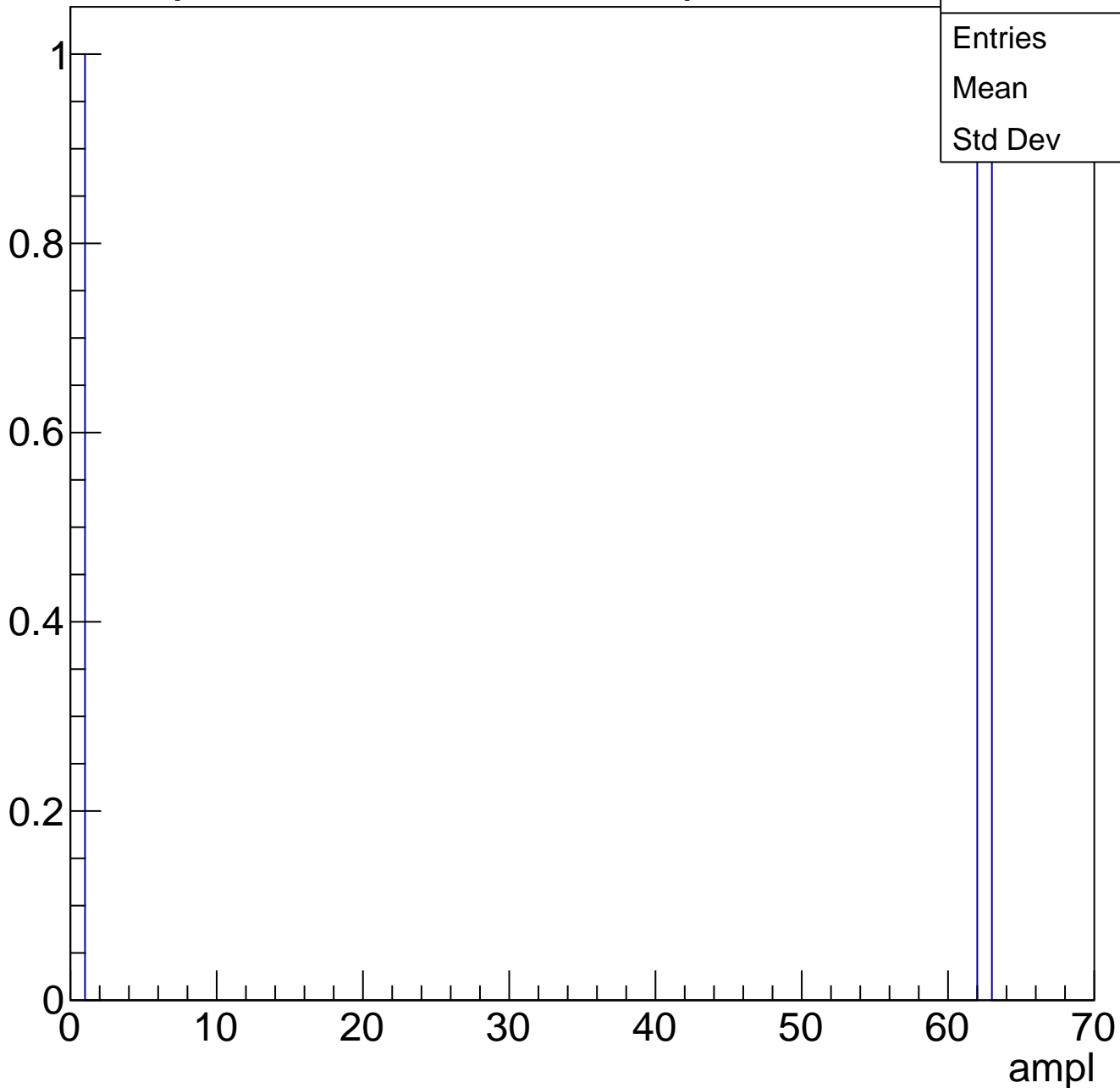
Entry



B0L101S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch20, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.78
Std Dev	4.937

Gaus mean : 29.7498

Gaus Width: 3.4706

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

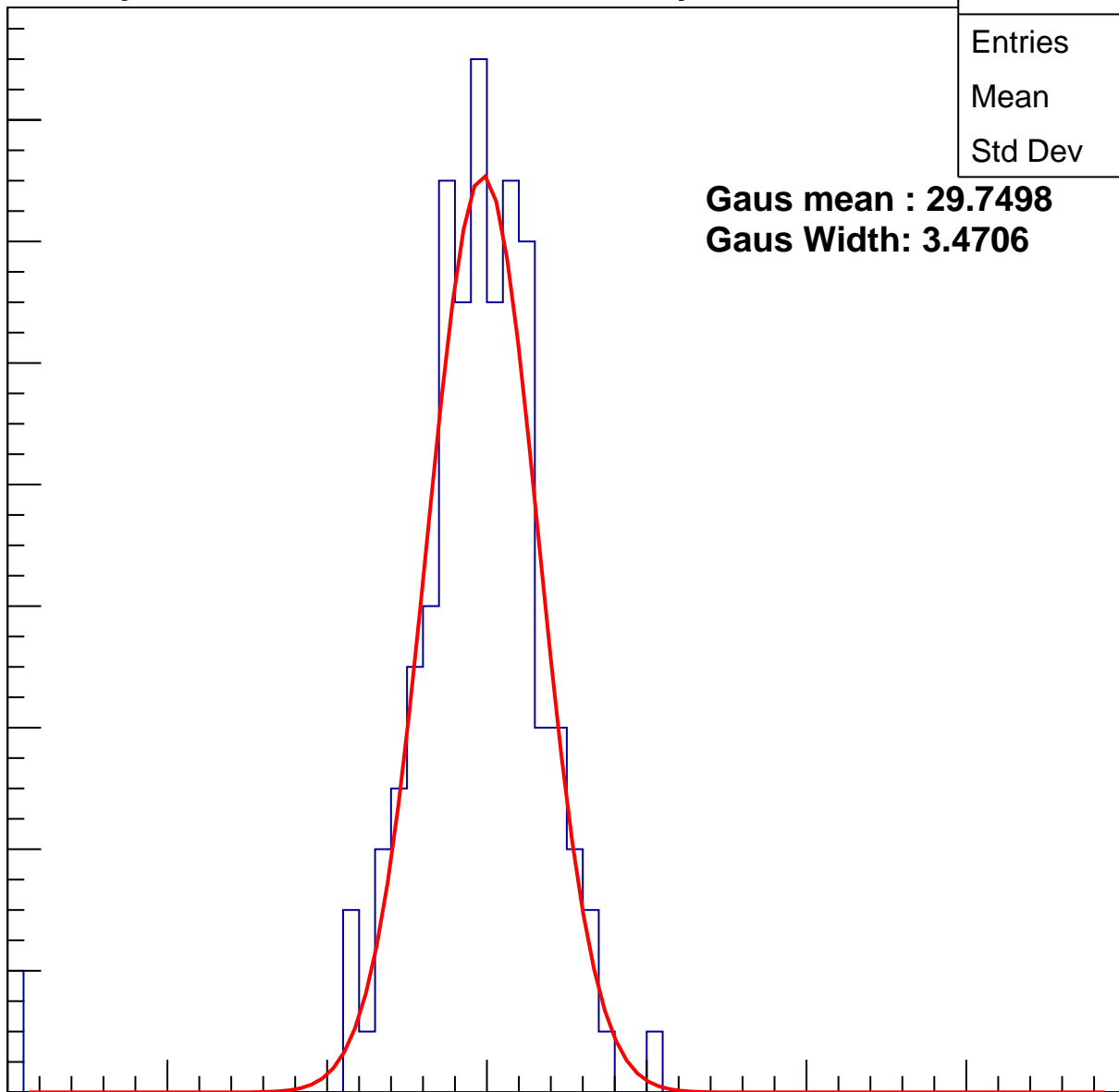
40

50

60

70

ampl



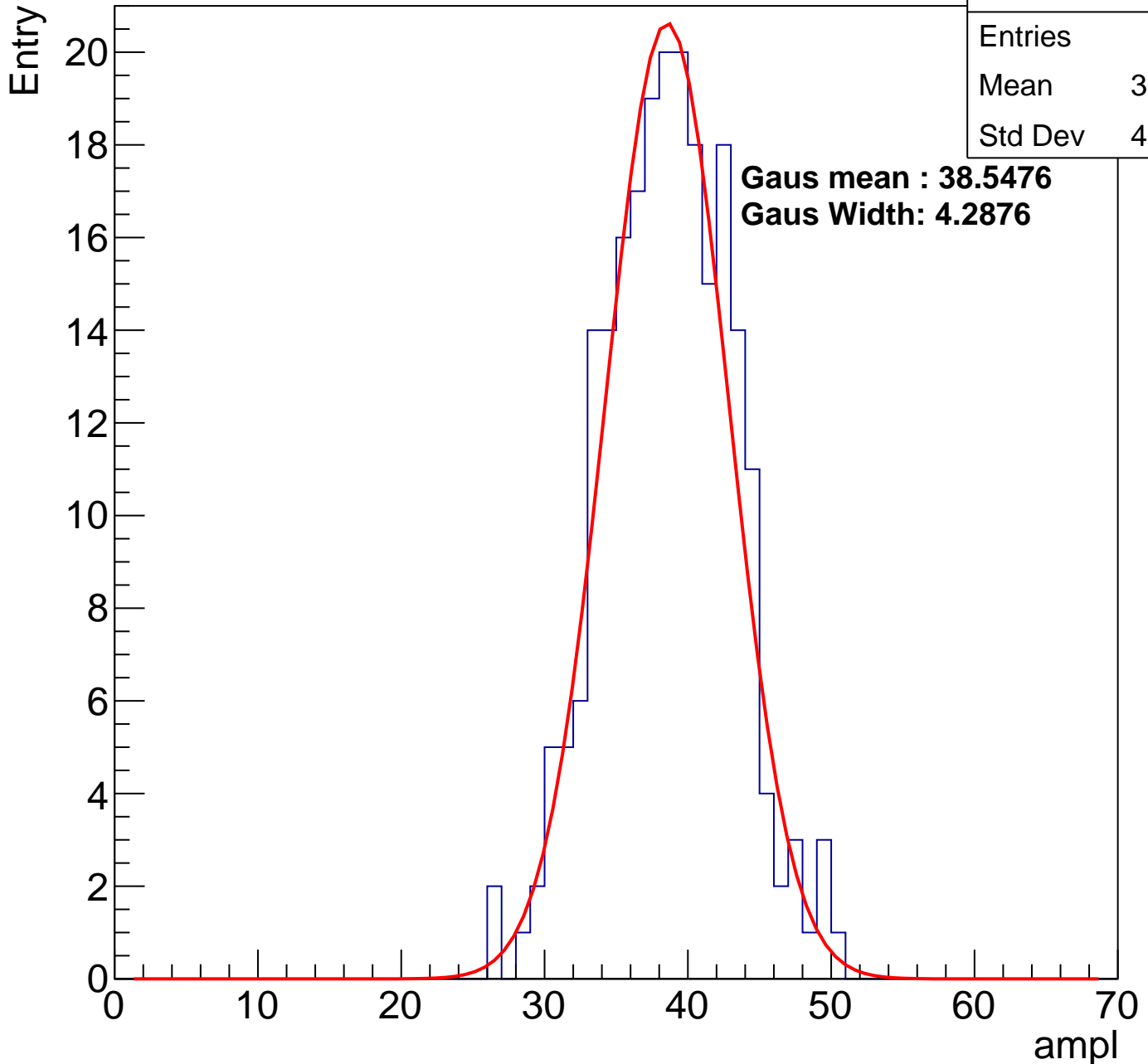
B0L101S, U14-ch20, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	231
Mean	38.19
Std Dev	4.444

Gaus mean : 38.5476

Gaus Width: 4.2876



B0L101S, U14-ch20, adc2

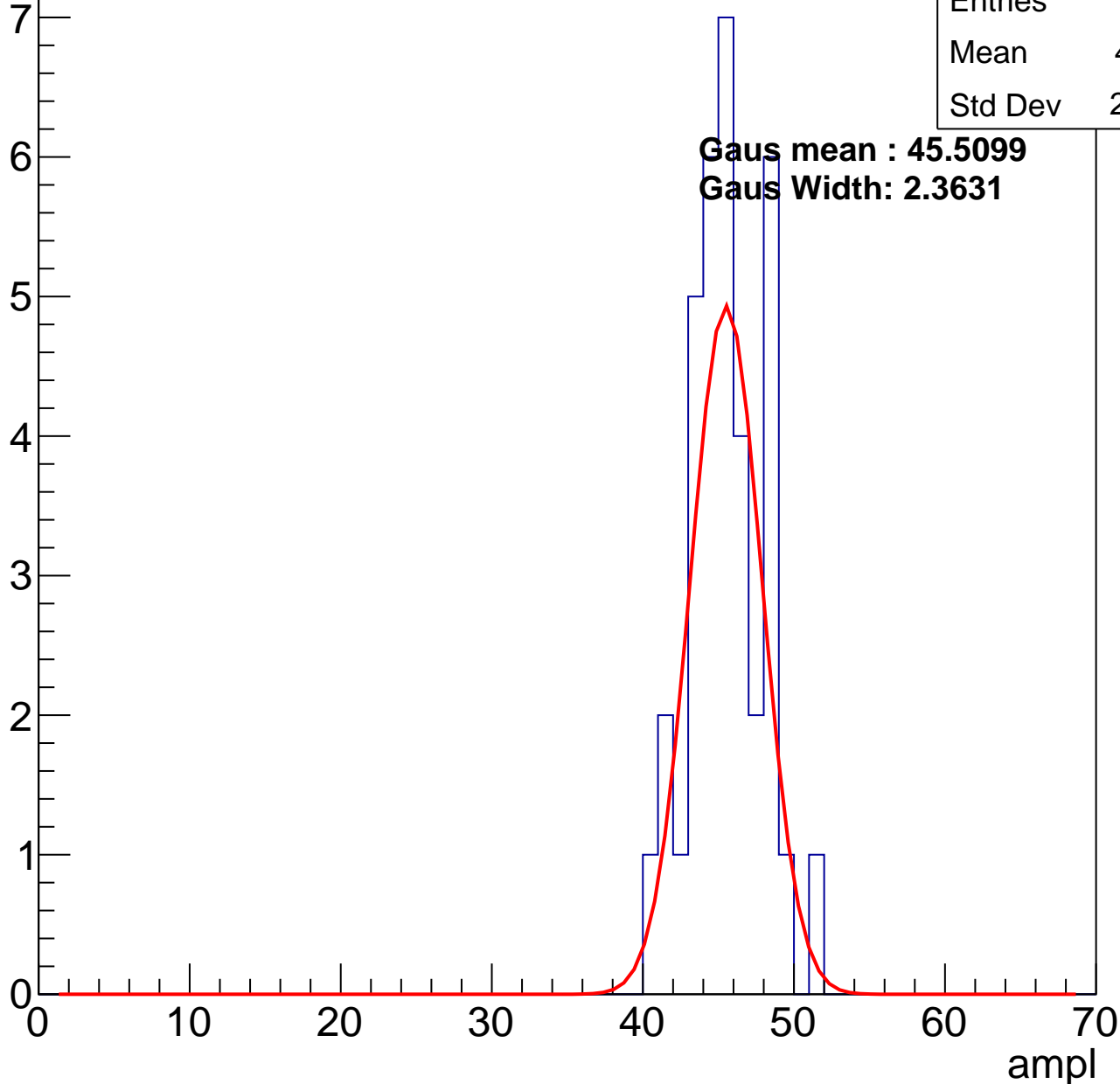
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	36
Mean	45.11
Std Dev	2.413

Gaus mean : 45.5099

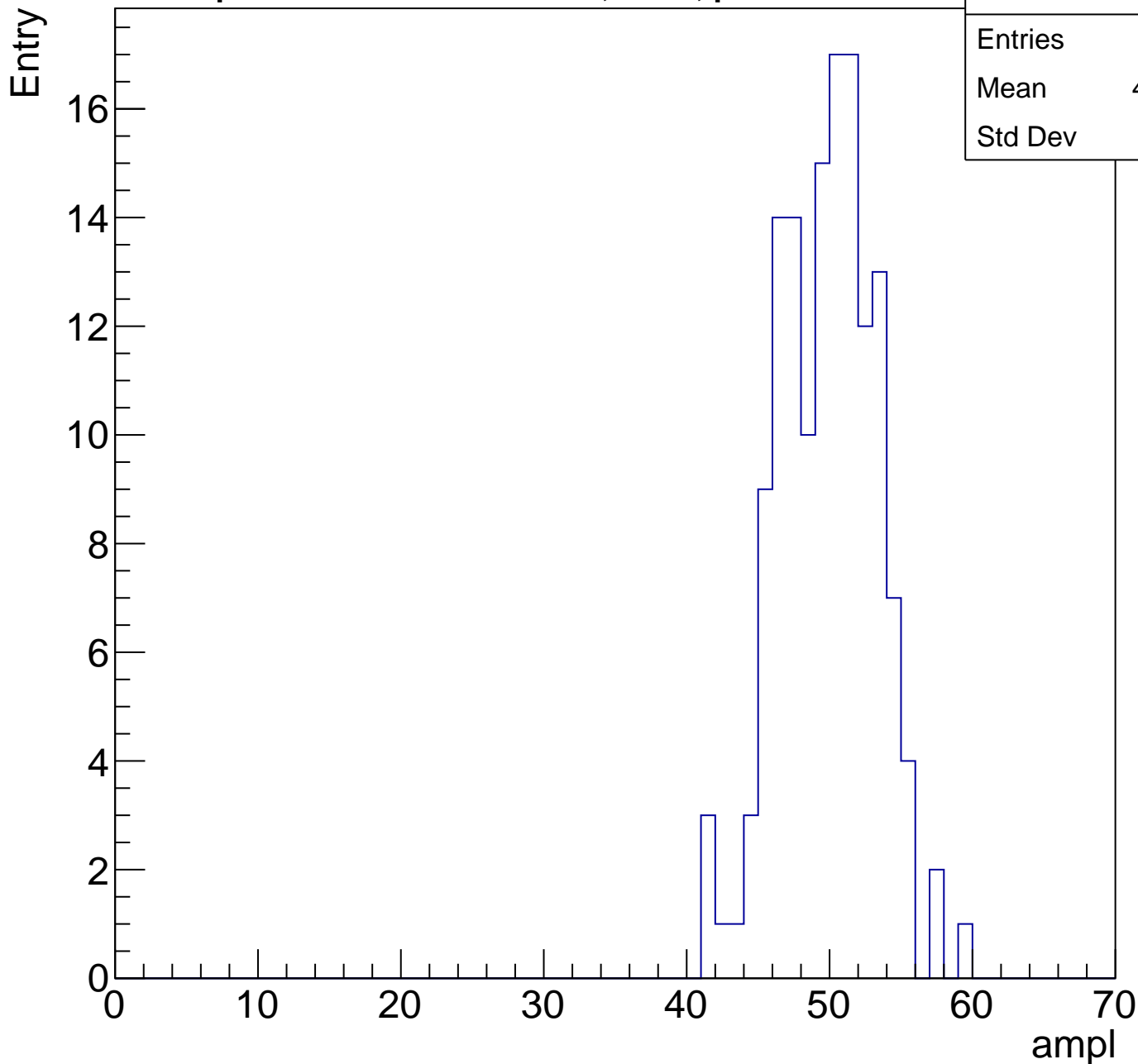
Gaus Width: 2.3631



B0L101S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	49.39
Std Dev	3.36



B0L101S, U14-ch20, adc4

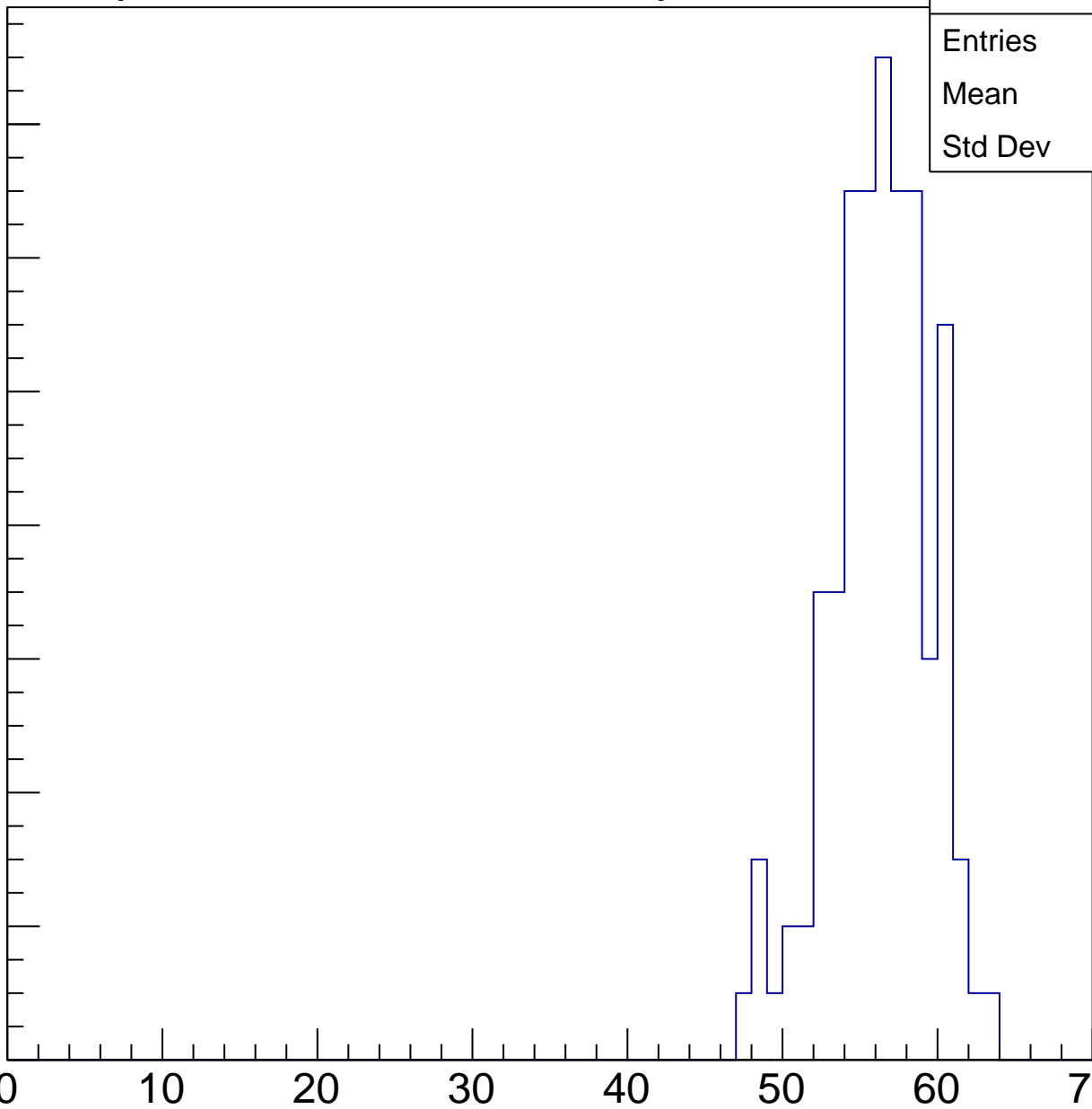
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	112
Mean	55.81
Std Dev	3.183

ampl



B0L101S, U14-ch20, adc5

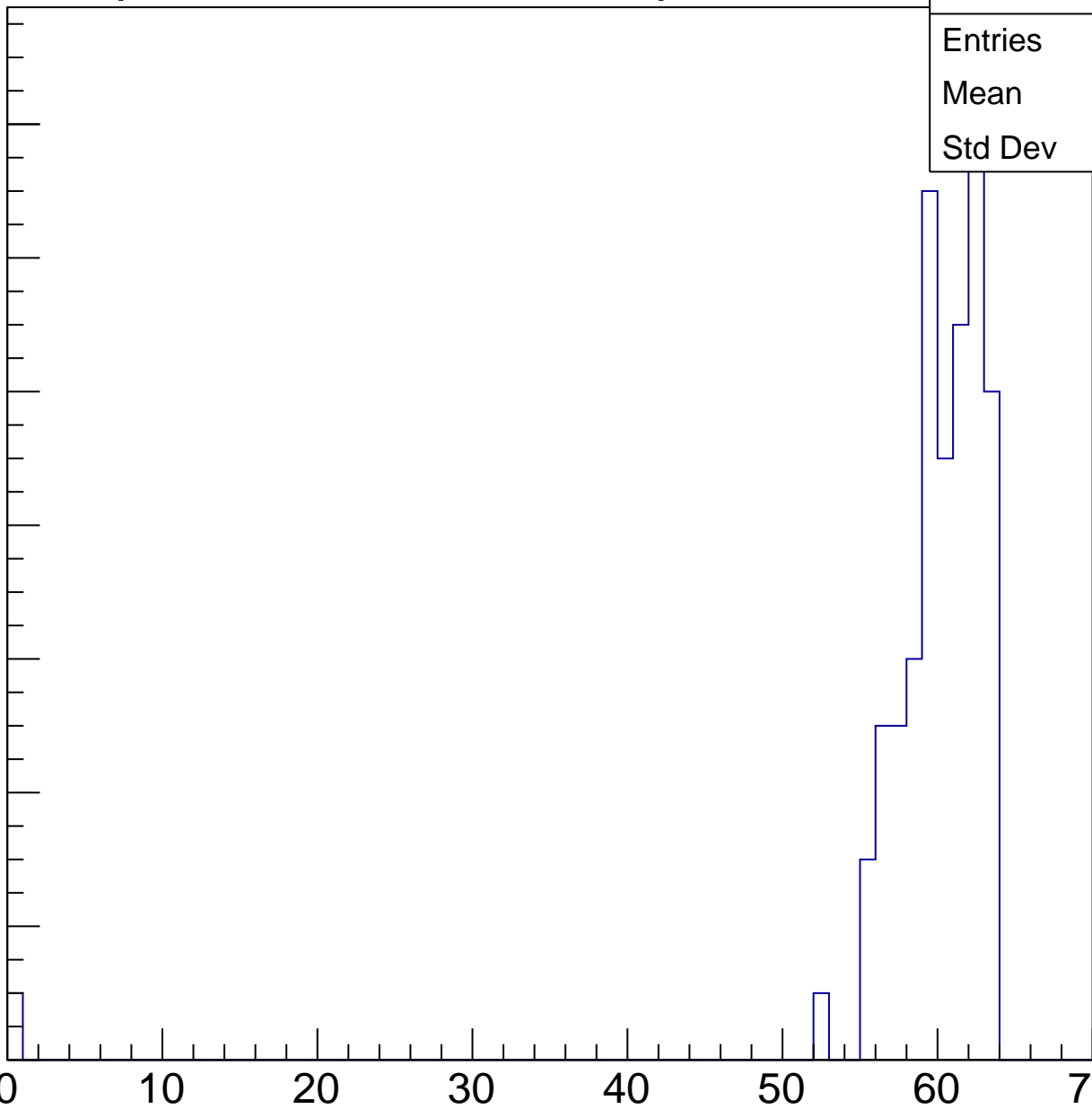
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	59.09
Std Dev	7.112

ampl

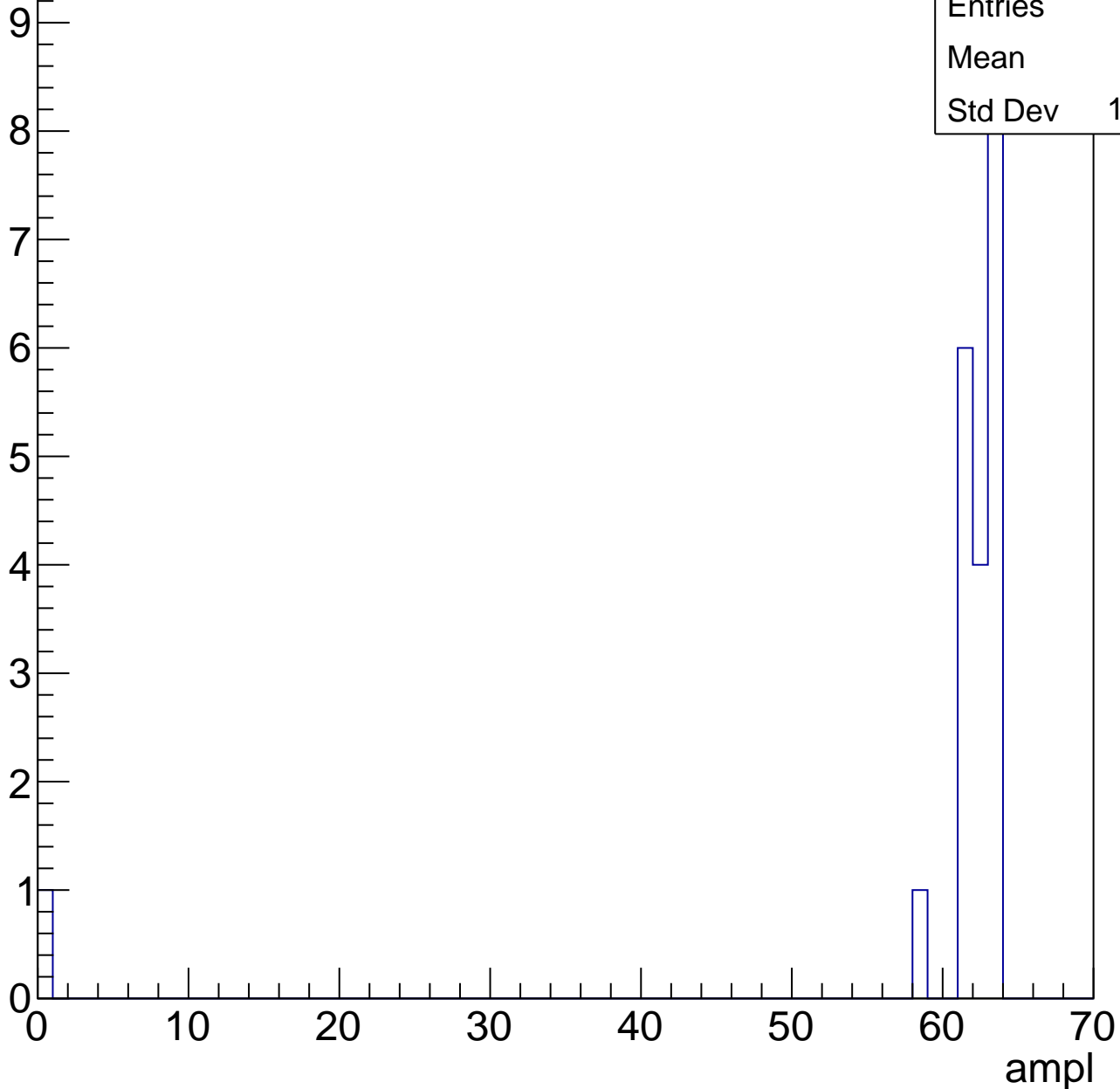


B0L101S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	59
Std Dev	13.25



B0L101S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch21, adc0

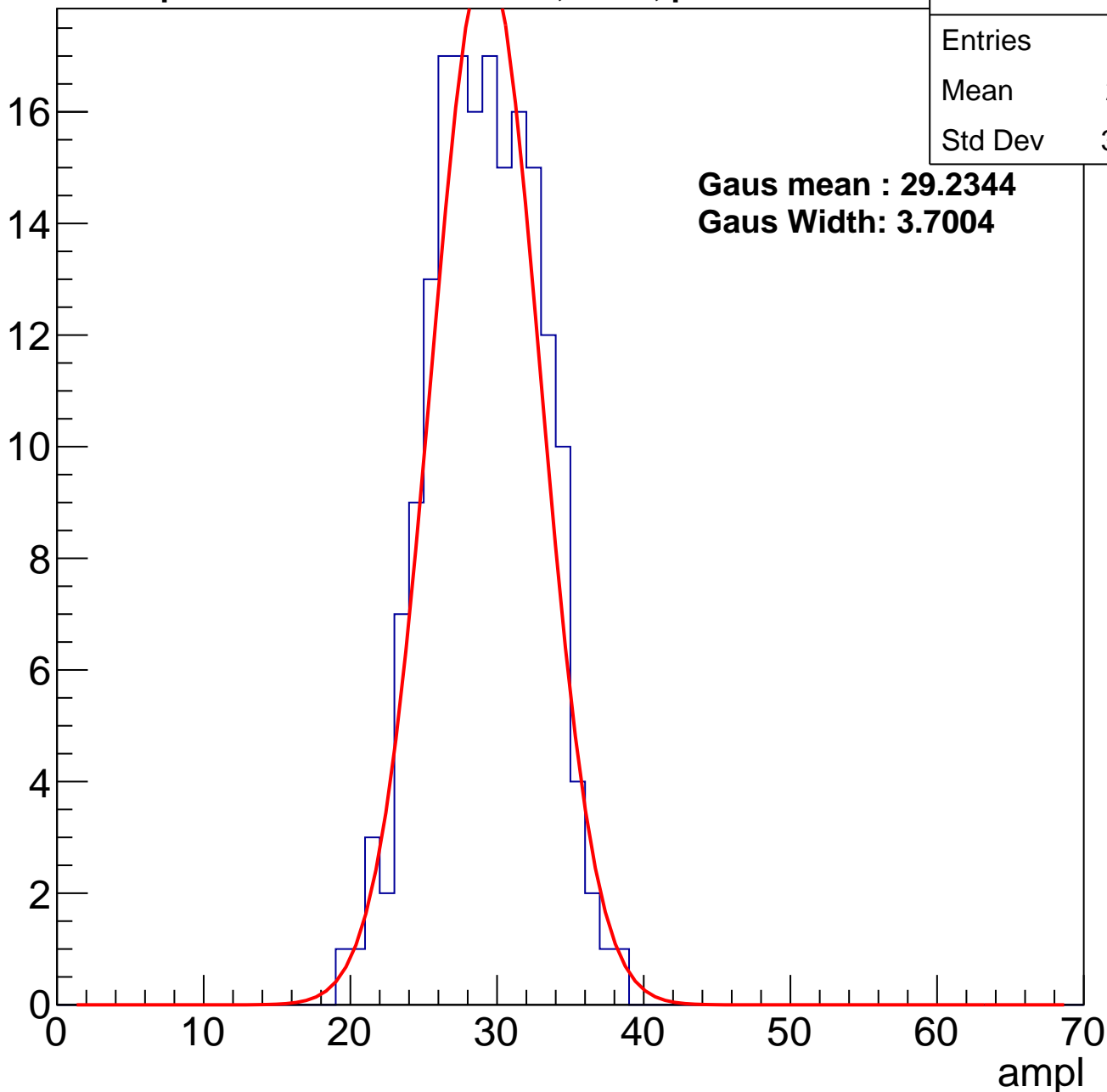
calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	28.71
Std Dev	3.678

Gaus mean : 29.2344

Gaus Width: 3.7004

Entry



B0L101S, U14-ch21, adc1

calib_packv5_042523_0143.root, FC#1, port C1

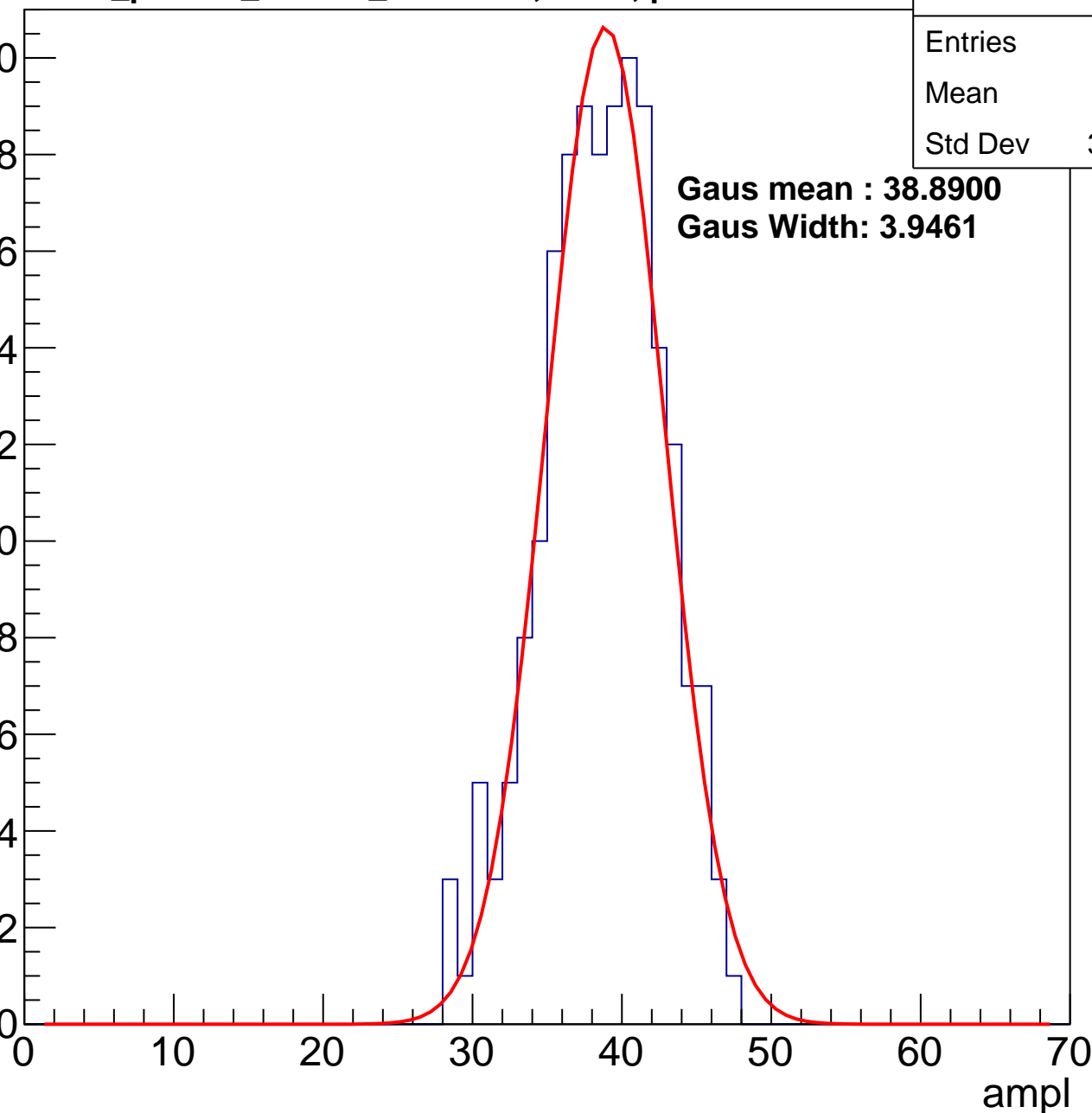
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	208
Mean	38.21
Std Dev	3.969

Gaus mean : 38.8900

Gaus Width: 3.9461



B0L101S, U14-ch21, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

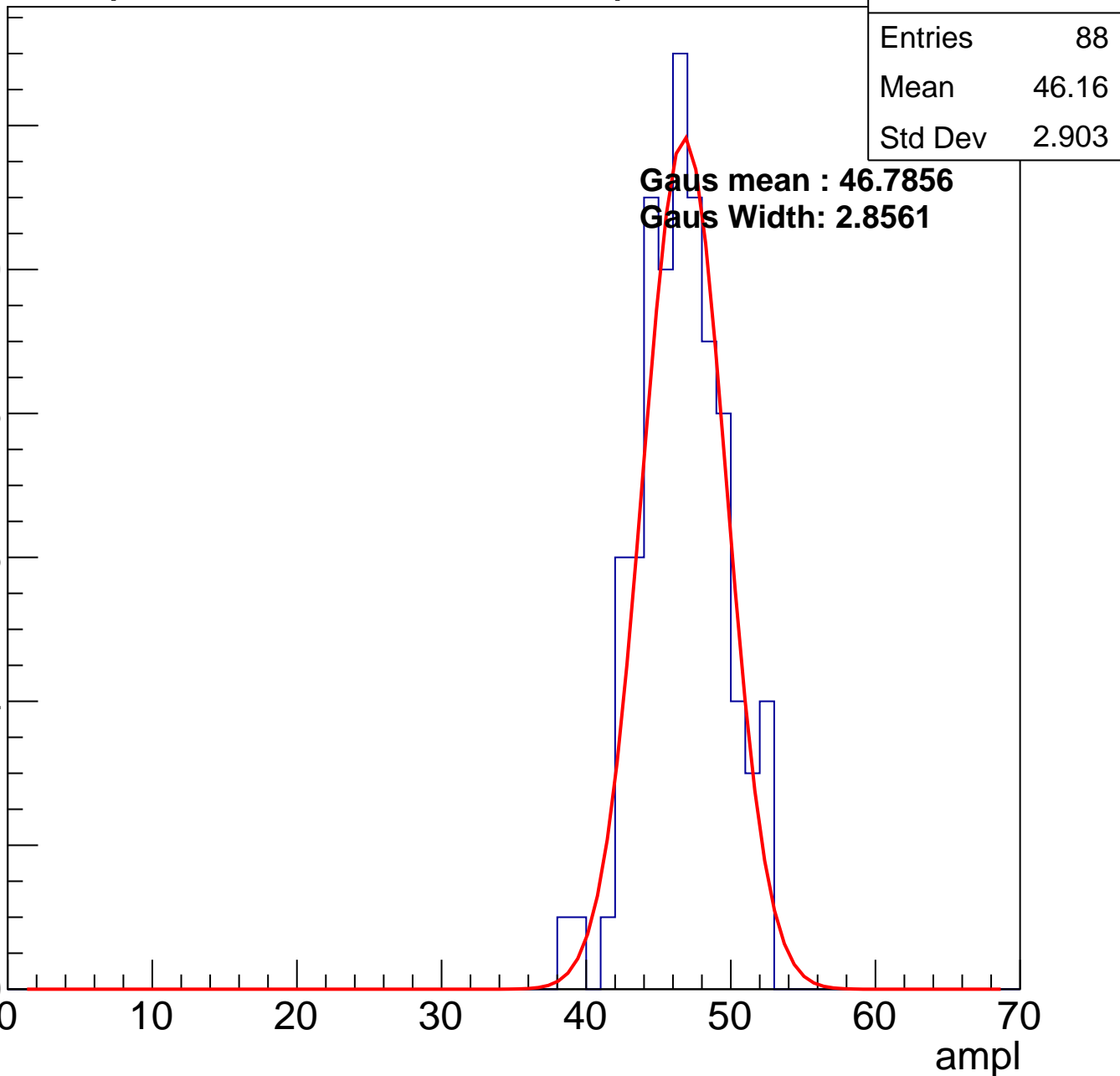
Entries	88
Mean	46.16
Std Dev	2.903

Gaus mean : 46.7856

Gaus Width: 2.8561

0 10 20 30 40 50 60 70

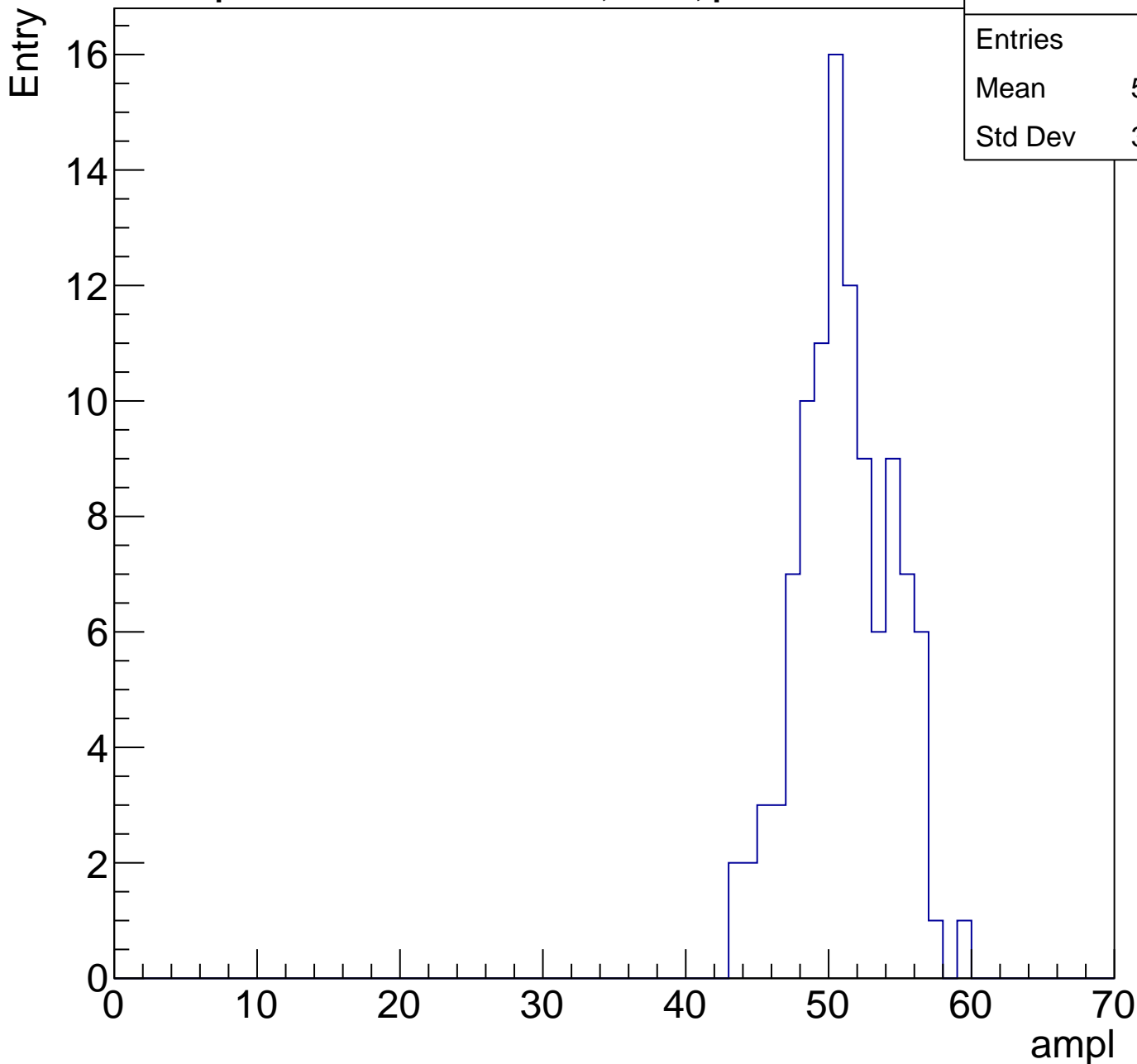
ampl



B0L101S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

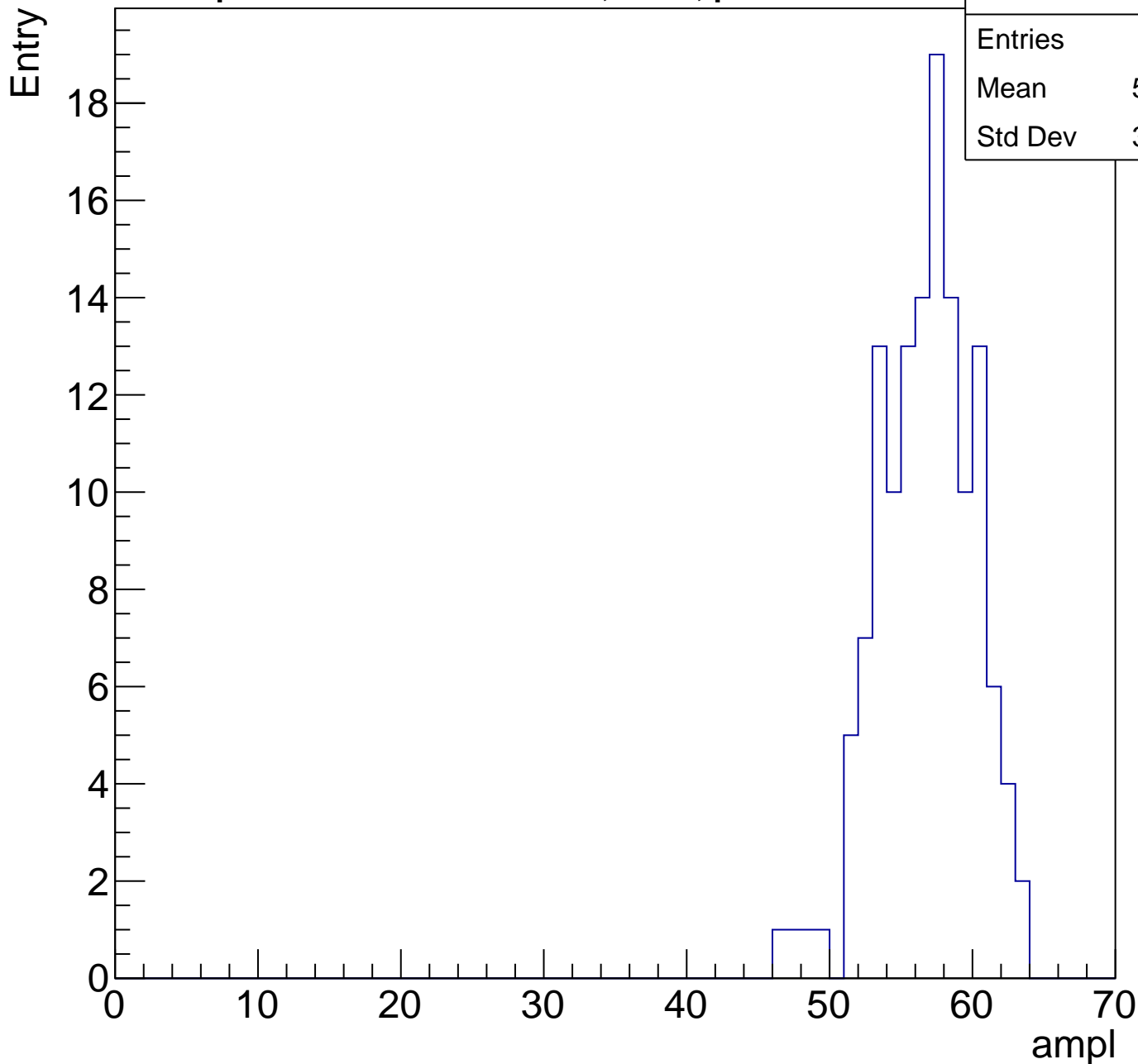
Entries	105
Mean	50.63
Std Dev	3.284



B0L101S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	56.28
Std Dev	3.295



B0L101S, U14-ch21, adc5

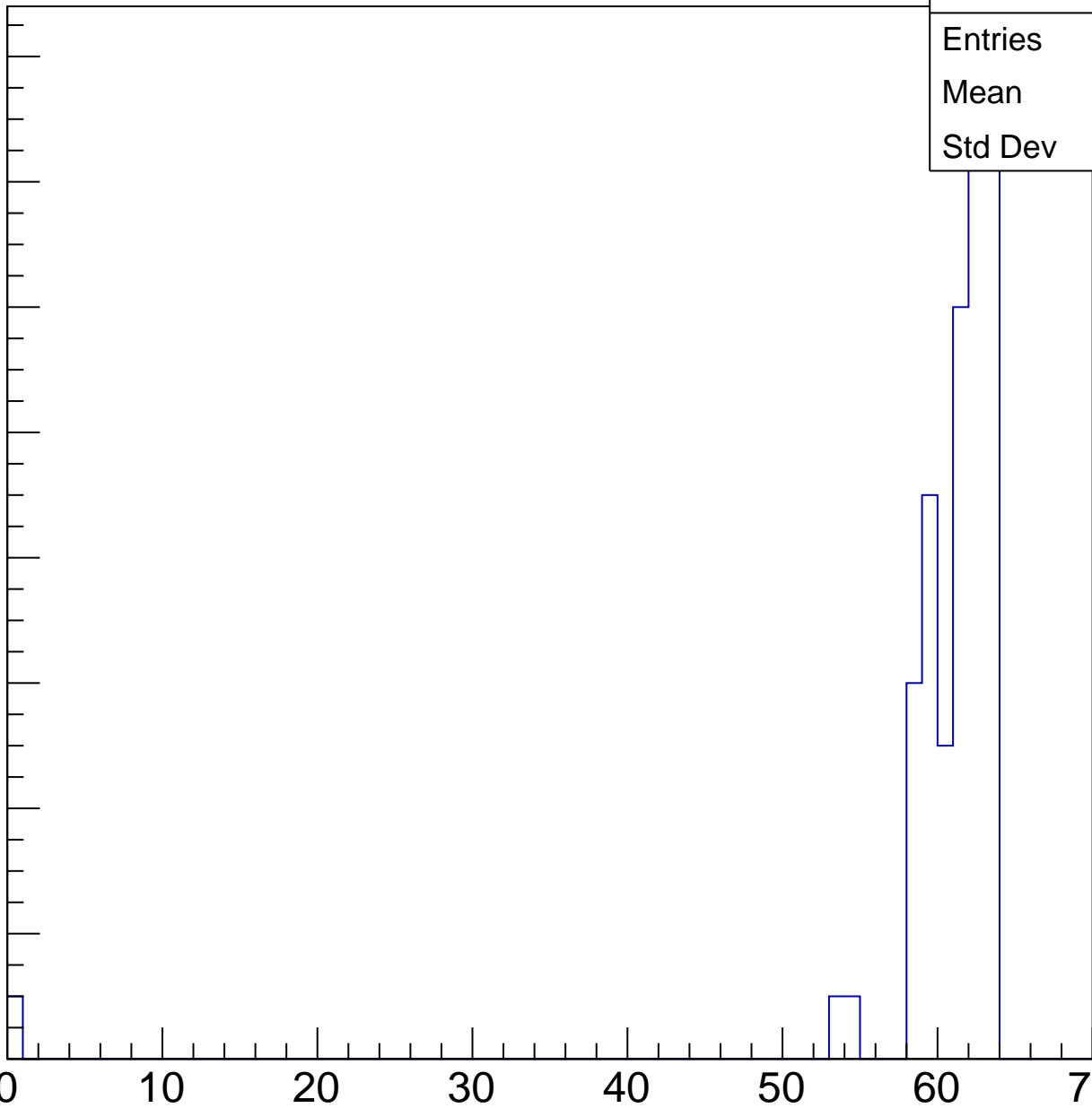
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	59.97
Std Dev	7.667

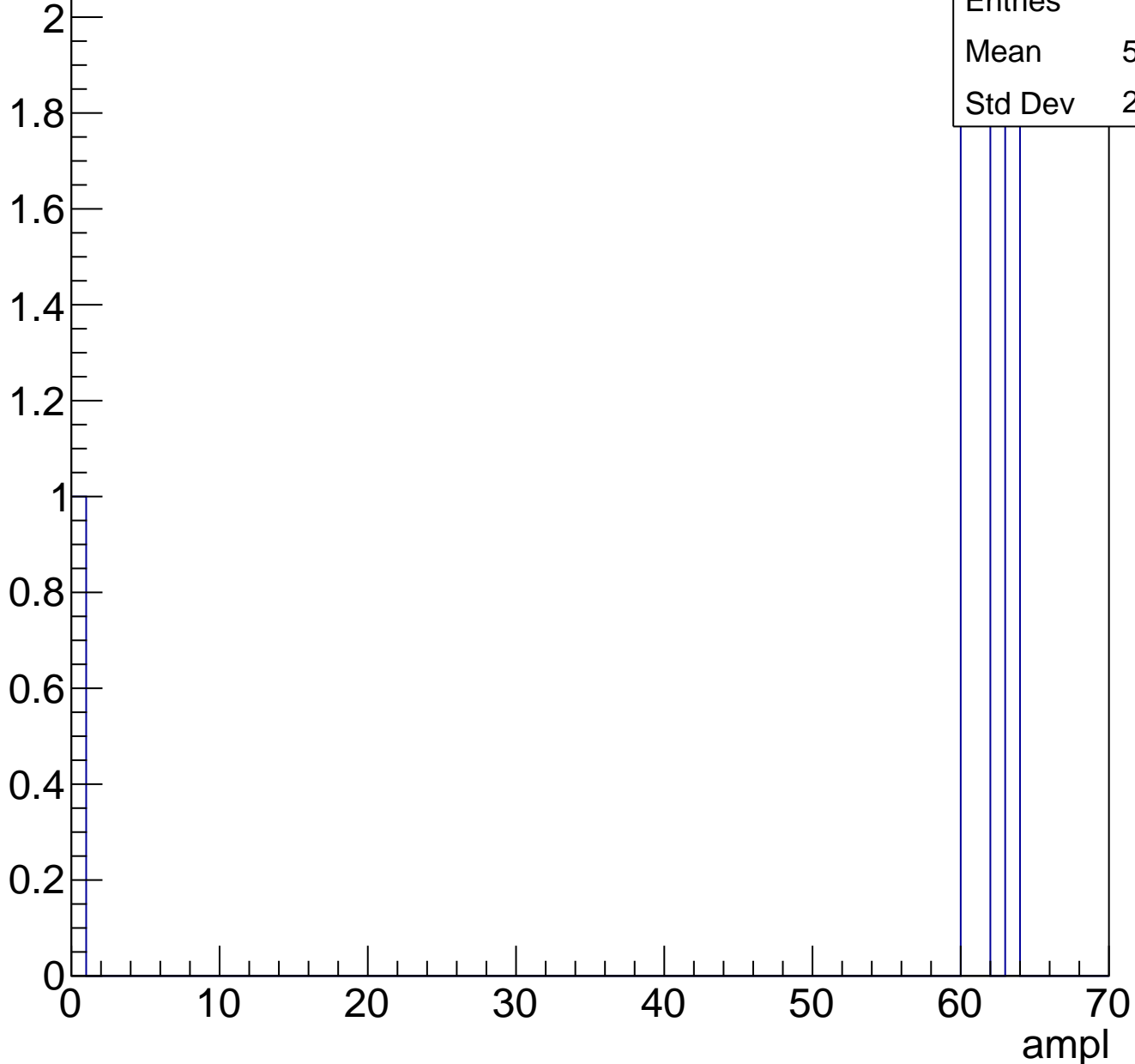
ampl



B0L101S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch22, adc0

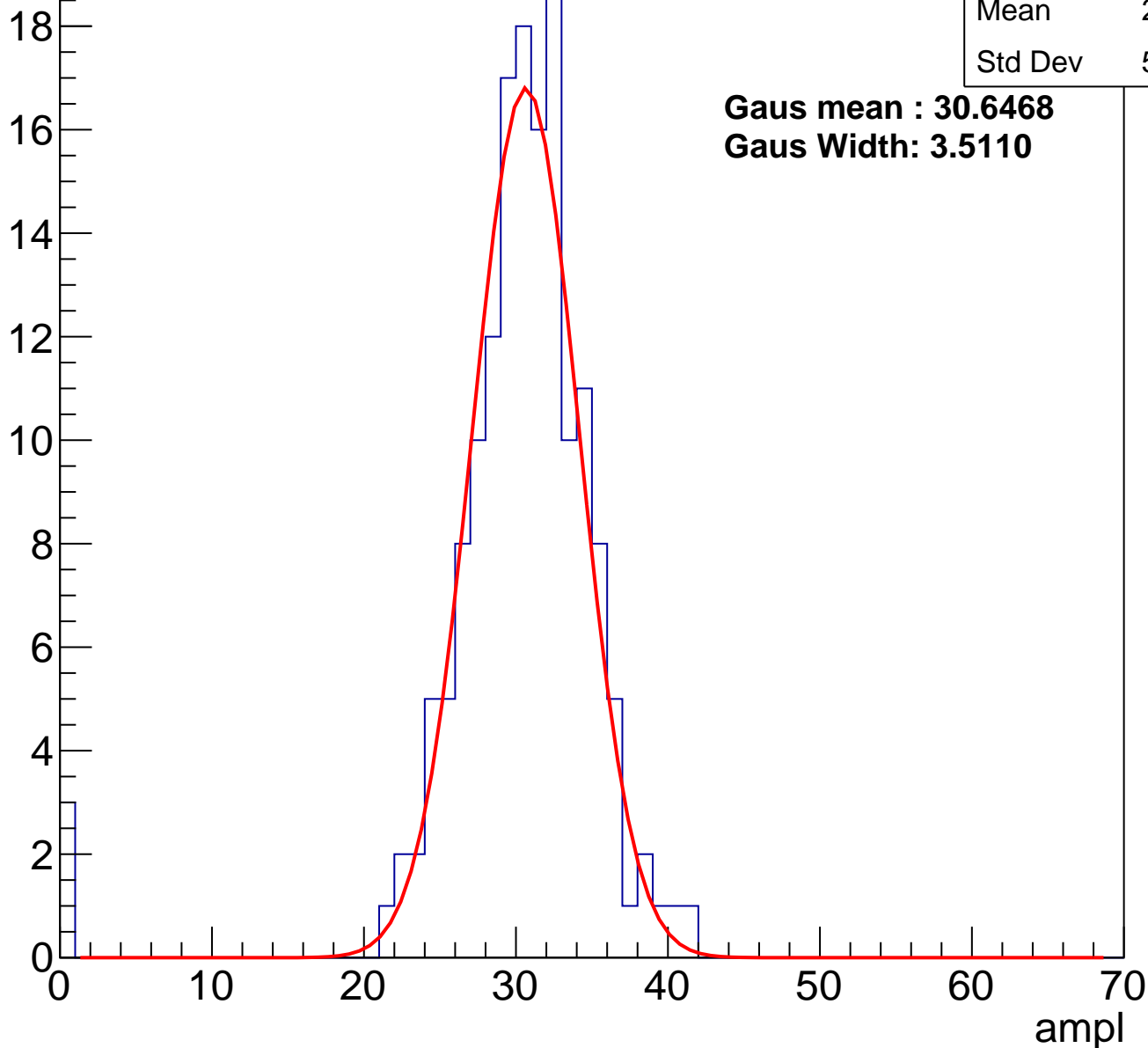
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	29.77
Std Dev	5.514

Gaus mean : 30.6468

Gaus Width: 3.5110

Entry



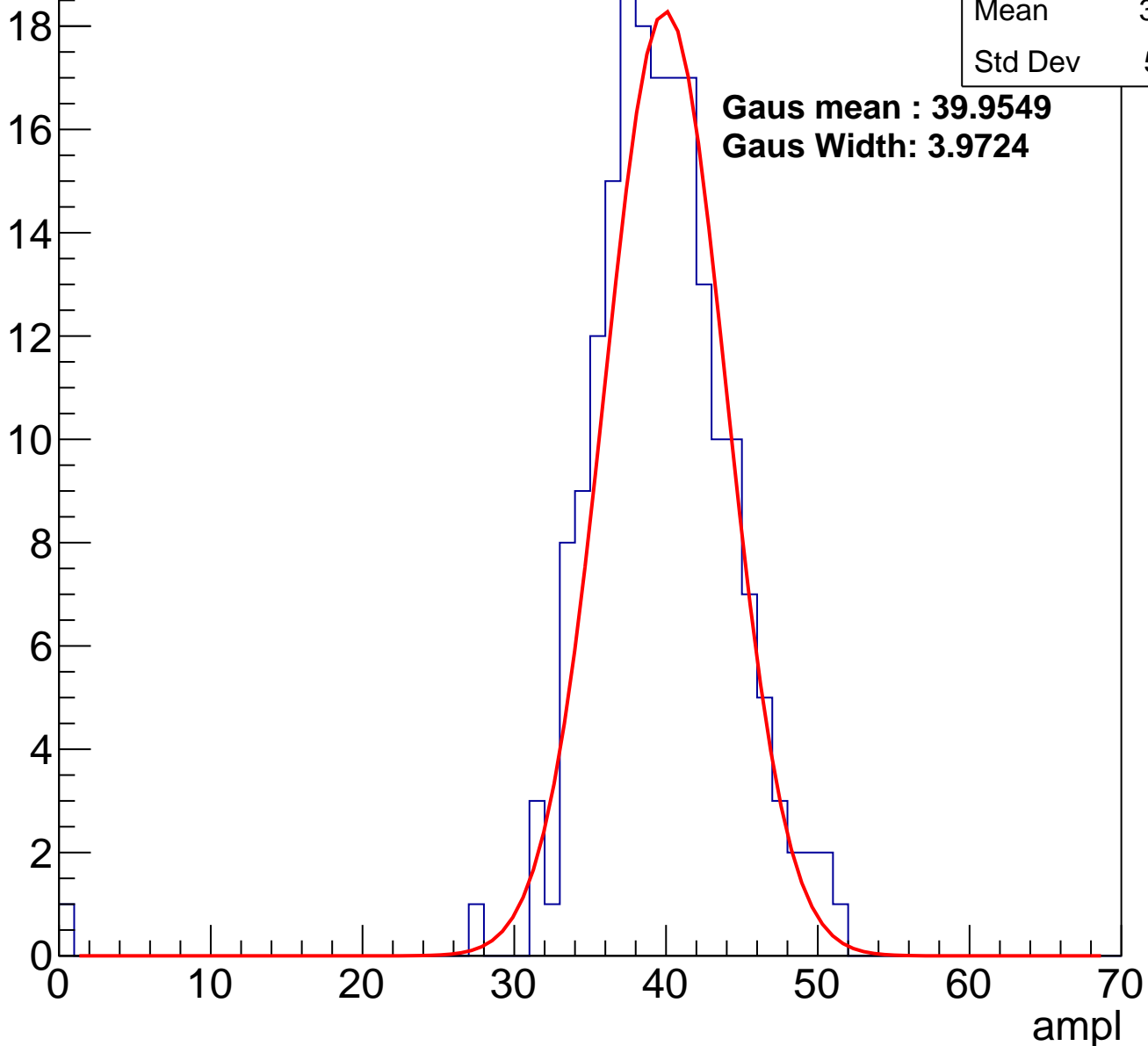
B0L101S, U14-ch22, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	193
Mean	39.15
Std Dev	5.031

Gaus mean : 39.9549
Gaus Width: 3.9724

Entry



B0L101S, U14-ch22, adc2

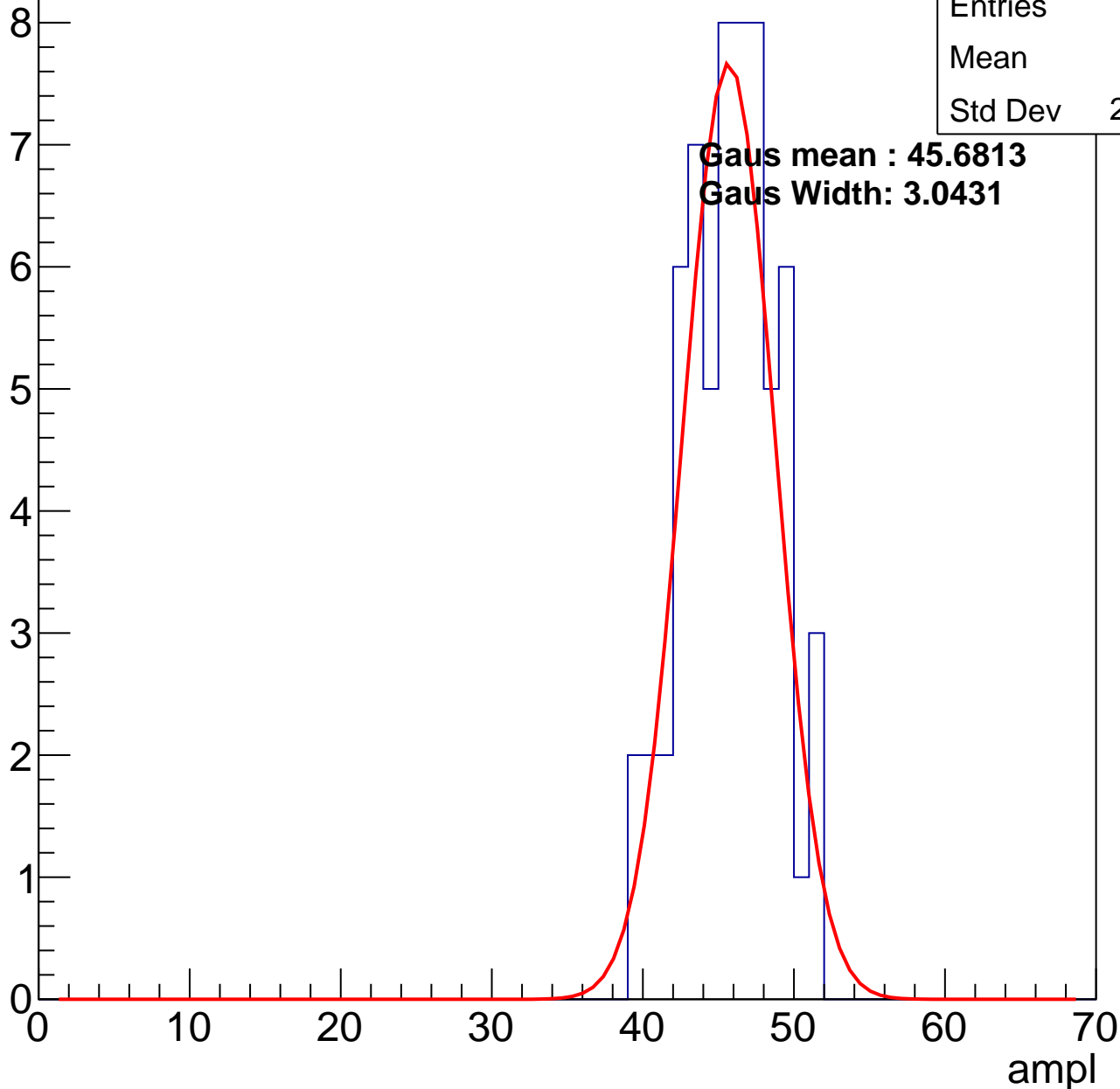
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	63
Mean	45.3
Std Dev	2.953

Gaus mean : 45.6813

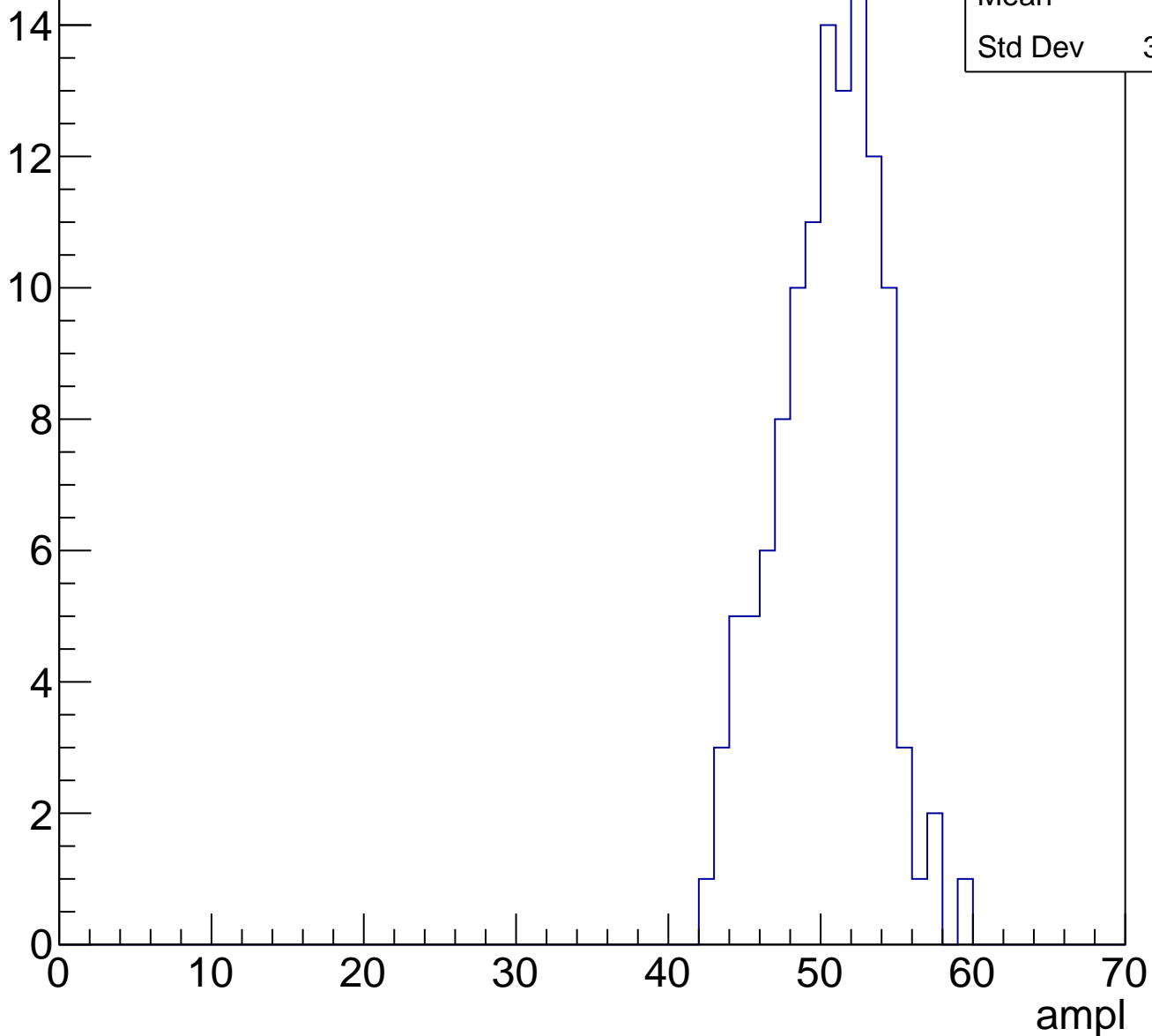
Gaus Width: 3.0431



B0L101S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

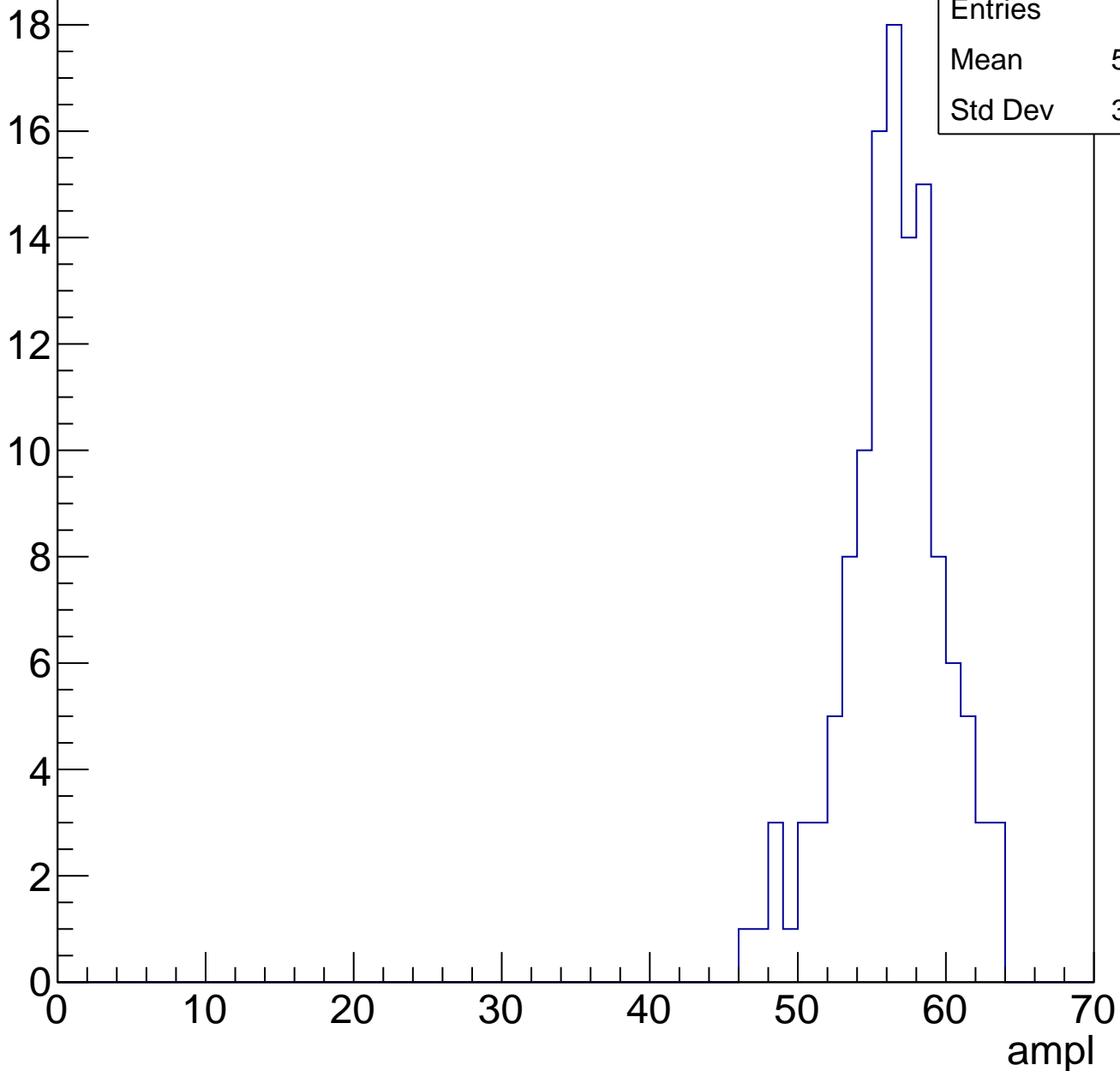


B0L101S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	55.94
Std Dev	3.415

Entry



B0L101S, U14-ch22, adc5

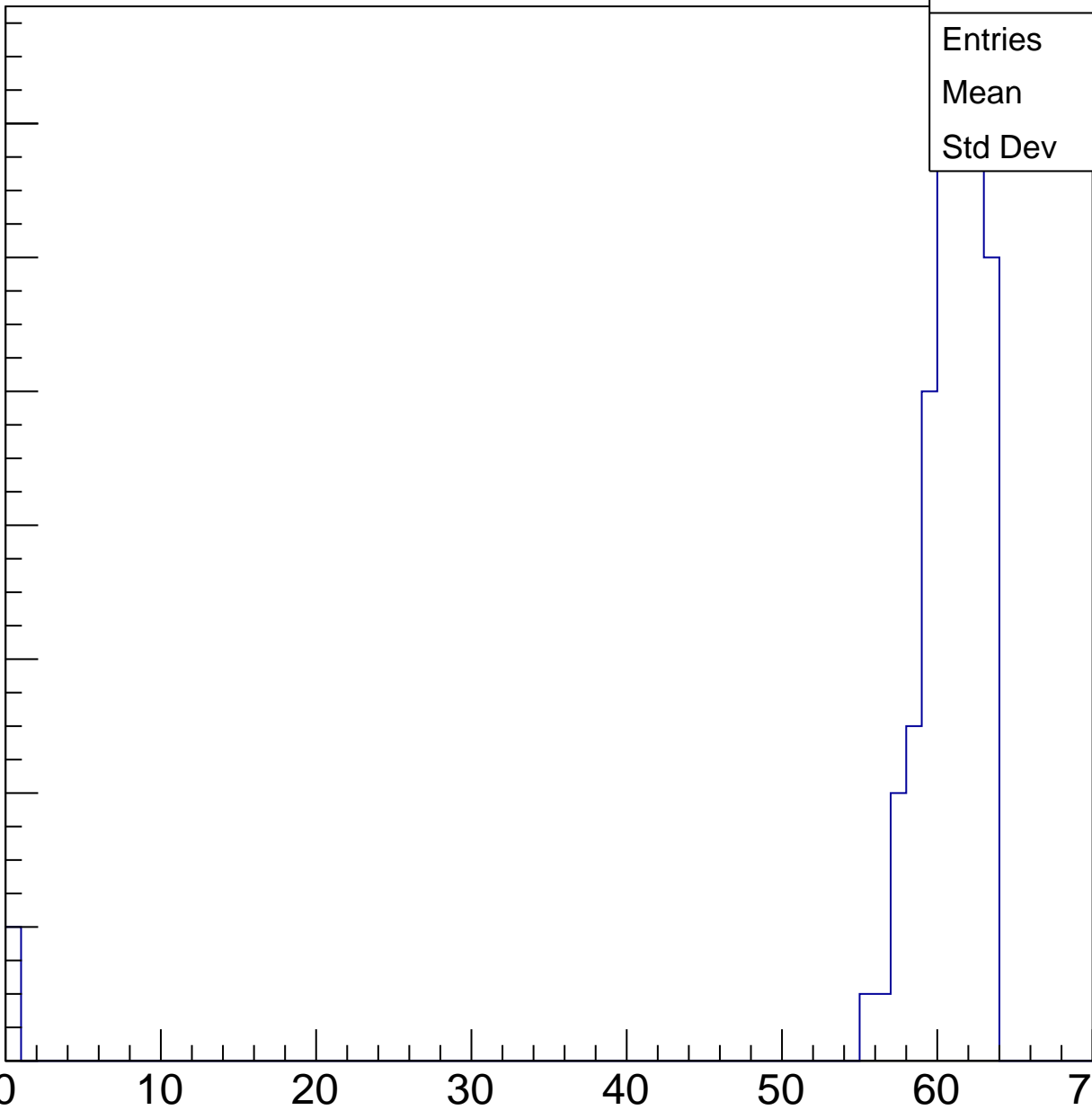
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.96
Std Dev	9.743

ampl

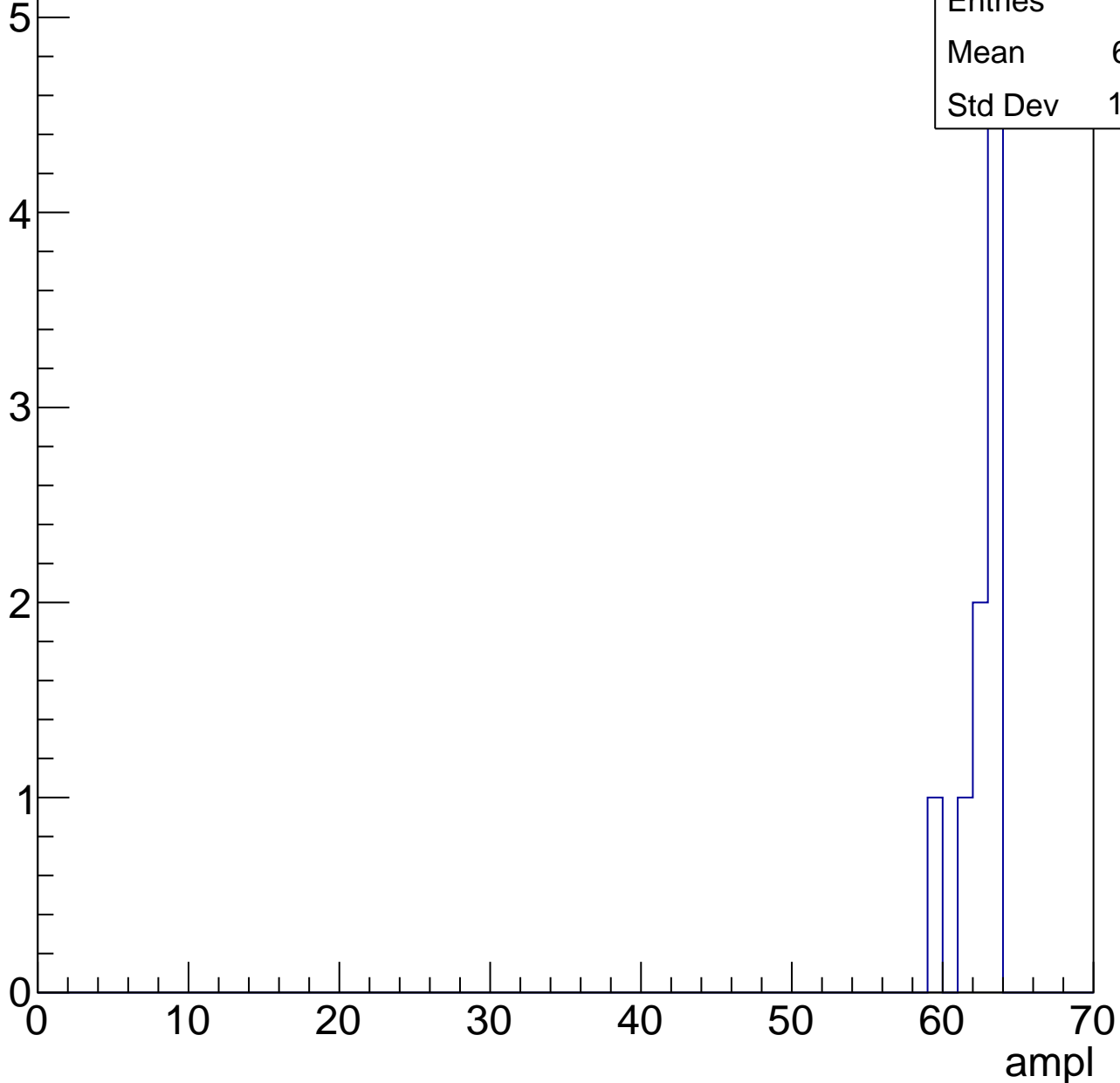


B0L101S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	62.11
Std Dev	1.286



B0L101S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



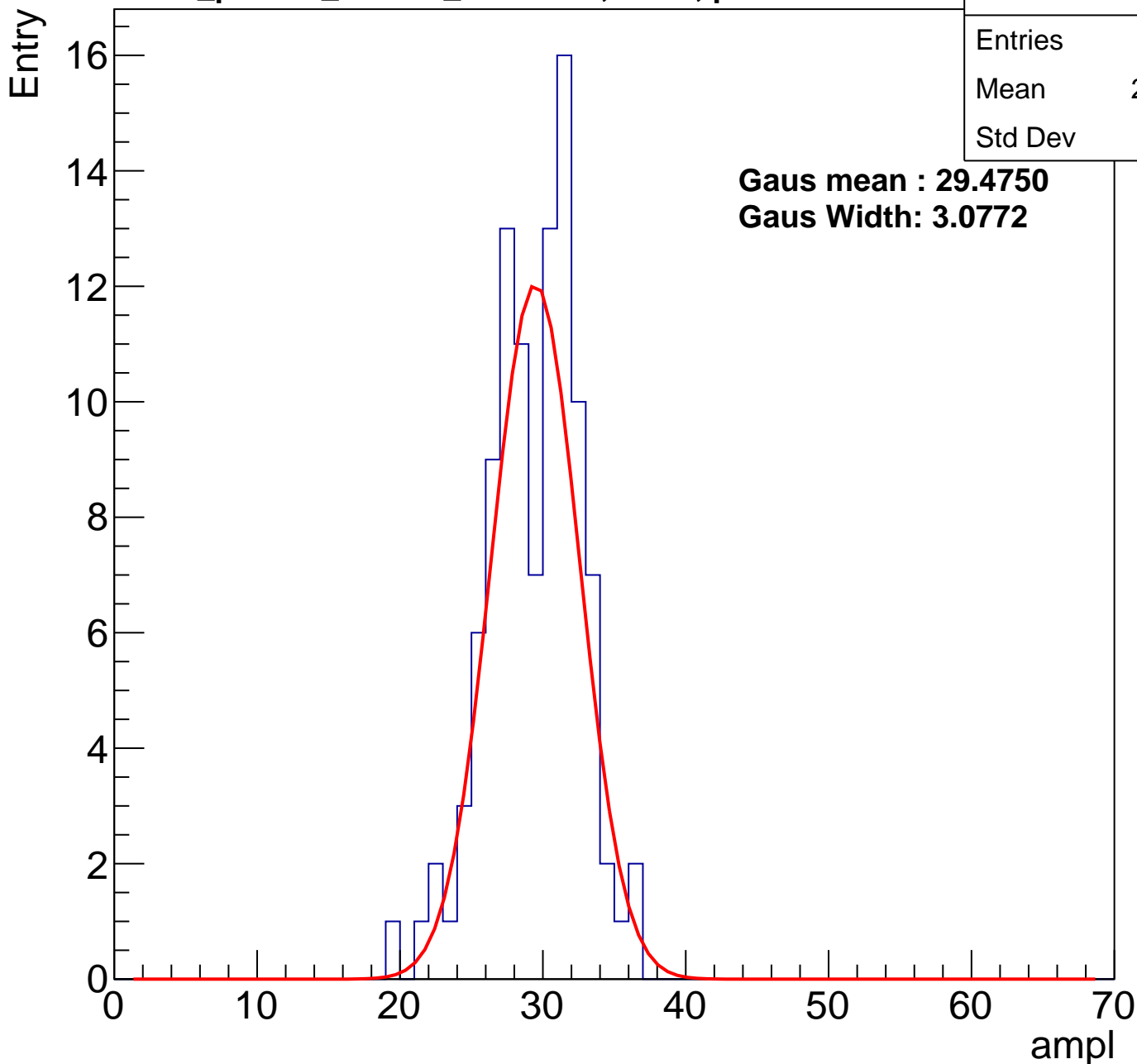
B0L101S, U14-ch23, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	28.92
Std Dev	3.2

Gaus mean : 29.4750

Gaus Width: 3.0772



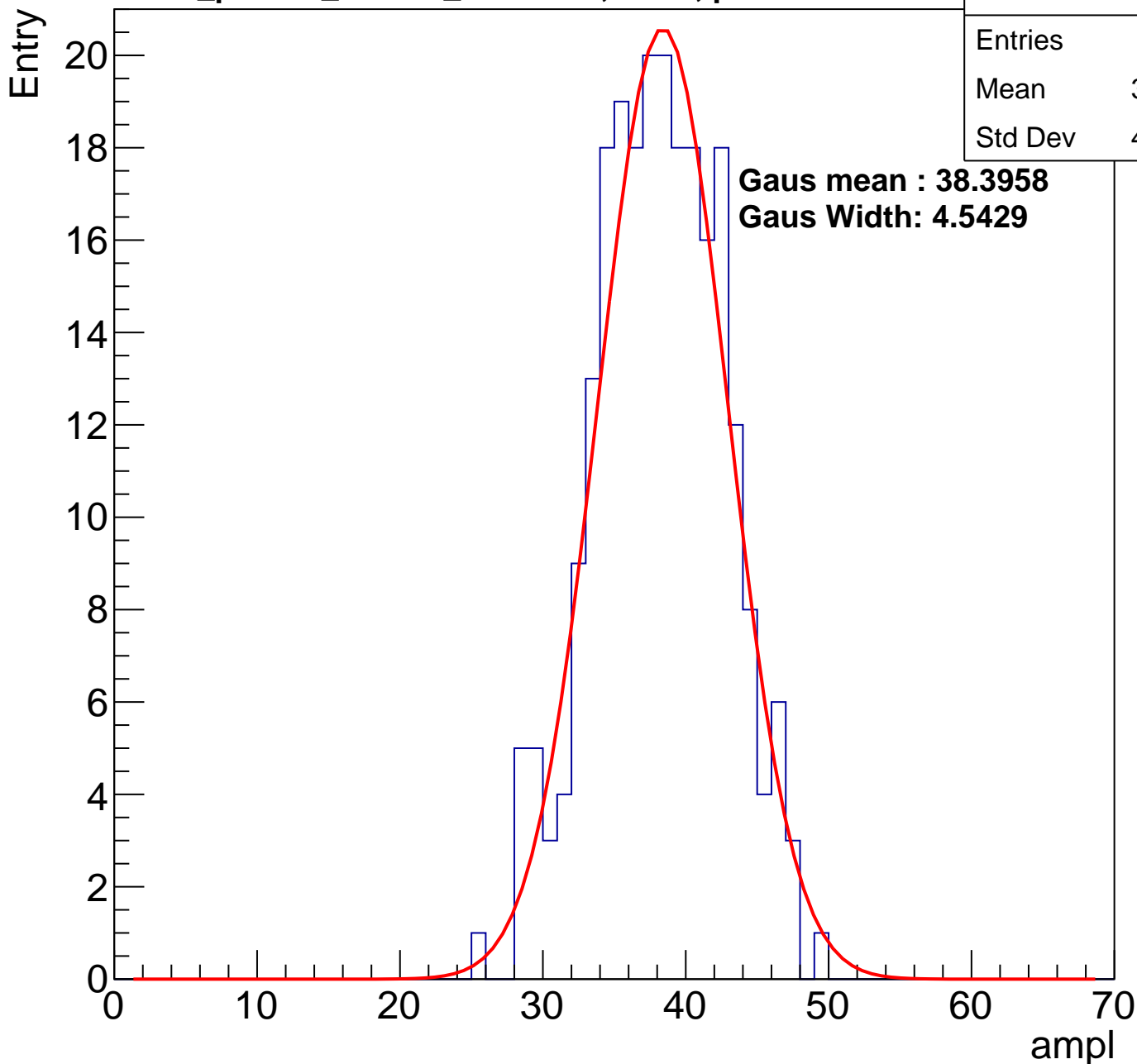
B0L101S, U14-ch23, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	239
Mean	37.72
Std Dev	4.433

Gaus mean : 38.3958

Gaus Width: 4.5429



B0L101S, U14-ch23, adc2

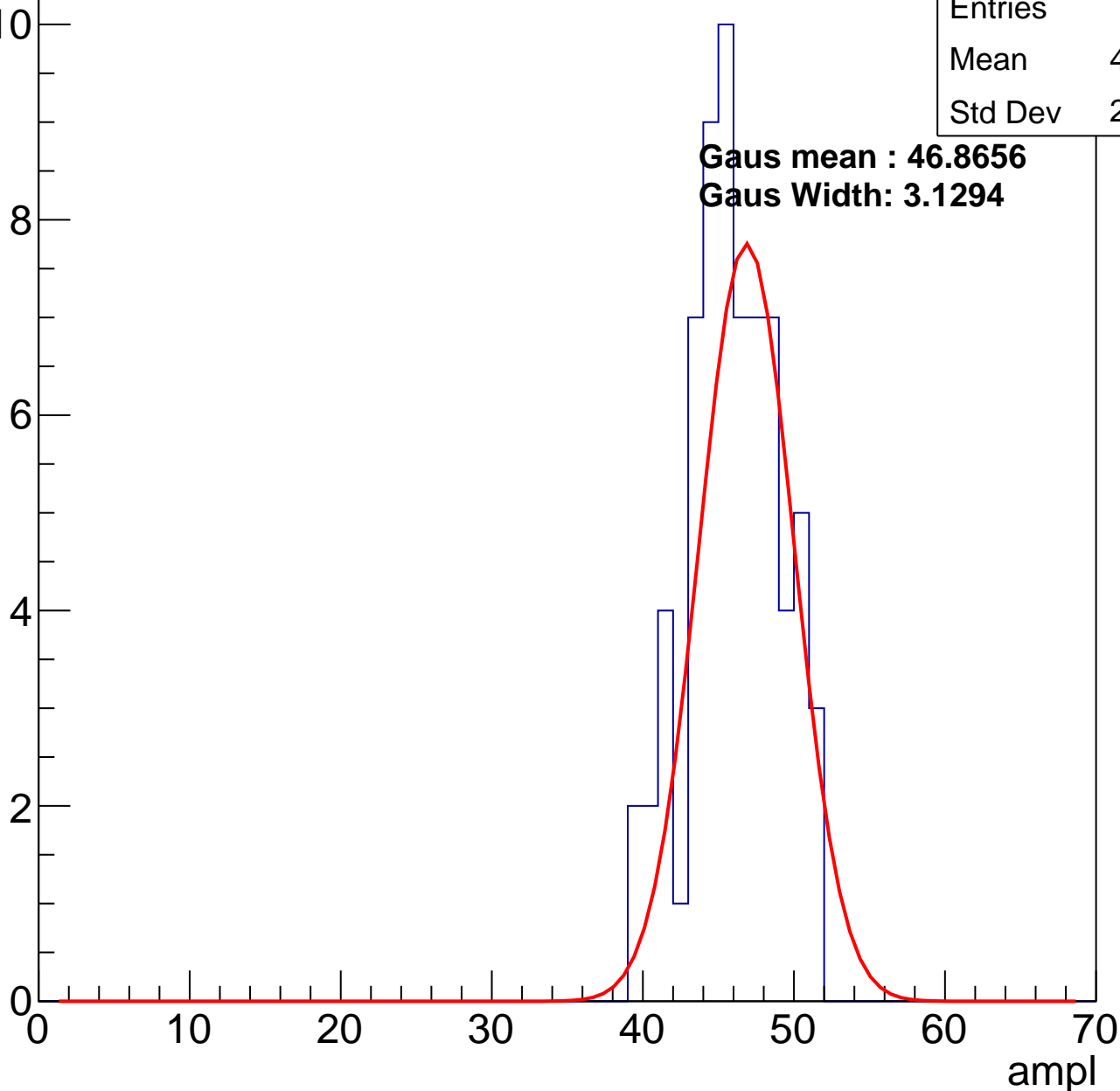
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	68
Mean	45.54
Std Dev	2.987

Gaus mean : 46.8656

Gaus Width: 3.1294



B0L101S, U14-ch23, adc3

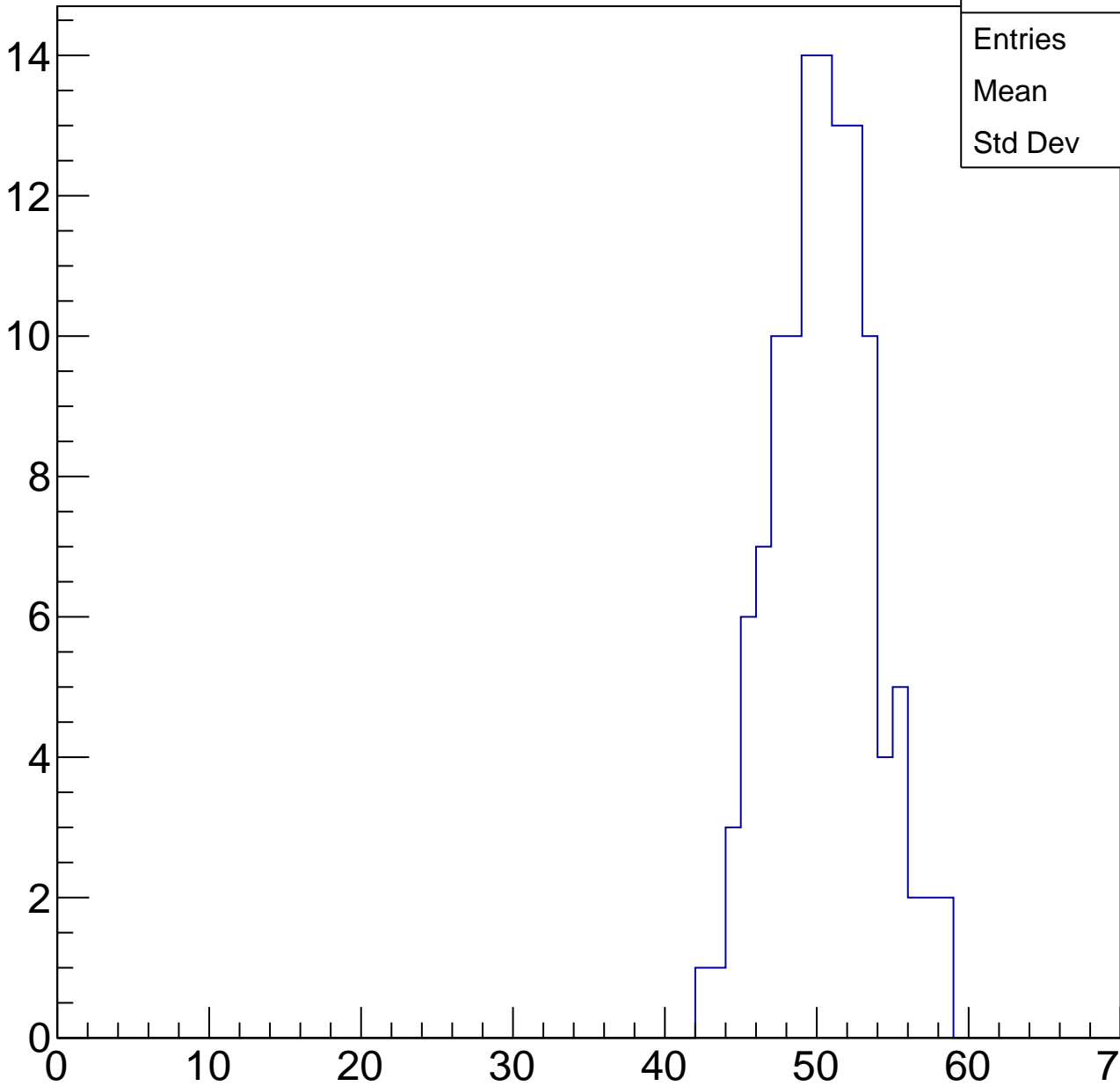
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	49.97
Std Dev	3.293

ampl



B0L101S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

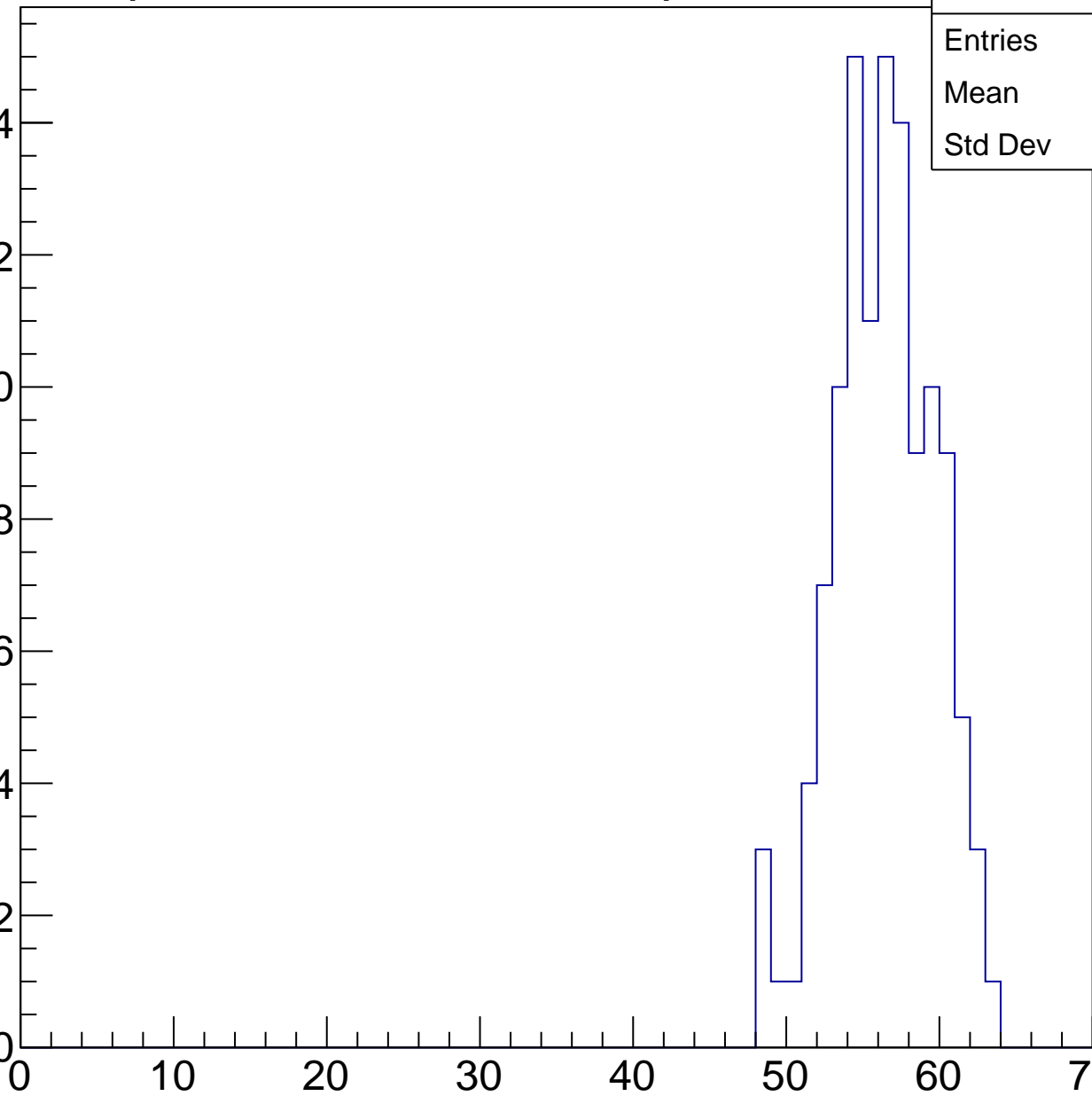
4

2

0

Entries	118
Mean	55.93
Std Dev	3.209

ampl



B0L101S, U14-ch23, adc5

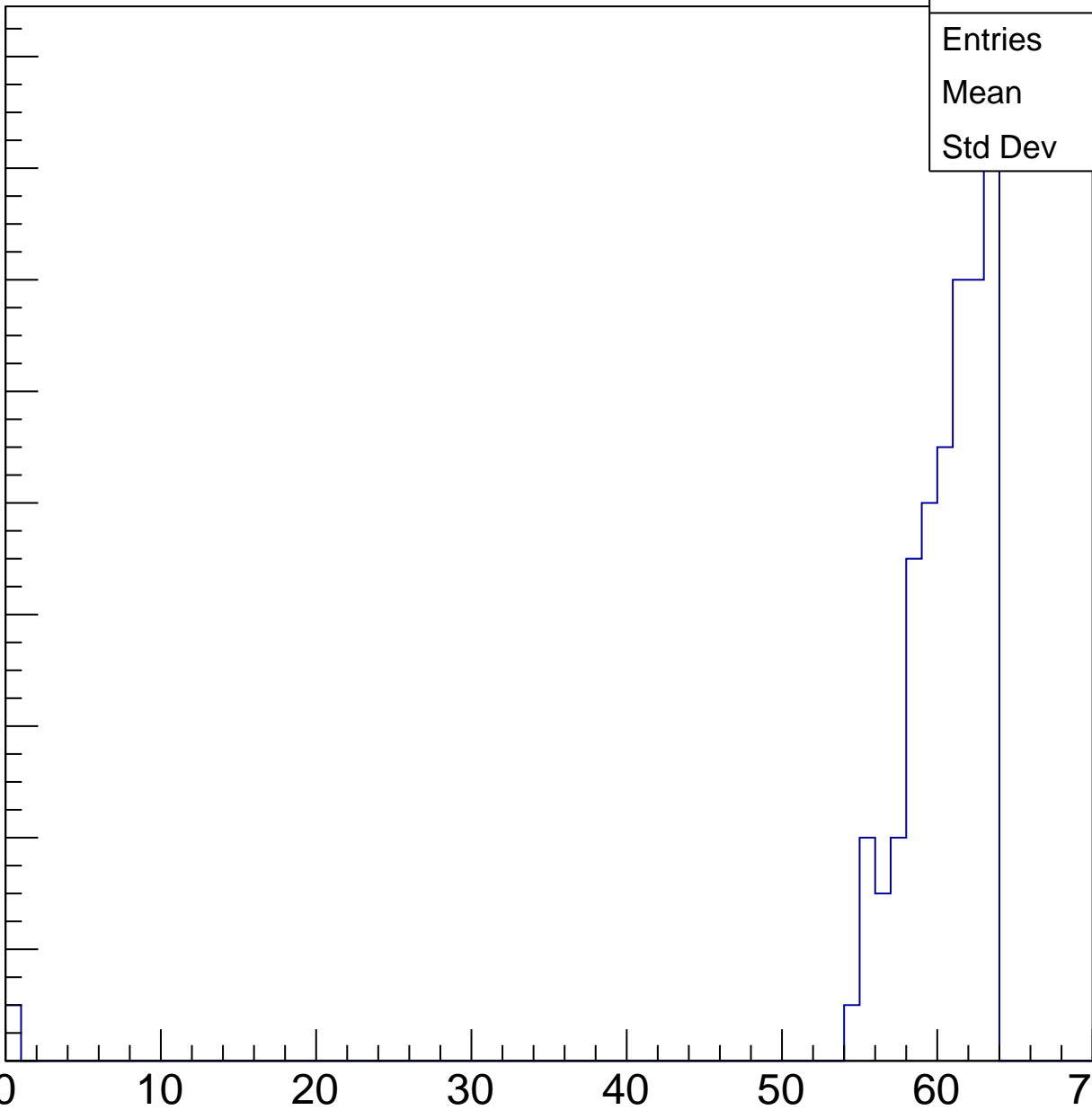
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.53
Std Dev	6.769

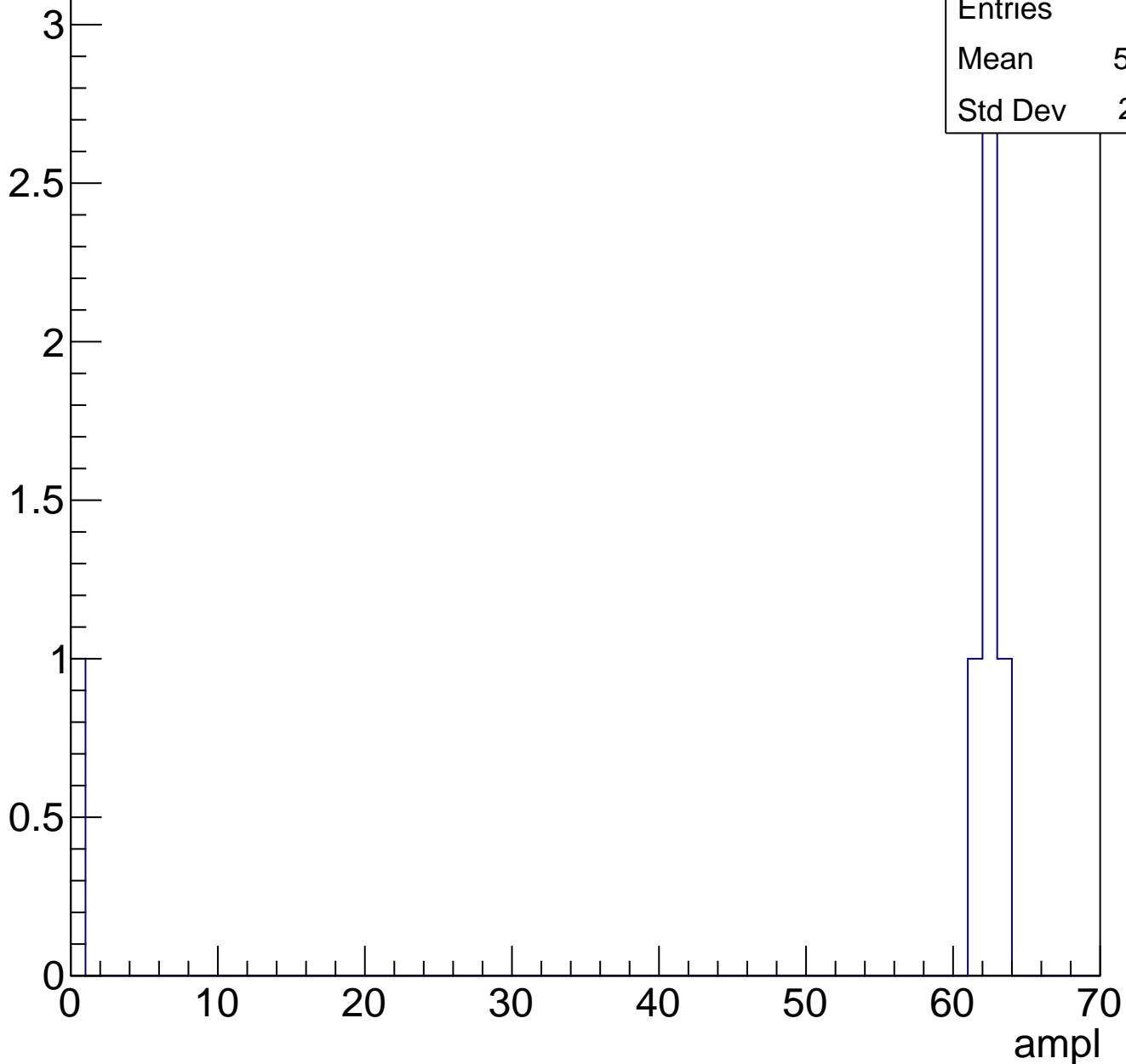
ampl



B0L101S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch24, adc0

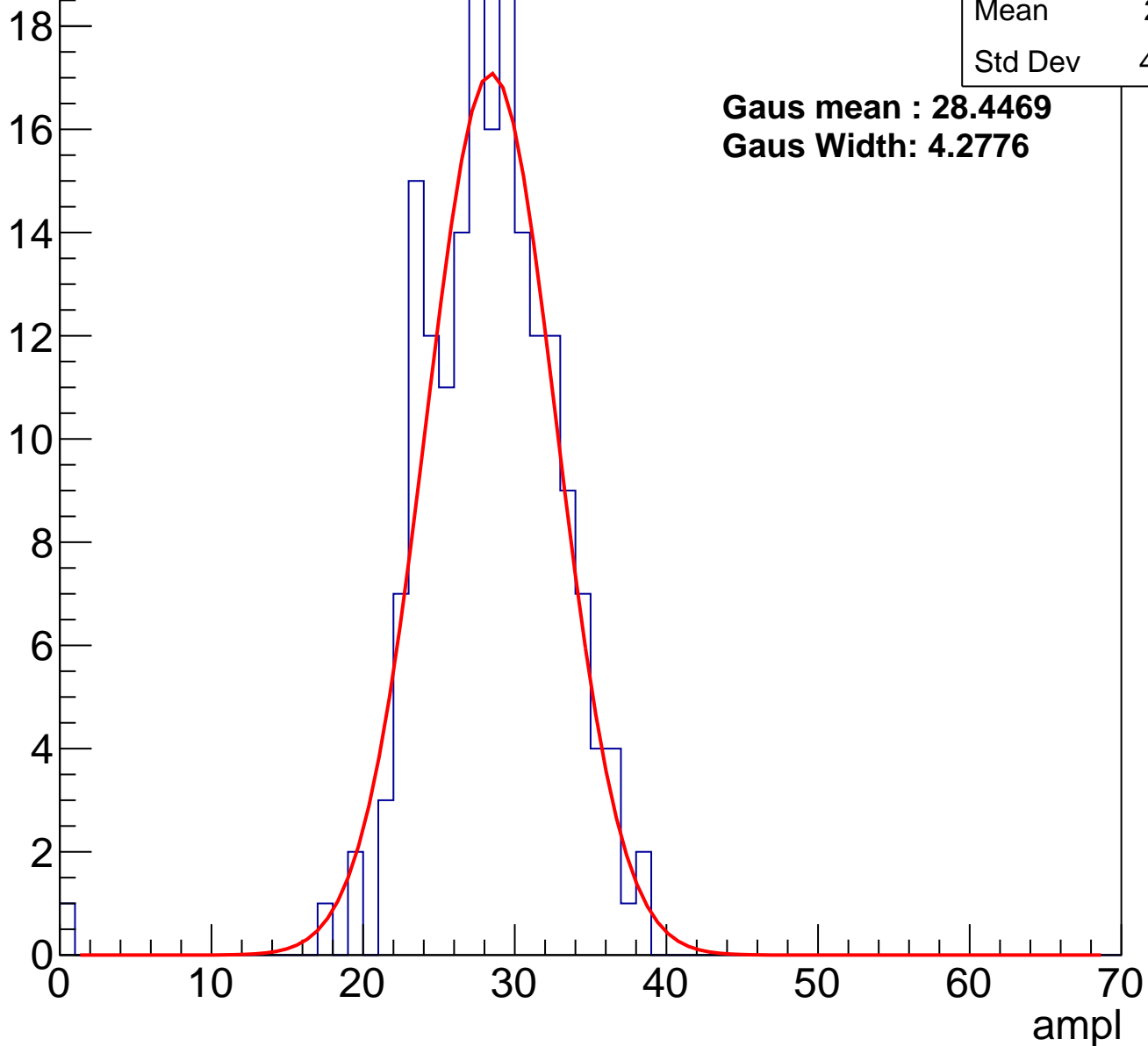
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	27.91
Std Dev	4.527

Gaus mean : 28.4469

Gaus Width: 4.2776

Entry



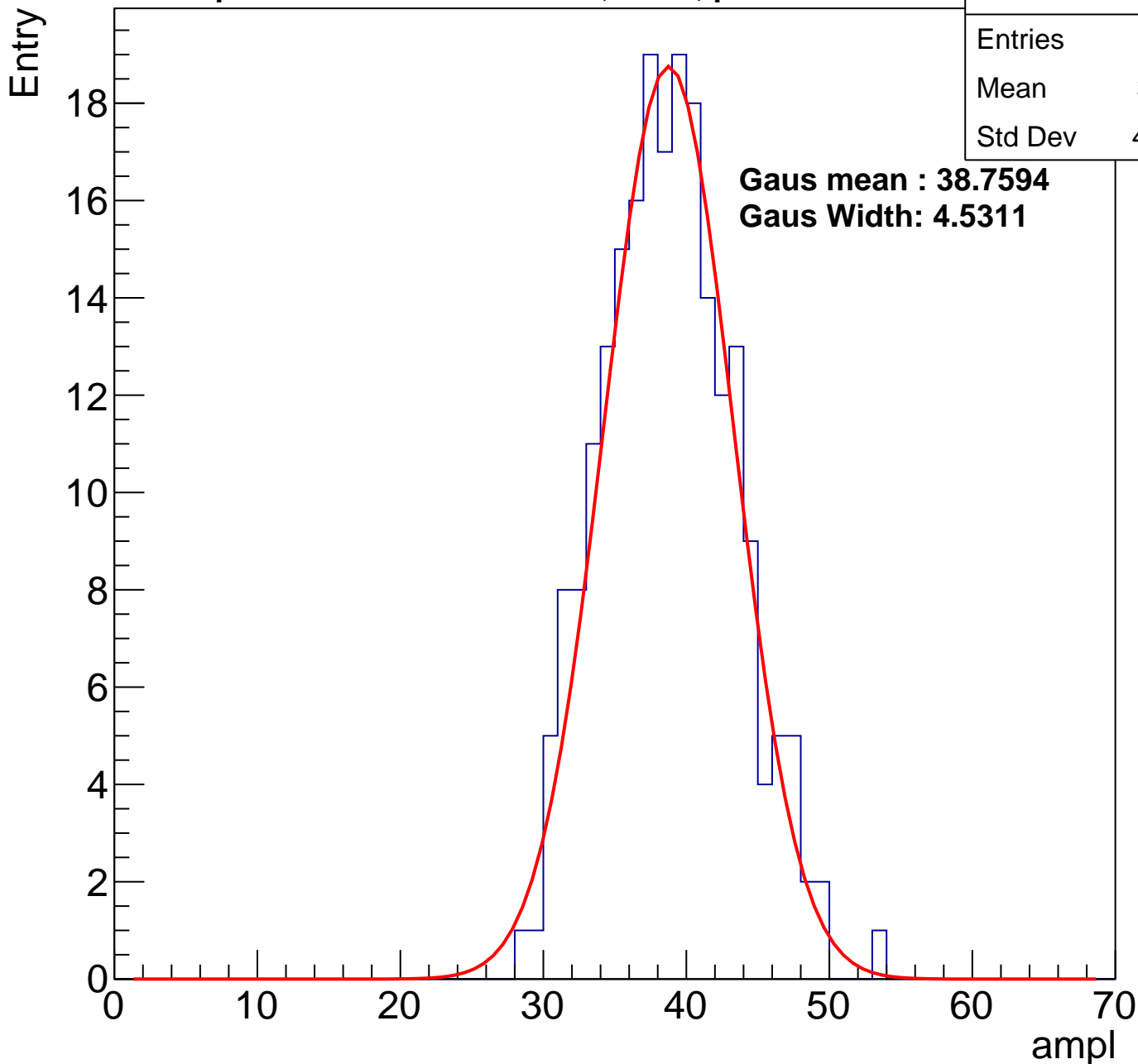
B0L101S, U14-ch24, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	218
Mean	38.31
Std Dev	4.547

Gaus mean : 38.7594

Gaus Width: 4.5311



B0L101S, U14-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	54
Mean	45.17
Std Dev	3.371

Gaus mean : 45.6005

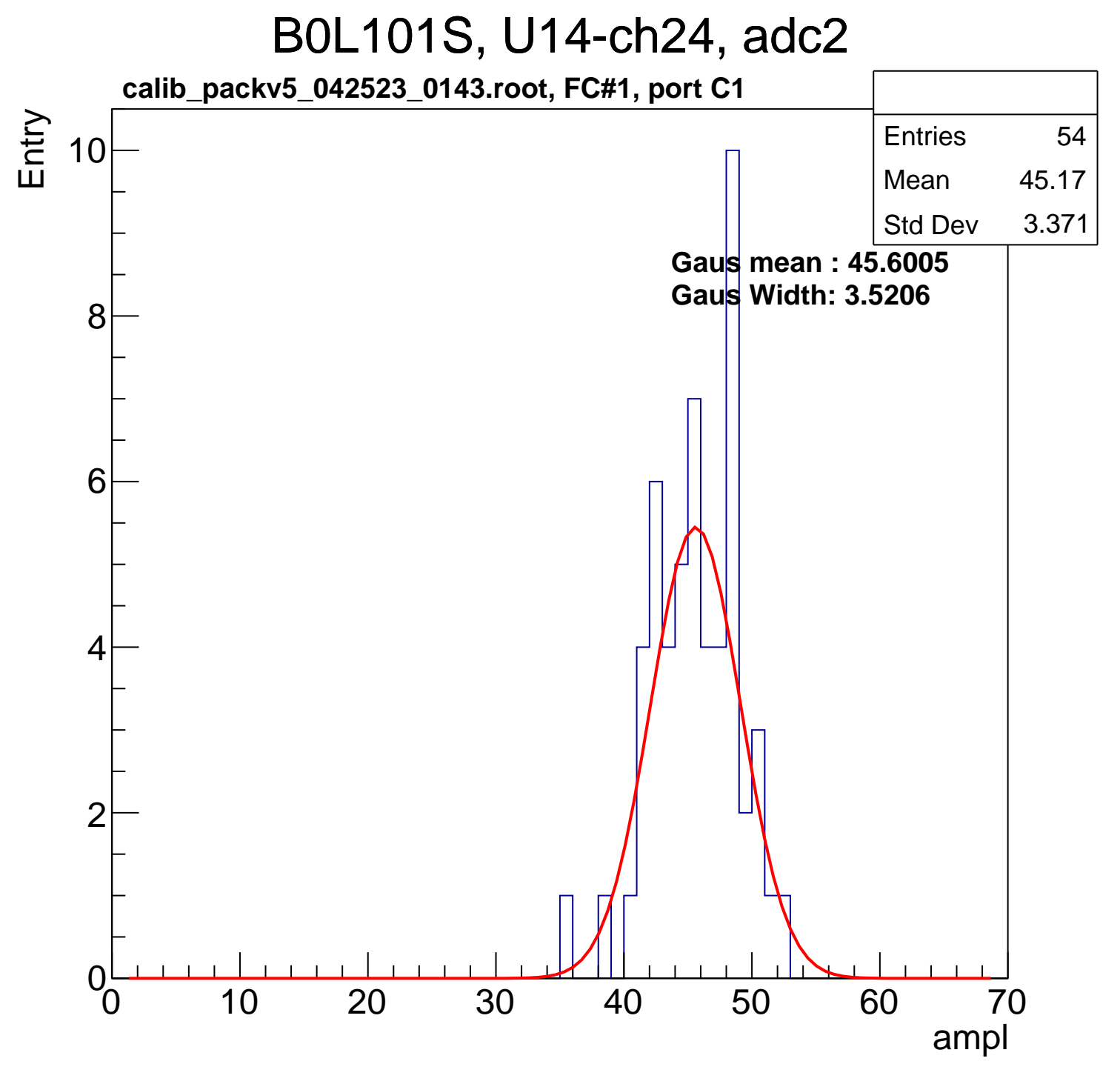
Gaus Width: 3.5206

Entry

10
8
6
4
2
0

ampl

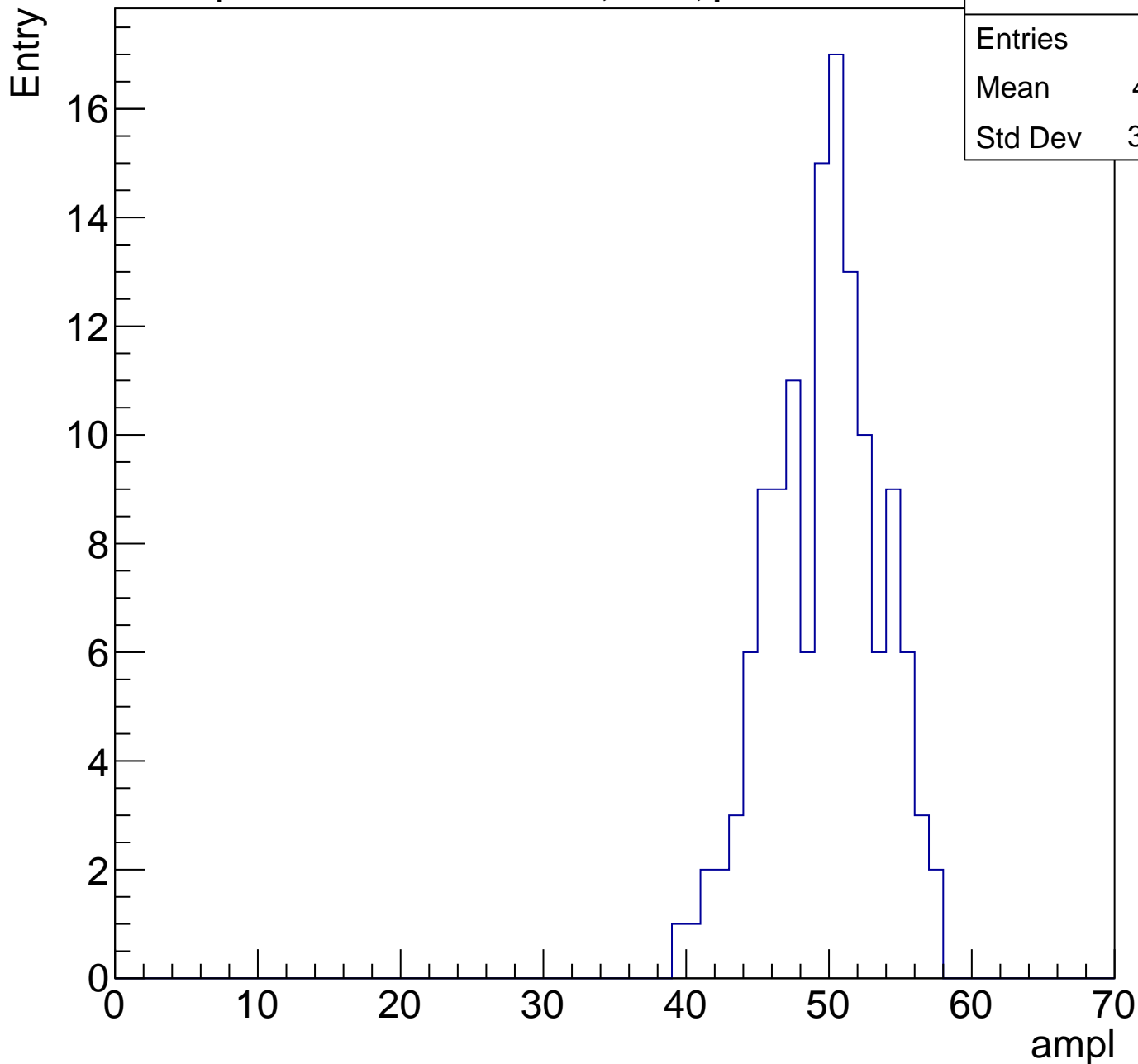
0 10 20 30 40 50 60 70



B0L101S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	49.21
Std Dev	3.814



B0L101S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

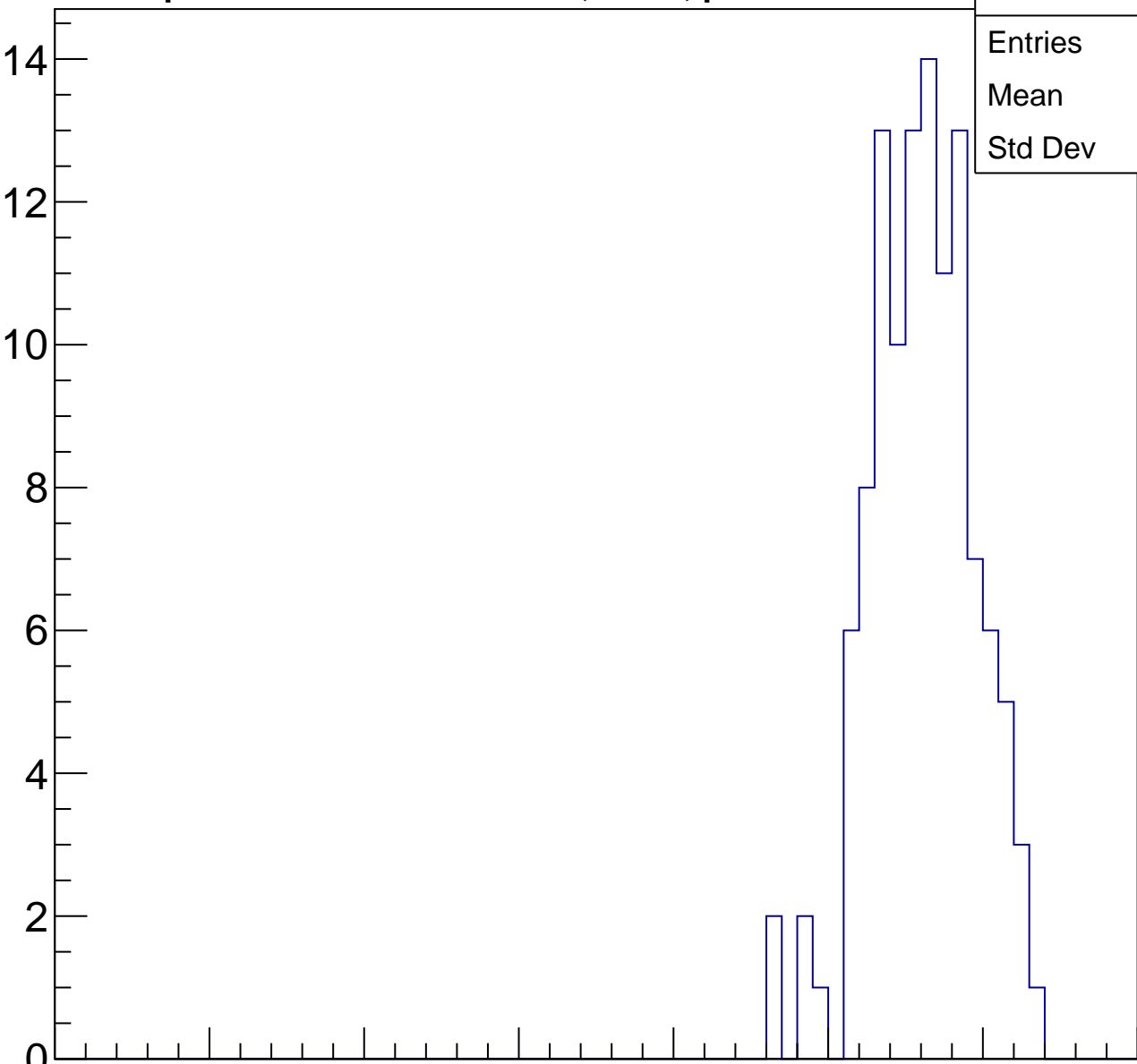
Entries	115
Mean	55.61
Std Dev	3.377

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch24, adc5

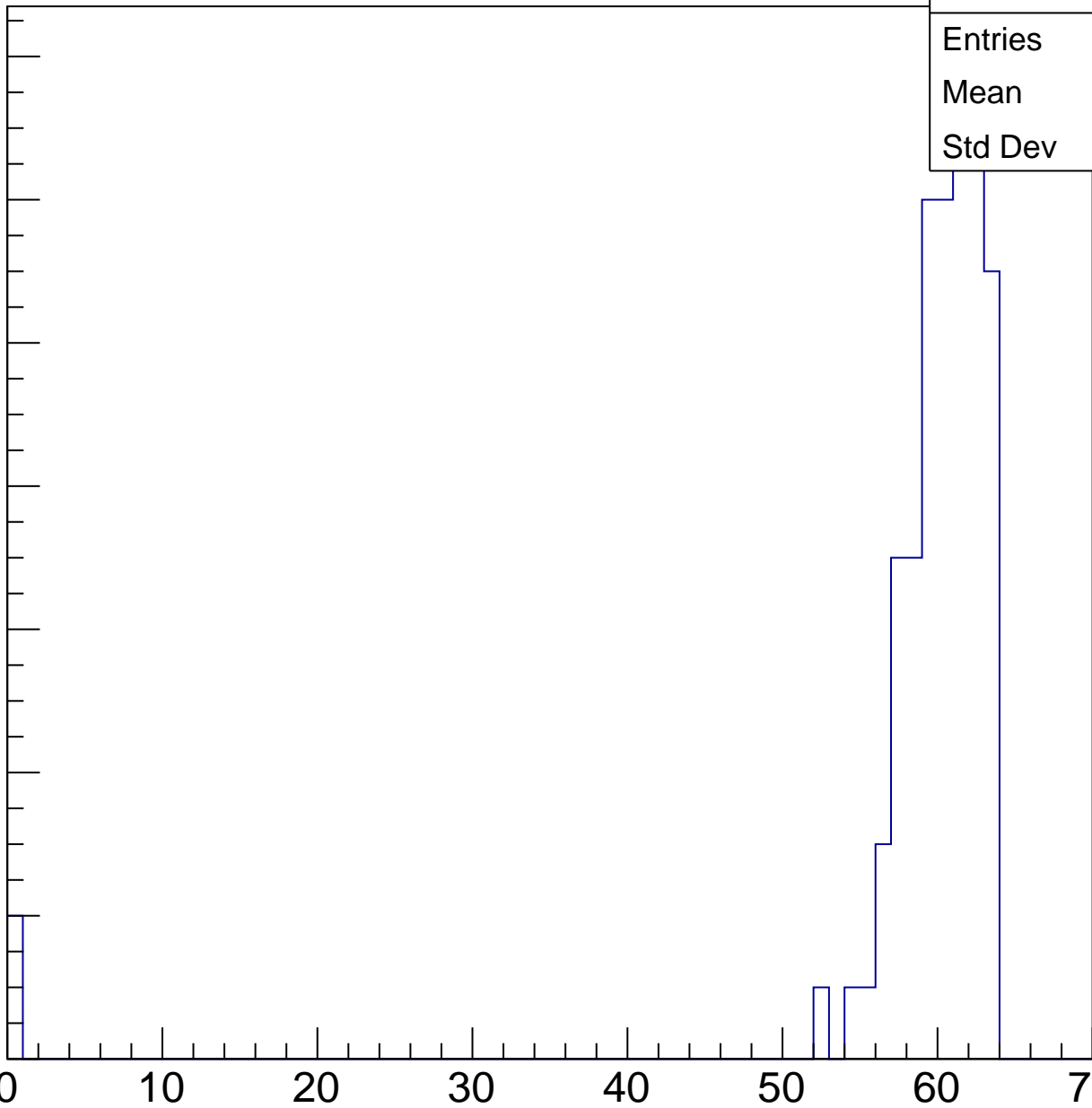
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	58.55
Std Dev	9.373

ampl

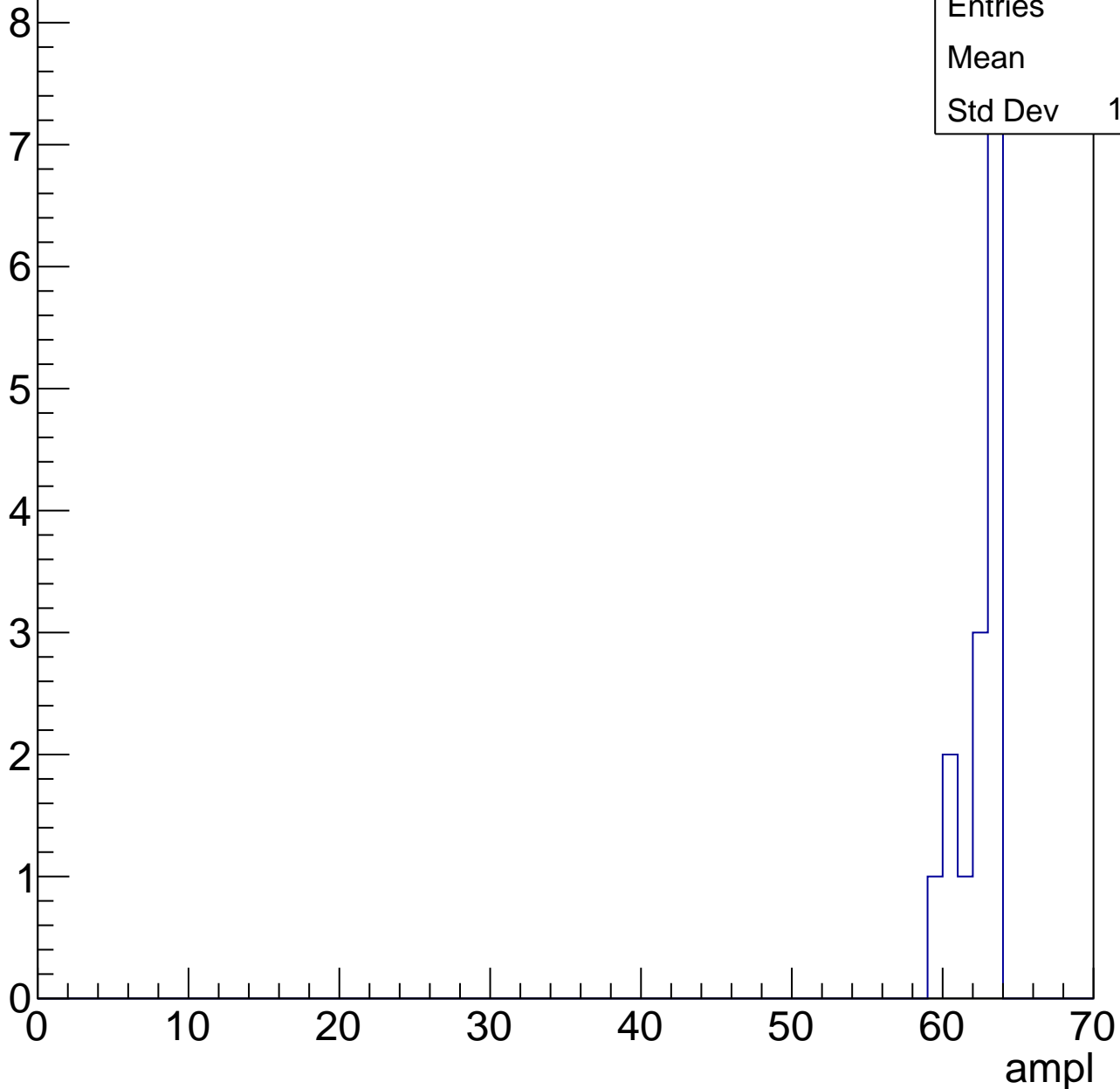


B0L101S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

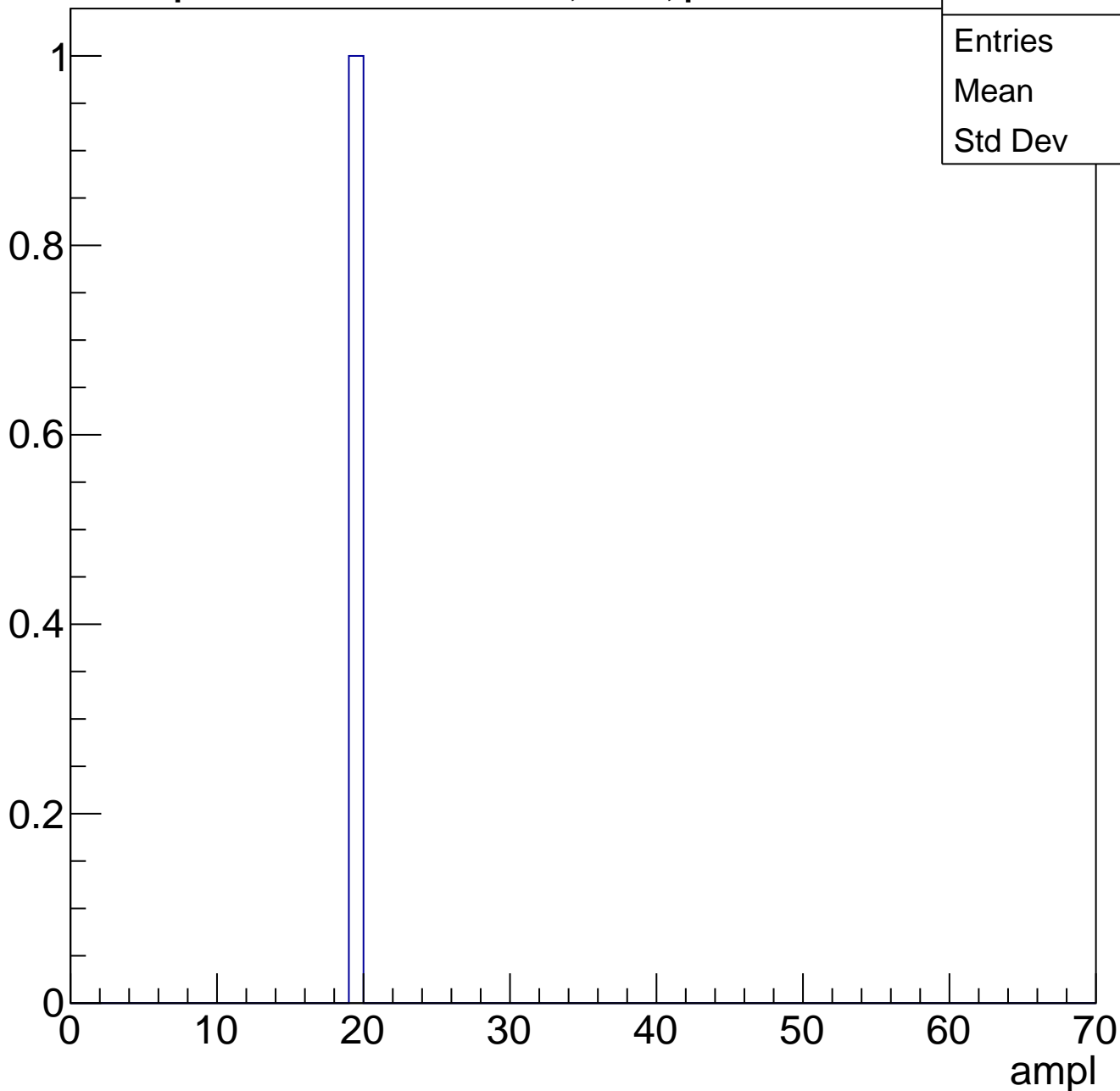
Entries	15
Mean	62
Std Dev	1.317



B0L101S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch25, adc0

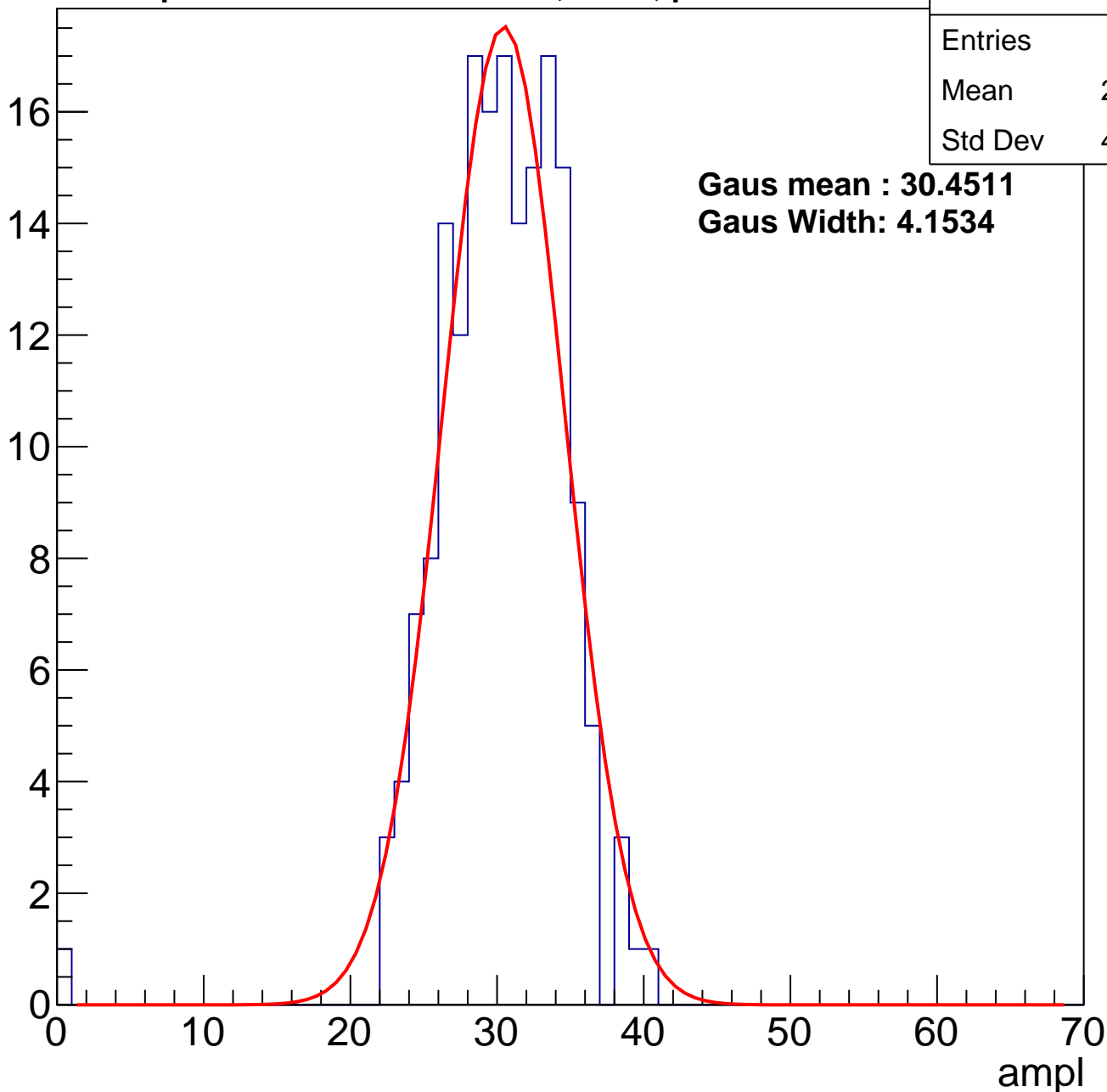
calib_packv5_042523_0143.root, FC#1, port C1

Entries	179
Mean	29.82
Std Dev	4.355

Gaus mean : 30.4511

Gaus Width: 4.1534

Entry



B0L101S, U14-ch25, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

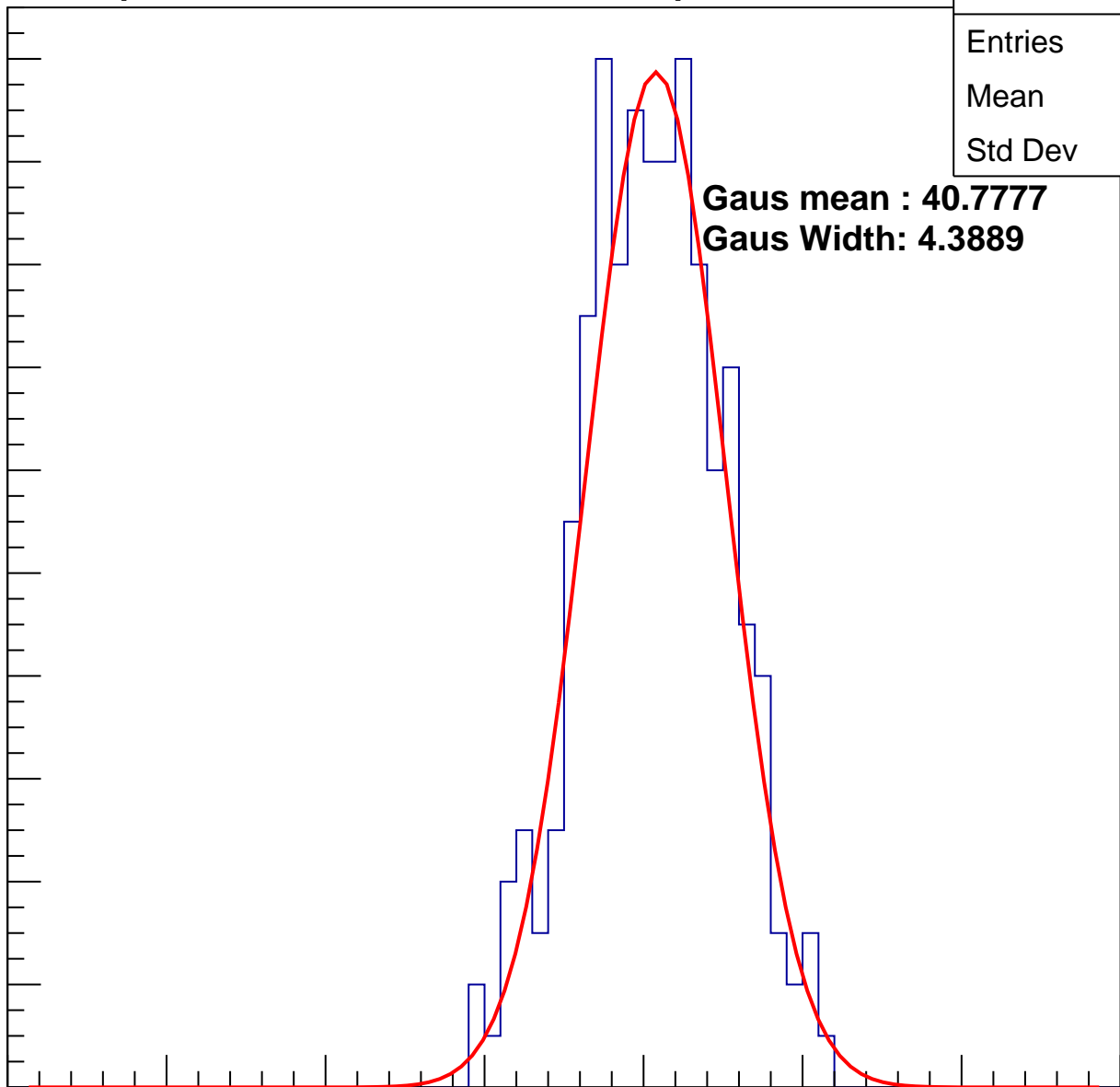
Entries	225
Mean	40.14
Std Dev	4.403

Gaus mean : 40.7777

Gaus Width: 4.3889

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch25, adc2

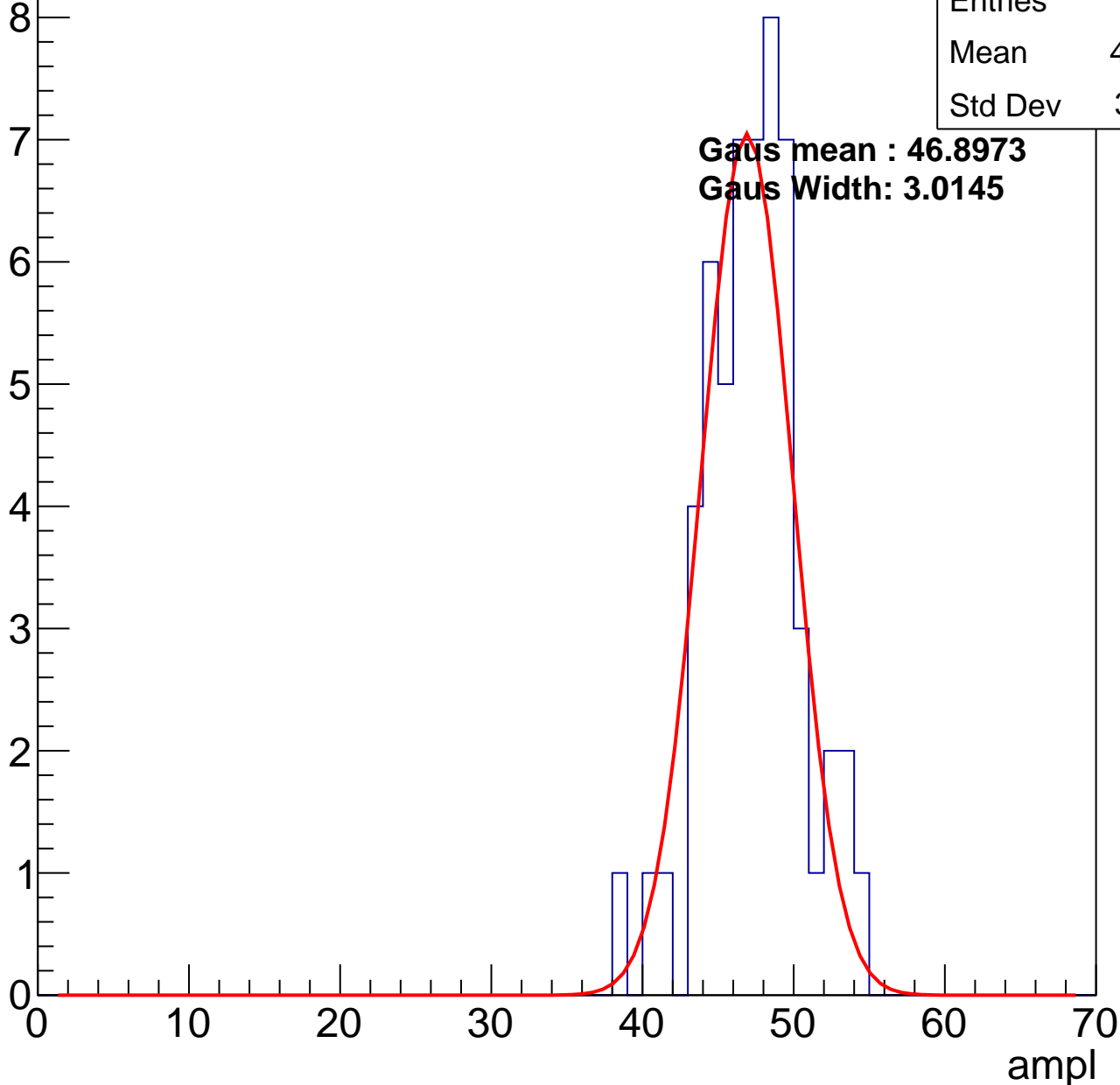
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	56
Mean	46.84
Std Dev	3.161

Gaus mean : 46.8973

Gaus Width: 3.0145

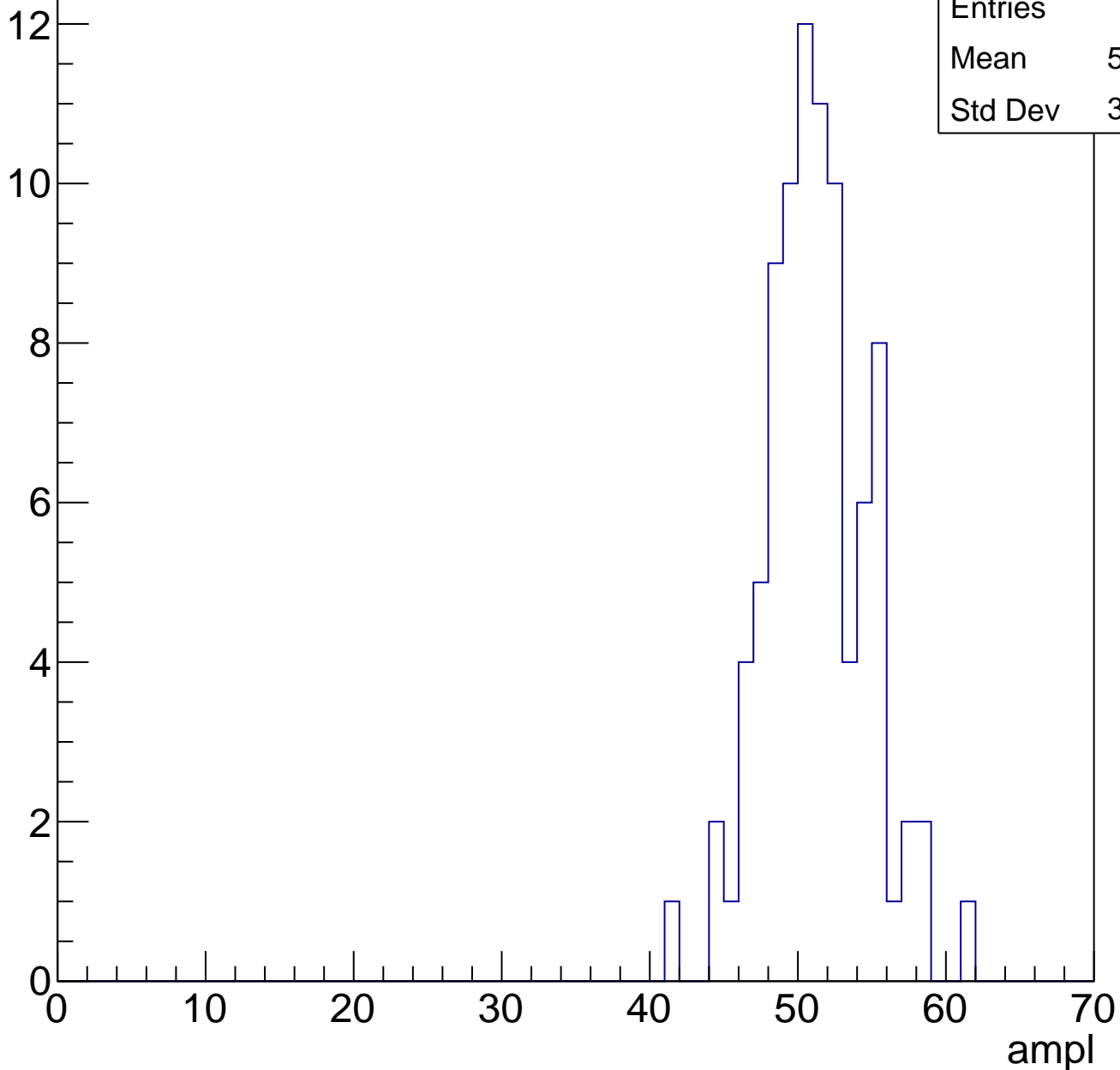


B0L101S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	50.78
Std Dev	3.427

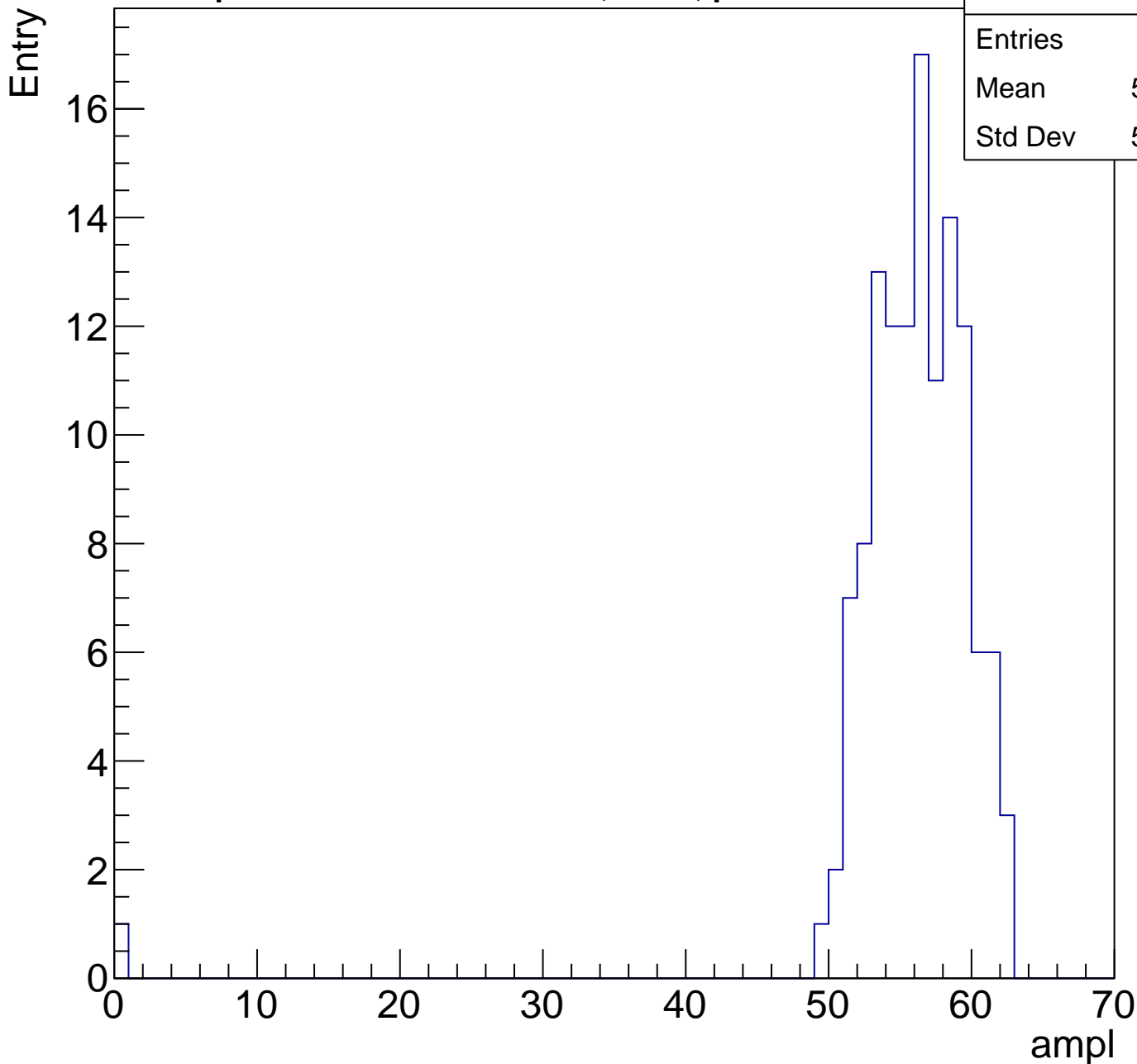
Entry



B0L101S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.44
Std Dev	5.815



B0L101S, U14-ch25, adc5

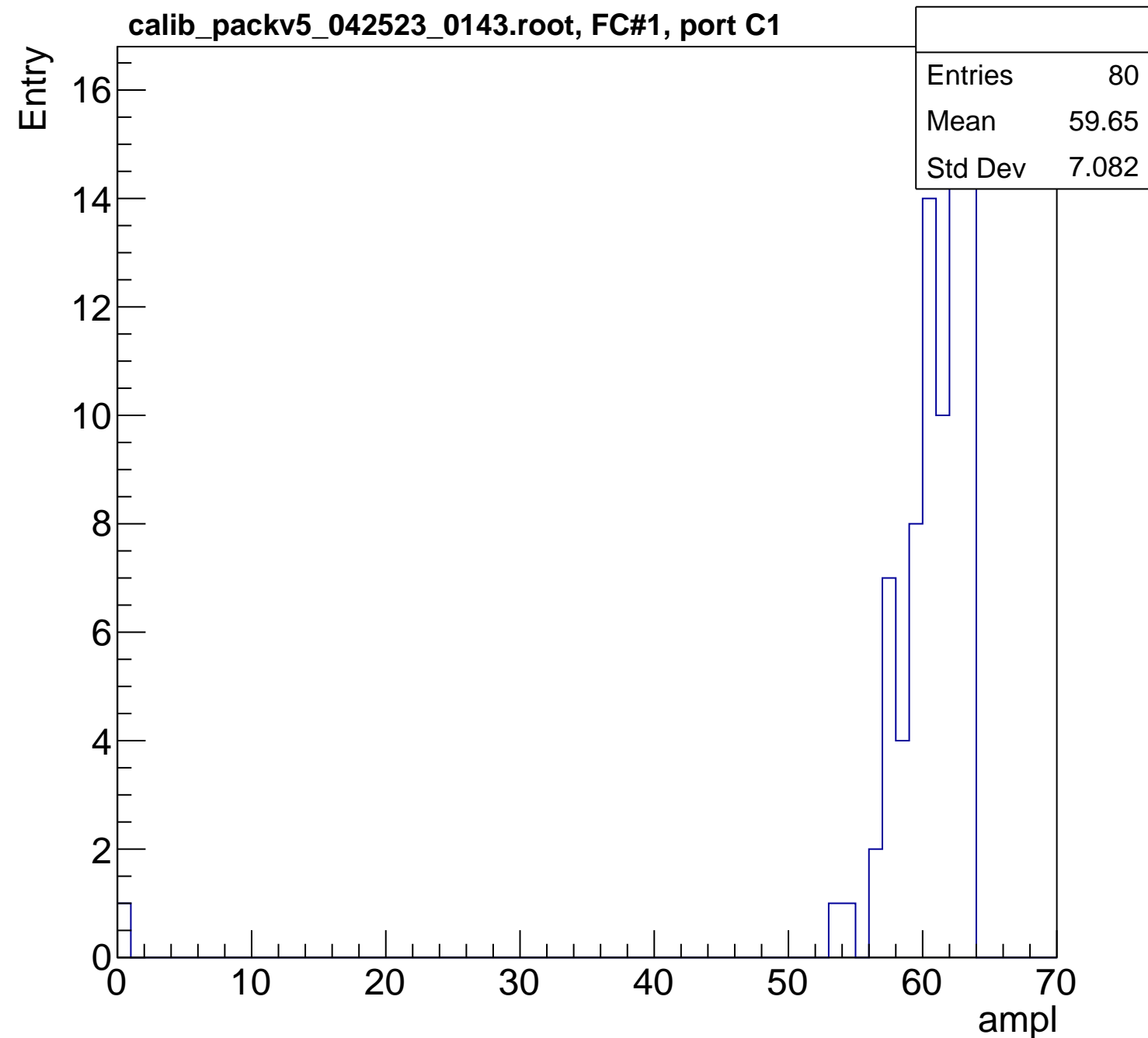
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.65
Std Dev	7.082

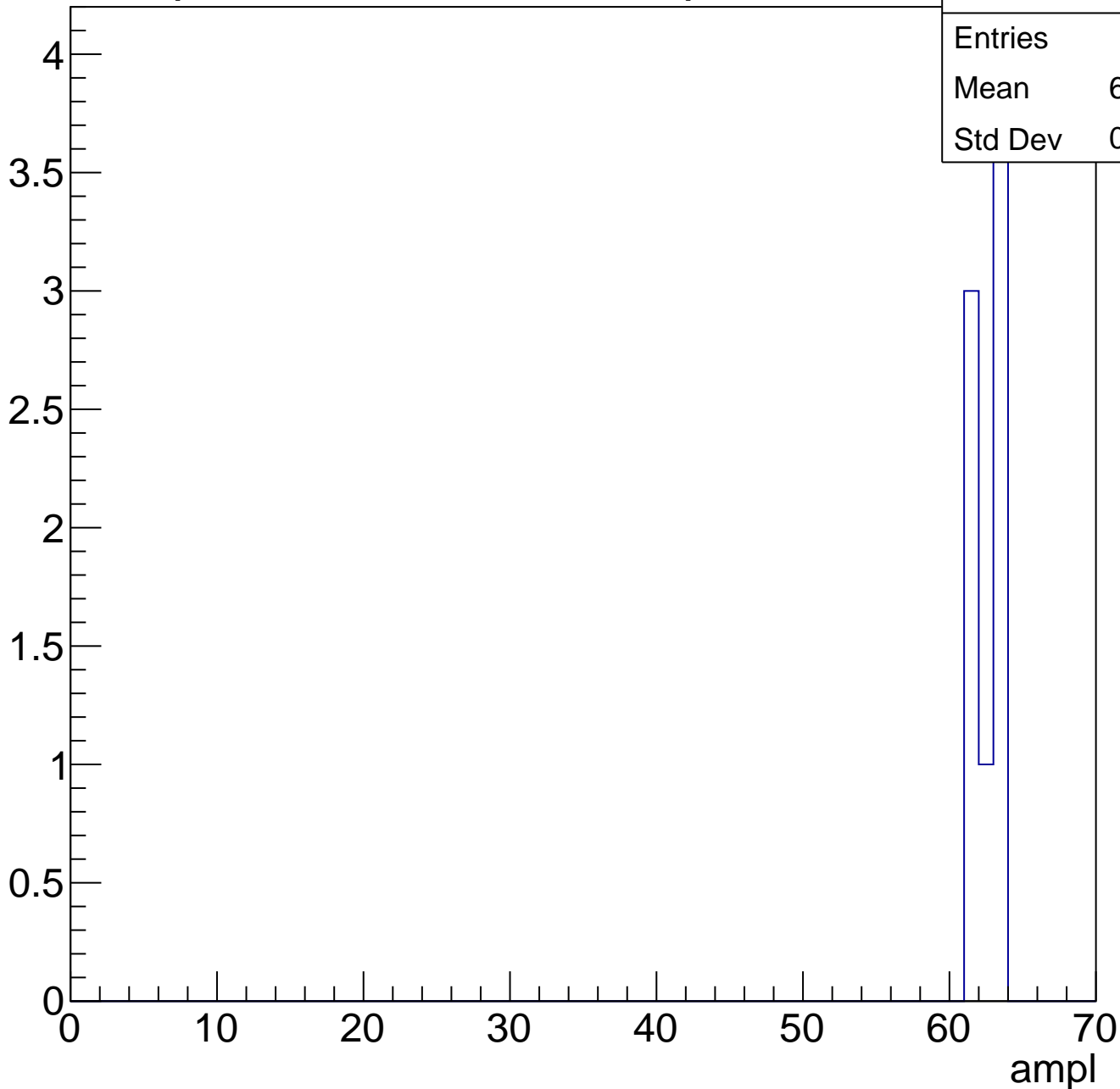
ampl



B0L101S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch26, adc0

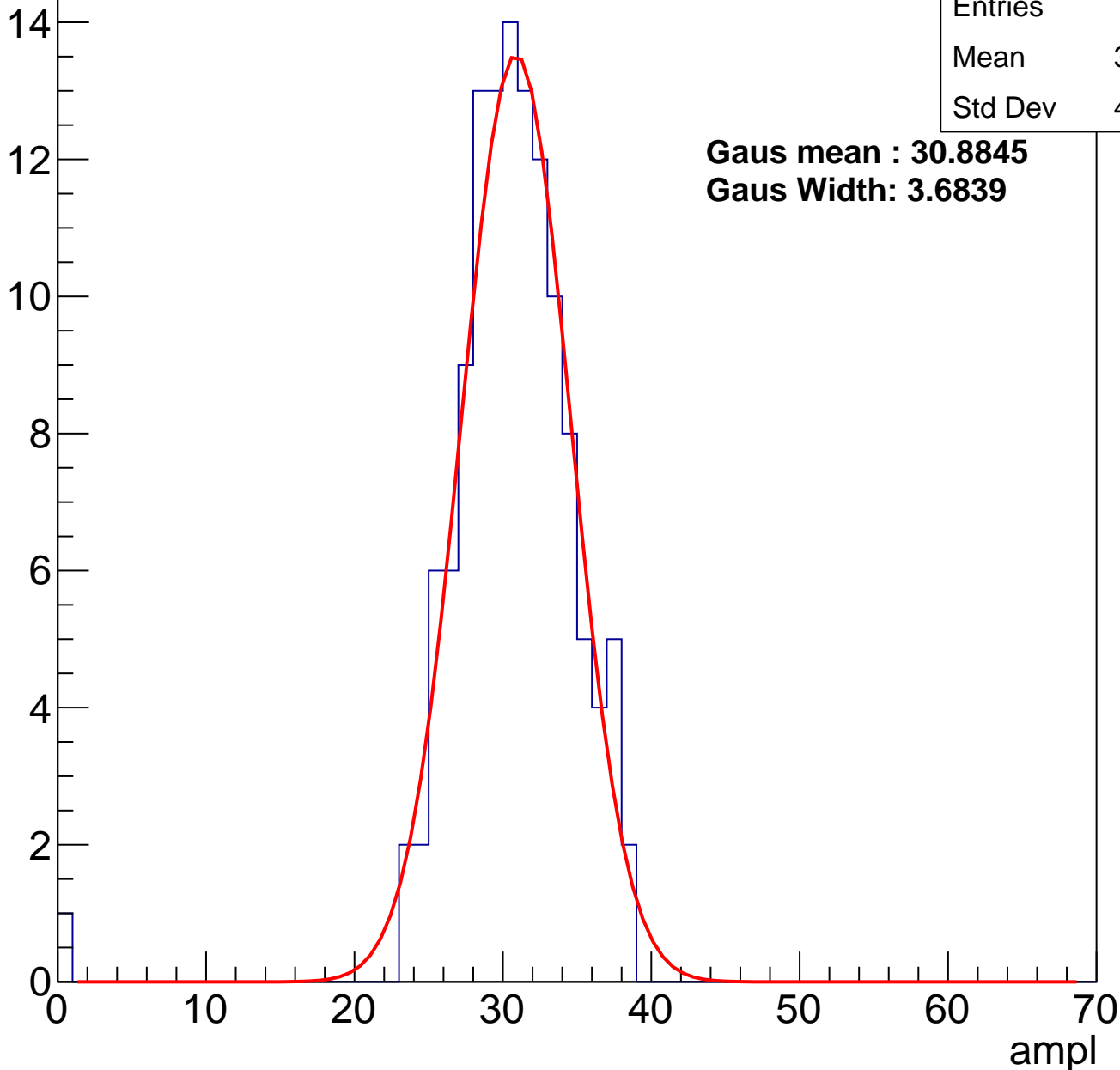
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	30.18
Std Dev	4.362

Gaus mean : 30.8845

Gaus Width: 3.6839

Entry



B0L101S, U14-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

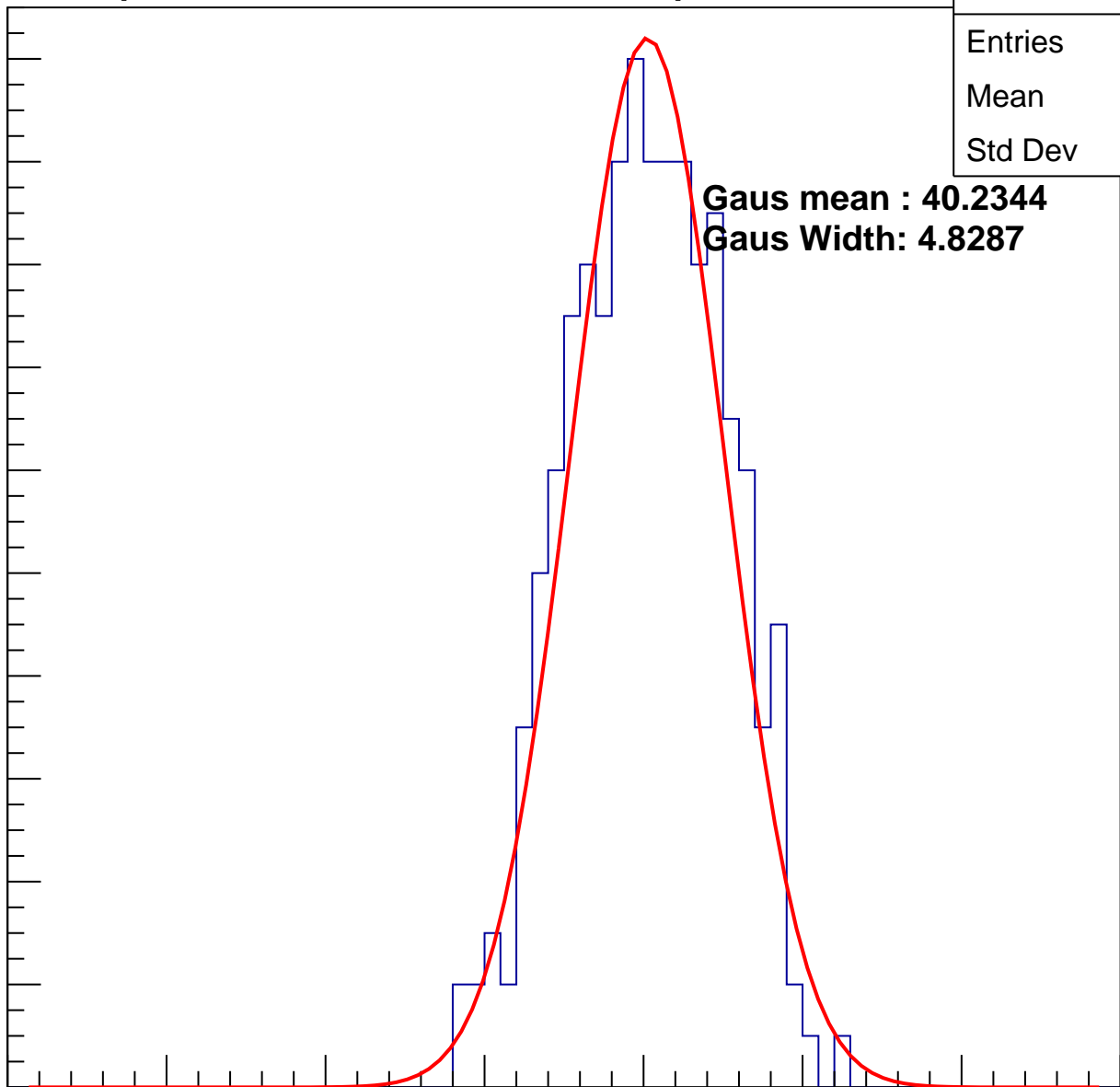
20
18
16
14
12
10
8
6
4
2
0

Entries	254
Mean	39.75
Std Dev	4.775

Gaus mean : 40.2344
Gaus Width: 4.8287

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch26, adc2

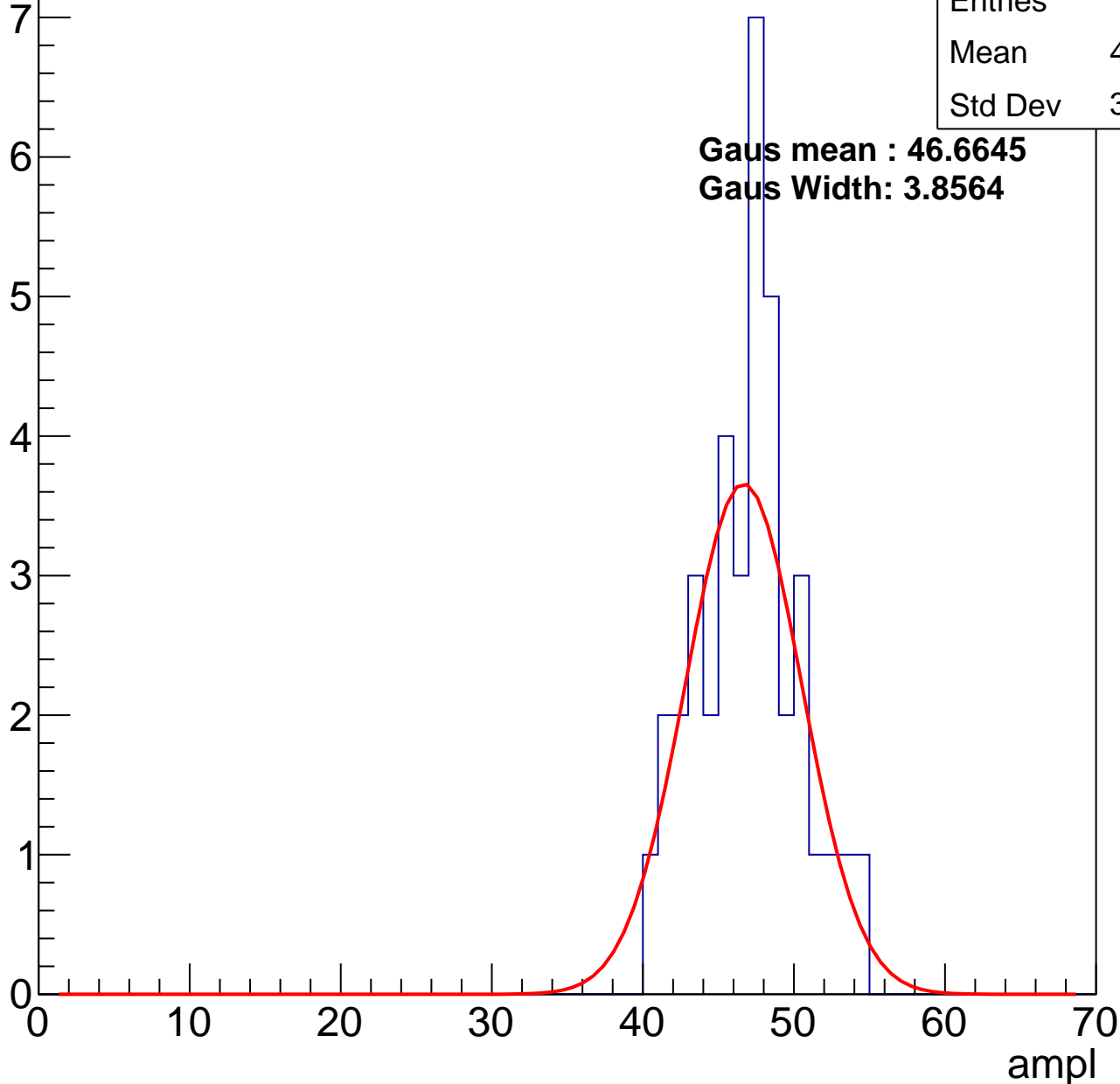
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	38
Mean	46.53
Std Dev	3.299

Gaus mean : 46.6645

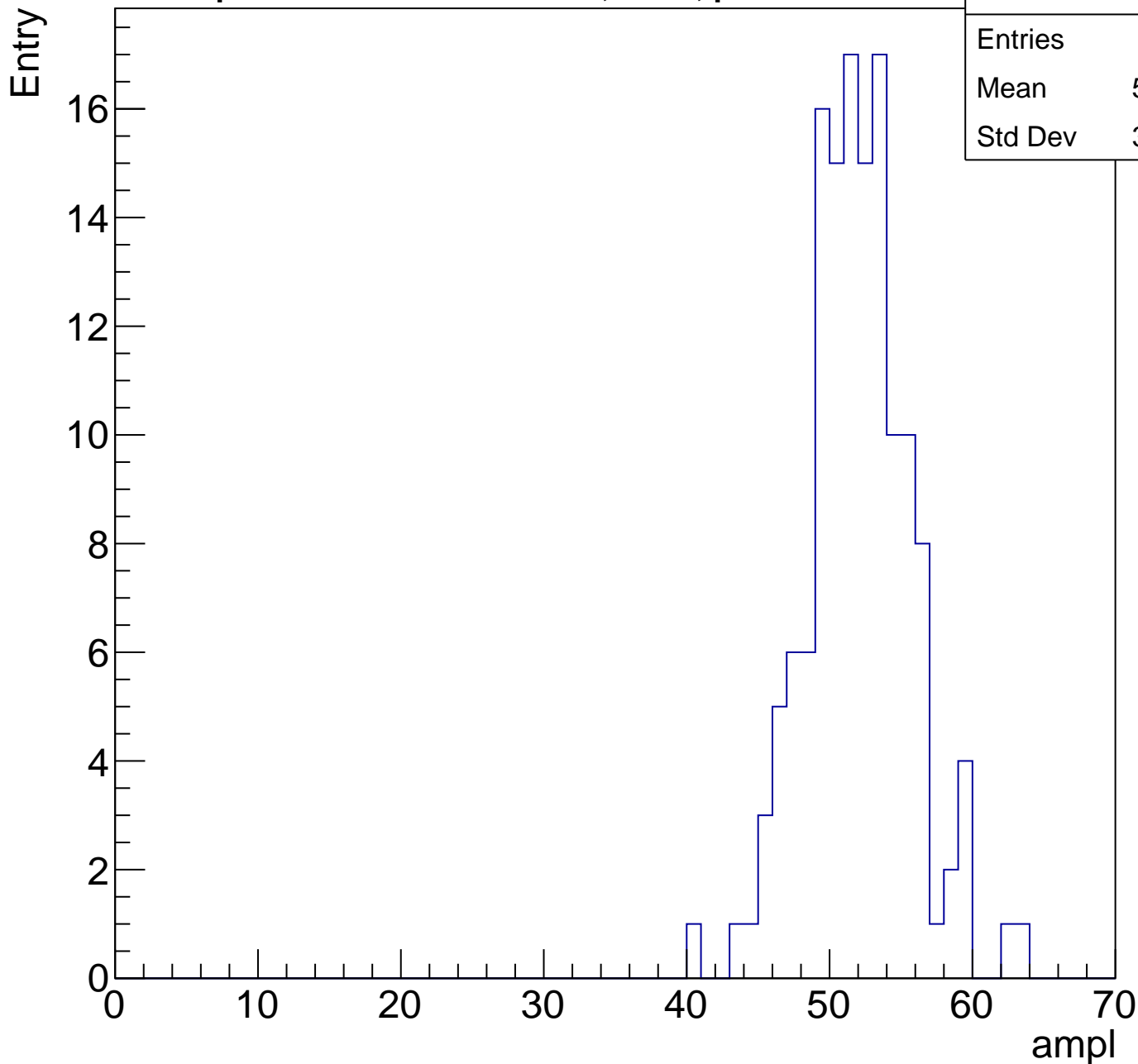
Gaus Width: 3.8564



B0L101S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

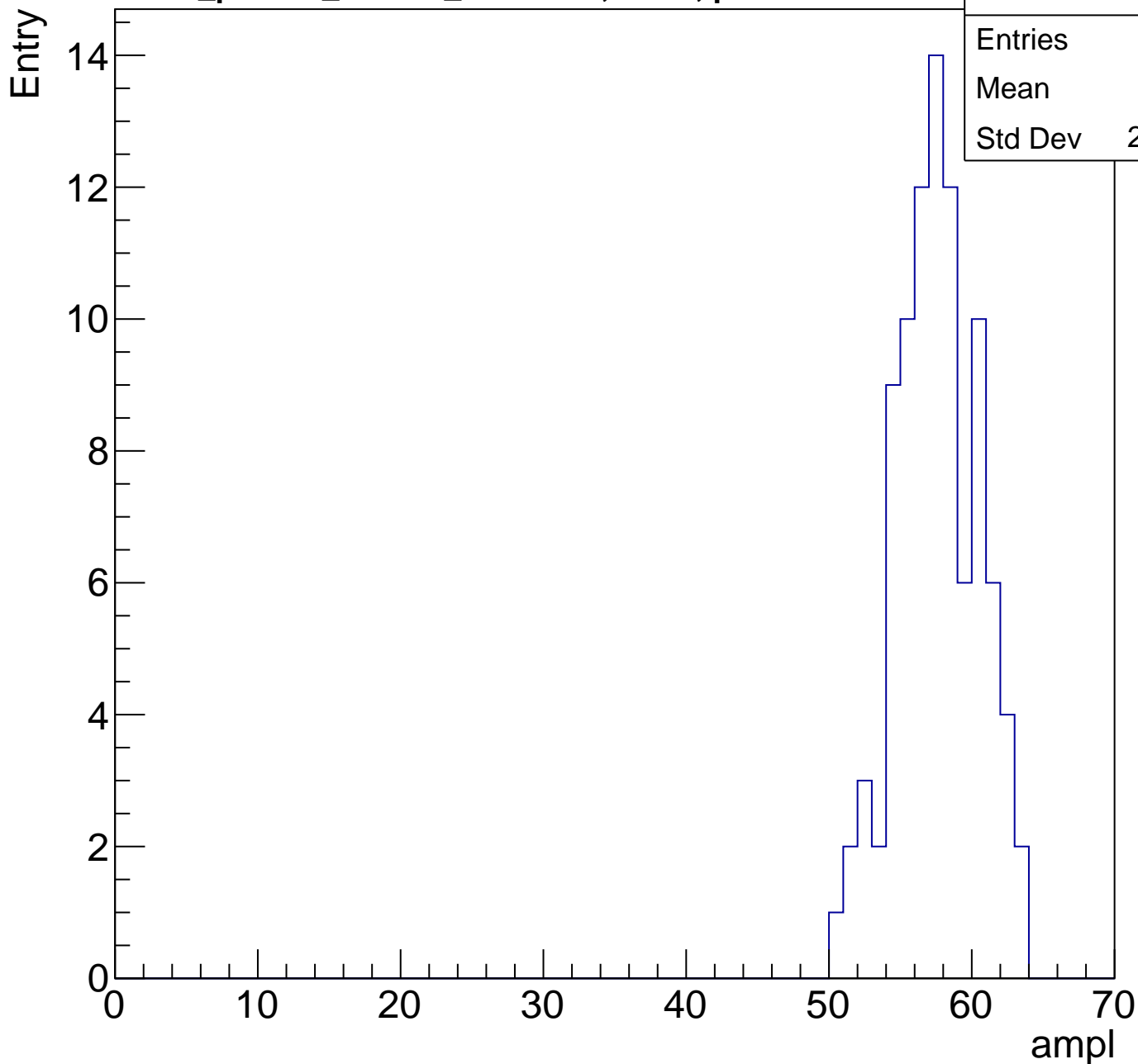
Entries	140
Mean	51.54
Std Dev	3.628



B0L101S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	57.1
Std Dev	2.848



B0L101S, U14-ch26, adc5

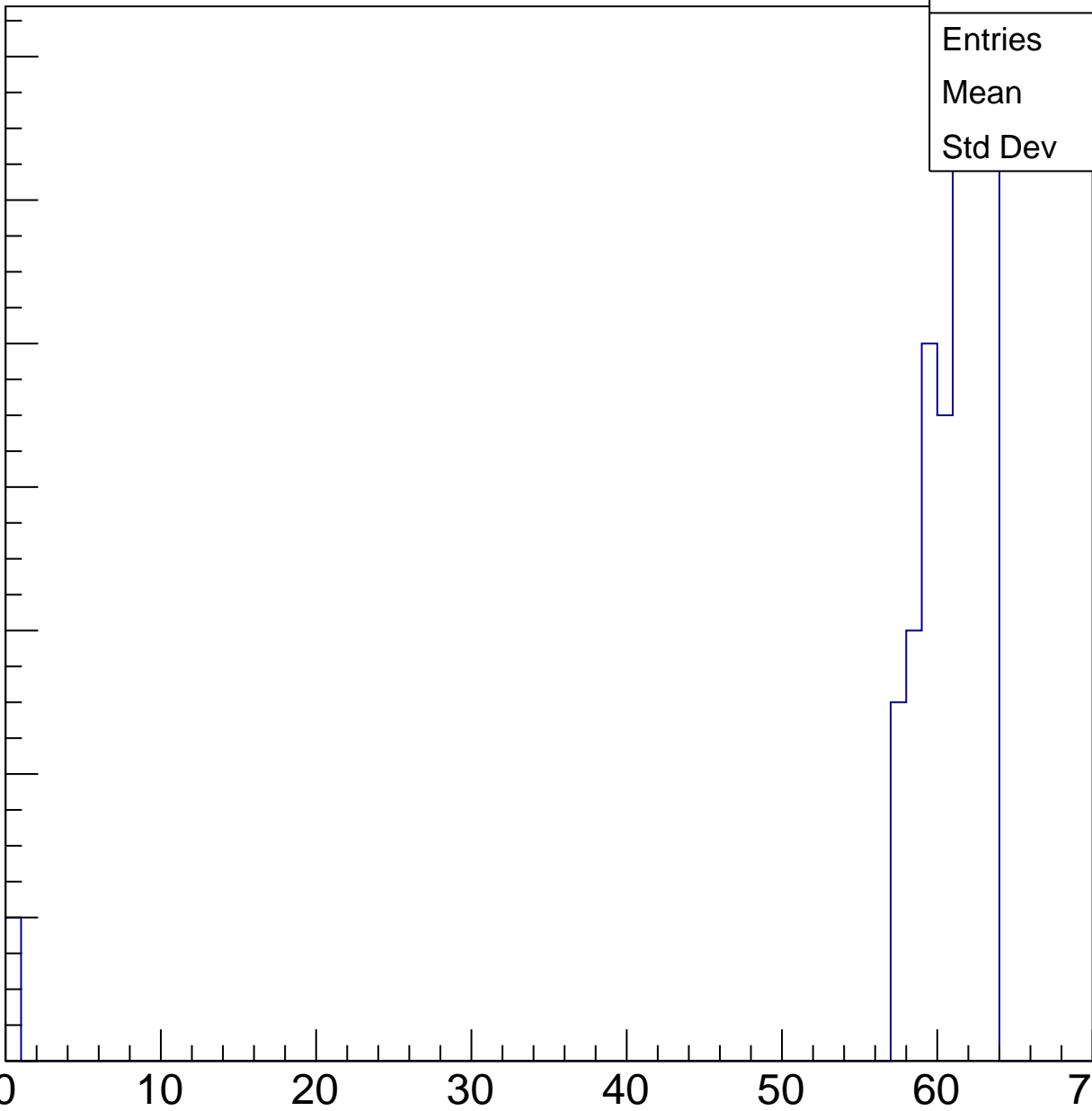
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.92
Std Dev	10.12

ampl



B0L101S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U14-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	29.33
Std Dev	4.339

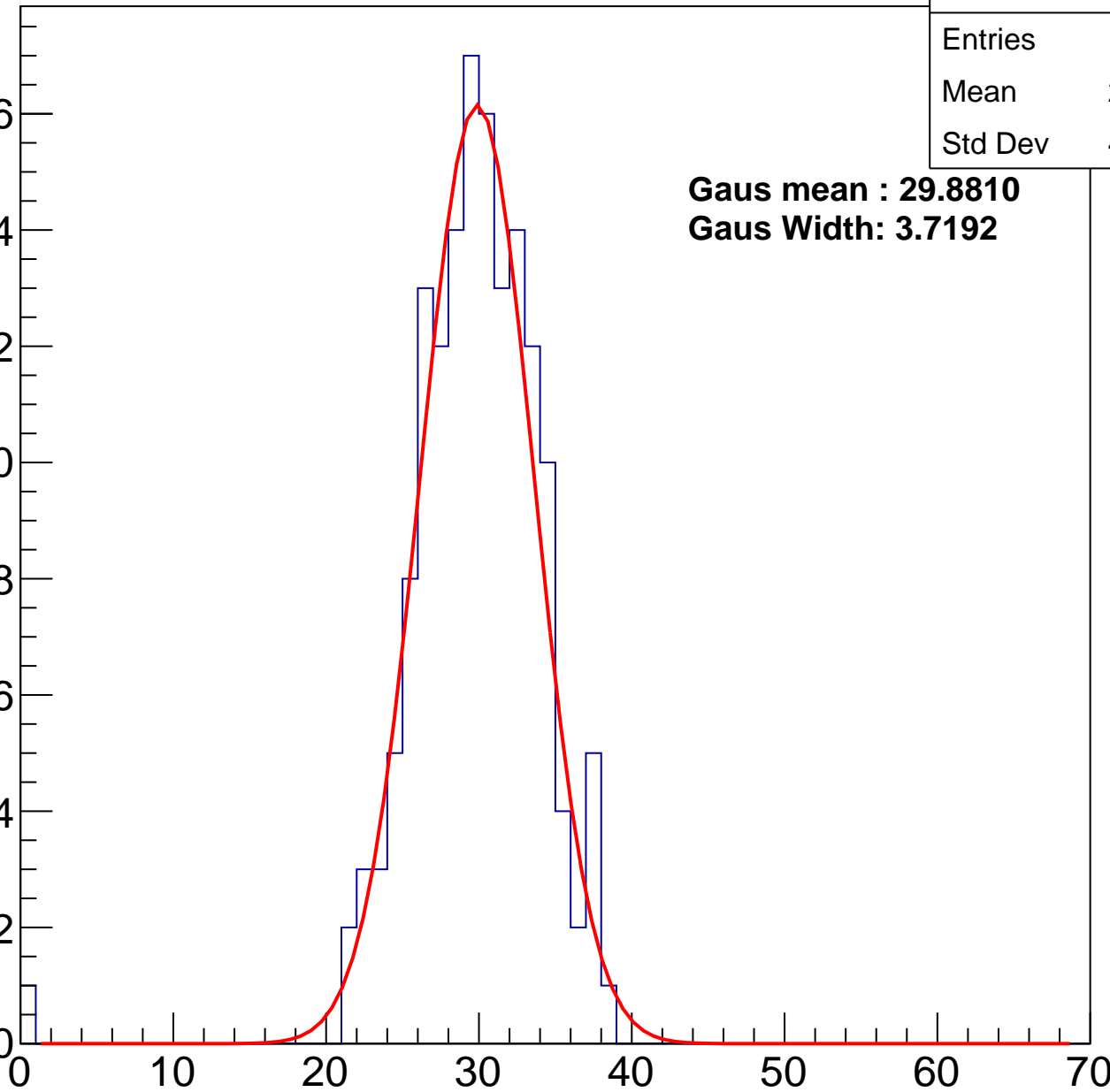
Gaus mean : 29.8810

Gaus Width: 3.7192

Entry

16
14
12
10
8
6
4
2
0

ampl



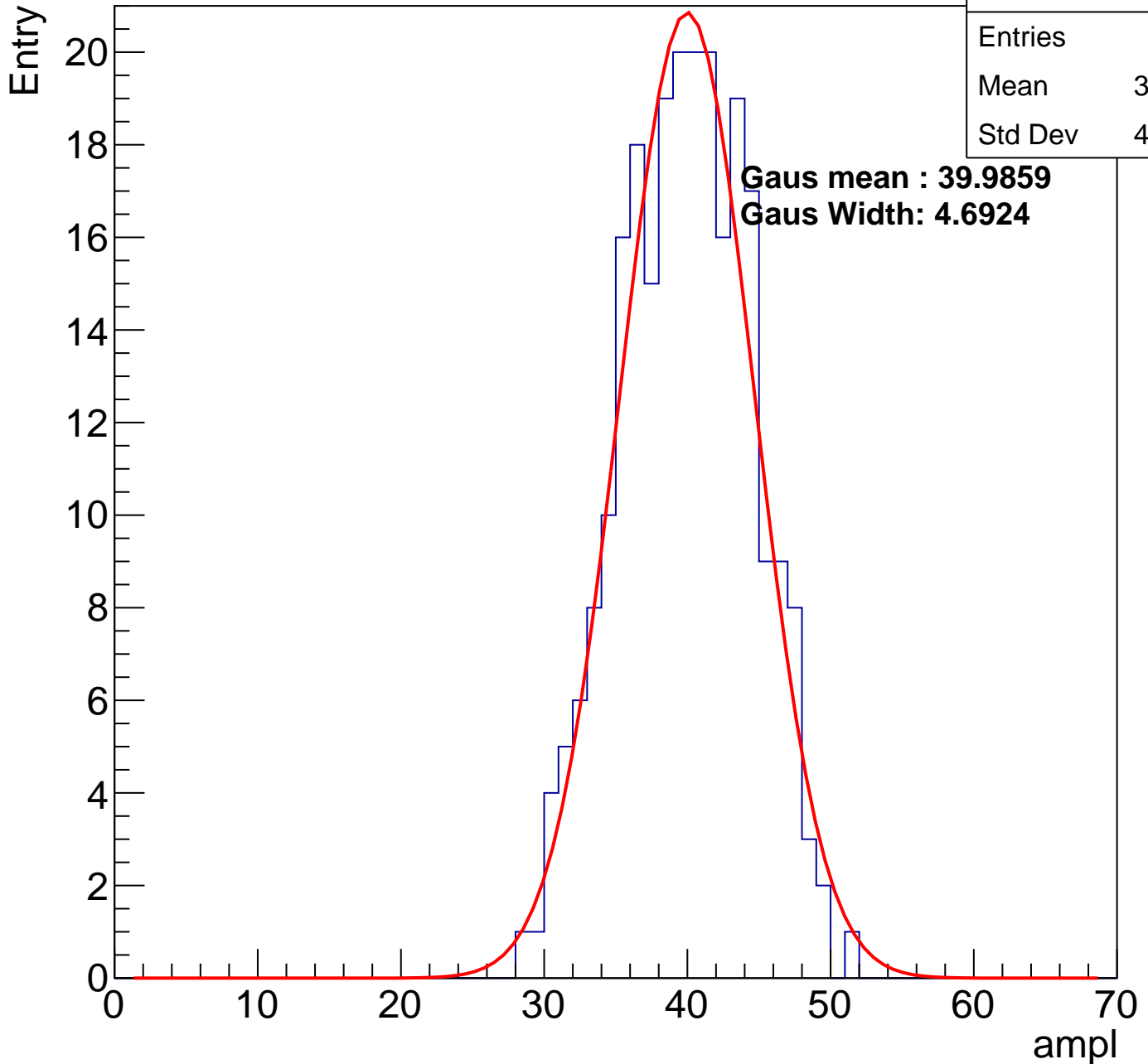
B0L101S, U14-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	247
Mean	39.43
Std Dev	4.493

Gaus mean : 39.9859

Gaus Width: 4.6924



B0L101S, U14-ch27, adc2

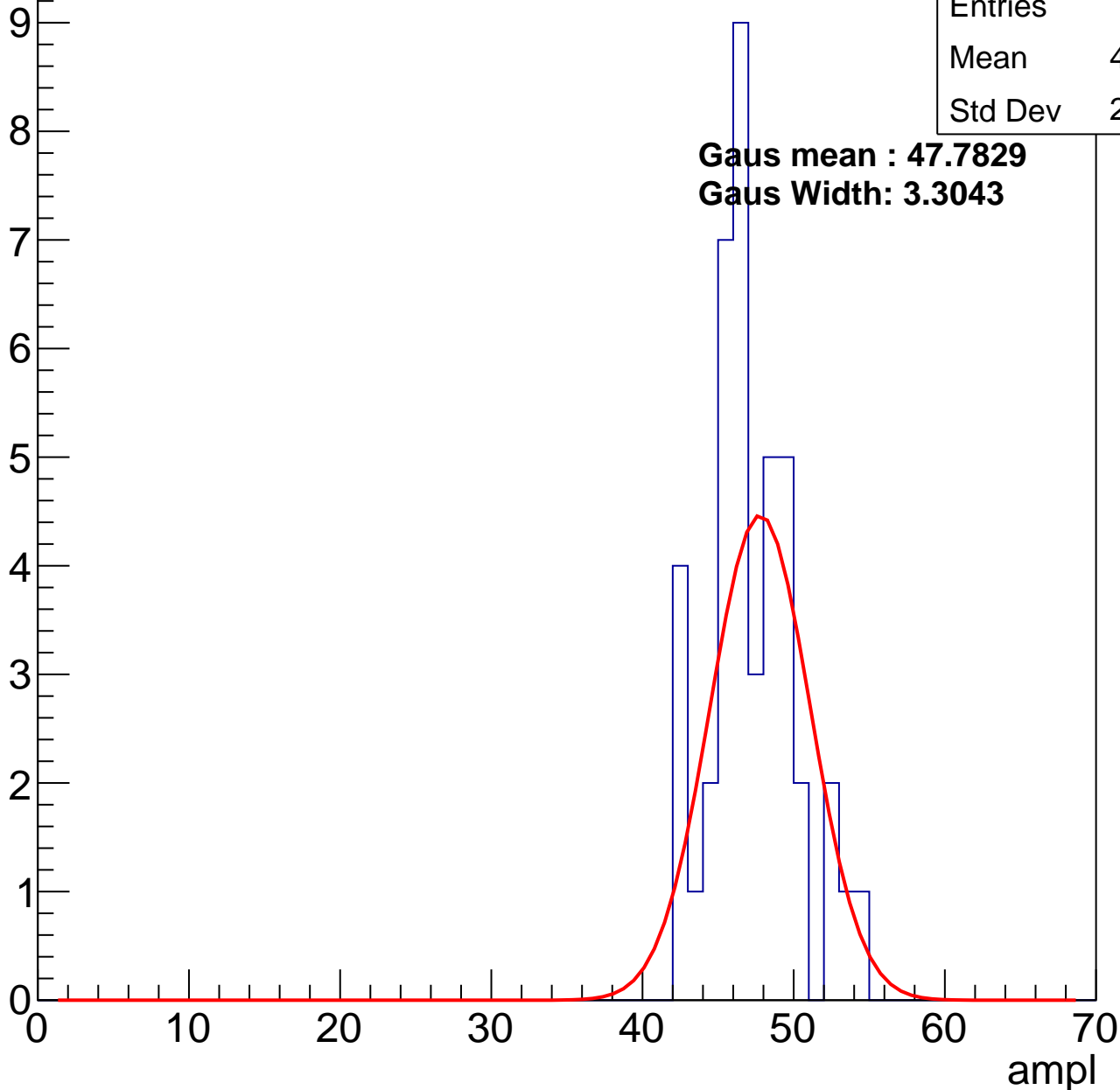
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	42
Mean	46.79
Std Dev	2.875

Gaus mean : 47.7829

Gaus Width: 3.3043

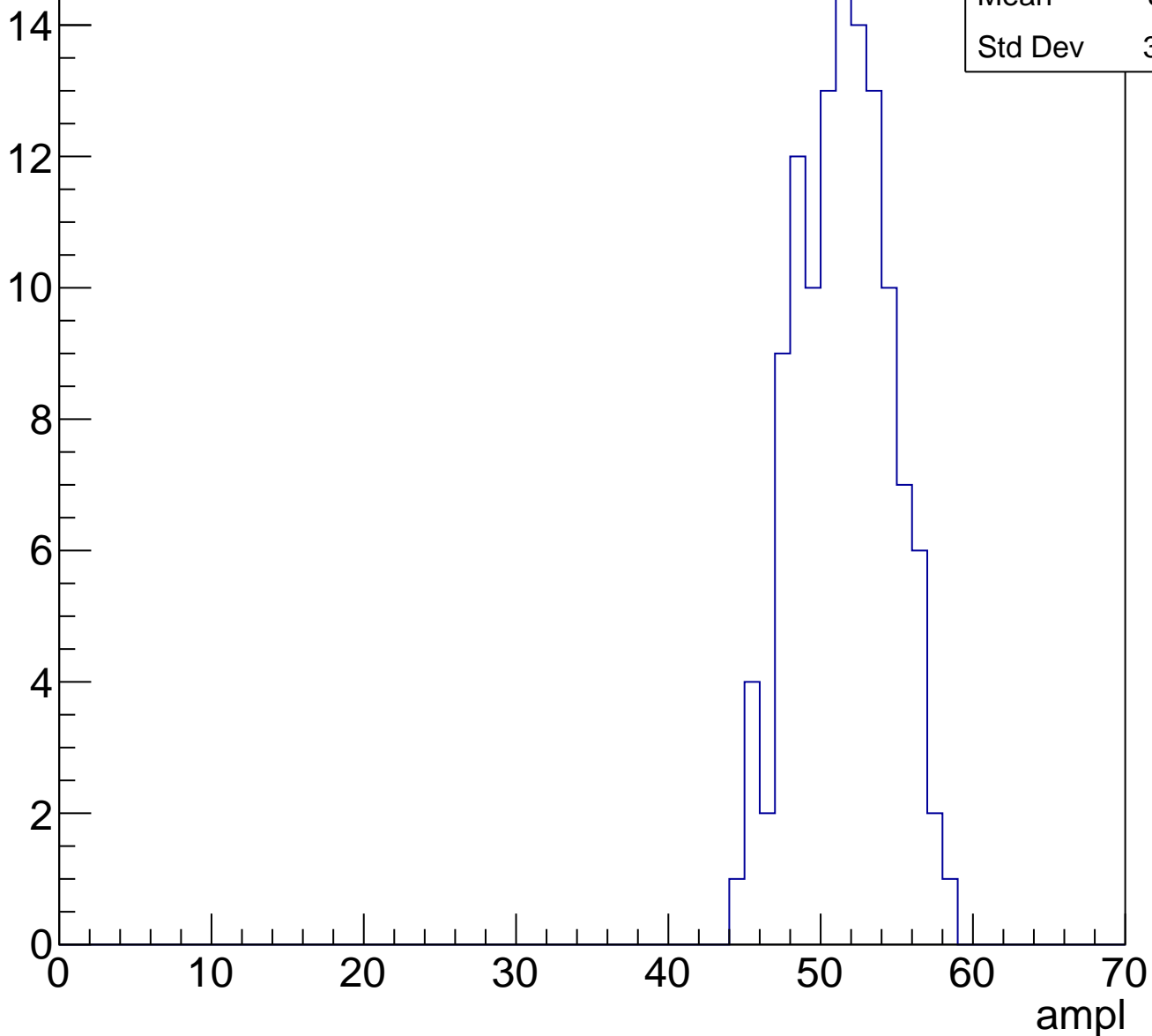


B0L101S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	51.01
Std Dev	3.022

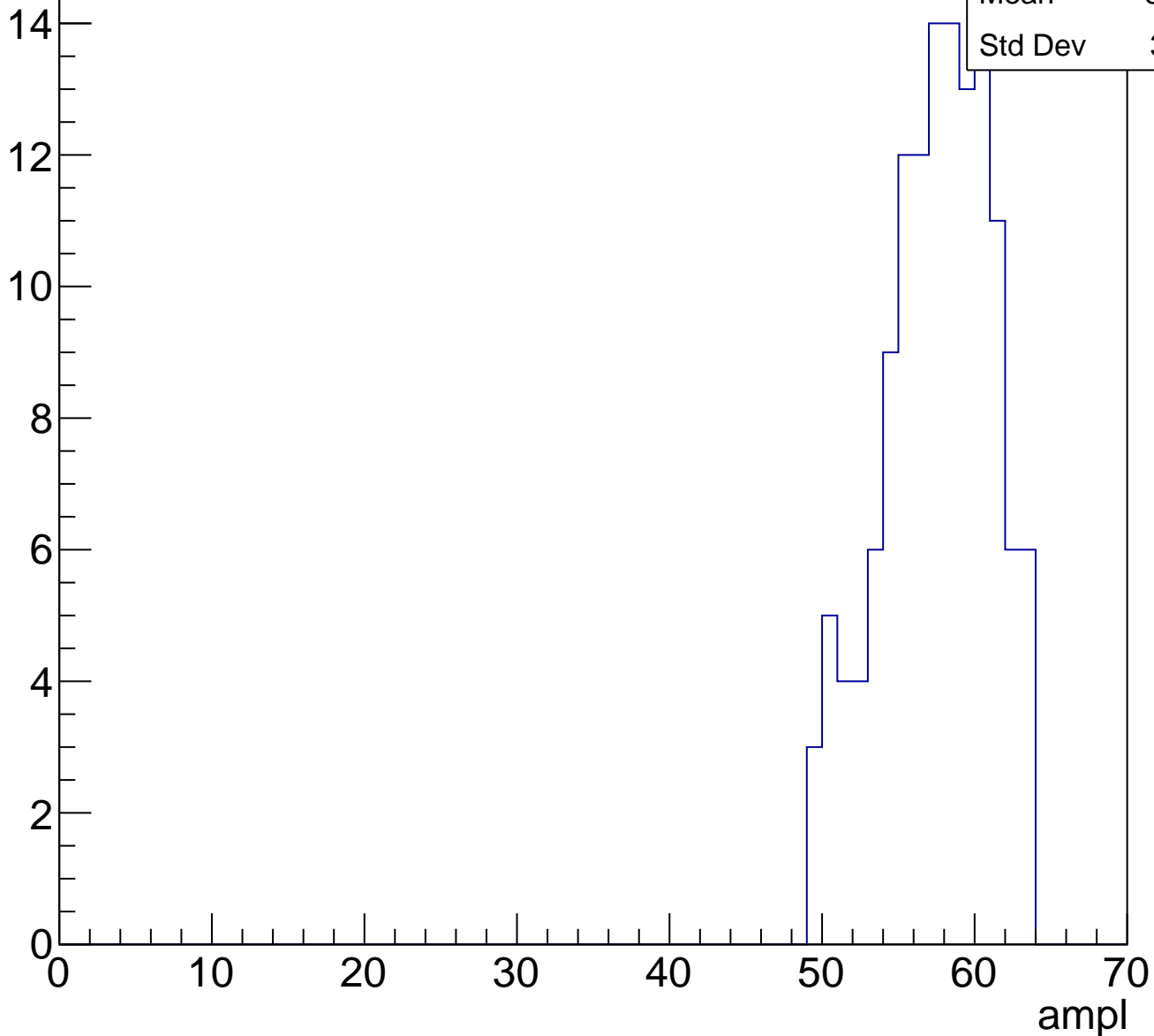
Entry



B0L101S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	134
Mean	57.04
Std Dev	3.521

B0L101S, U14-ch27, adc5

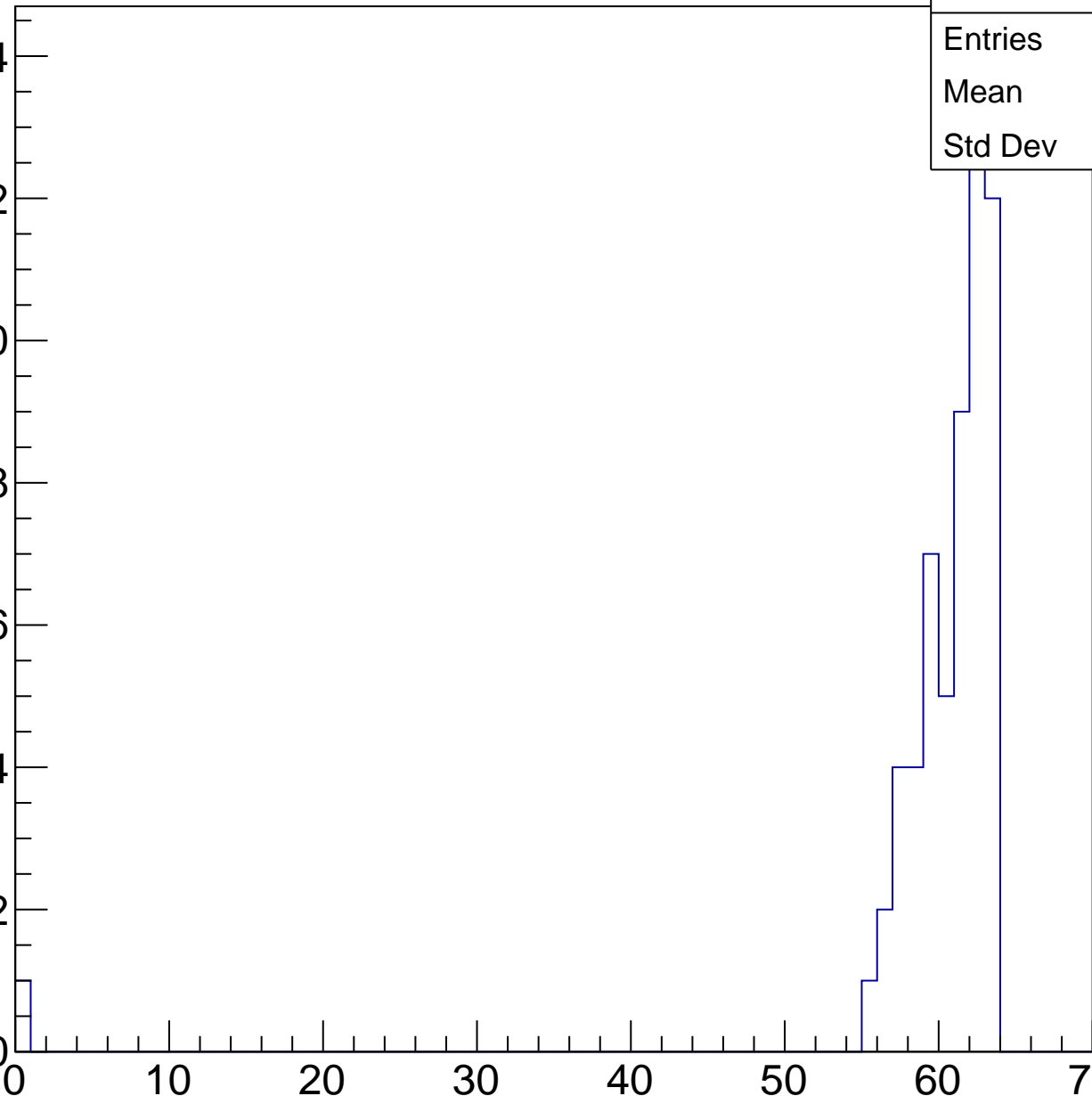
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	59
Mean	59.54
Std Dev	8.106

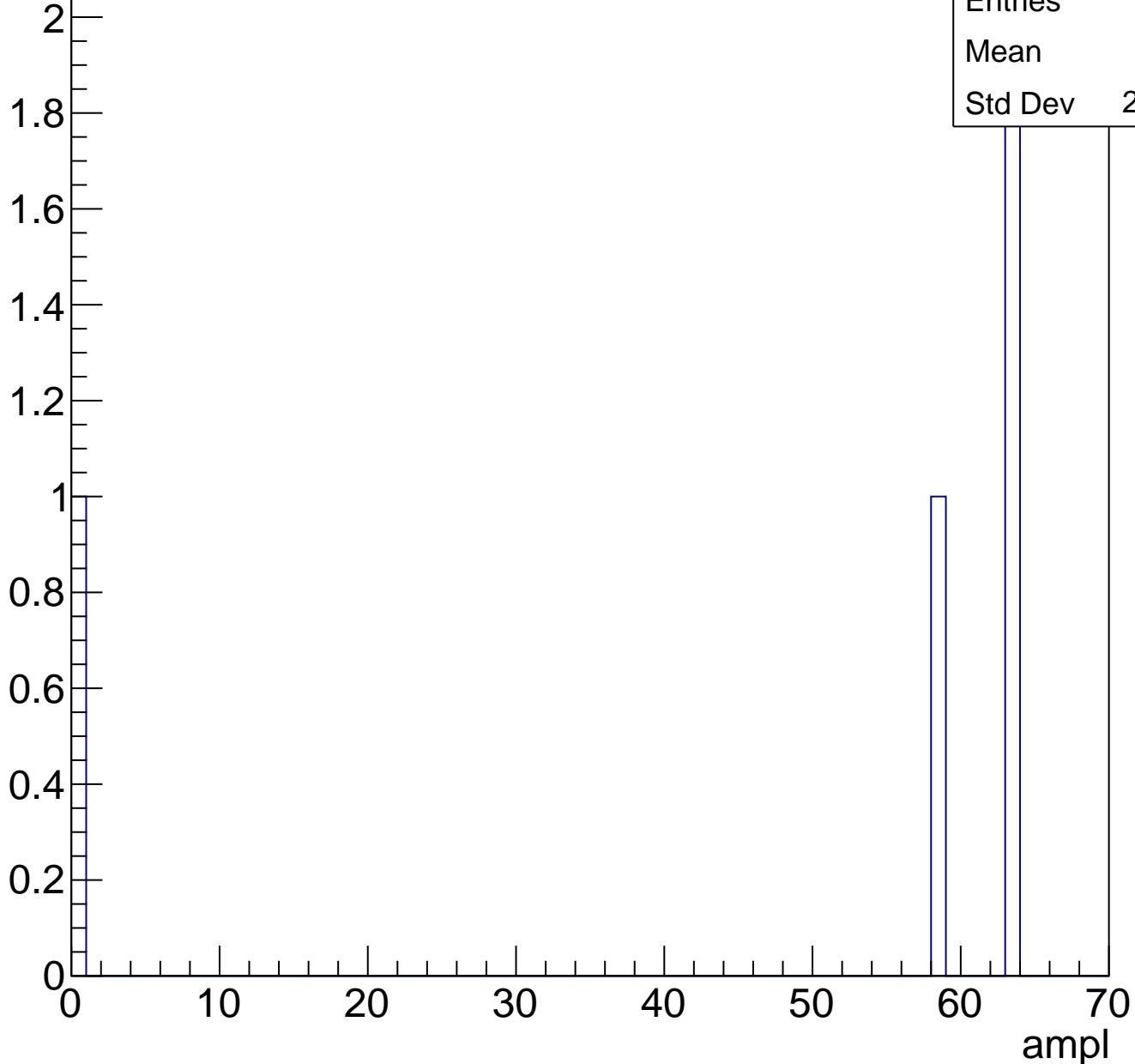
ampl



B0L101S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch28, adc0

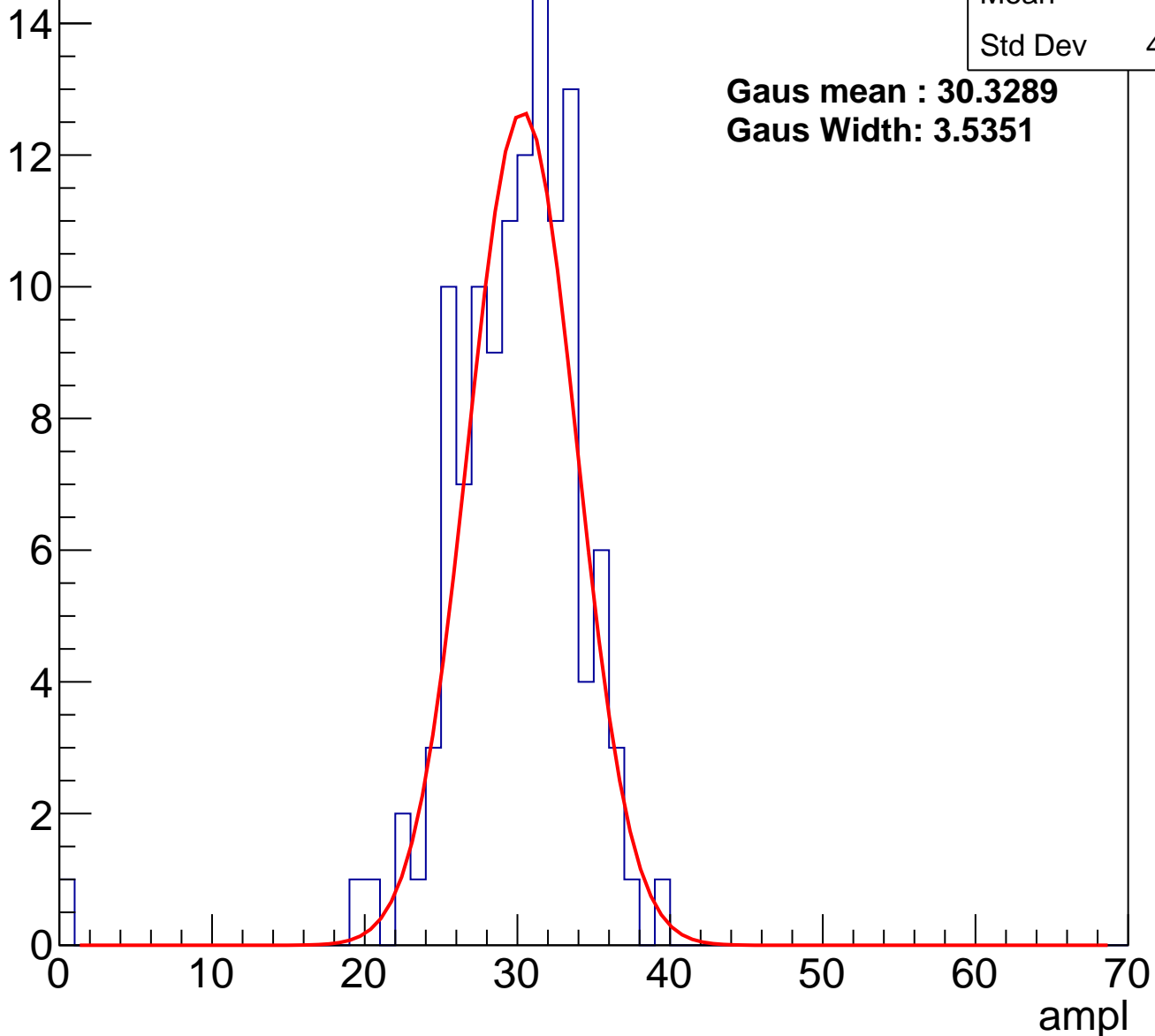
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	29.4
Std Dev	4.517

Gaus mean : 30.3289

Gaus Width: 3.5351

Entry



B0L101S, U14-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

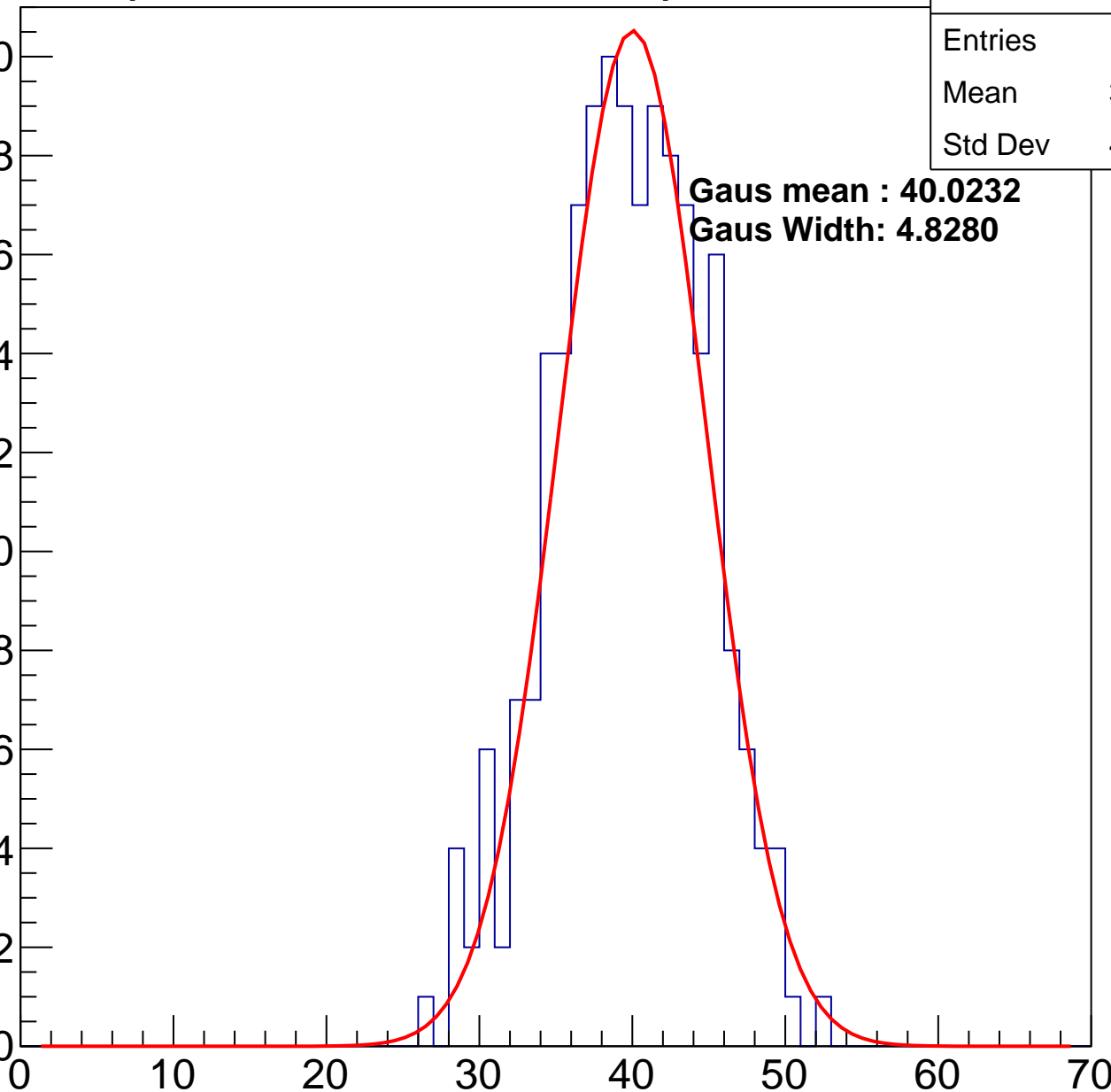
20
18
16
14
12
10
8
6
4
2
0

Entries	257
Mean	39.29
Std Dev	4.875

Gaus mean : 40.0232

Gaus Width: 4.8280

ampl



B0L101S, U14-ch28, adc2

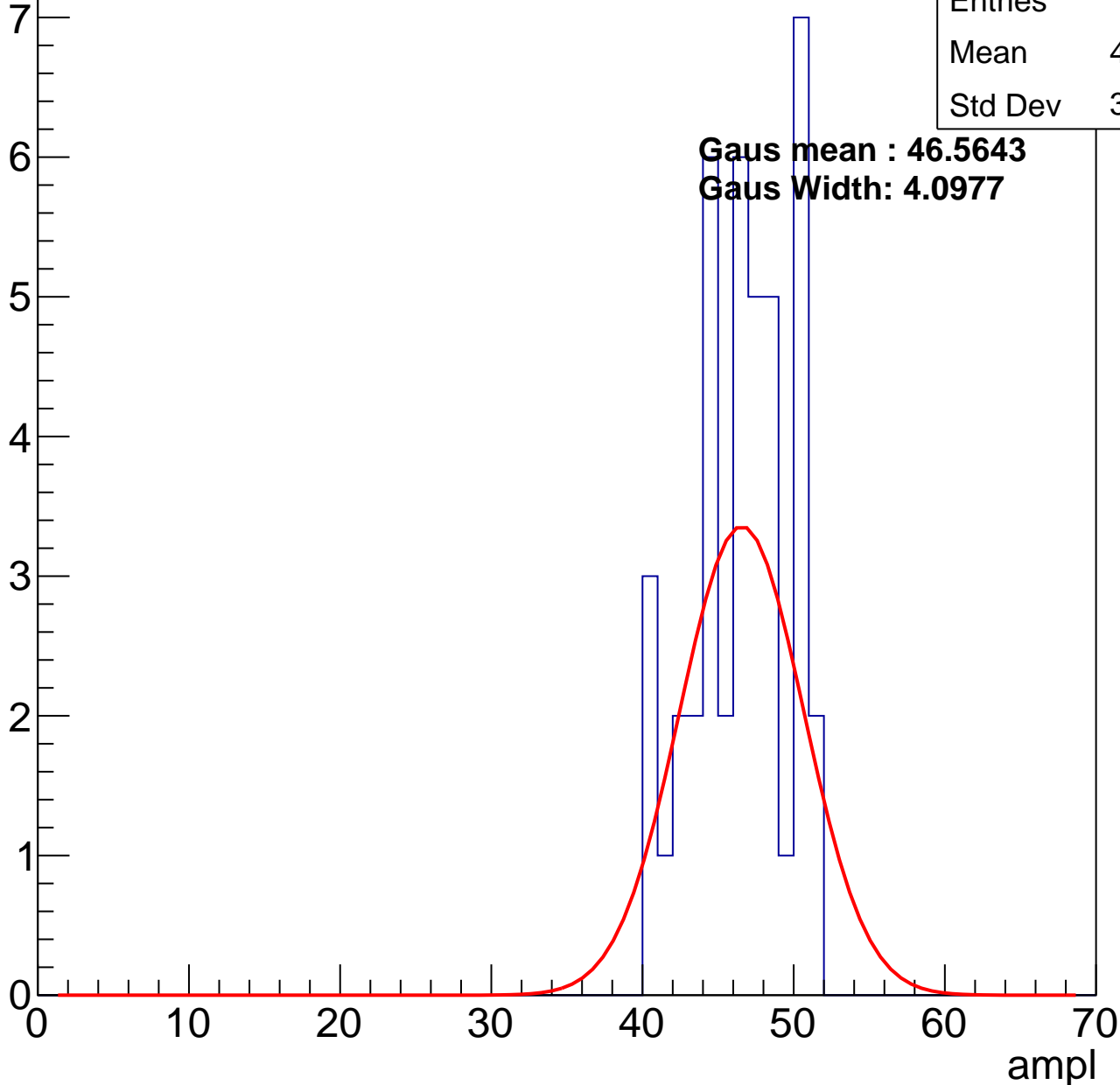
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	42
Mean	46.12
Std Dev	3.103

Gaus mean : 46.5643

Gaus Width: 4.0977



B0L101S, U14-ch28, adc3

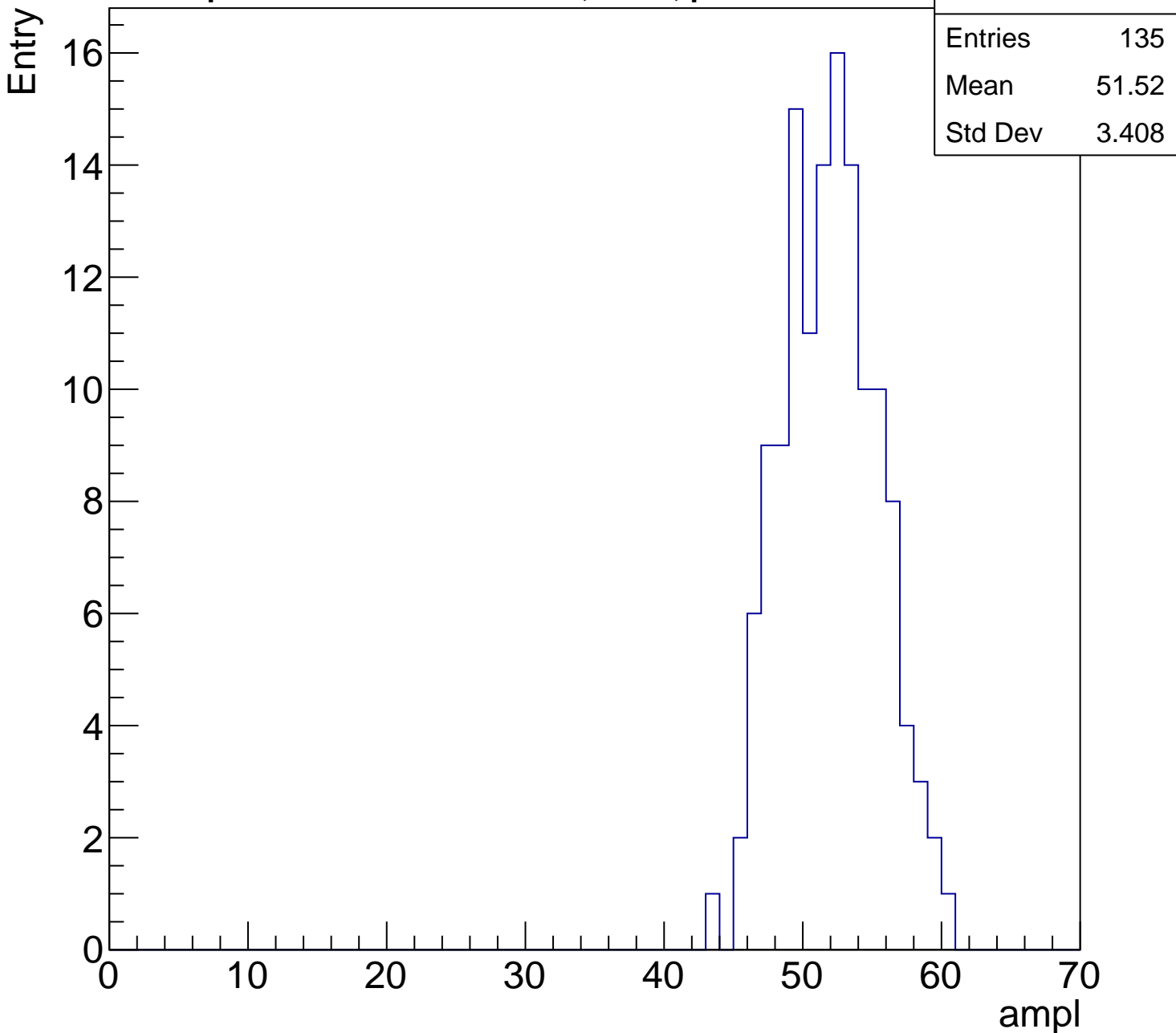
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	51.52
Std Dev	3.408

ampl

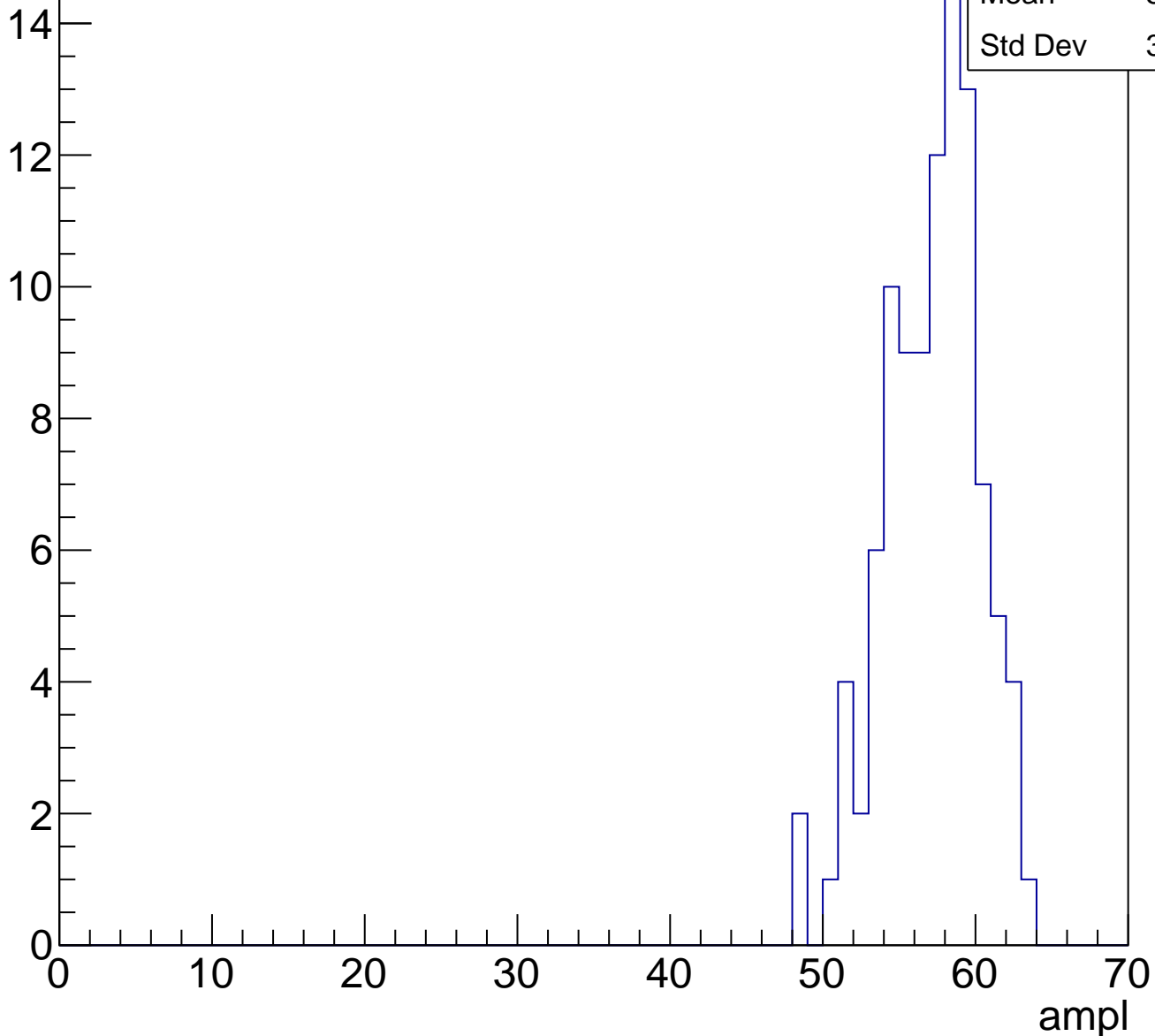


B0L101S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	56.68
Std Dev	3.117

Entry



B0L101S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	65
Mean	59.55
Std Dev	7.79

ampl

0

10

20

30

40

50

60

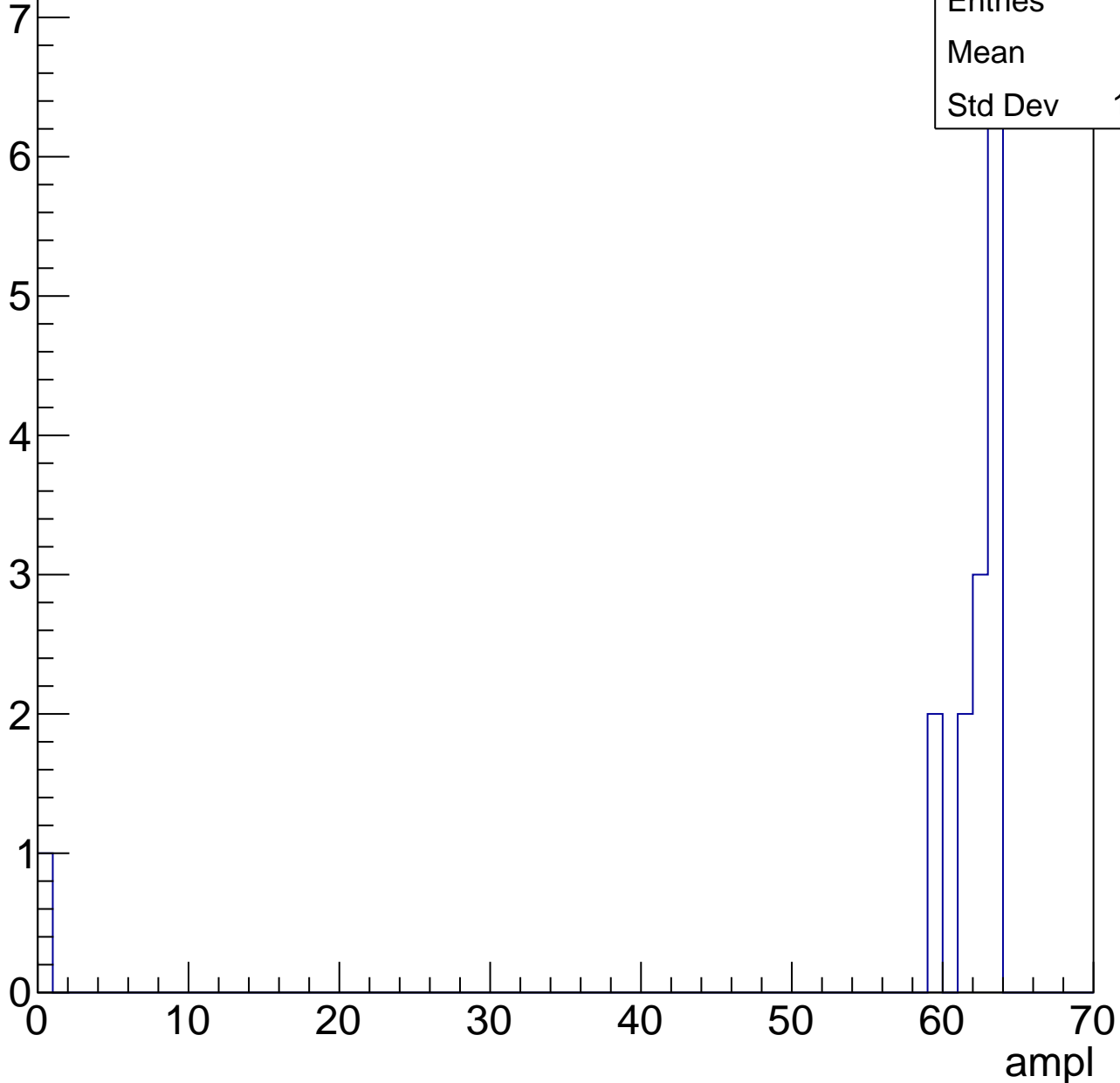
70

B0L101S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.8
Std Dev	15.51



B0L101S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch29, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	29.14
Std Dev	3.486

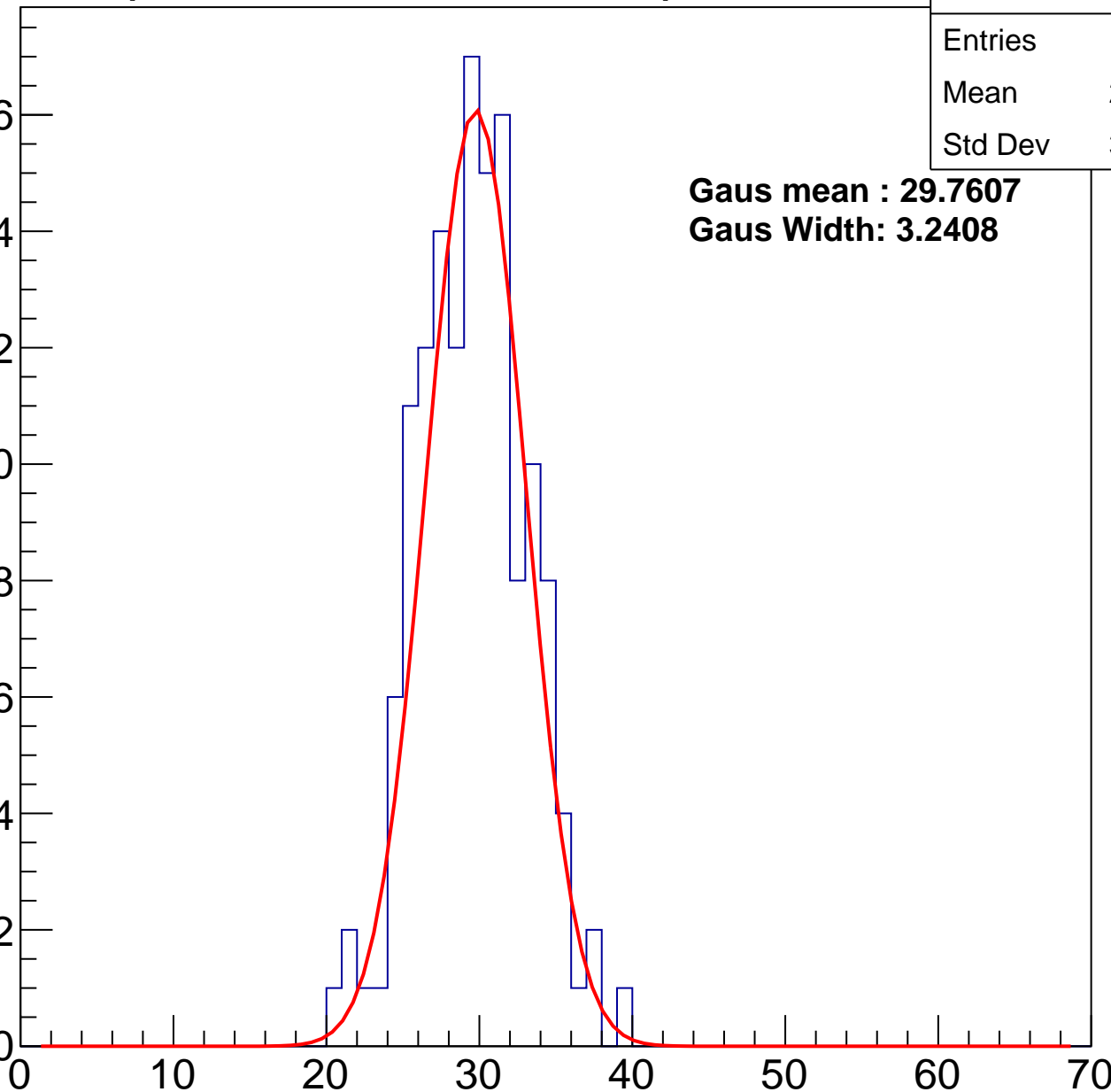
Gaus mean : 29.7607

Gaus Width: 3.2408

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	237
Mean	38.75
Std Dev	4.336

Gaus mean : 39.0297

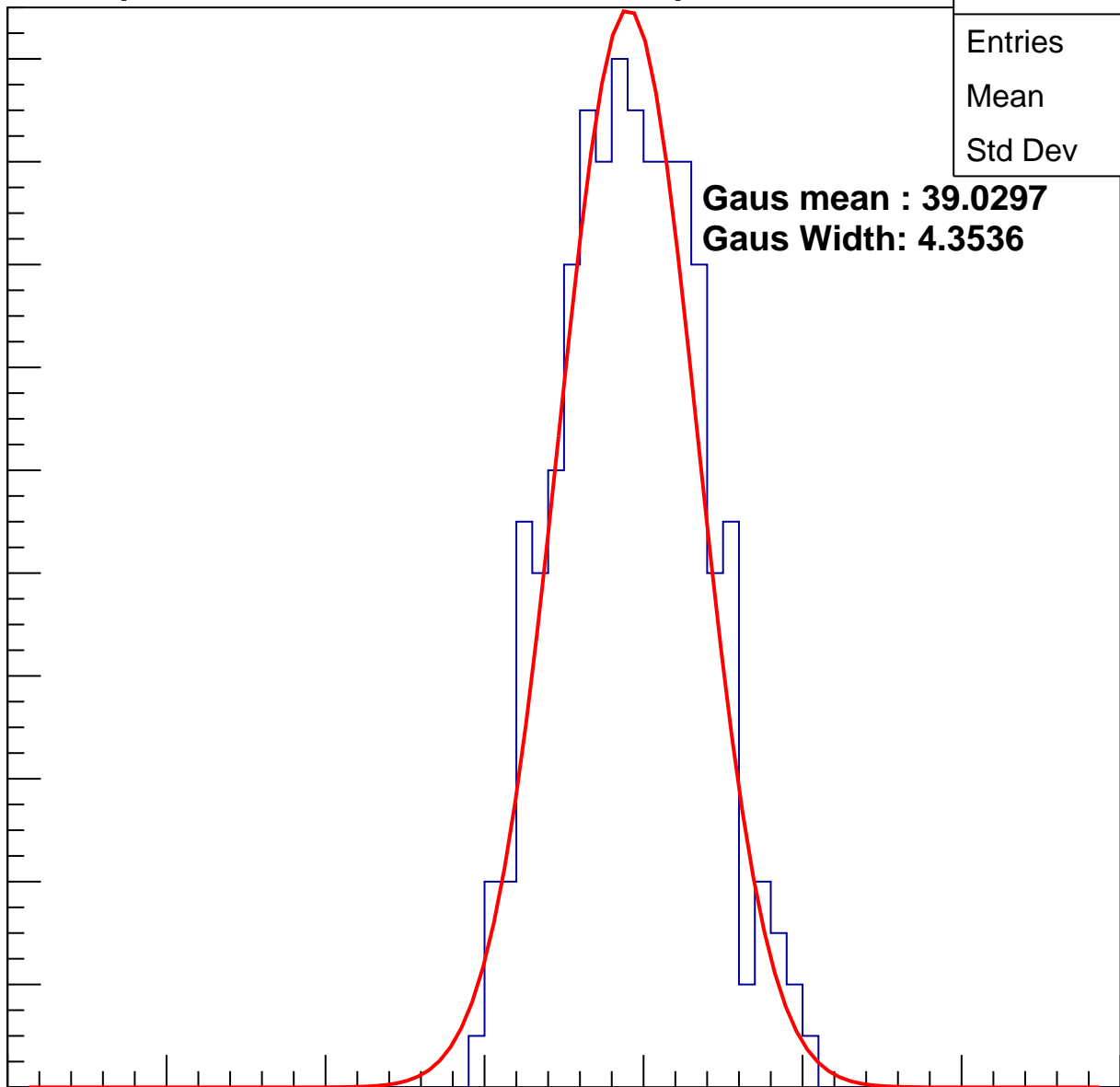
Gaus Width: 4.3536

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch29, adc2

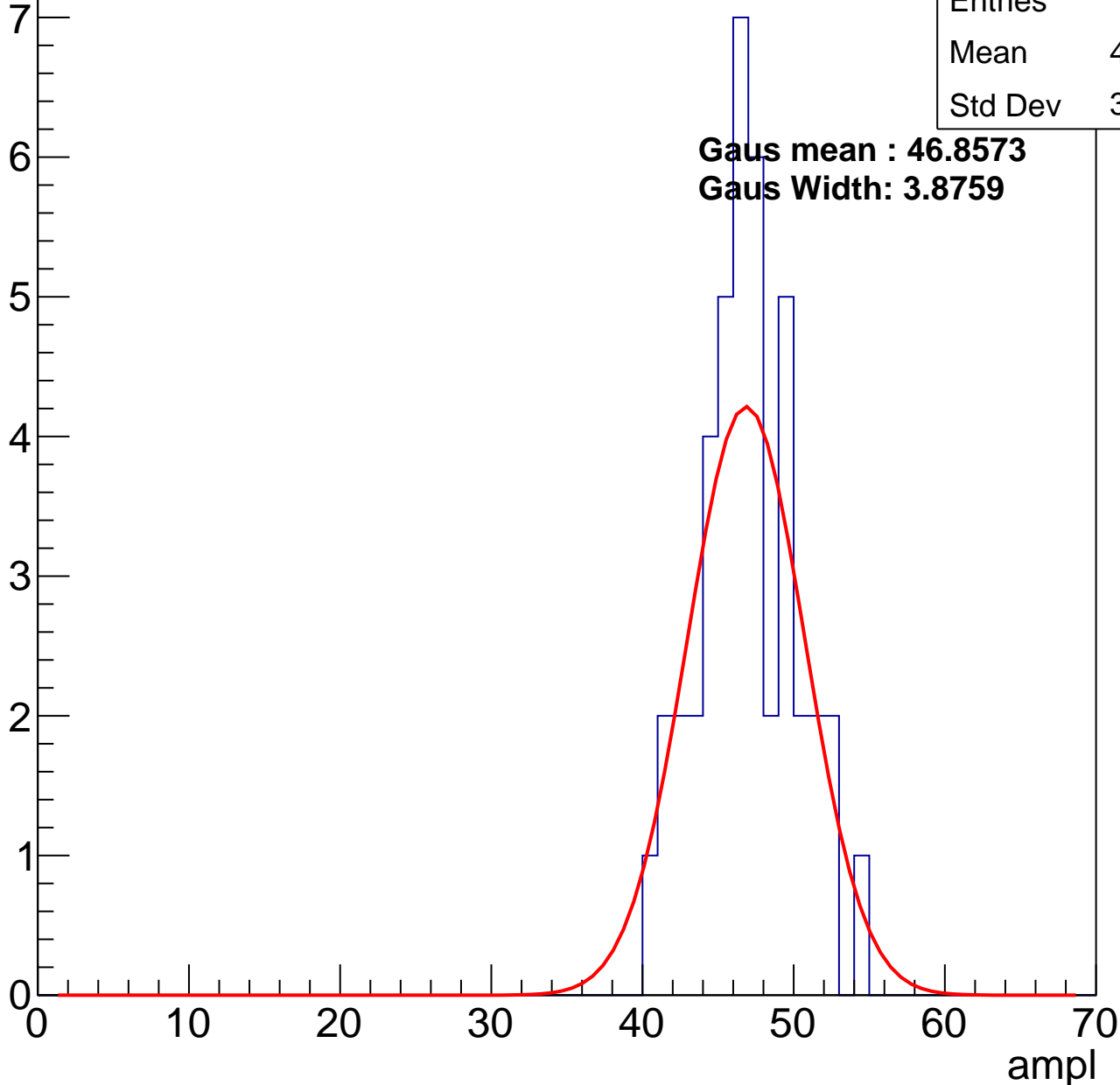
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	43
Mean	46.47
Std Dev	3.143

Gaus mean : 46.8573

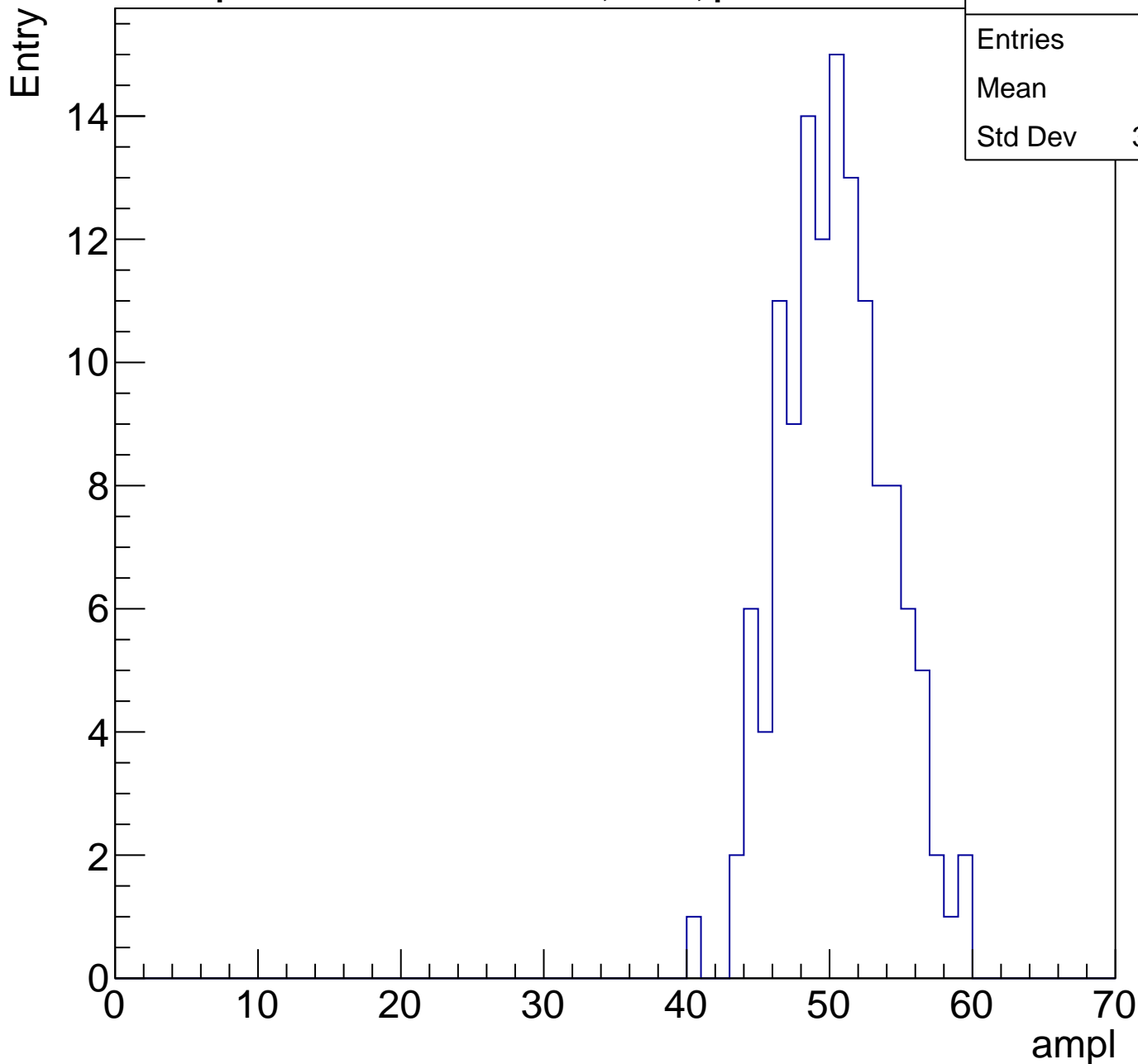
Gaus Width: 3.8759



B0L101S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	50
Std Dev	3.669



B0L101S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	55.26
Std Dev	6.216

Entry

12

10

8

6

4

2

0

0

10

20

30

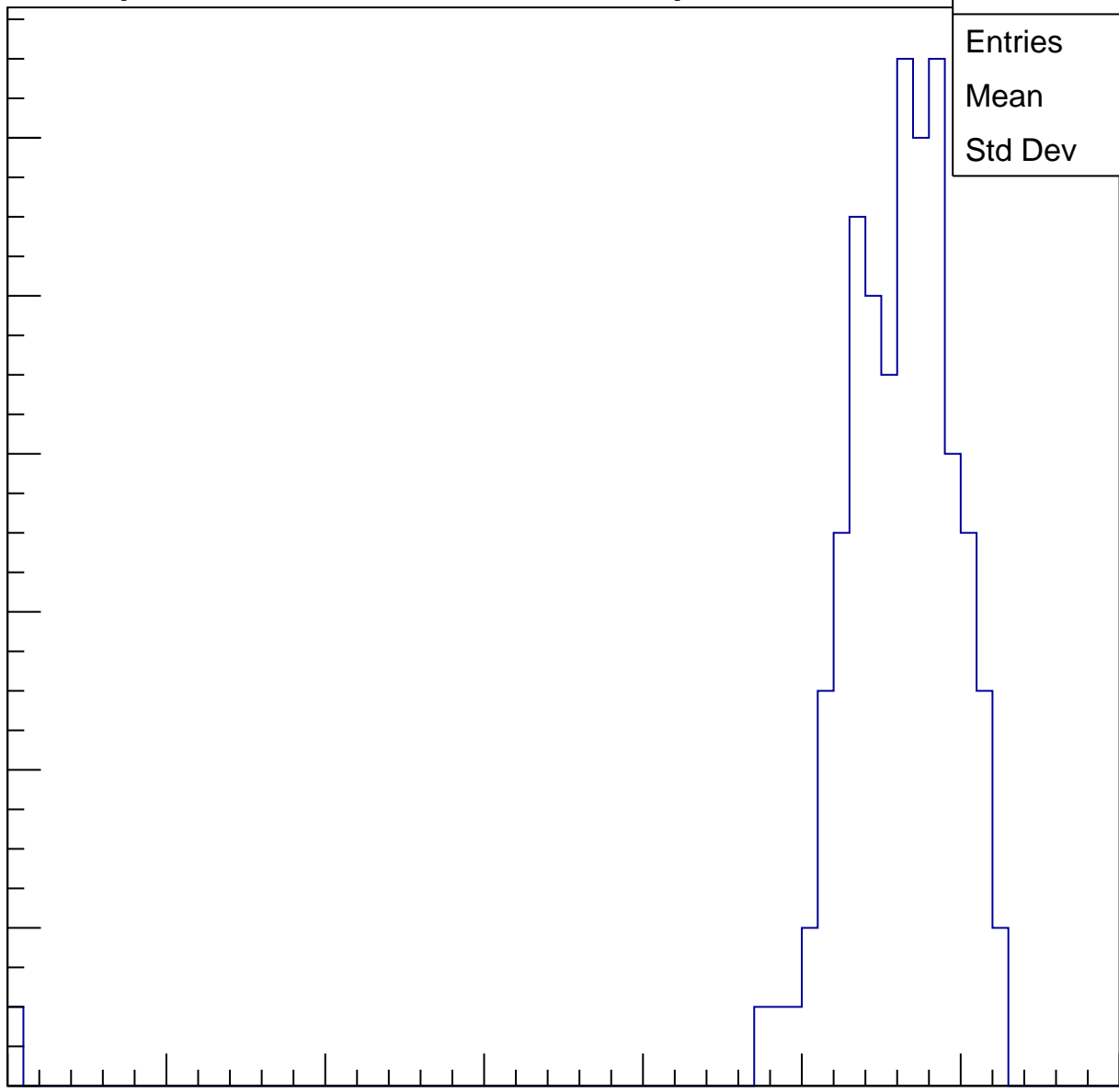
40

50

60

70

ampl



B0L101S, U14-ch29, adc5

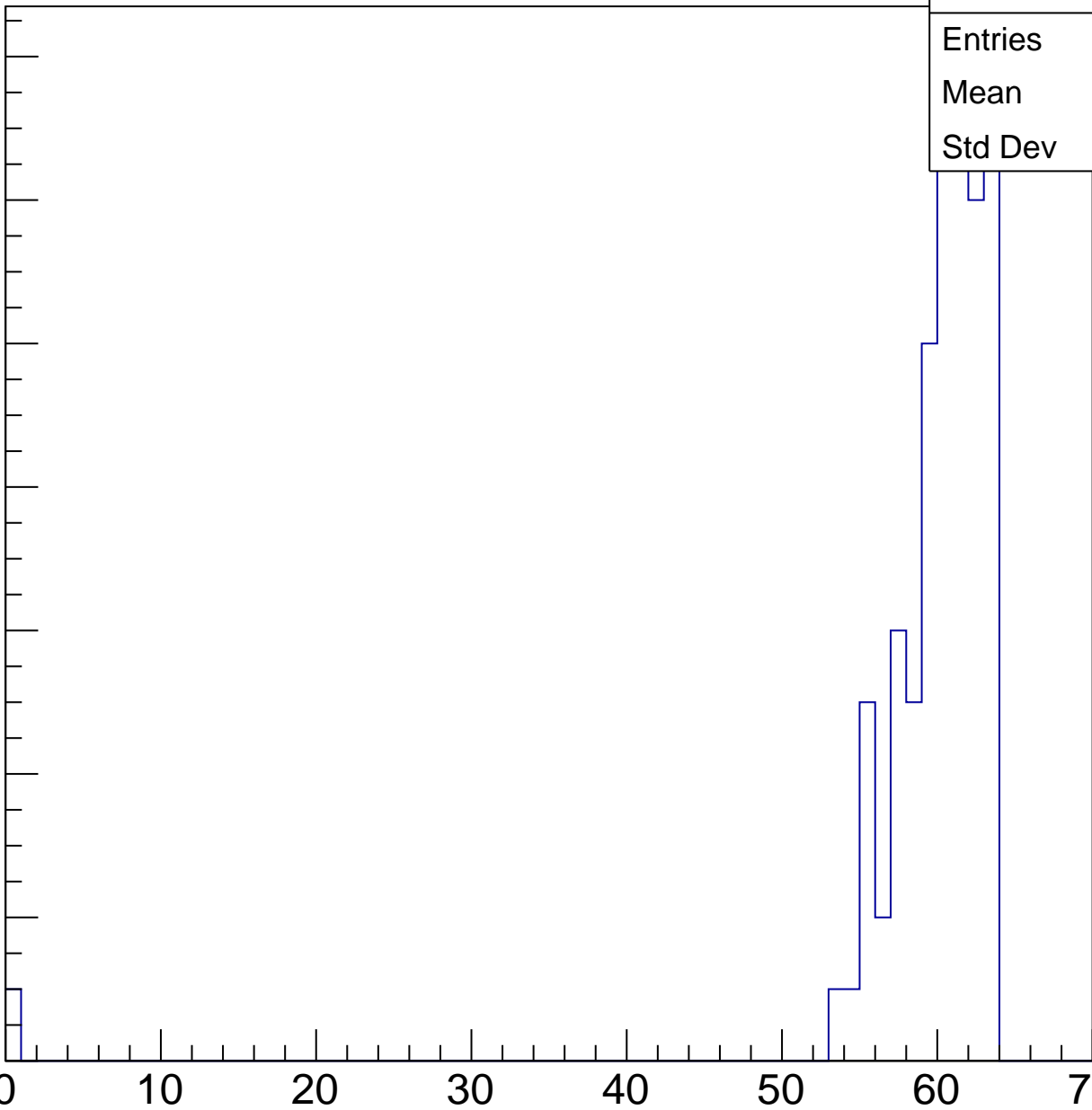
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	59.22
Std Dev	6.996

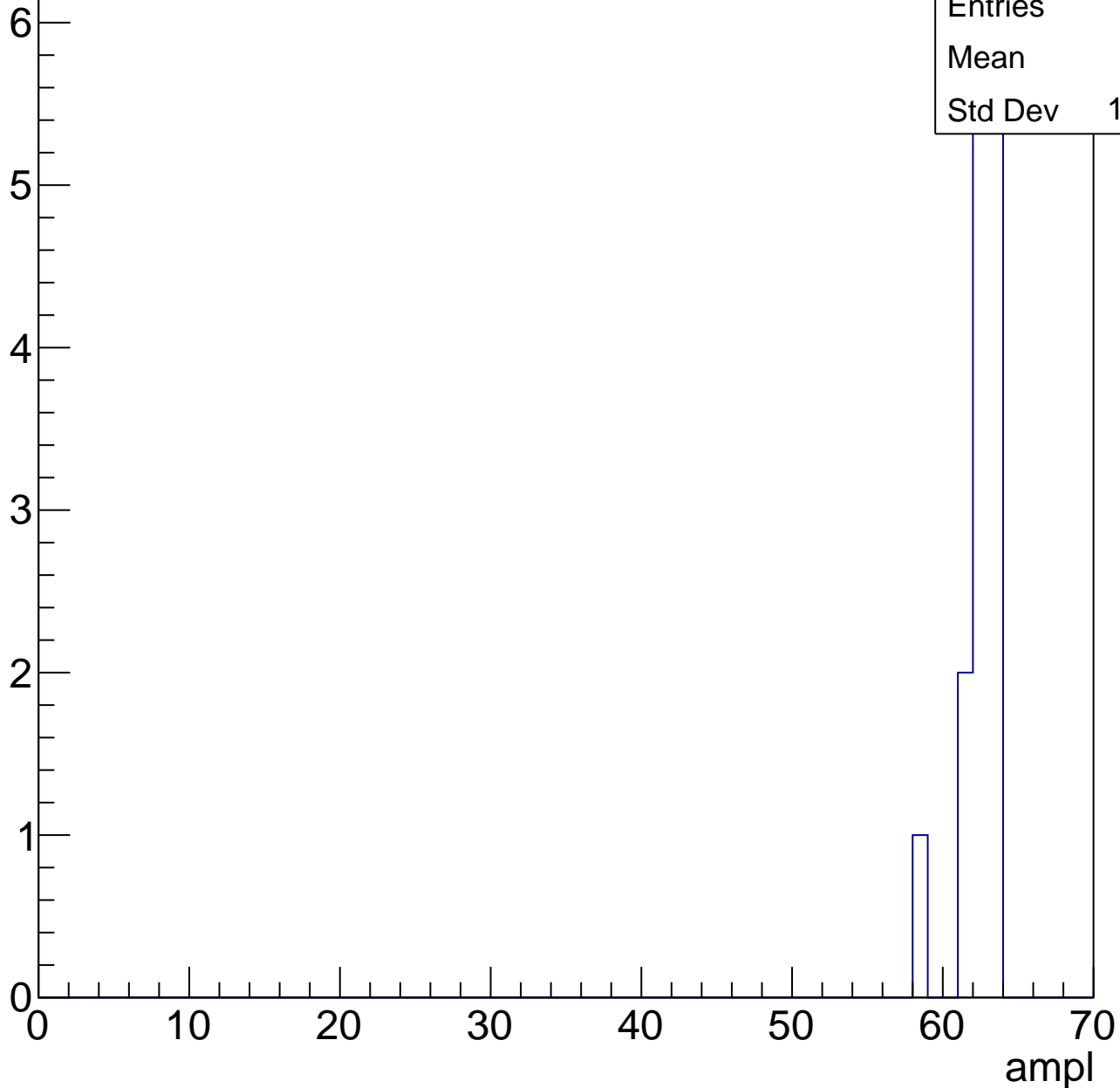
ampl



B0L101S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L101S, U14-ch30, adc0

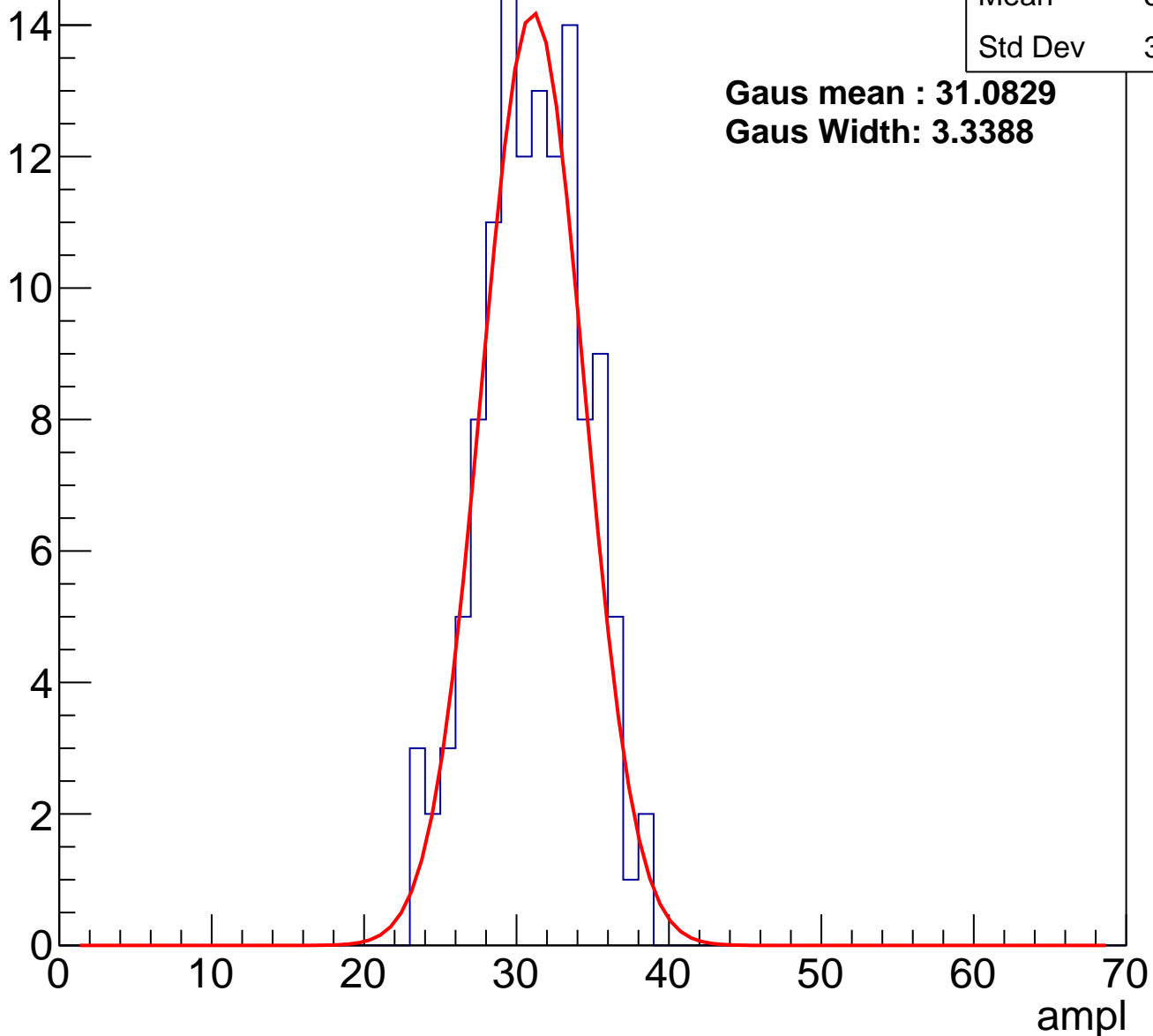
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	30.65
Std Dev	3.313

Gaus mean : 31.0829

Gaus Width: 3.3388

Entry



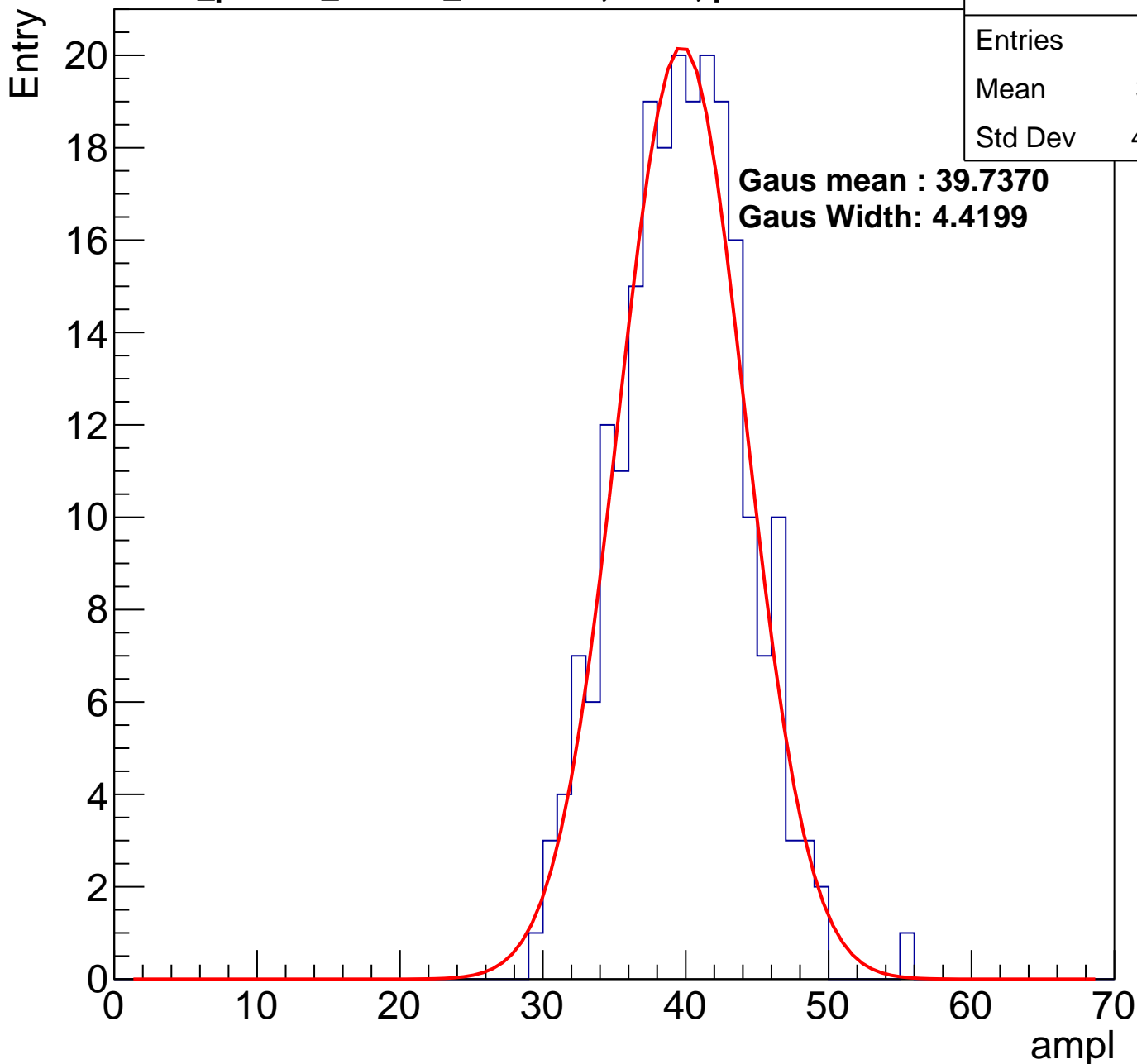
B0L101S, U14-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	226
Mean	39.31
Std Dev	4.337

Gaus mean : 39.7370

Gaus Width: 4.4199



B0L101S, U14-ch30, adc2

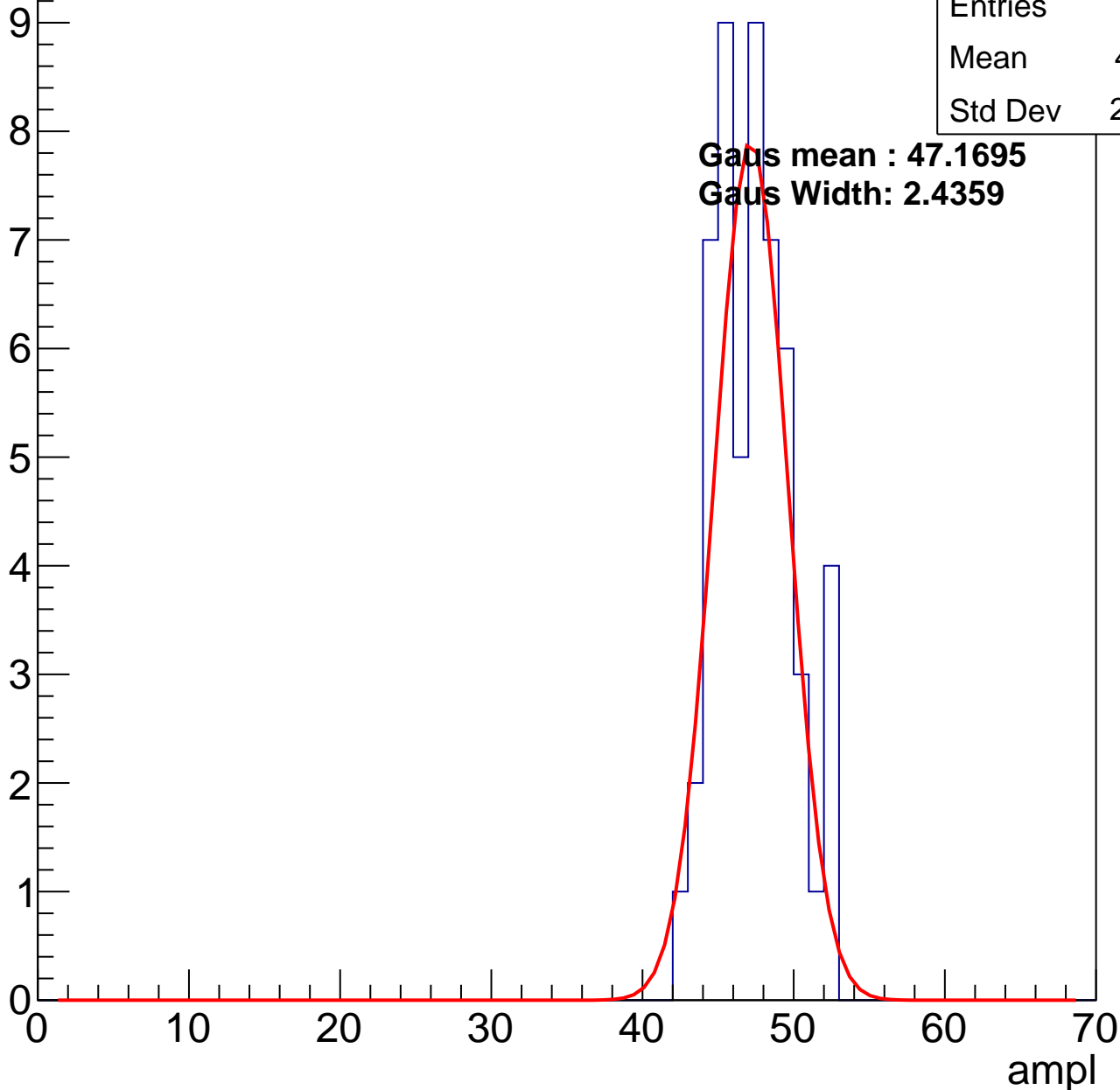
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	46.91
Std Dev	2.489

Gaus mean : 47.1695

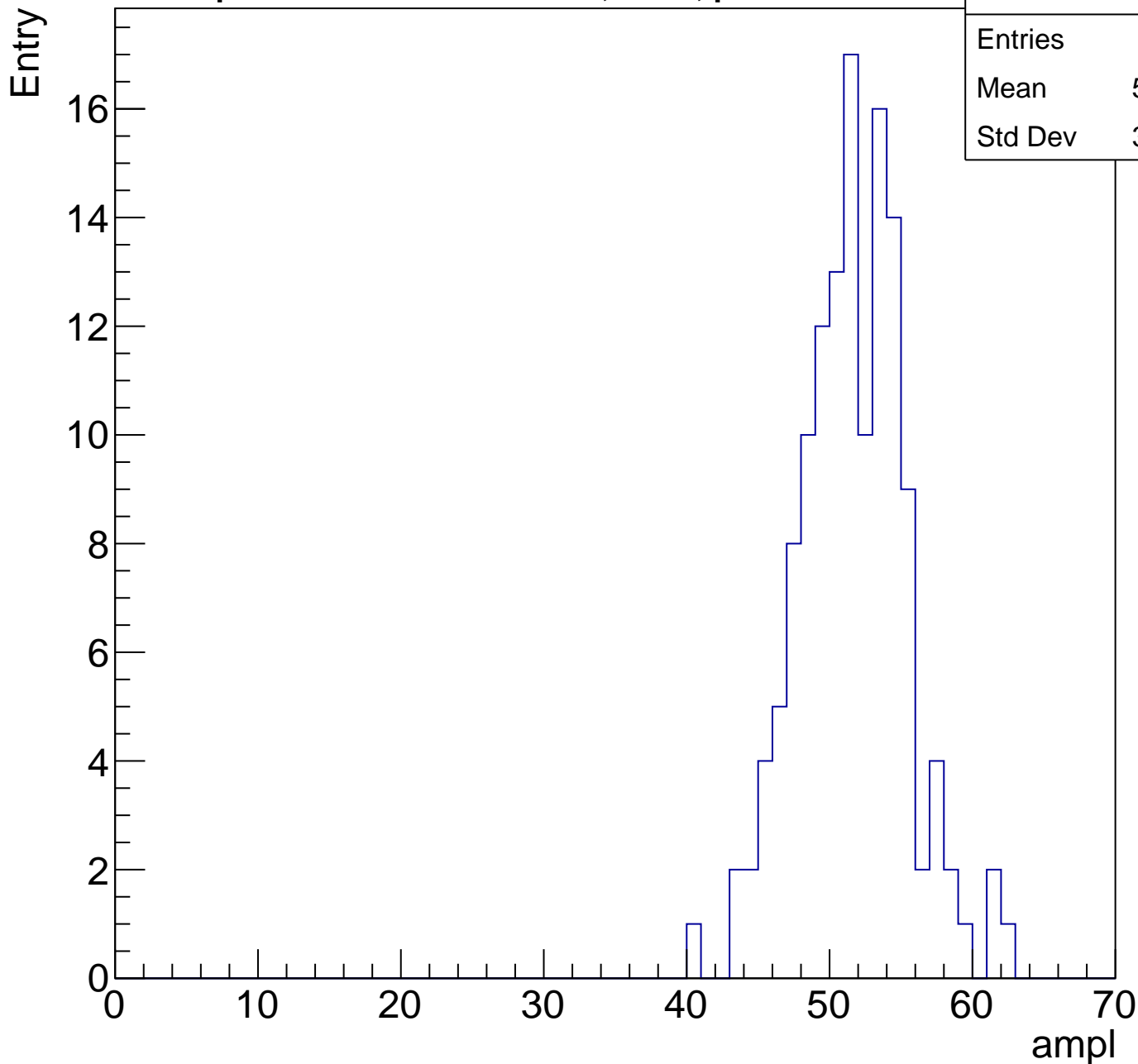
Gaus Width: 2.4359



B0L101S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	51.13
Std Dev	3.765

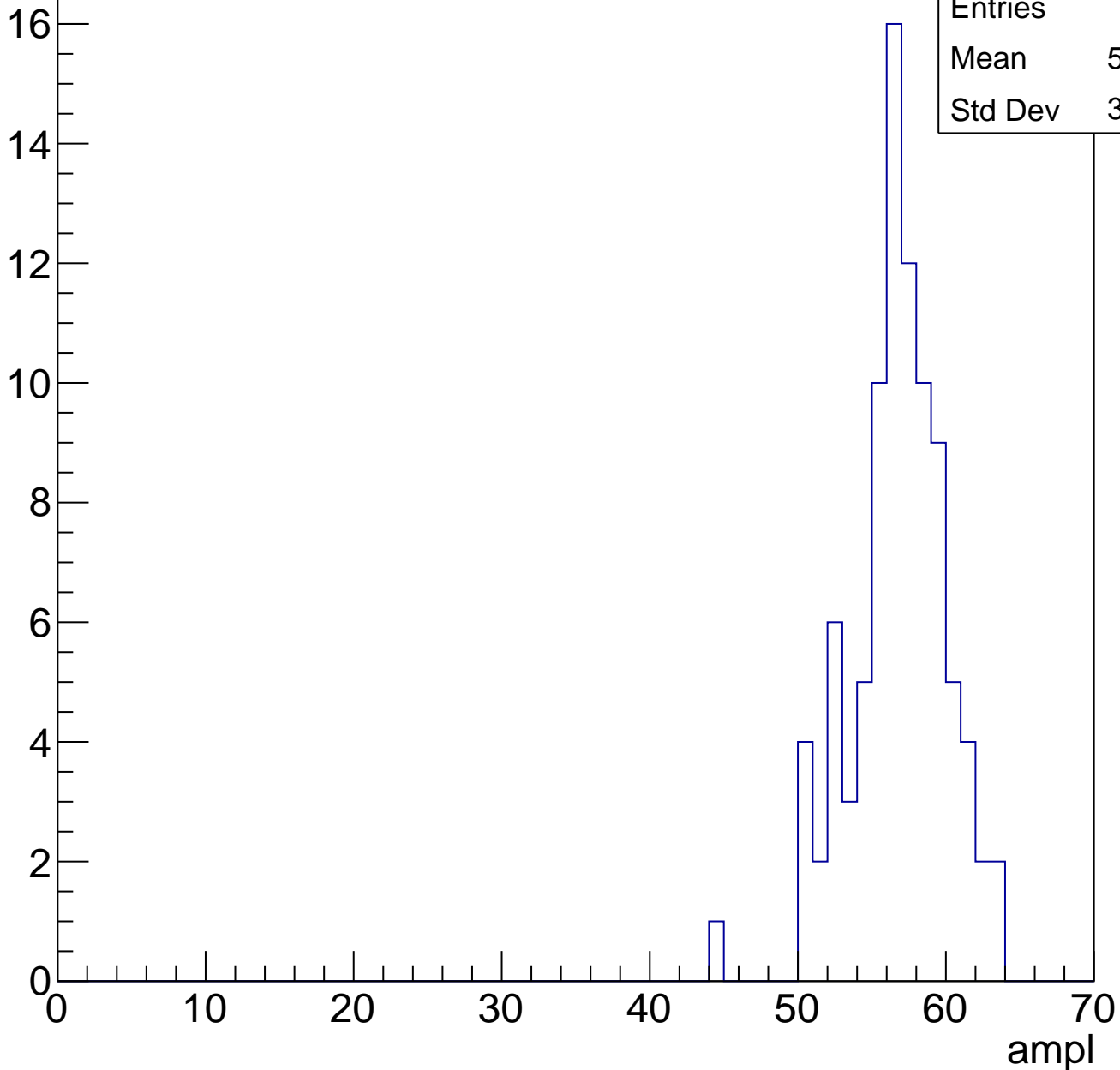


B0L101S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	91
Mean	56.29
Std Dev	3.259

Entry



B0L101S, U14-ch30, adc5

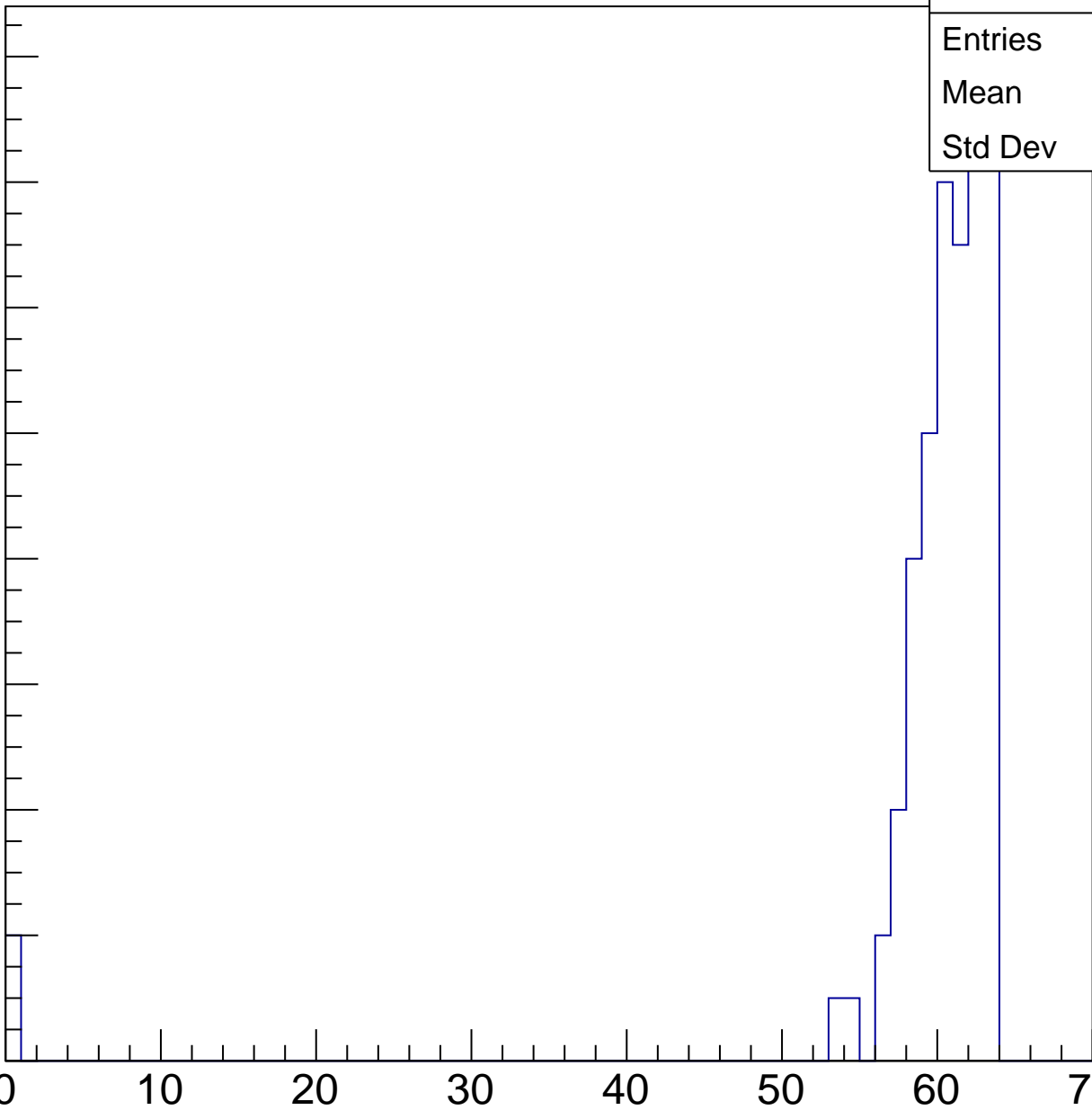
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.97
Std Dev	9.348

ampl



B0L101S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

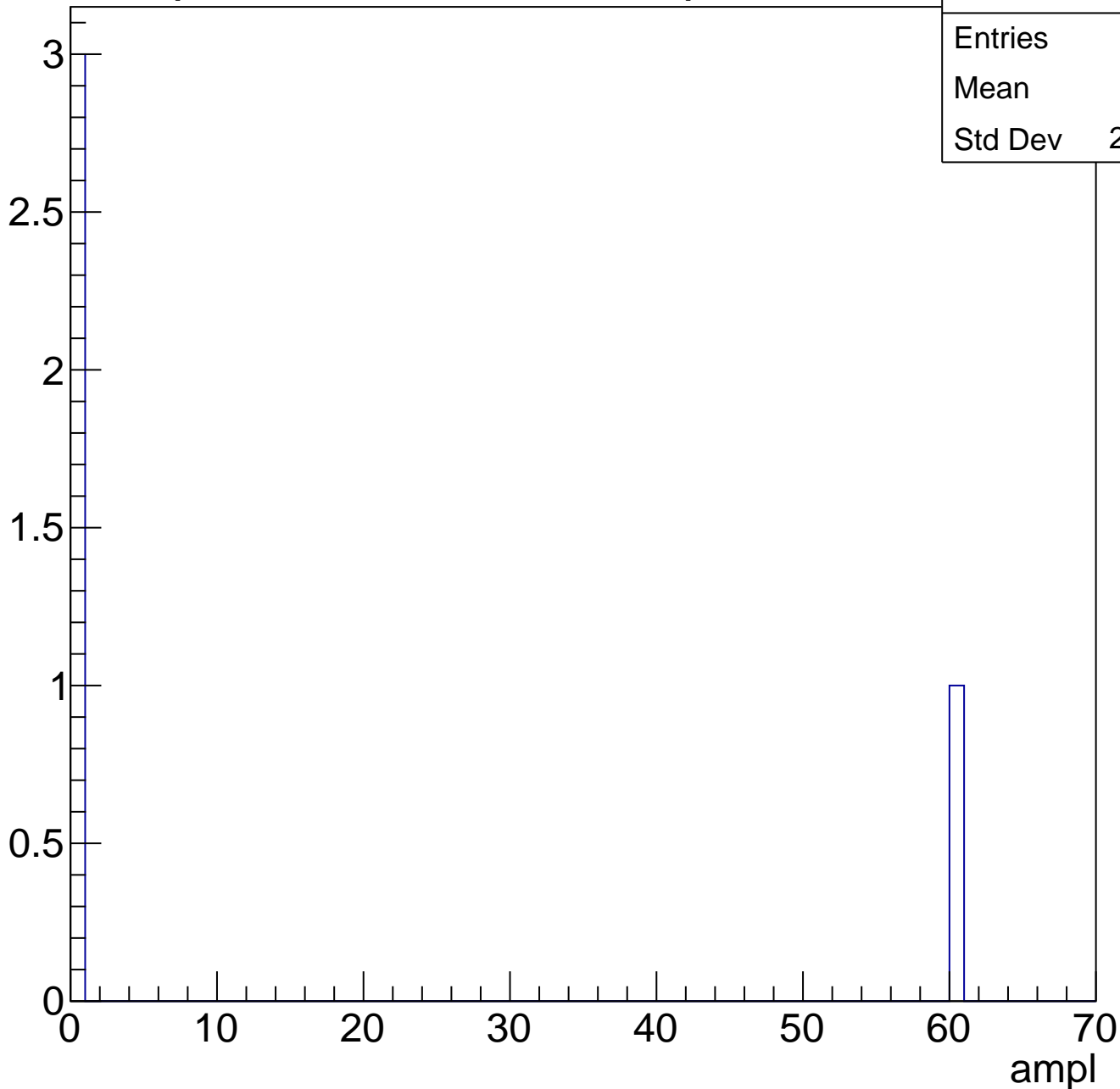
Entry



B0L101S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



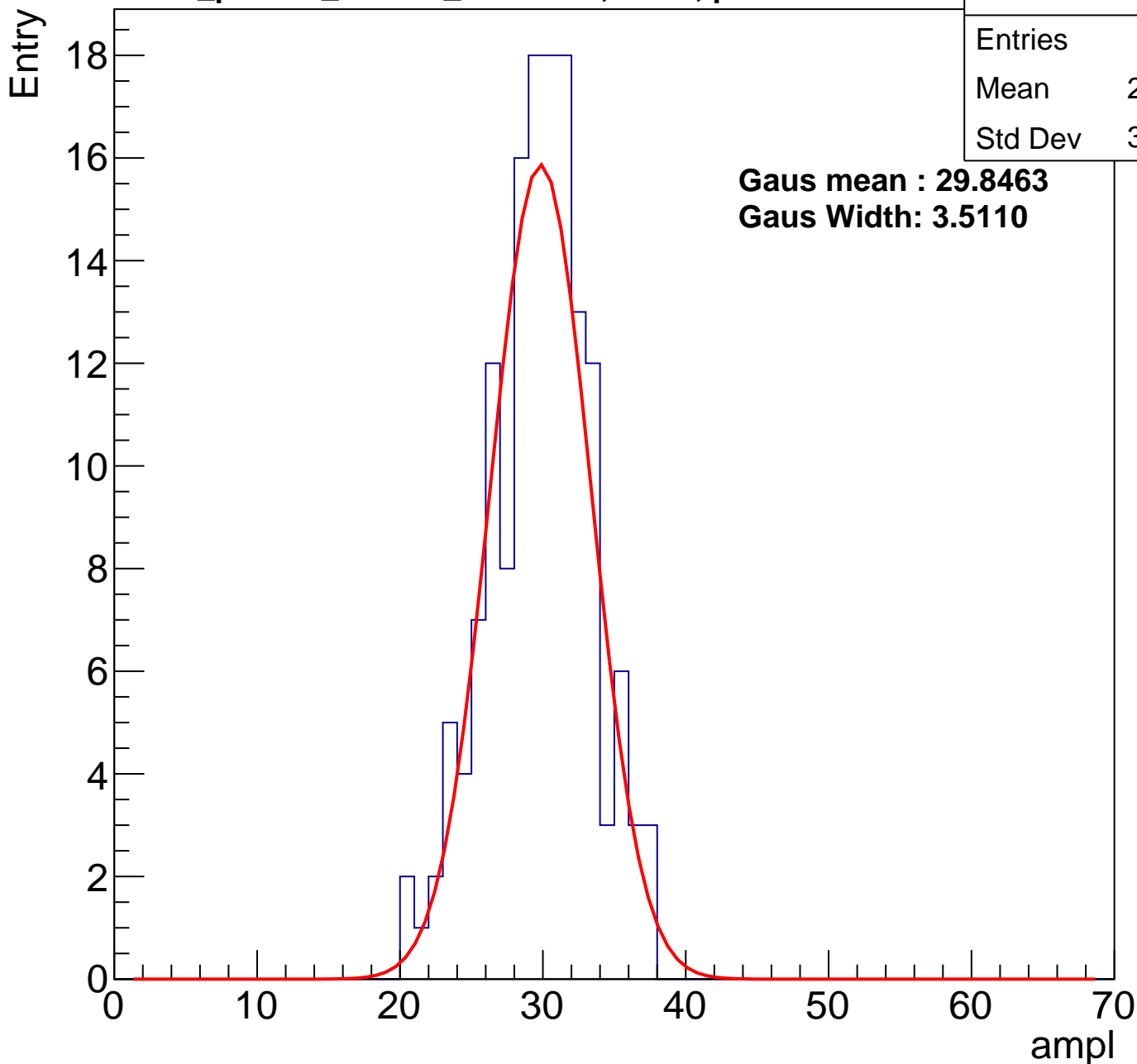
B0L101S, U14-ch31, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	29.34
Std Dev	3.532

Gaus mean : 29.8463

Gaus Width: 3.5110



B0L101S, U14-ch31, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

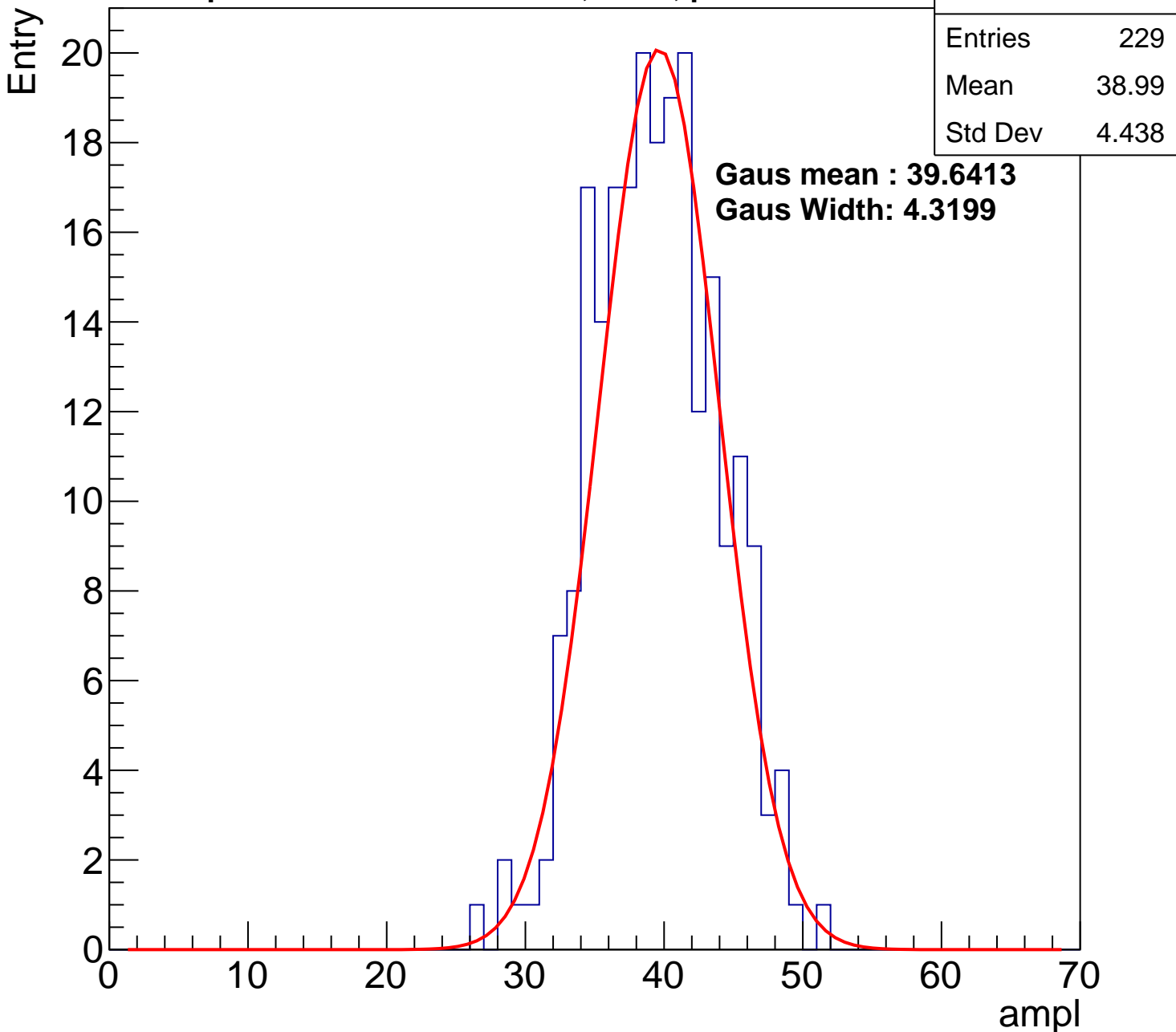
20
18
16
14
12
10
8
6
4
2
0

Entries	229
Mean	38.99
Std Dev	4.438

Gaus mean : 39.6413
Gaus Width: 4.3199

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch31, adc2

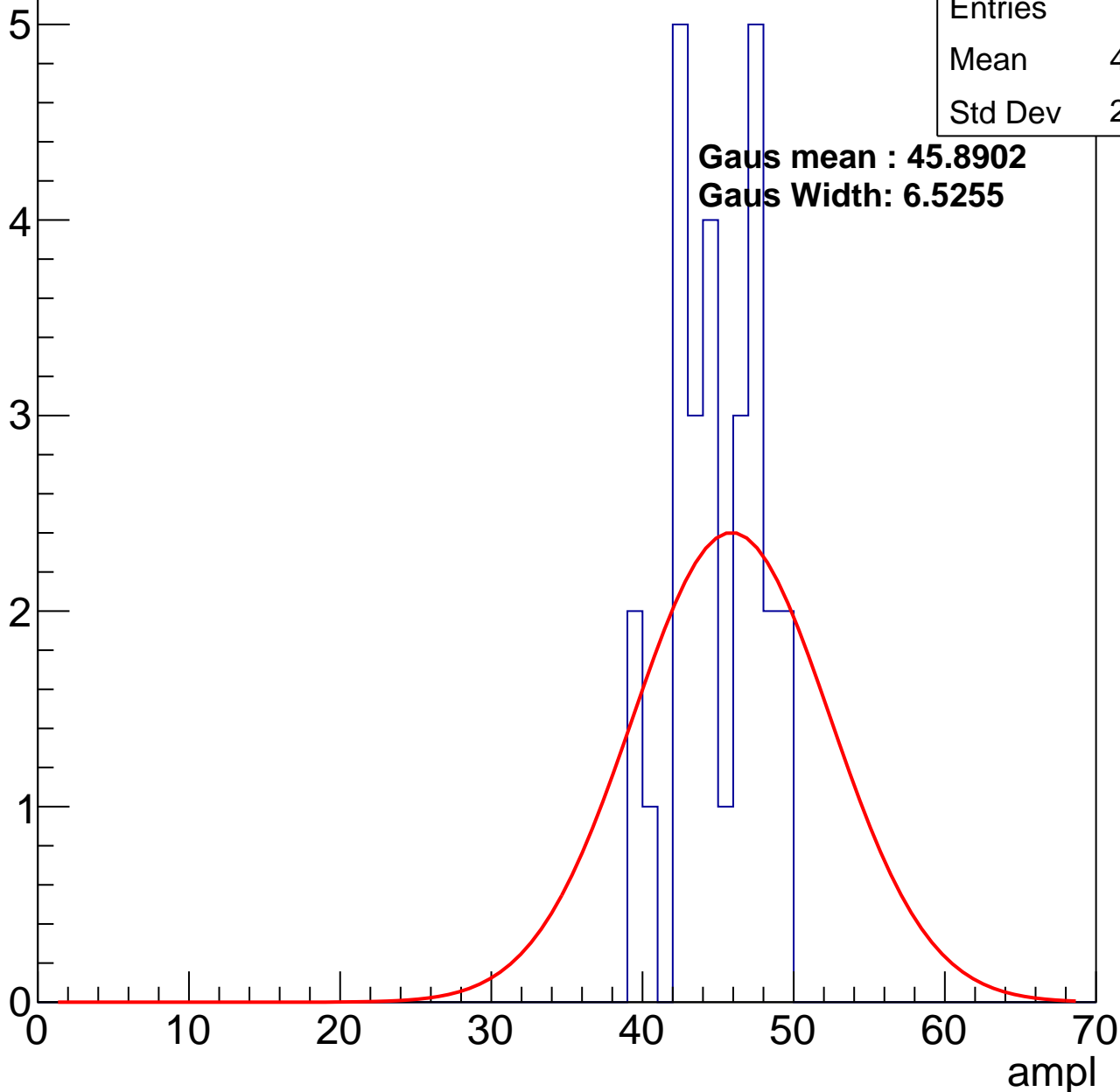
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	44.46
Std Dev	2.822

Gaus mean : 45.8902

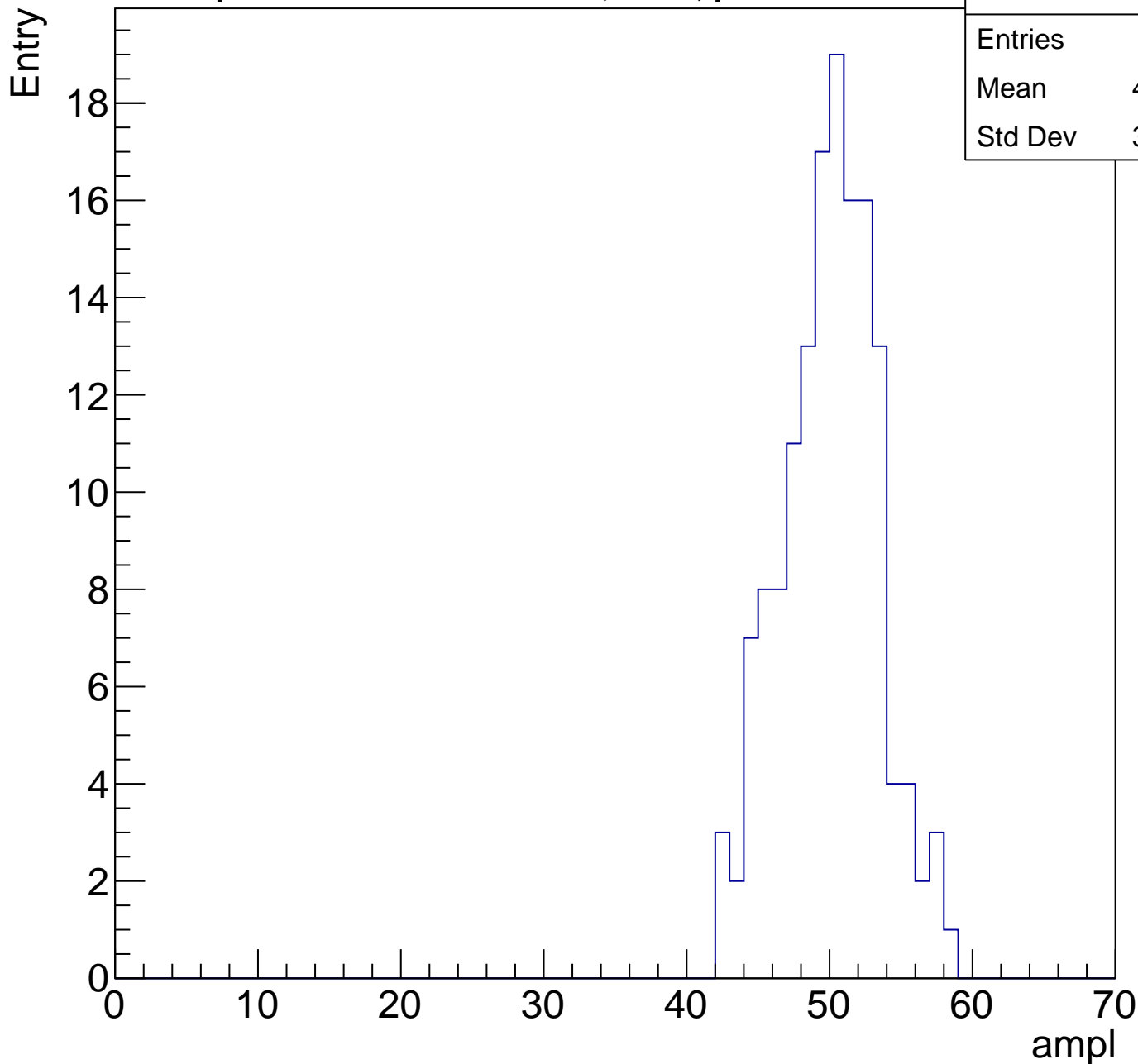
Gaus Width: 6.5255



B0L101S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	49.56
Std Dev	3.354

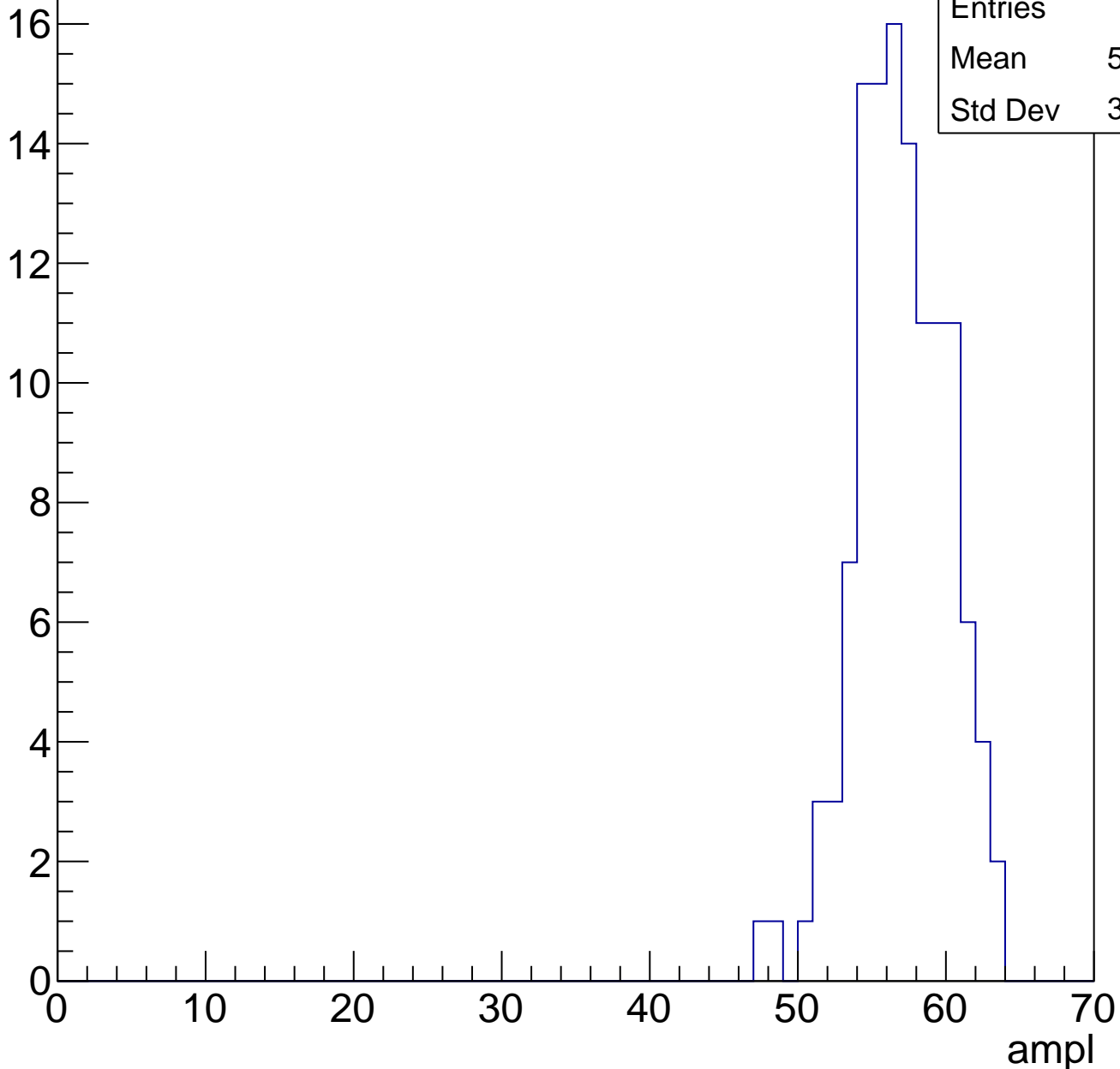


B0L101S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	56.54
Std Dev	3.059

Entry



B0L101S, U14-ch31, adc5

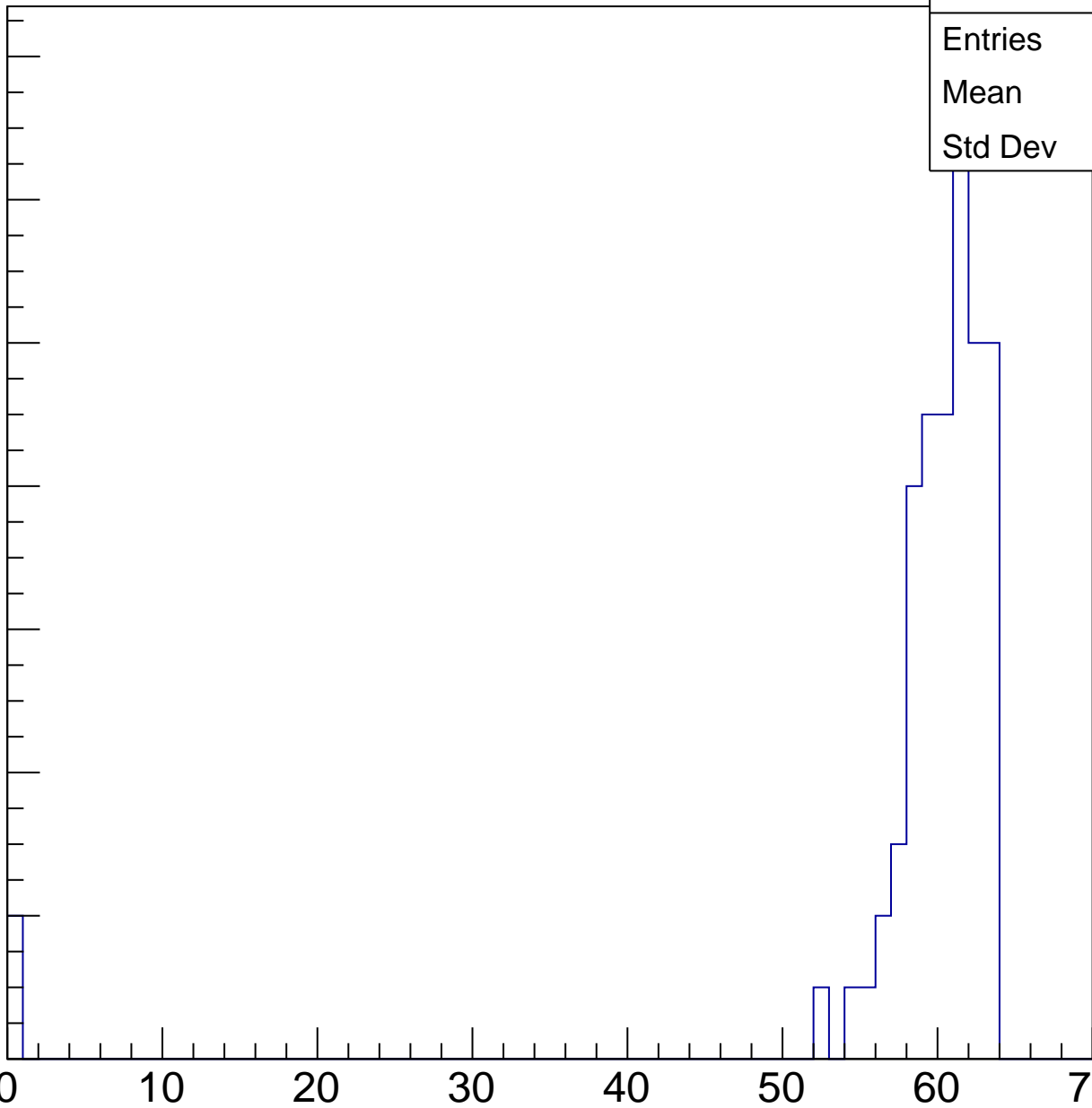
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.33
Std Dev	10.26

ampl

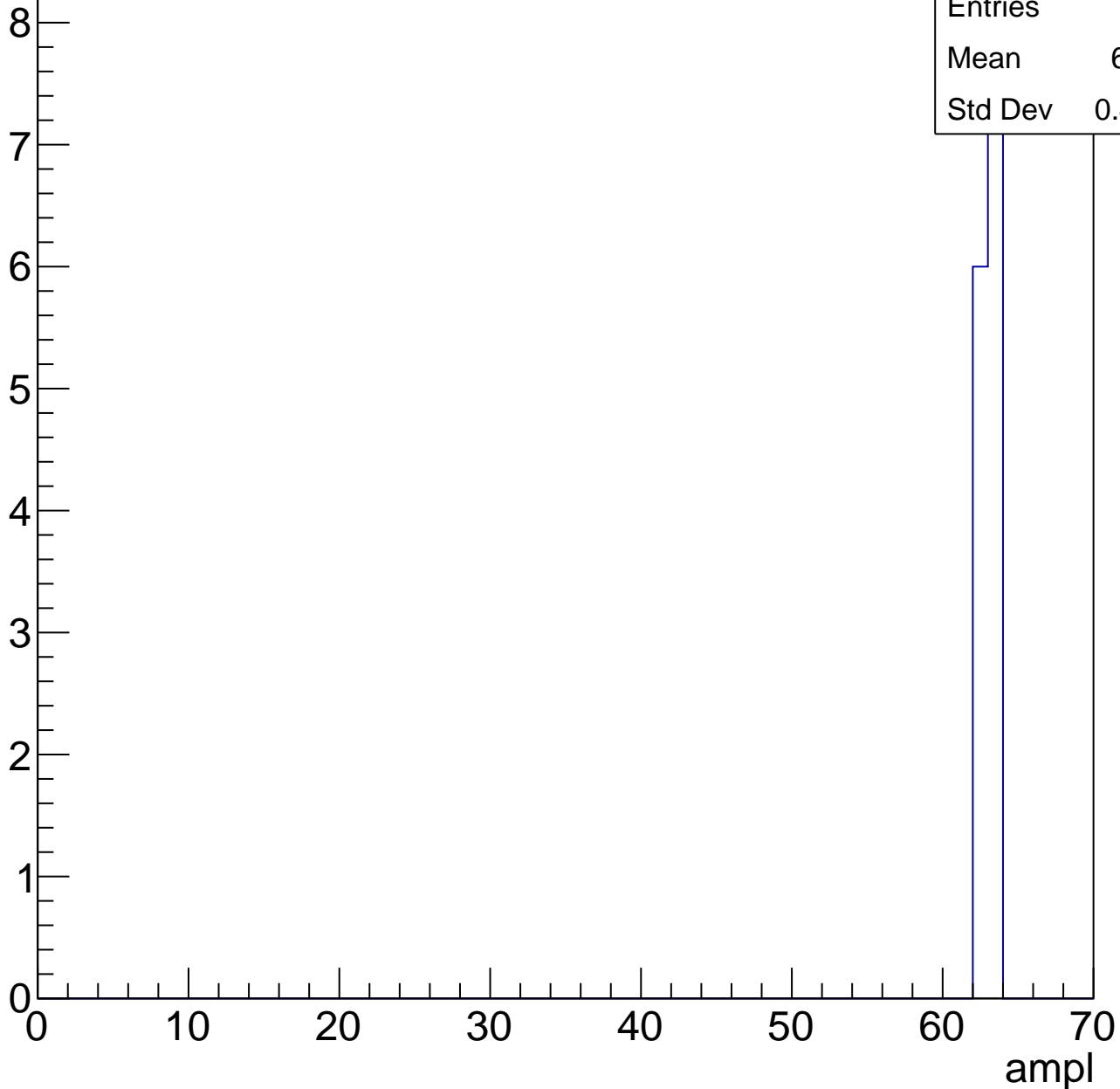


B0L101S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	62.57
Std Dev	0.4949



B0L101S, U14-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



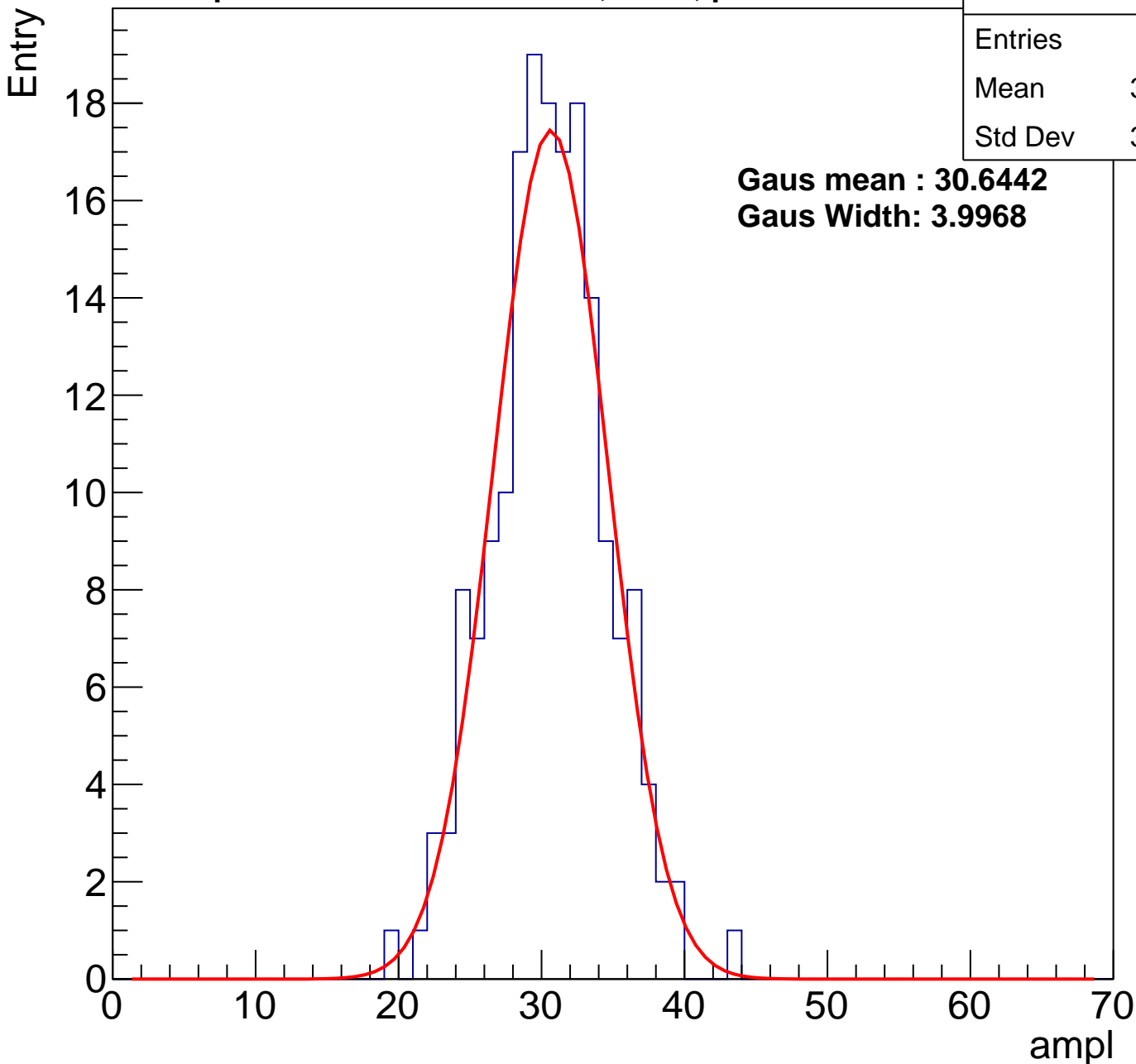
Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	178
Mean	30.12
Std Dev	3.966

Gaus mean : 30.6442
Gaus Width: 3.9968



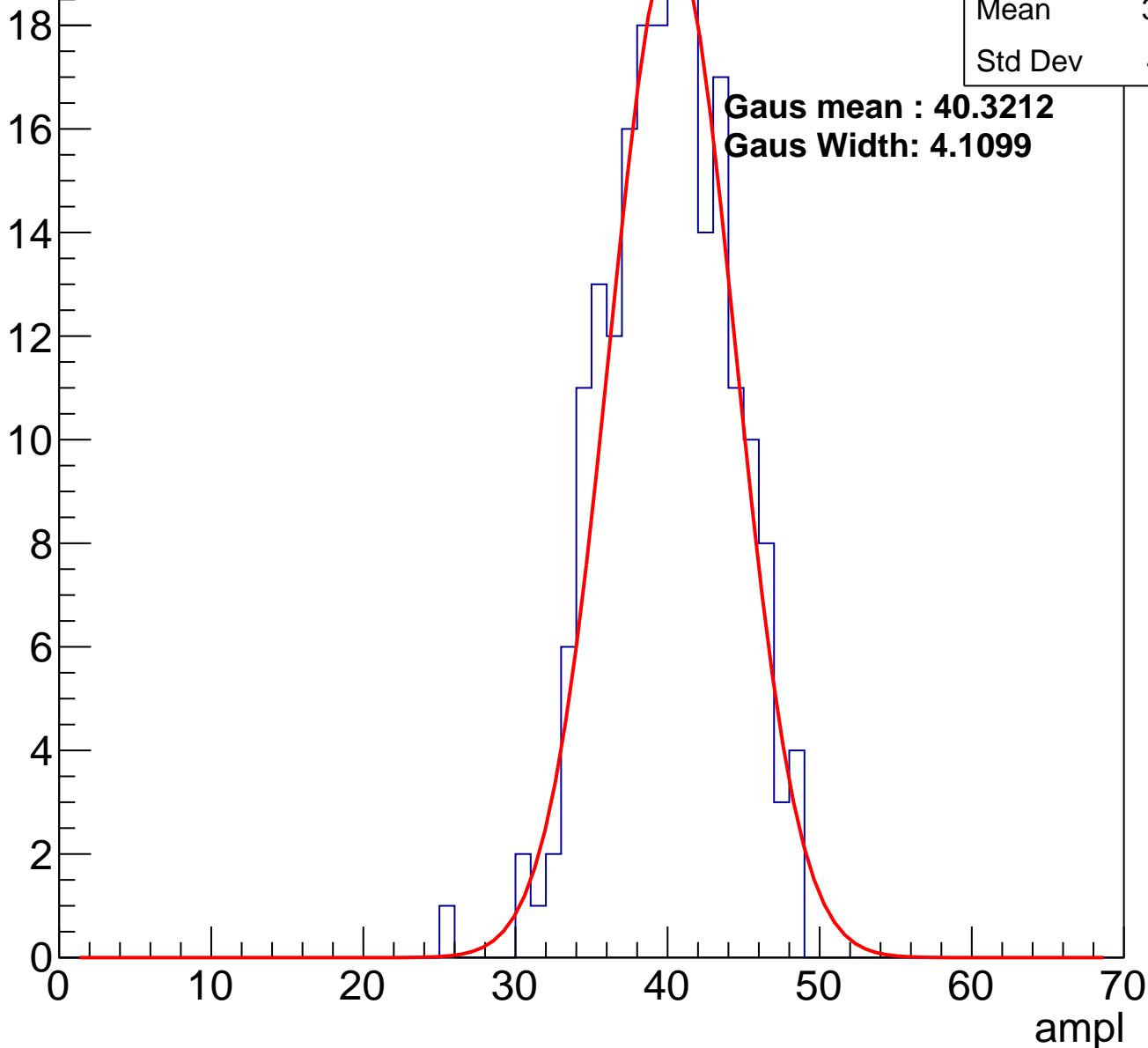
B0L101S, U14-ch32, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	205
Mean	39.56
Std Dev	4.061

Gaus mean : 40.3212
Gaus Width: 4.1099

Entry



B0L101S, U14-ch32, adc2

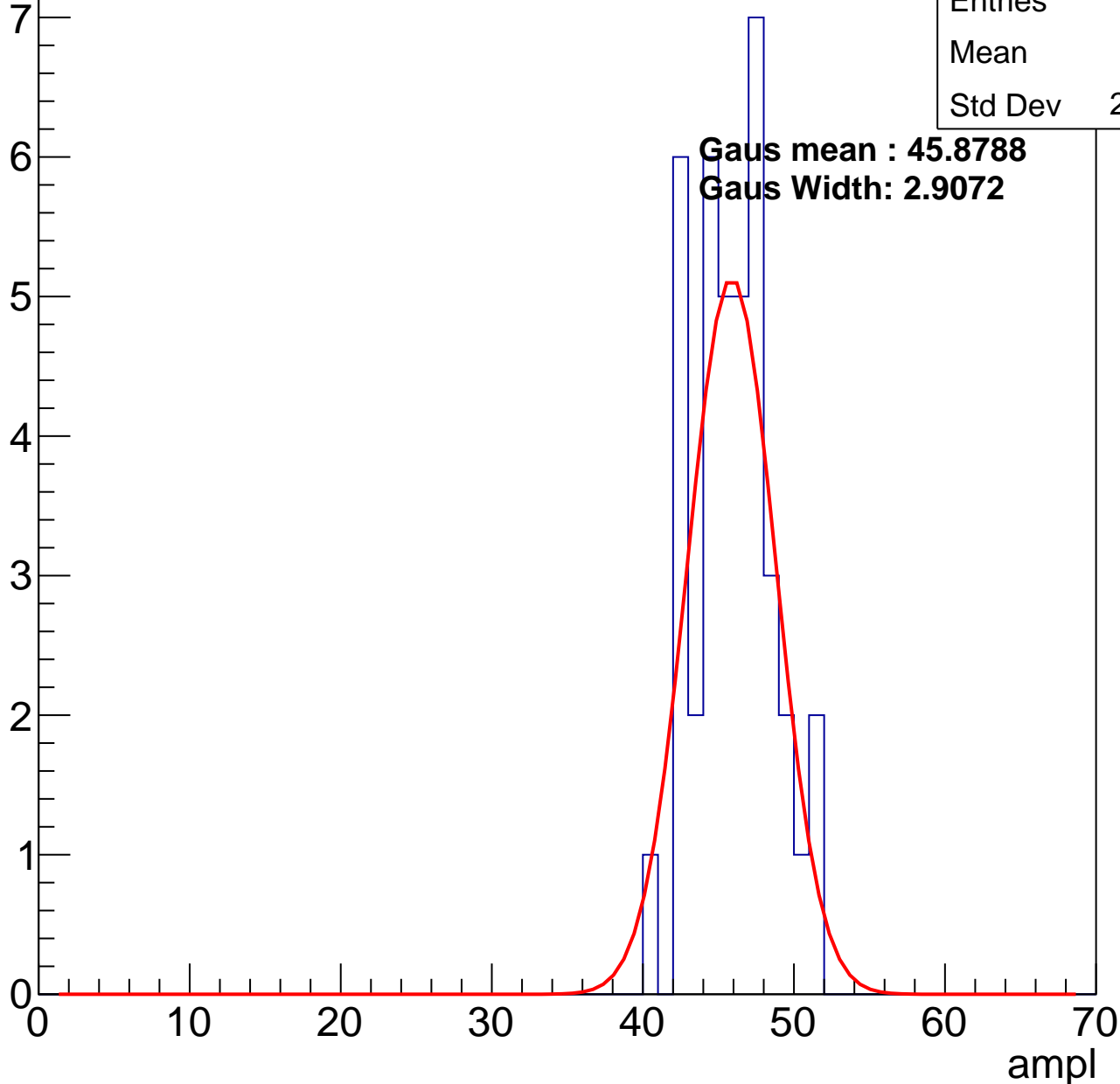
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	40
Mean	45.5
Std Dev	2.608

Gaus mean : 45.8788

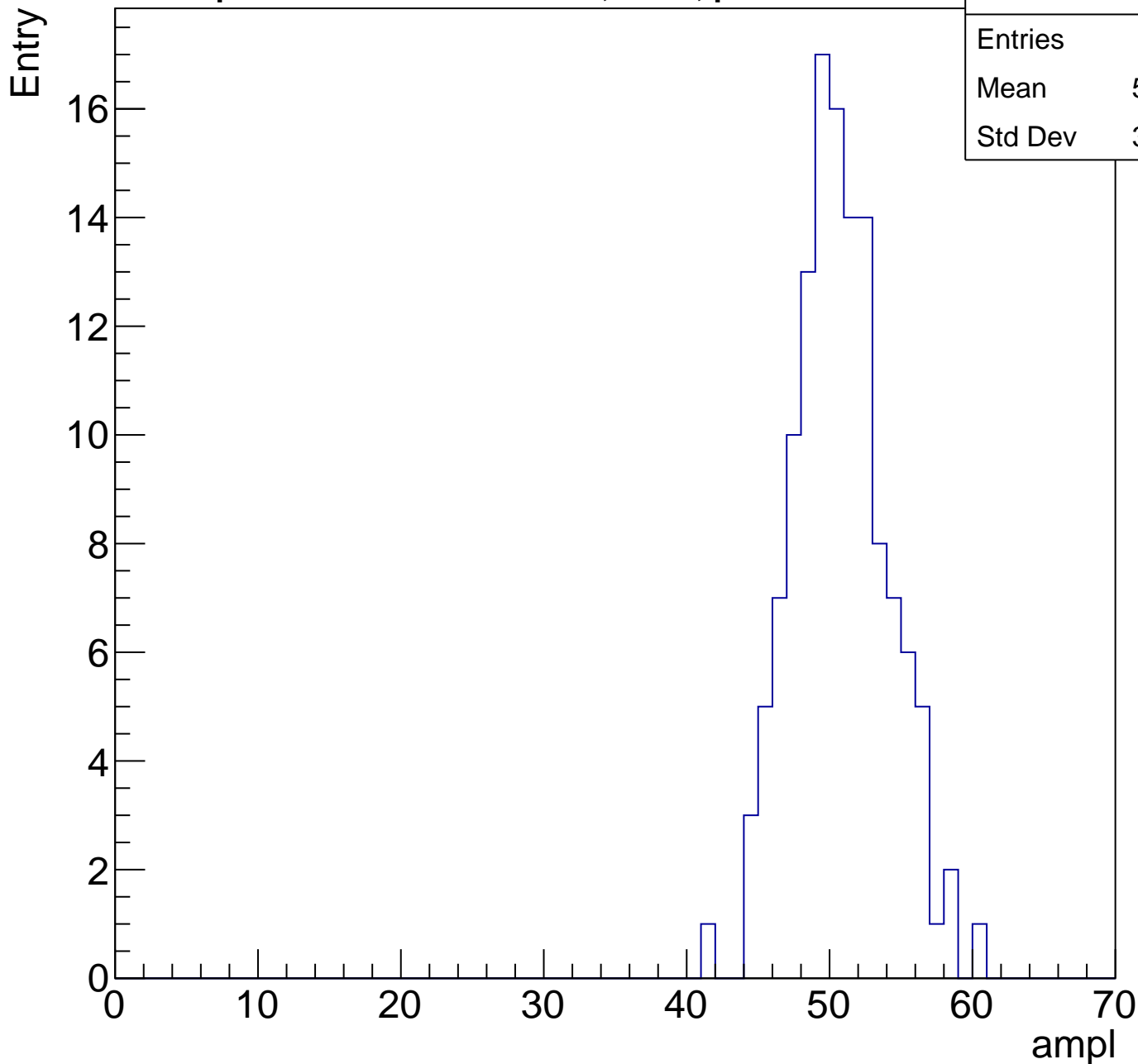
Gaus Width: 2.9072



B0L101S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

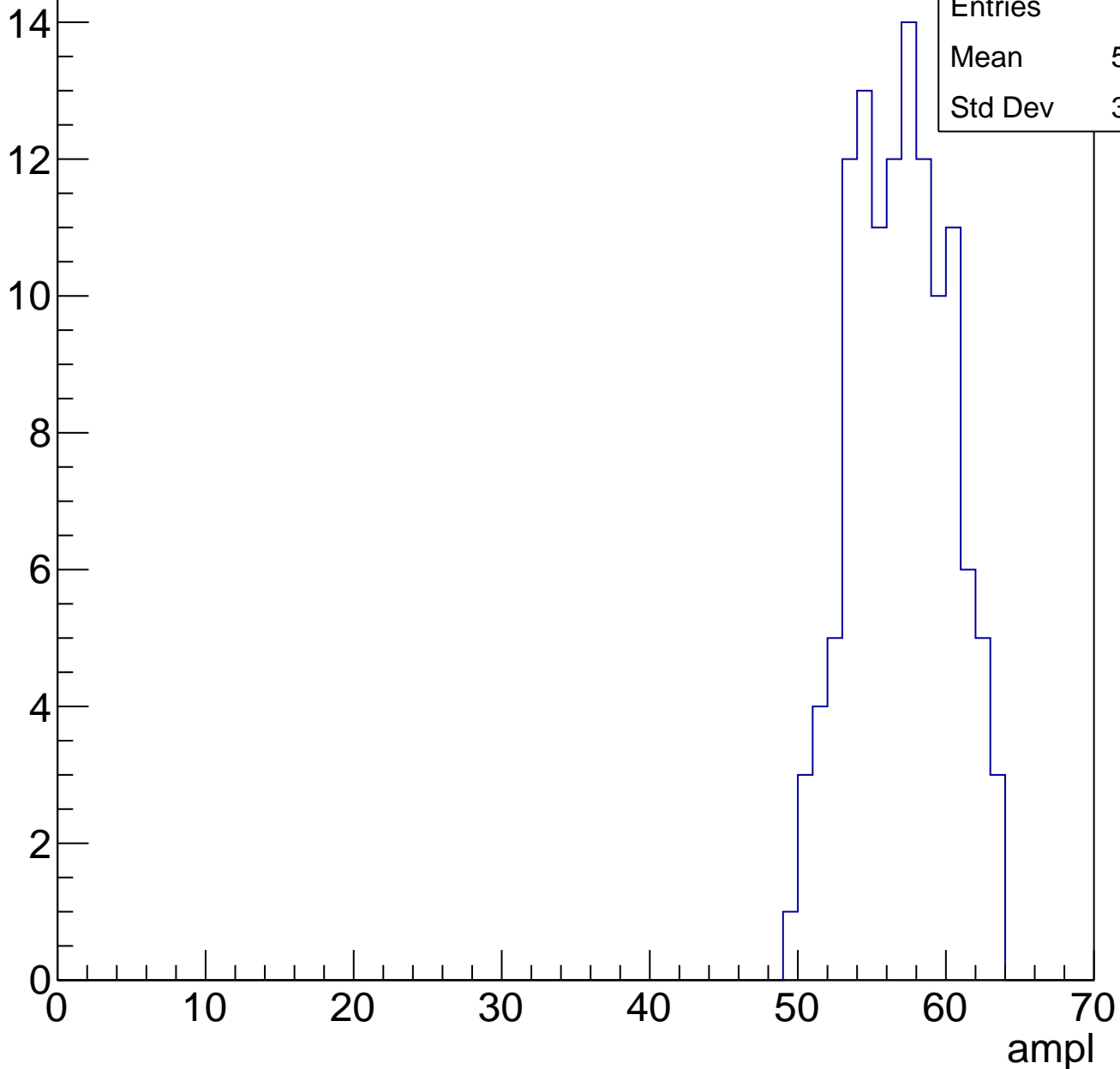
Entries	130
Mean	50.26
Std Dev	3.325



B0L101S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	122
Mean	56.45
Std Dev	3.249

B0L101S, U14-ch32, adc5

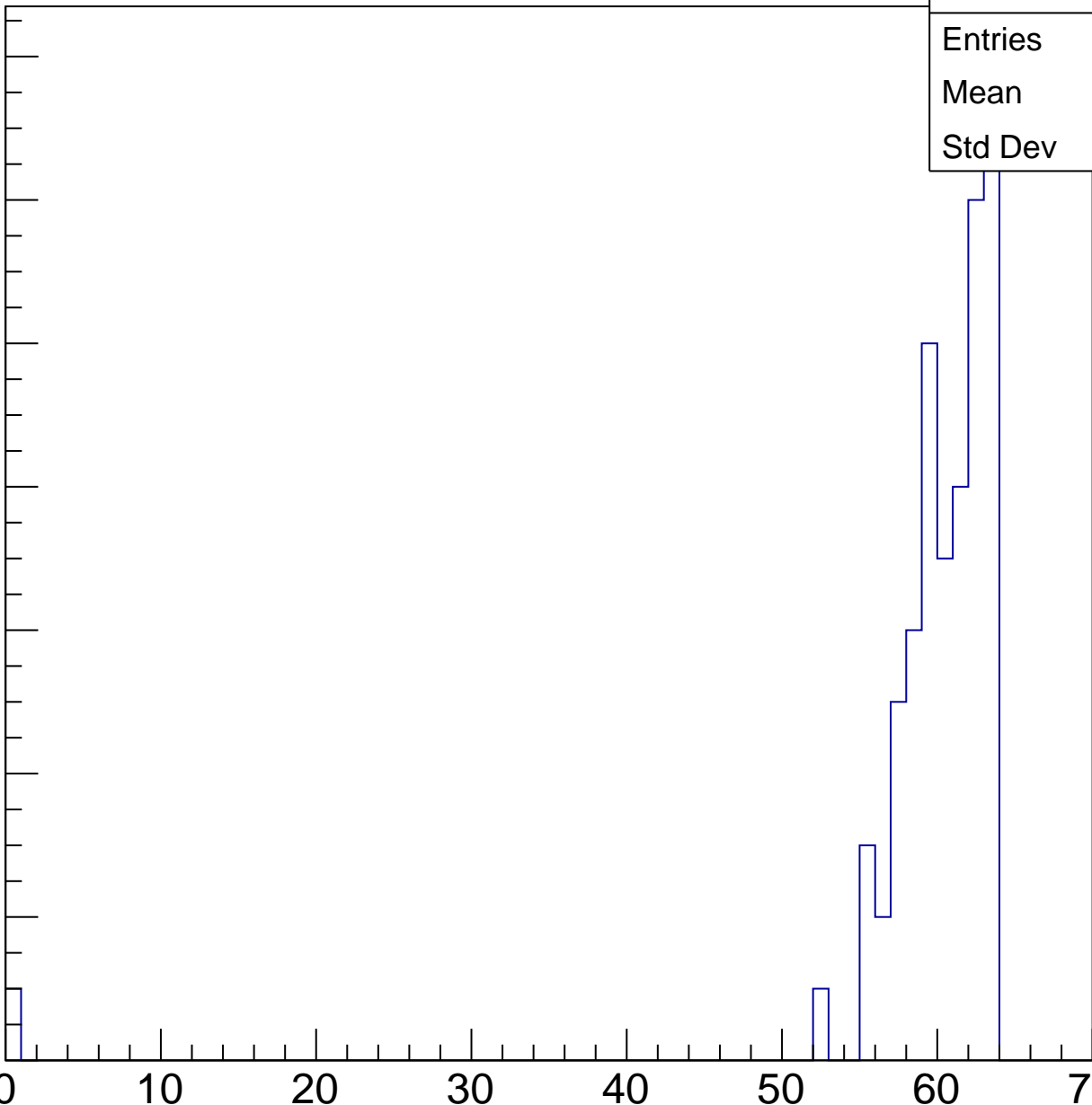
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	59.22
Std Dev	7.604

ampl

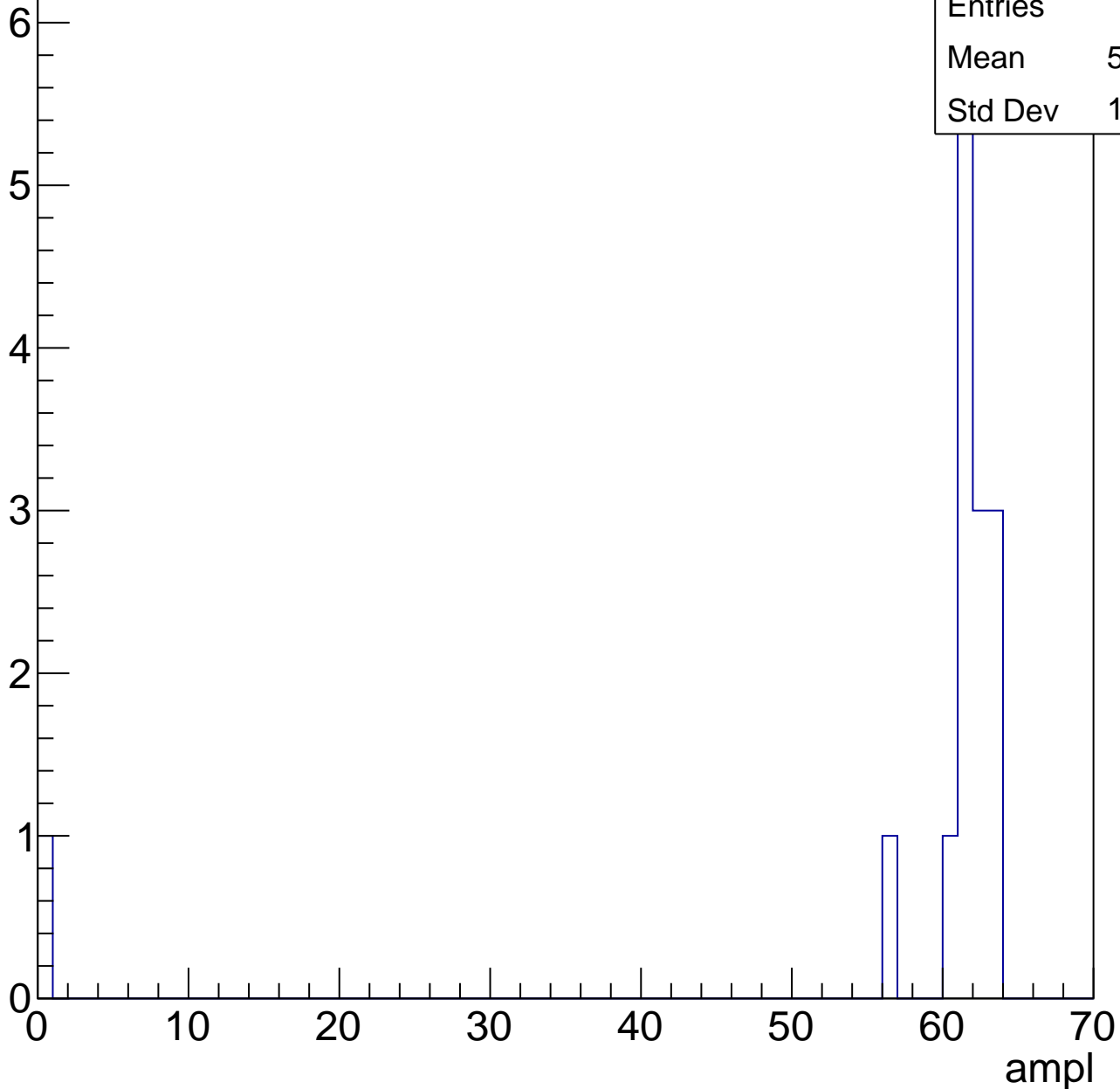


B0L101S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.13
Std Dev	15.36



B0L101S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



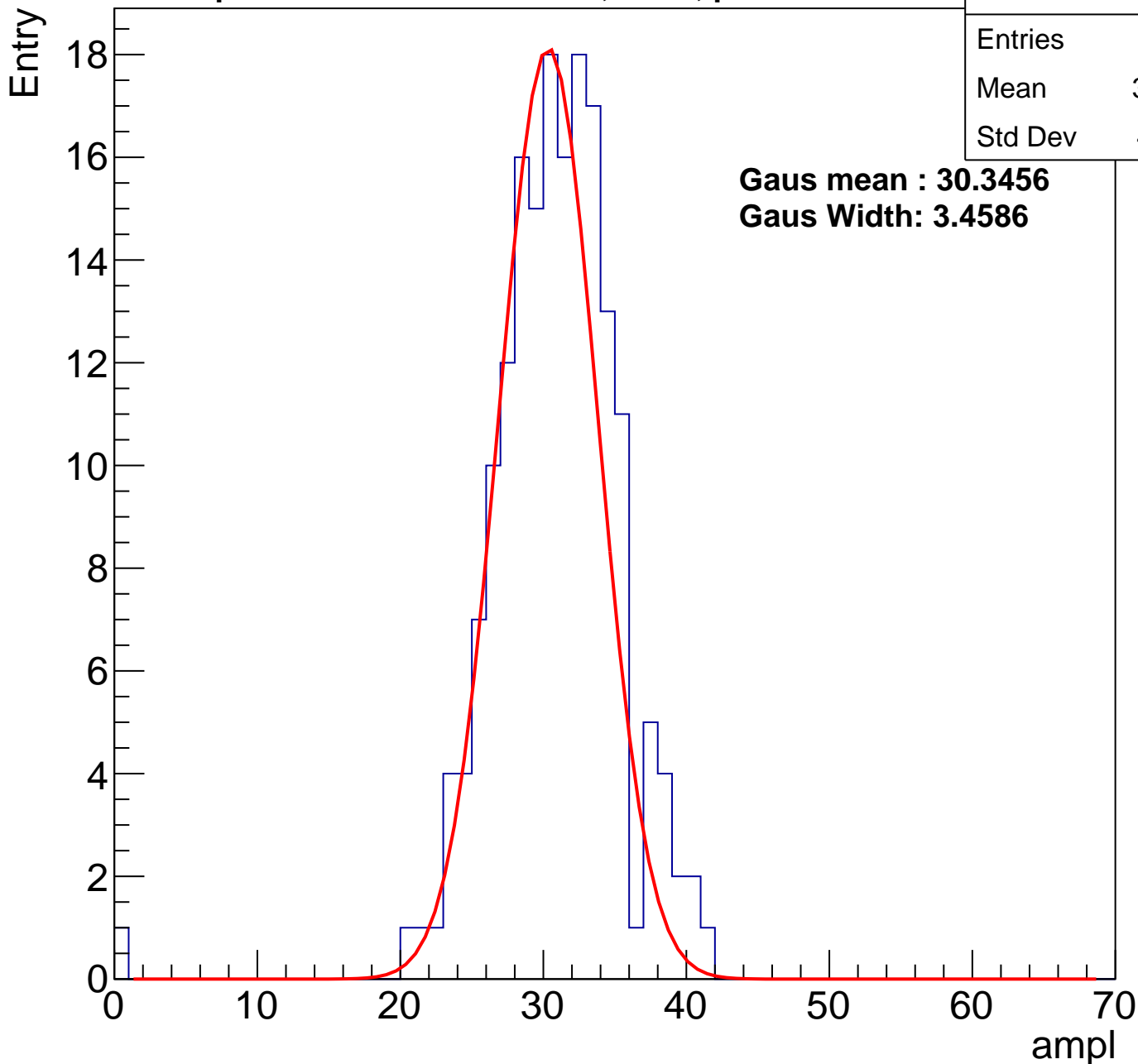
B0L101S, U14-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	30.36
Std Dev	4.551

Gaus mean : 30.3456

Gaus Width: 3.4586



B0L101S, U14-ch33, adc1

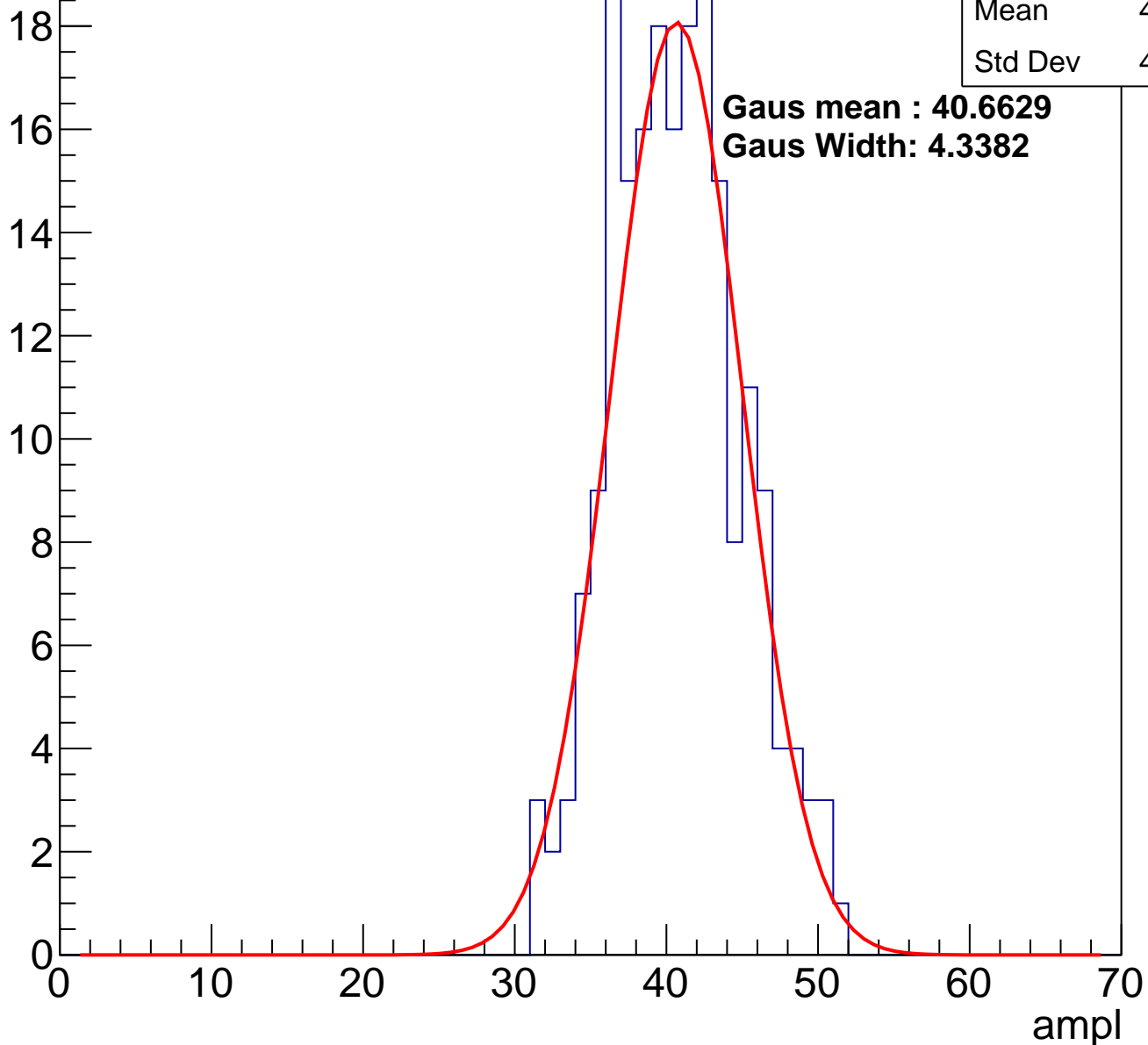
calib_packv5_042523_0143.root, FC#1, port C1

Entries	203
Mean	40.24
Std Dev	4.197

Gaus mean : 40.6629

Gaus Width: 4.3382

Entry



B0L101S, U14-ch33, adc2

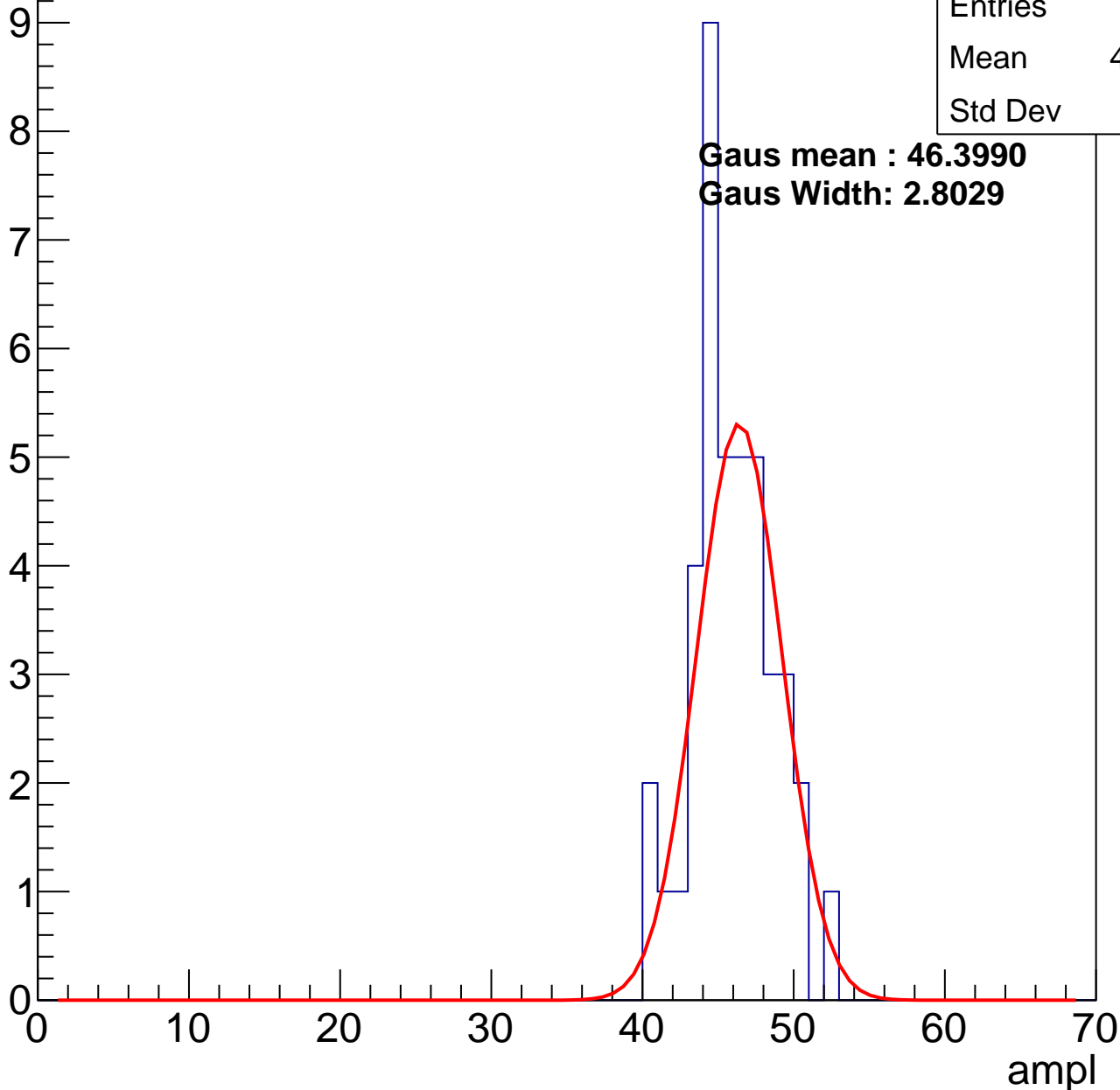
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	41
Mean	45.46
Std Dev	2.66

Gaus mean : 46.3990

Gaus Width: 2.8029

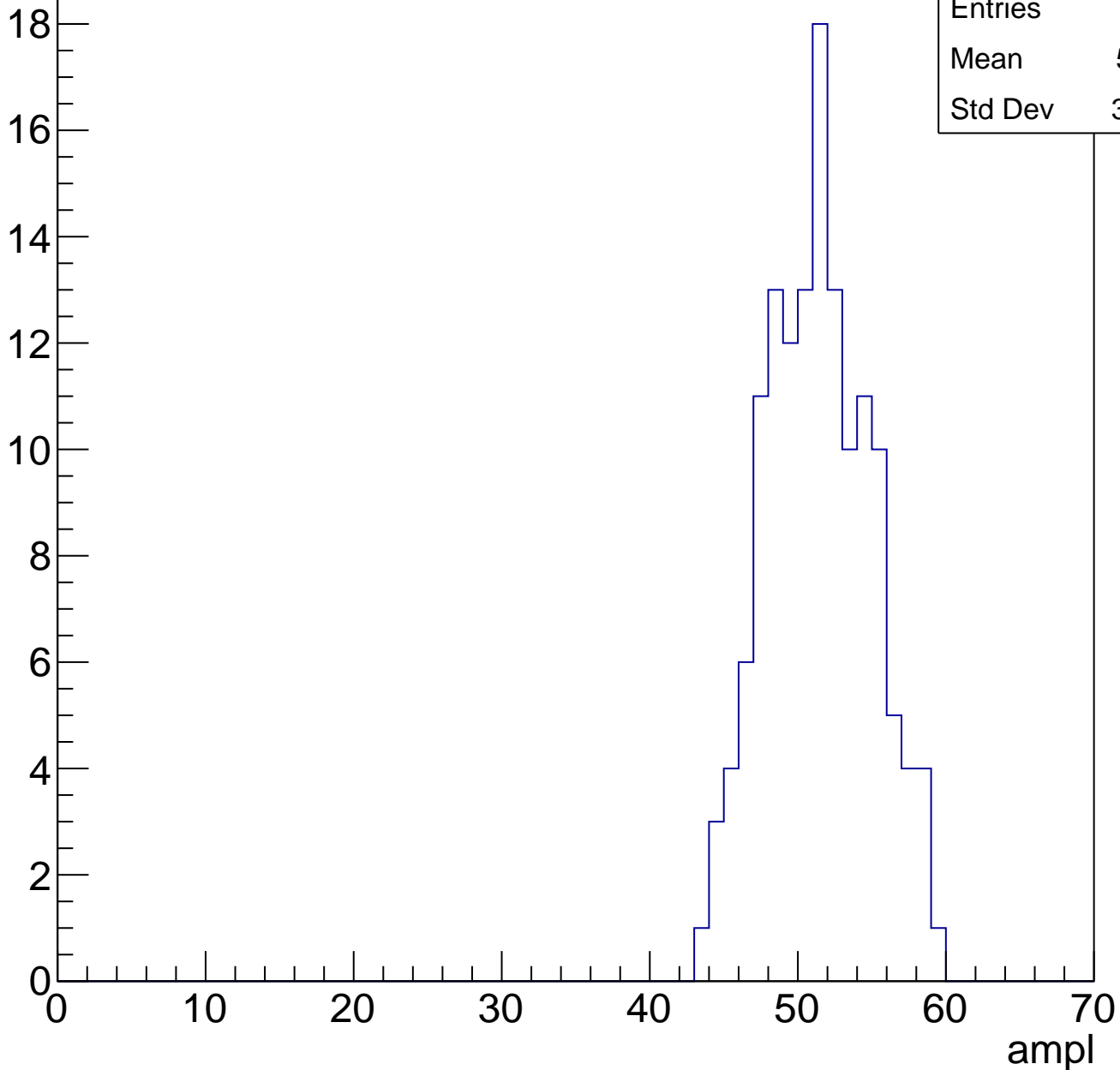


B0L101S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.91
Std Dev	3.496

Entry

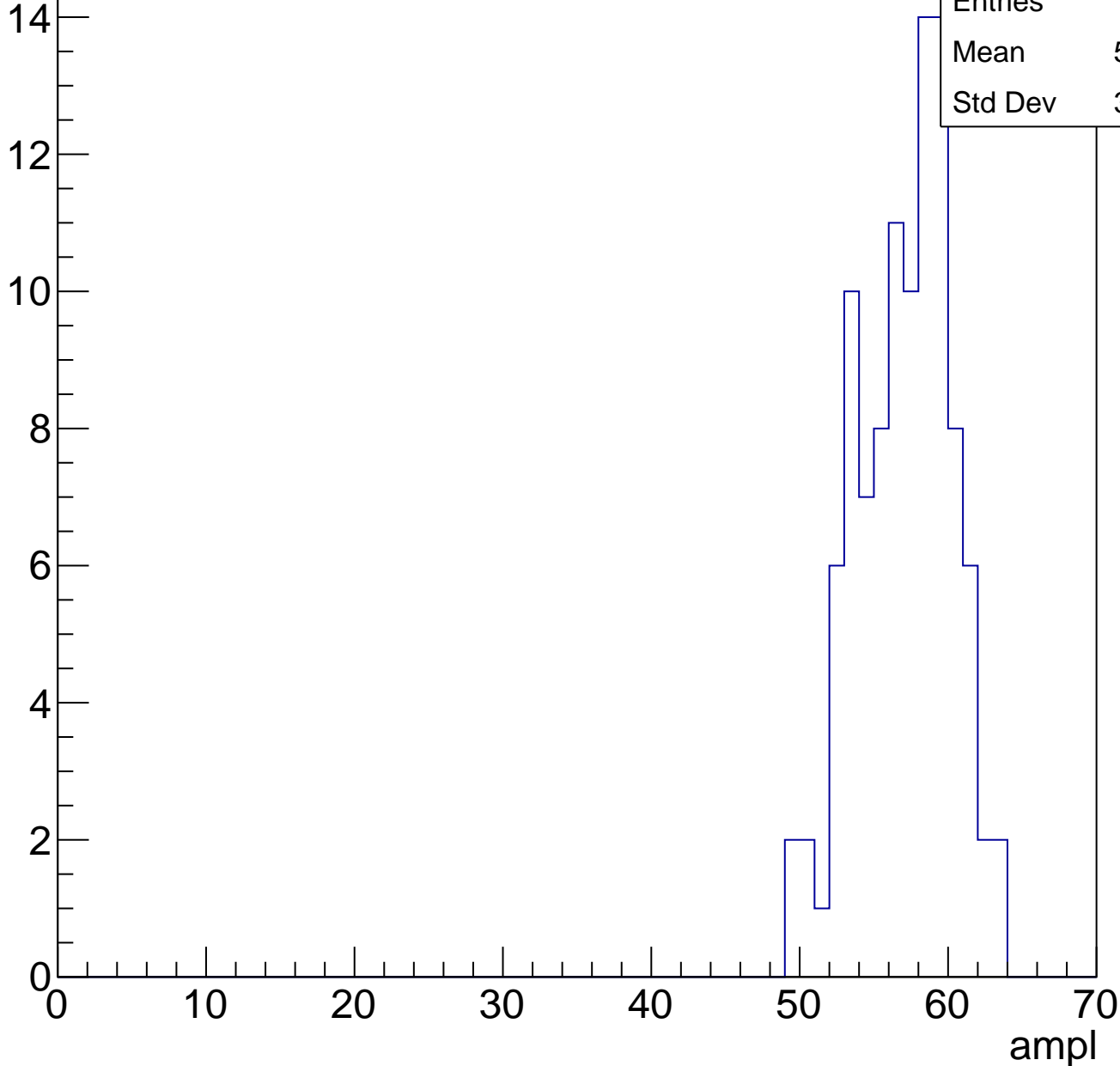


B0L101S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	56.59
Std Dev	3.148

Entry



B0L101S, U14-ch33, adc5

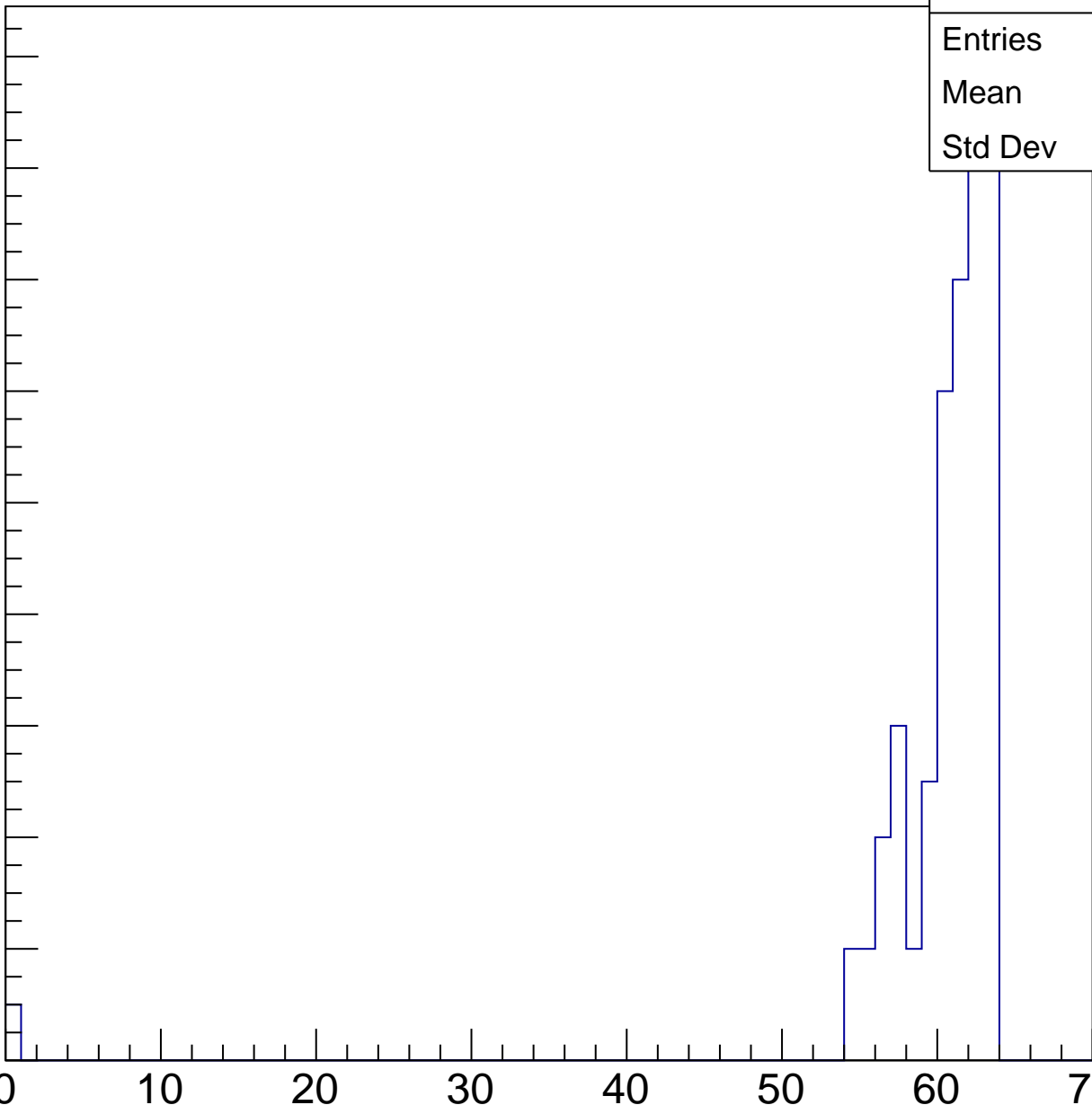
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.72
Std Dev	7.02

ampl



B0L101S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch34, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	29.59
Std Dev	3.365

Gaus mean : 30.0610

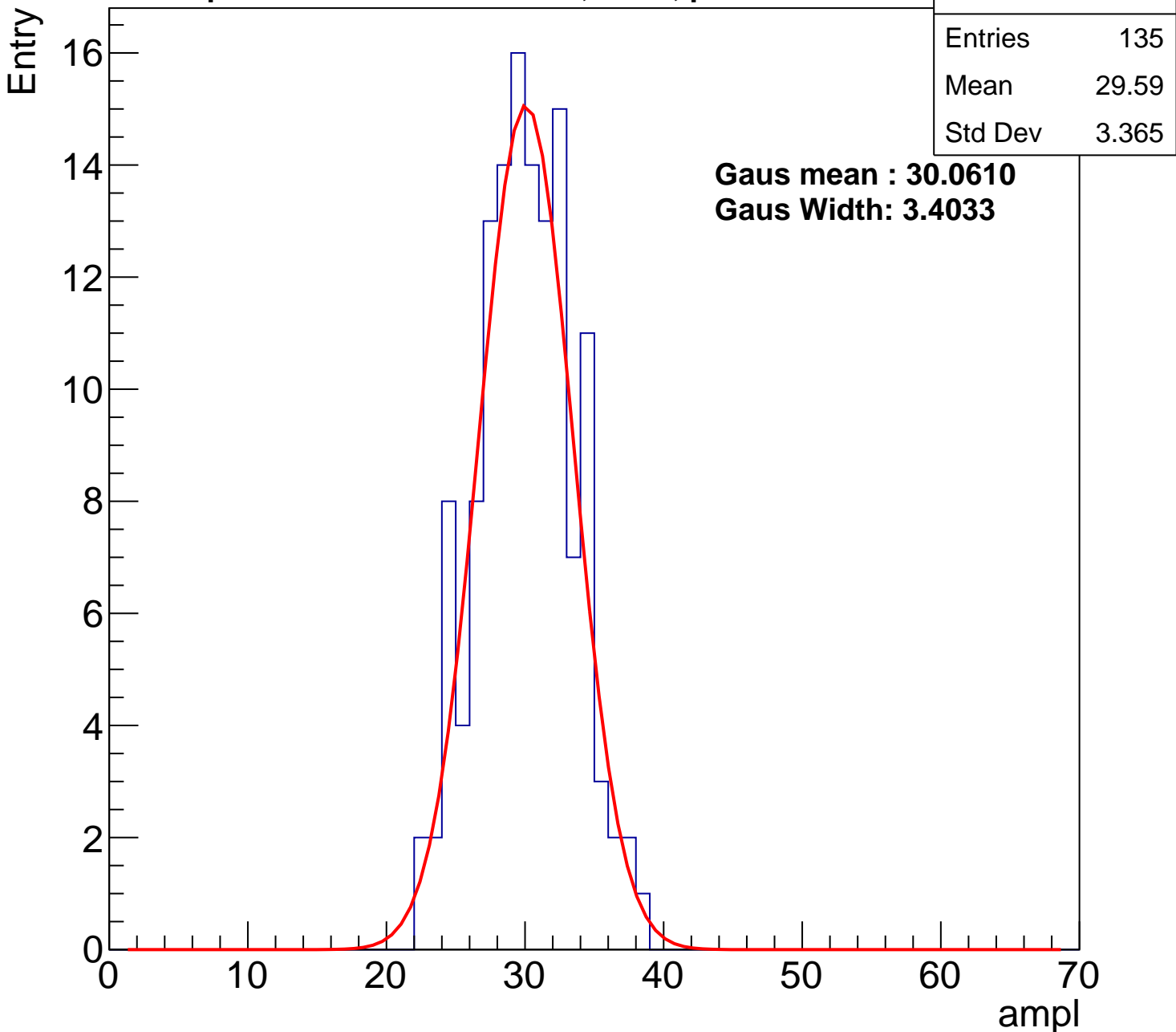
Gaus Width: 3.4033

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

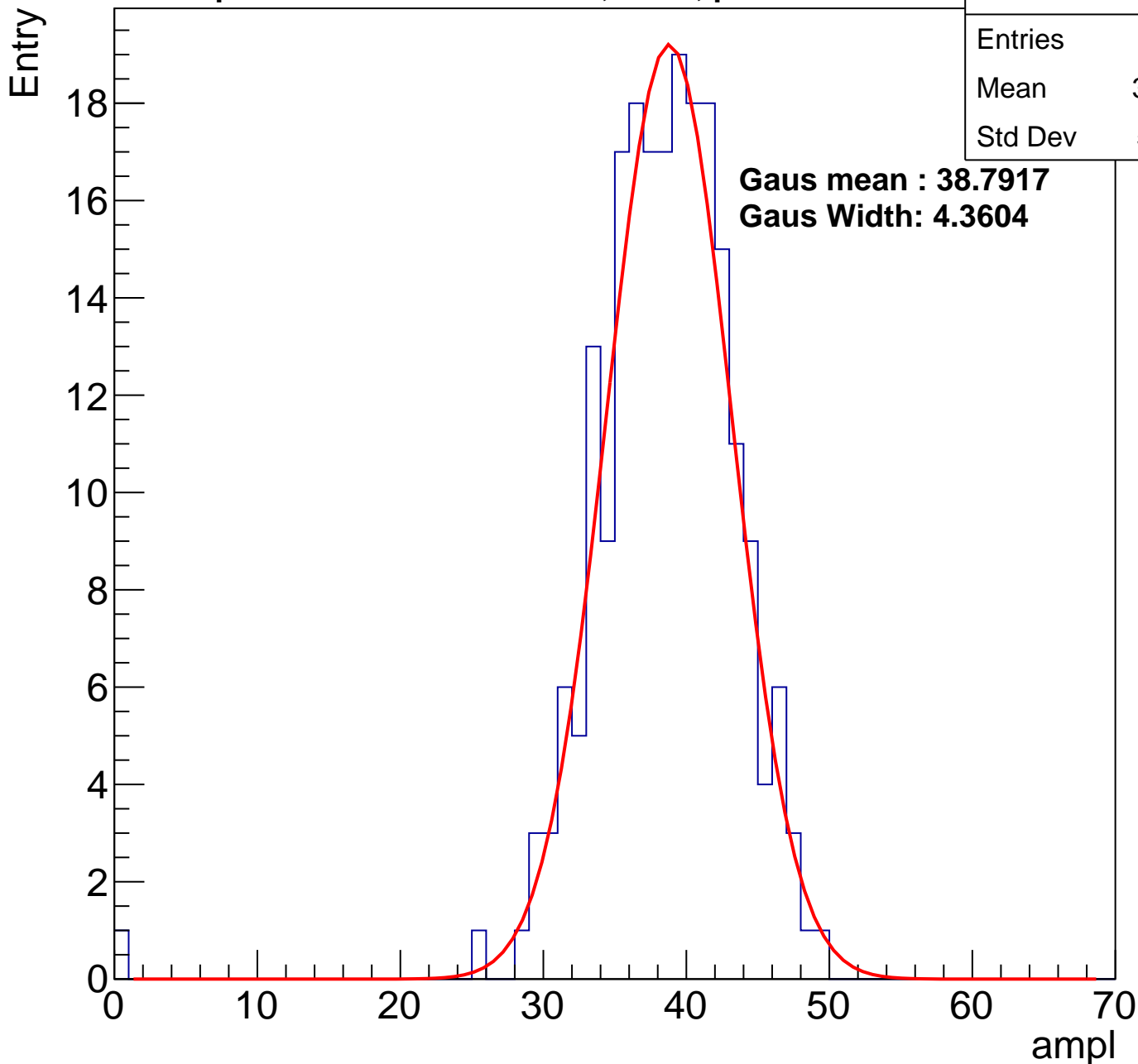


B0L101S, U14-ch34, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	216
Mean	38.06
Std Dev	5.031

Gaus mean : 38.7917
Gaus Width: 4.3604



B0L101S, U14-ch34, adc2

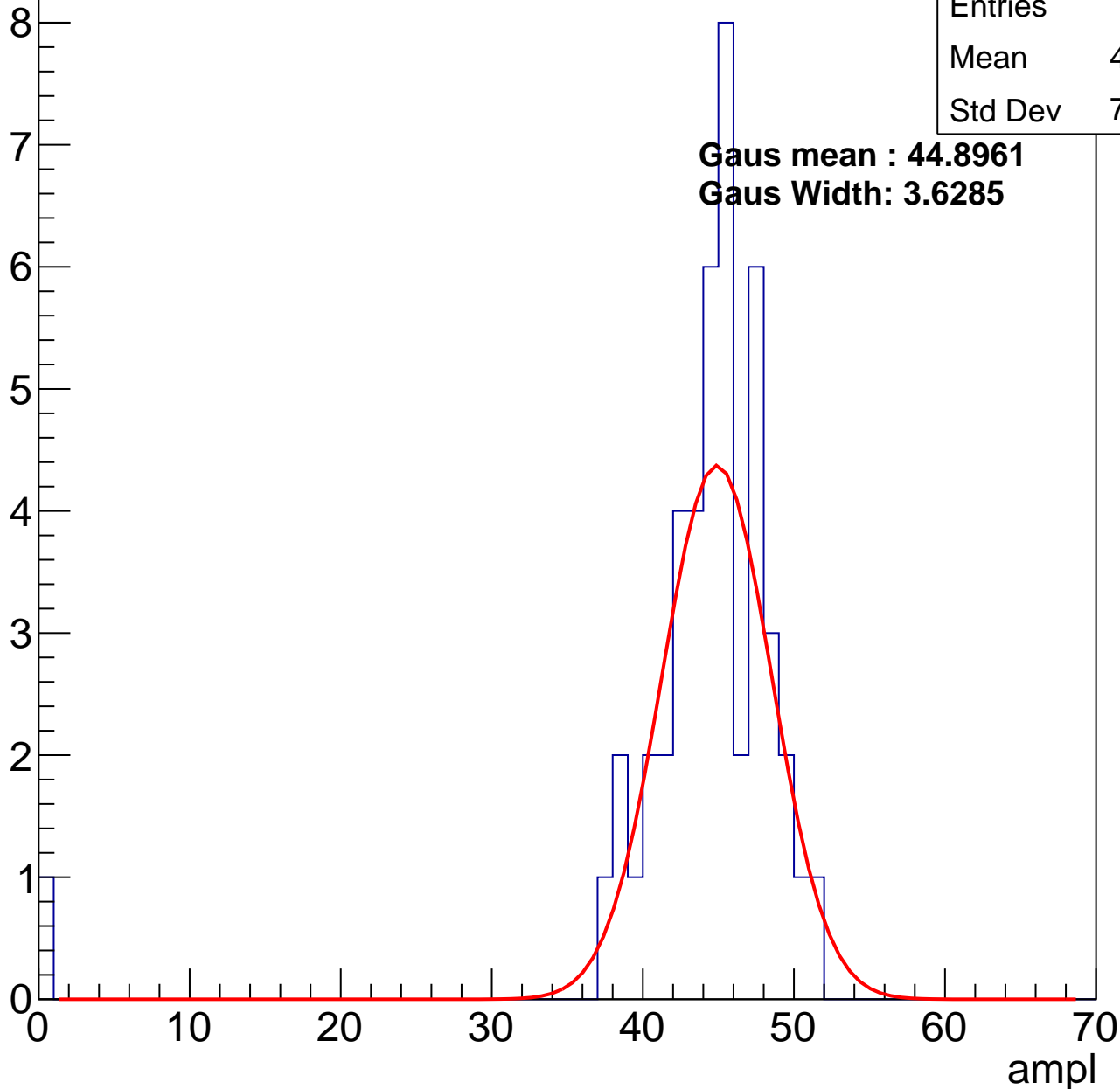
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	46
Mean	43.37
Std Dev	7.203

Gaus mean : 44.8961

Gaus Width: 3.6285



B0L101S, U14-ch34, adc3

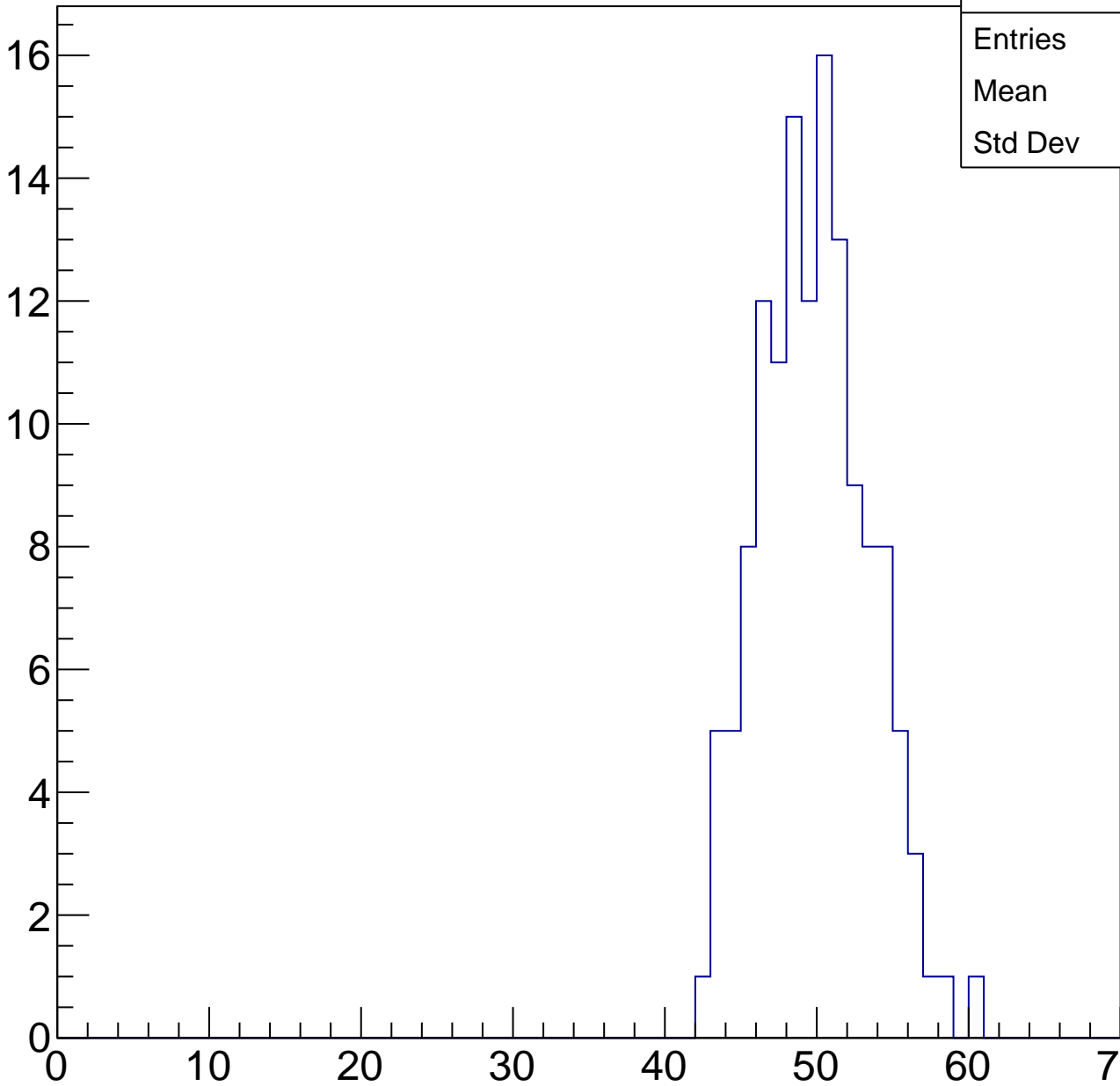
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	49.4
Std Dev	3.56

ampl



B0L101S, U14-ch34, adc4

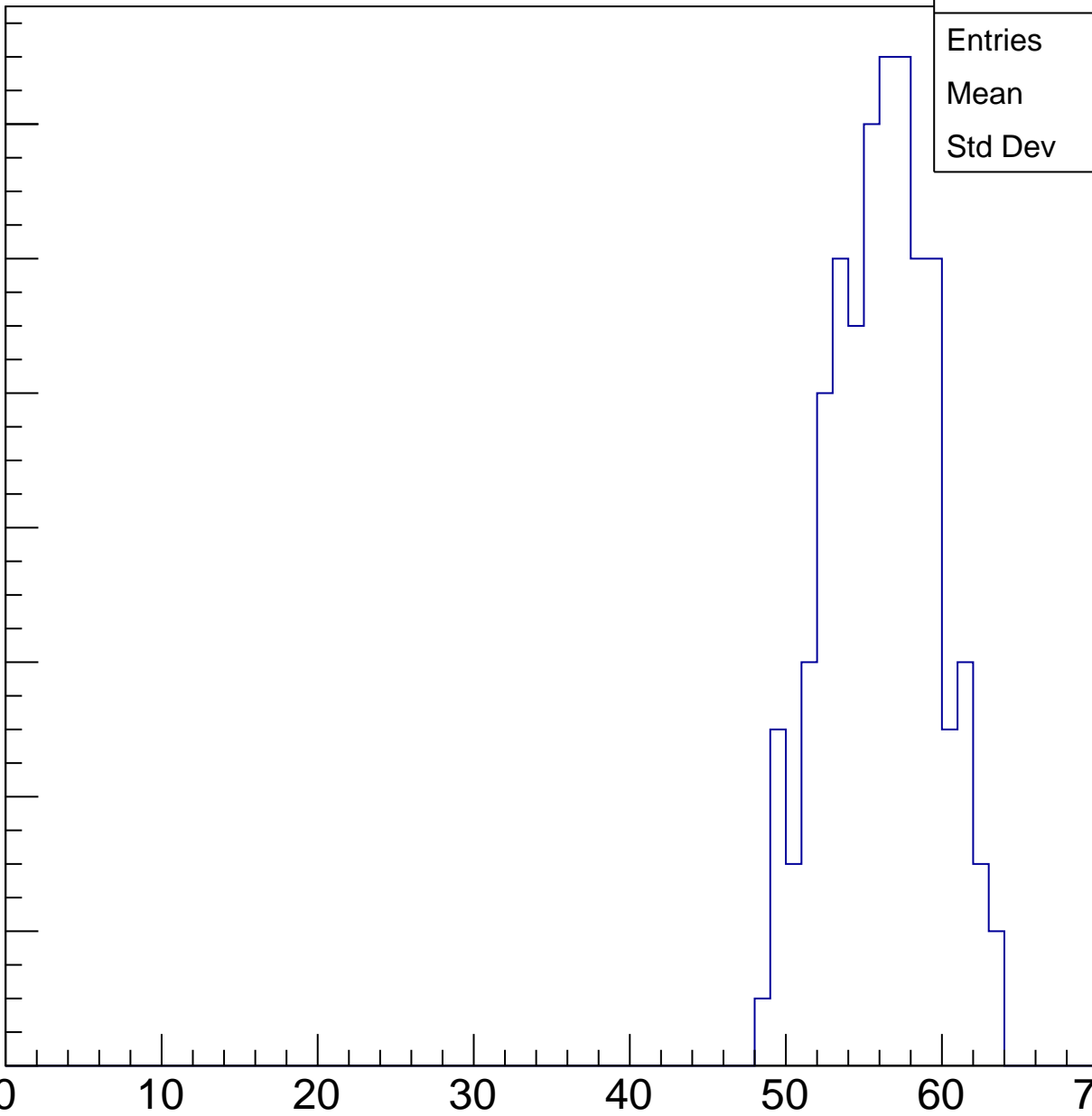
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	132
Mean	55.65
Std Dev	3.353

ampl



B0L101S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	59
Mean	58.98
Std Dev	8.098

0

2

4

6

8

10

B0L101S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

Entries	30
Mean	59.37
Std Dev	11.15

ampl

0

10

20

30

40

50

60

70

B0L101S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch35, adc0

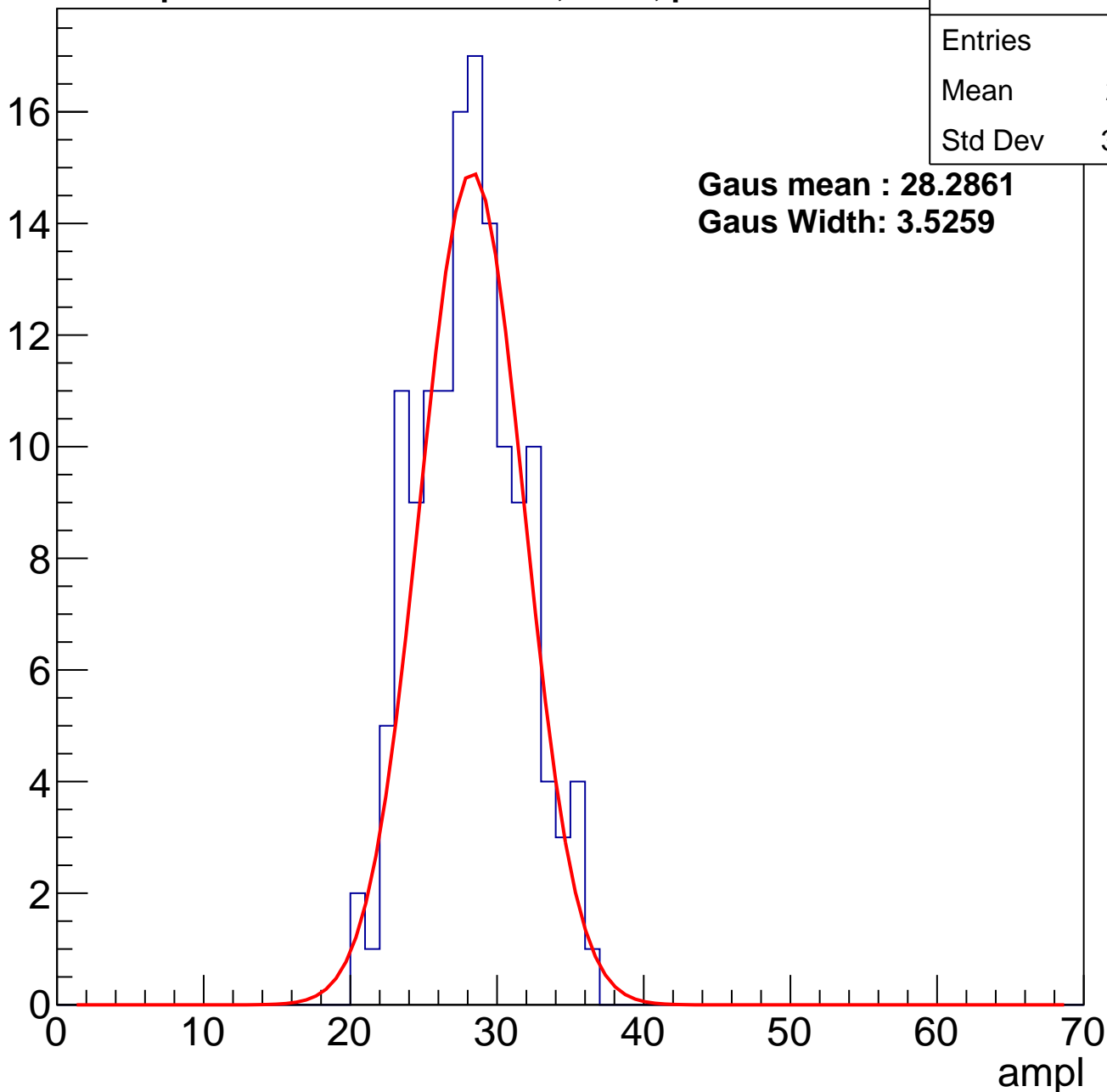
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	27.71
Std Dev	3.496

Gaus mean : 28.2861

Gaus Width: 3.5259

Entry

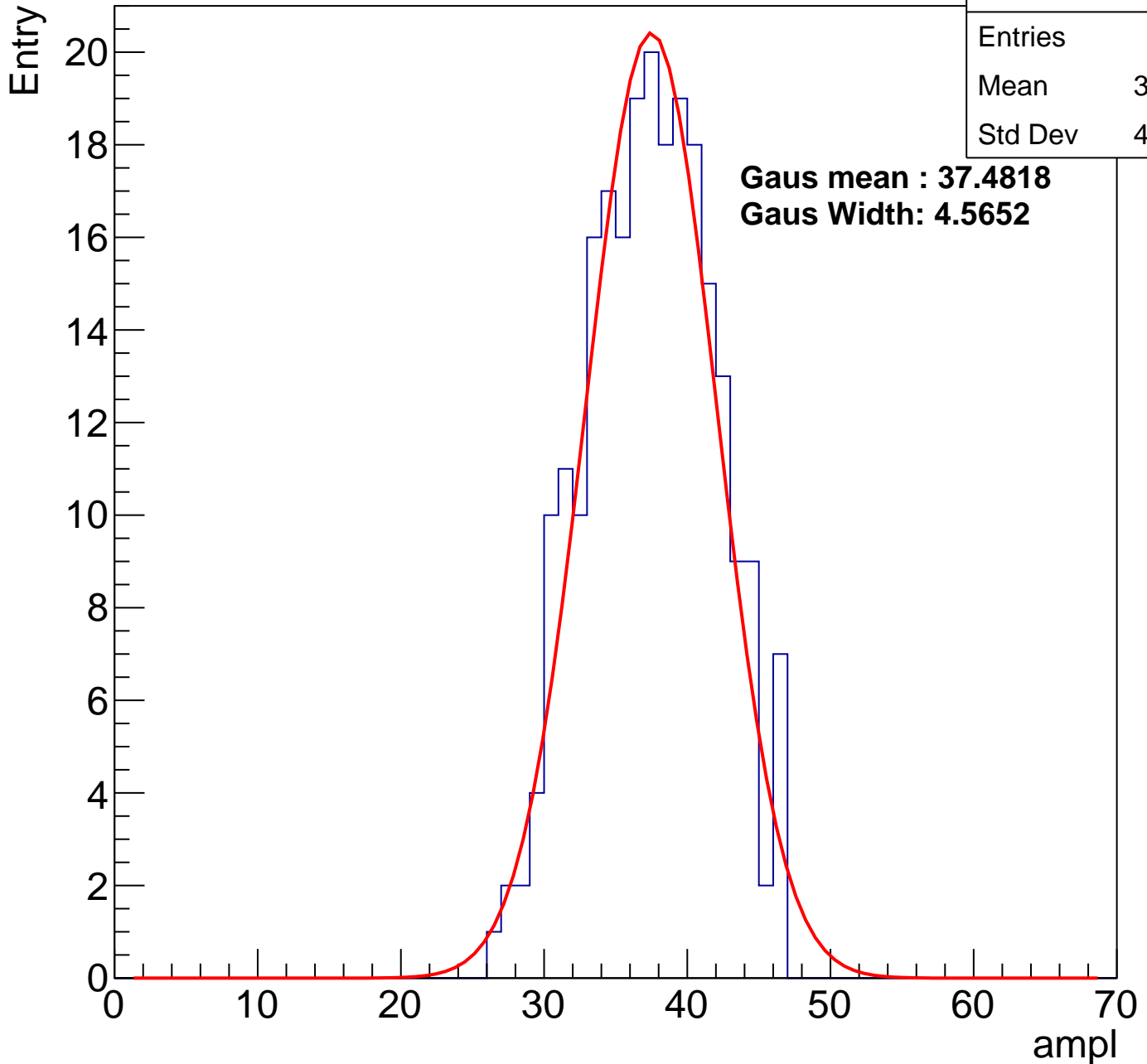


B0L101S, U14-ch35, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	238
Mean	36.99
Std Dev	4.419

Gaus mean : 37.4818
Gaus Width: 4.5652



B0L101S, U14-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	70
Mean	45.29
Std Dev	3.085

Gaus mean : 45.8736

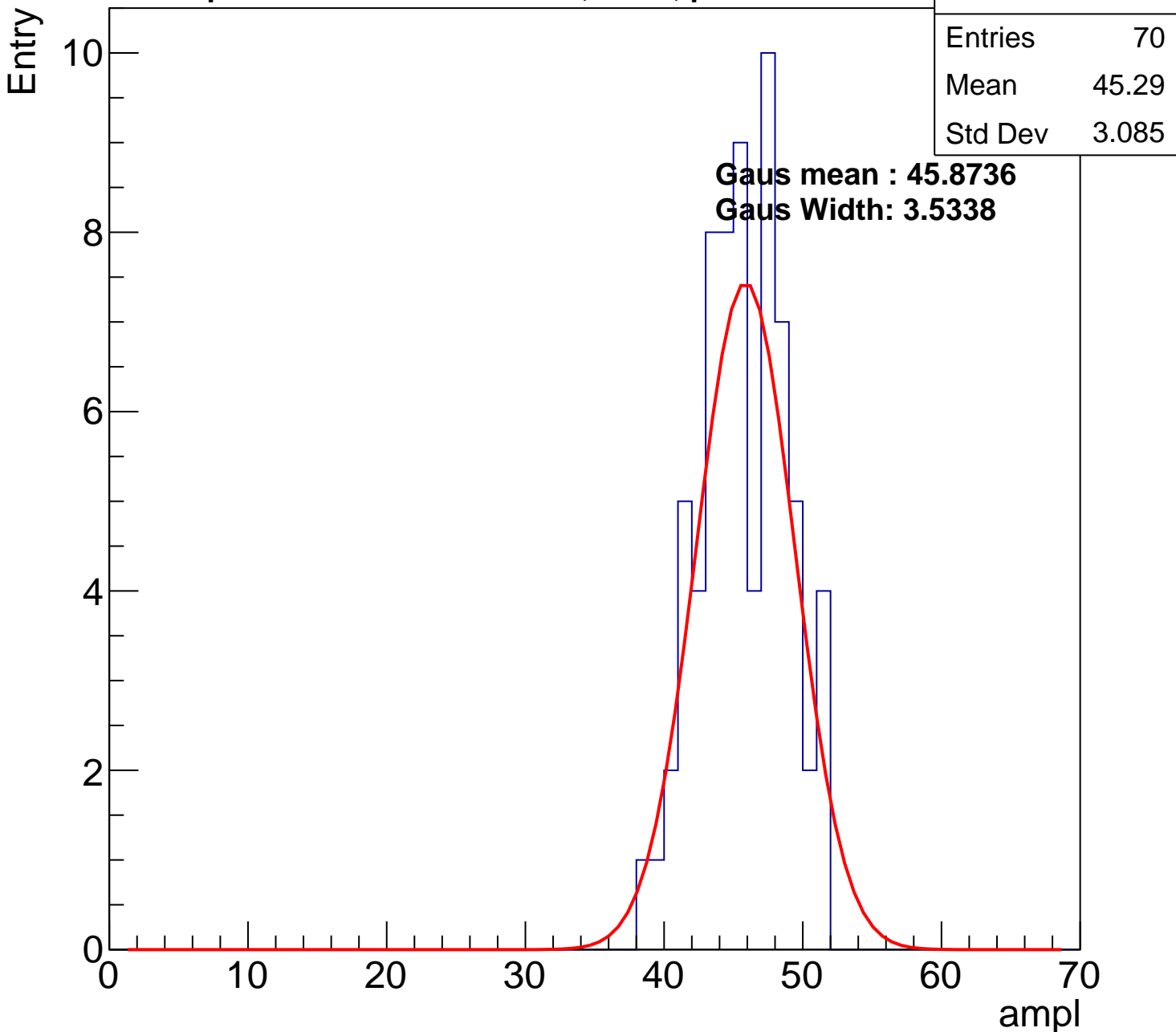
Gaus Width: 3.5338

Entry

10
8
6
4
2
0

ampl

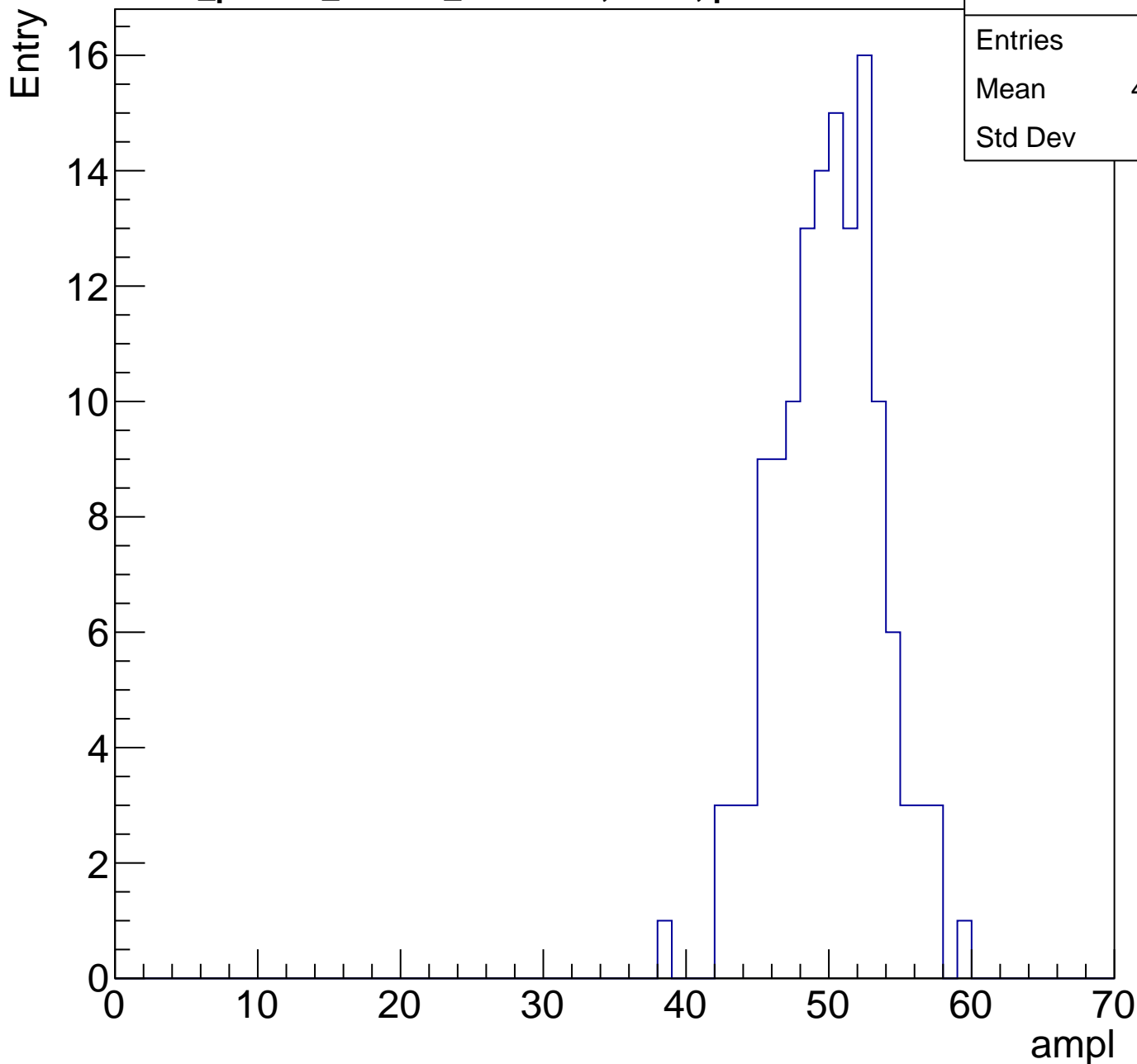
0 10 20 30 40 50 60 70



B0L101S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	49.53
Std Dev	3.61



B0L101S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

Entries	65
Mean	55.03
Std Dev	2.768

B0L101S, U14-ch35, adc5

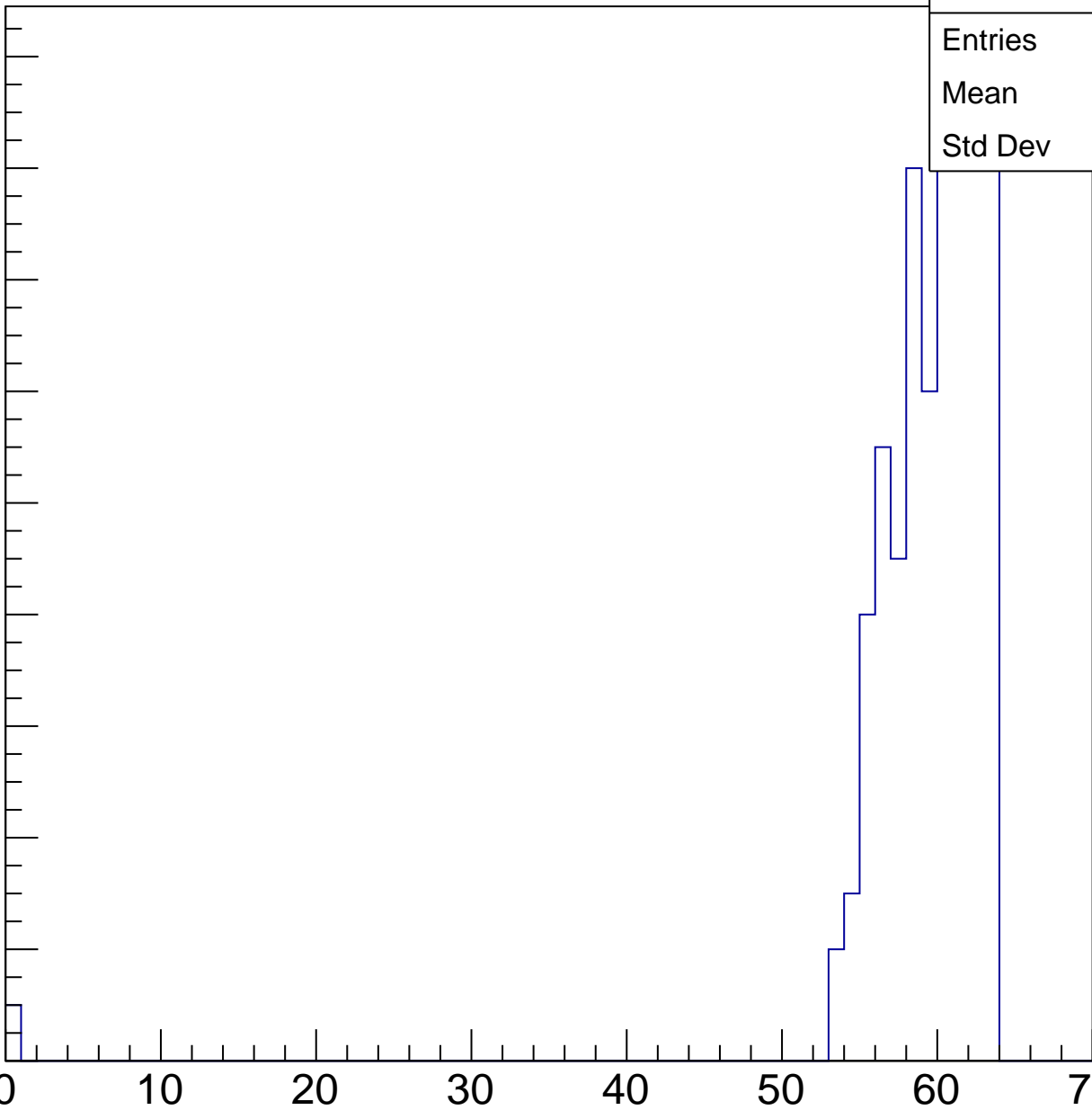
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	132
Mean	58.92
Std Dev	5.791

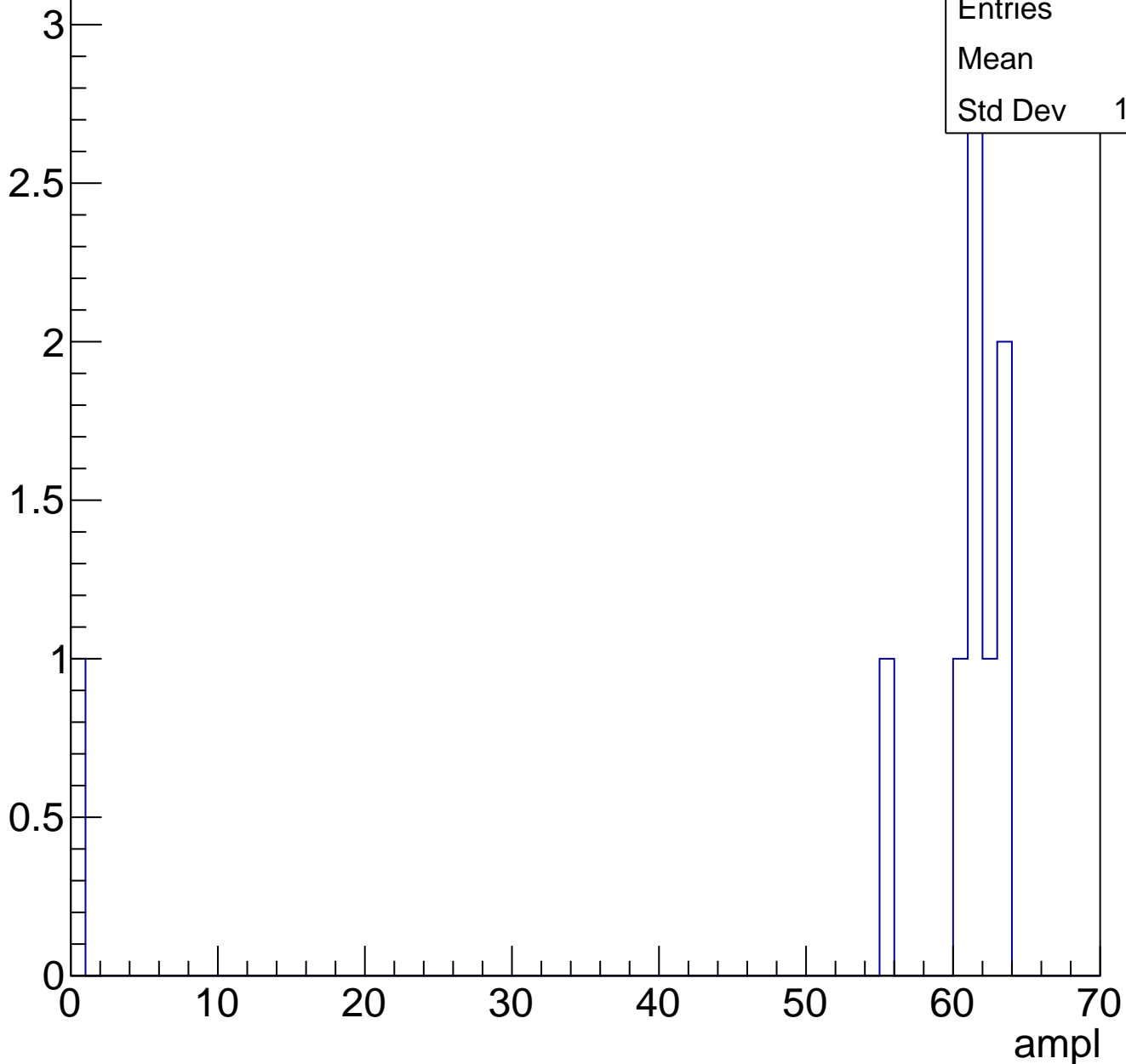
ampl



B0L101S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

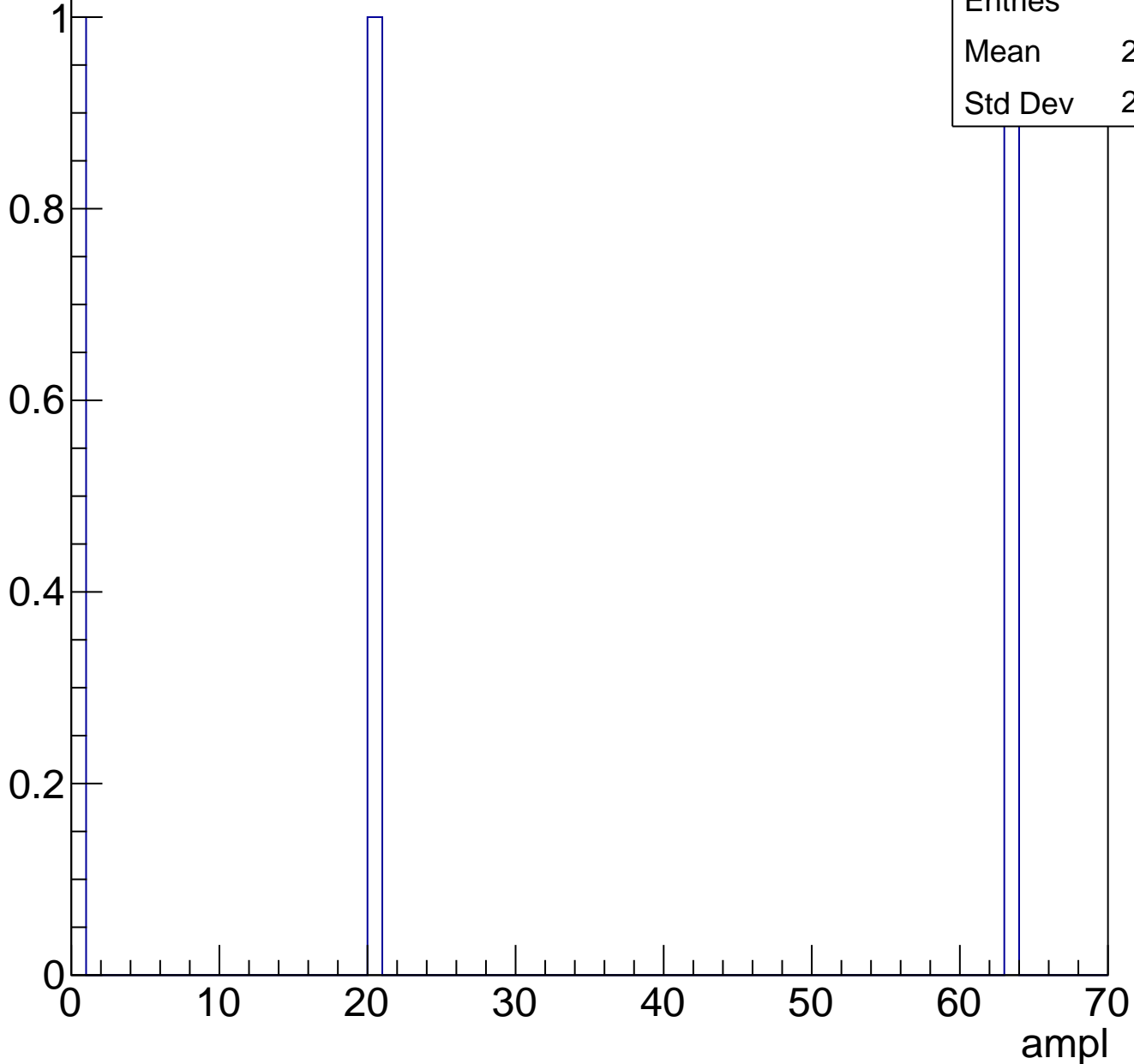
Entry



B0L101S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B0L101S, U14-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	30.74
Std Dev	4.534

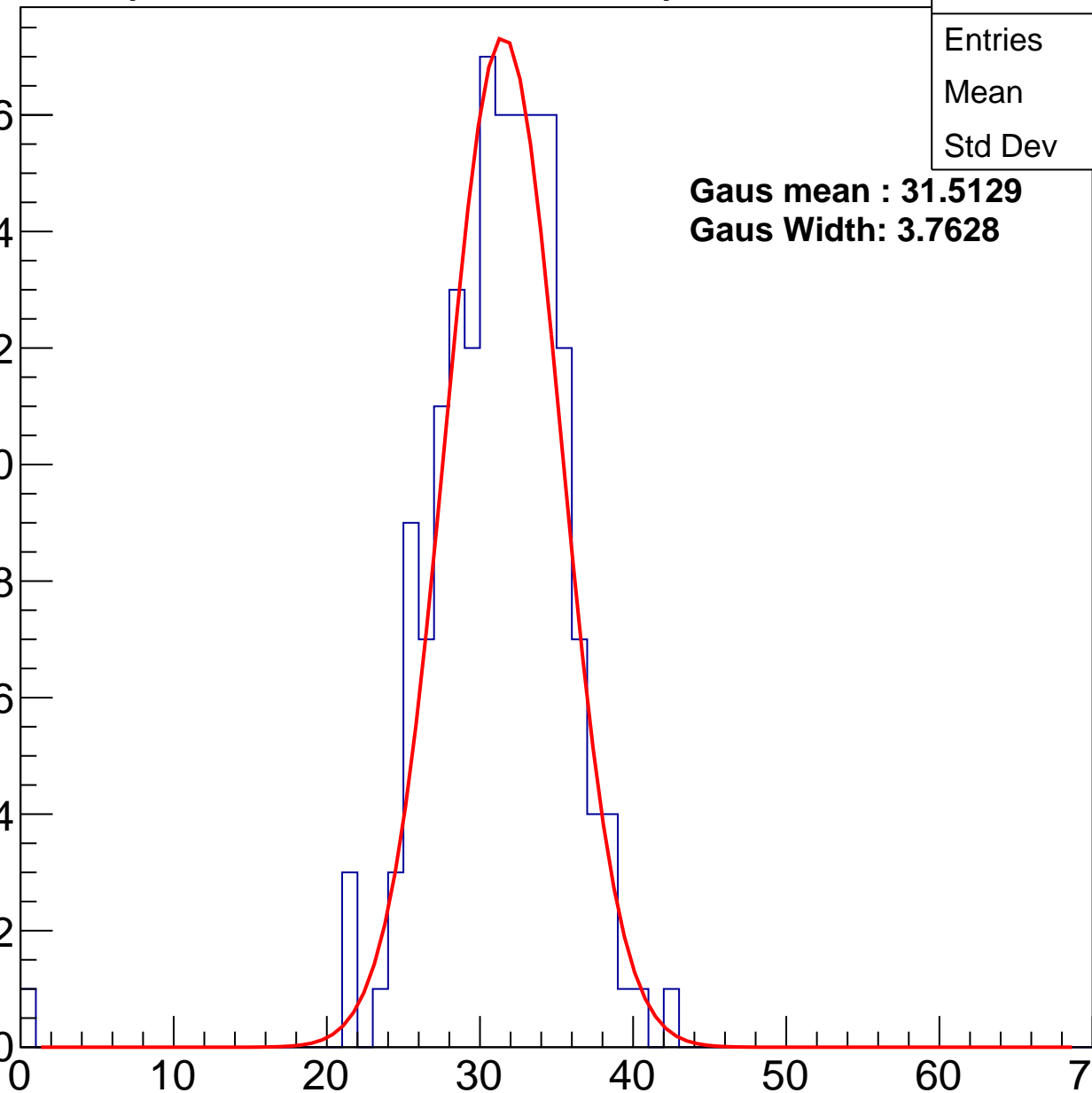
Gaus mean : 31.5129

Gaus Width: 3.7628

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch36, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

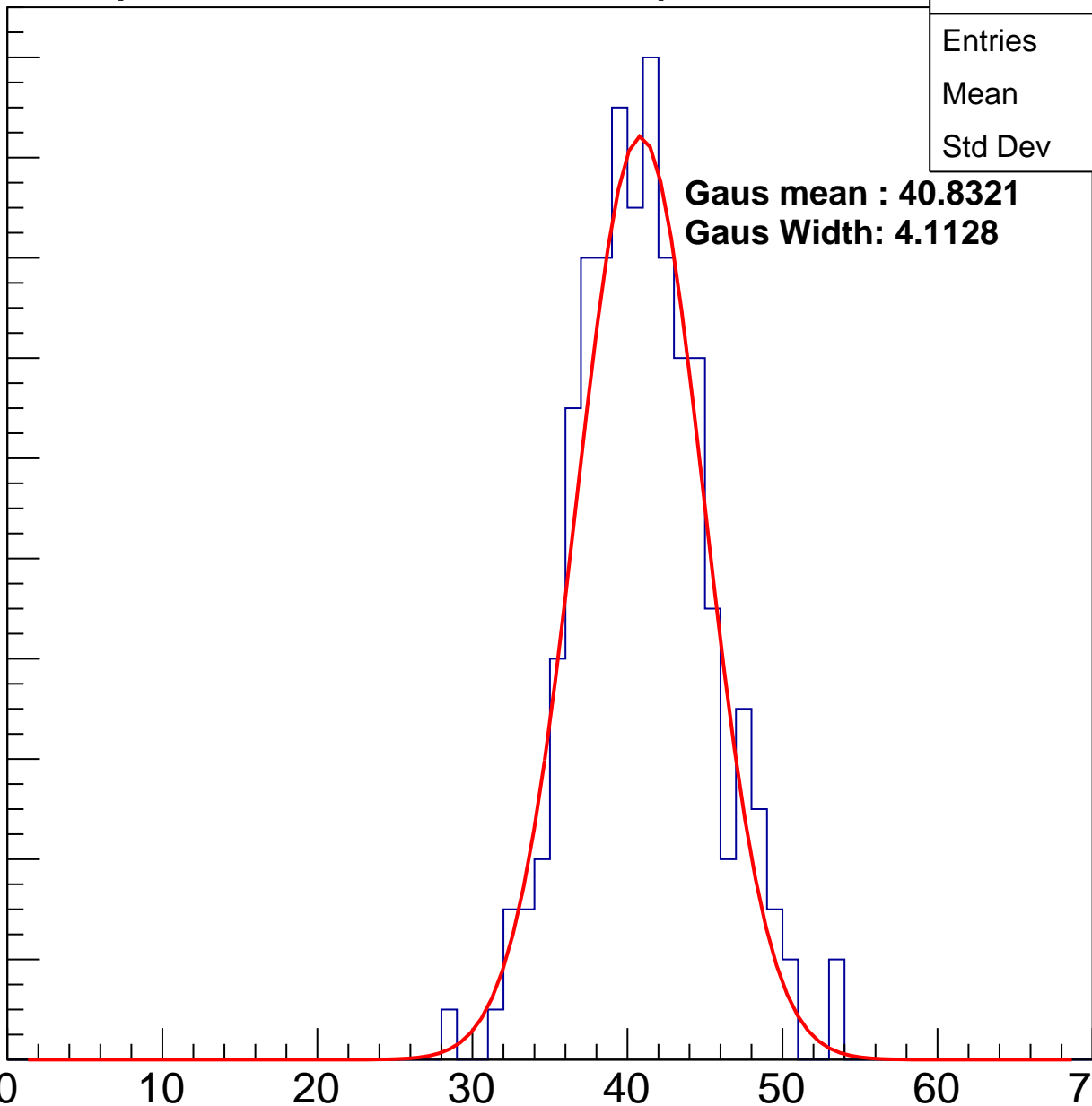
20
18
16
14
12
10
8
6
4
2
0

Entries	197
Mean	40.51
Std Dev	4.239

Gaus mean : 40.8321

Gaus Width: 4.1128

0 10 20 30 40 50 60 70
ampl



B0L101S, U14-ch36, adc2

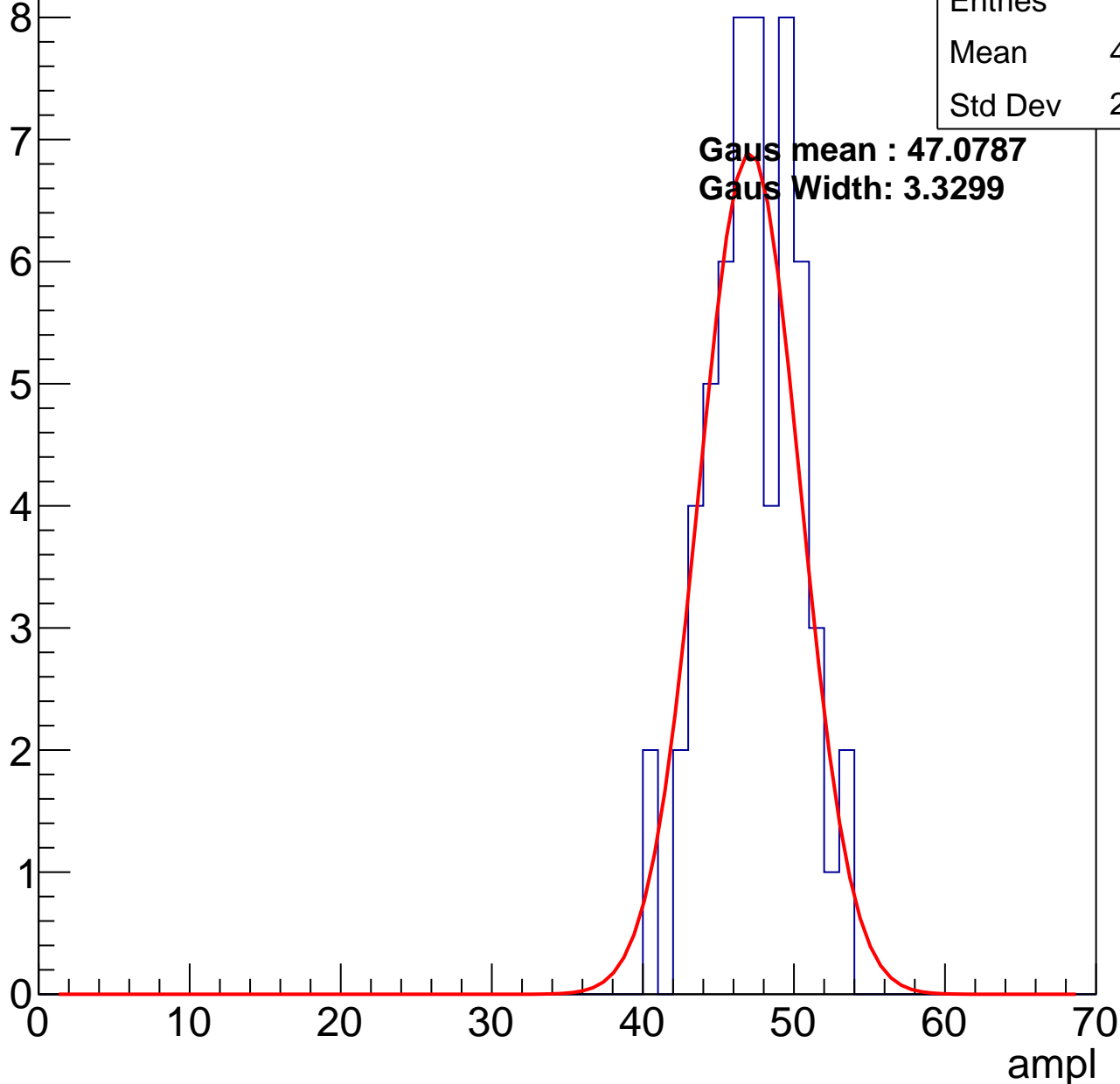
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	59
Mean	46.86
Std Dev	2.977

Gaus mean : 47.0787

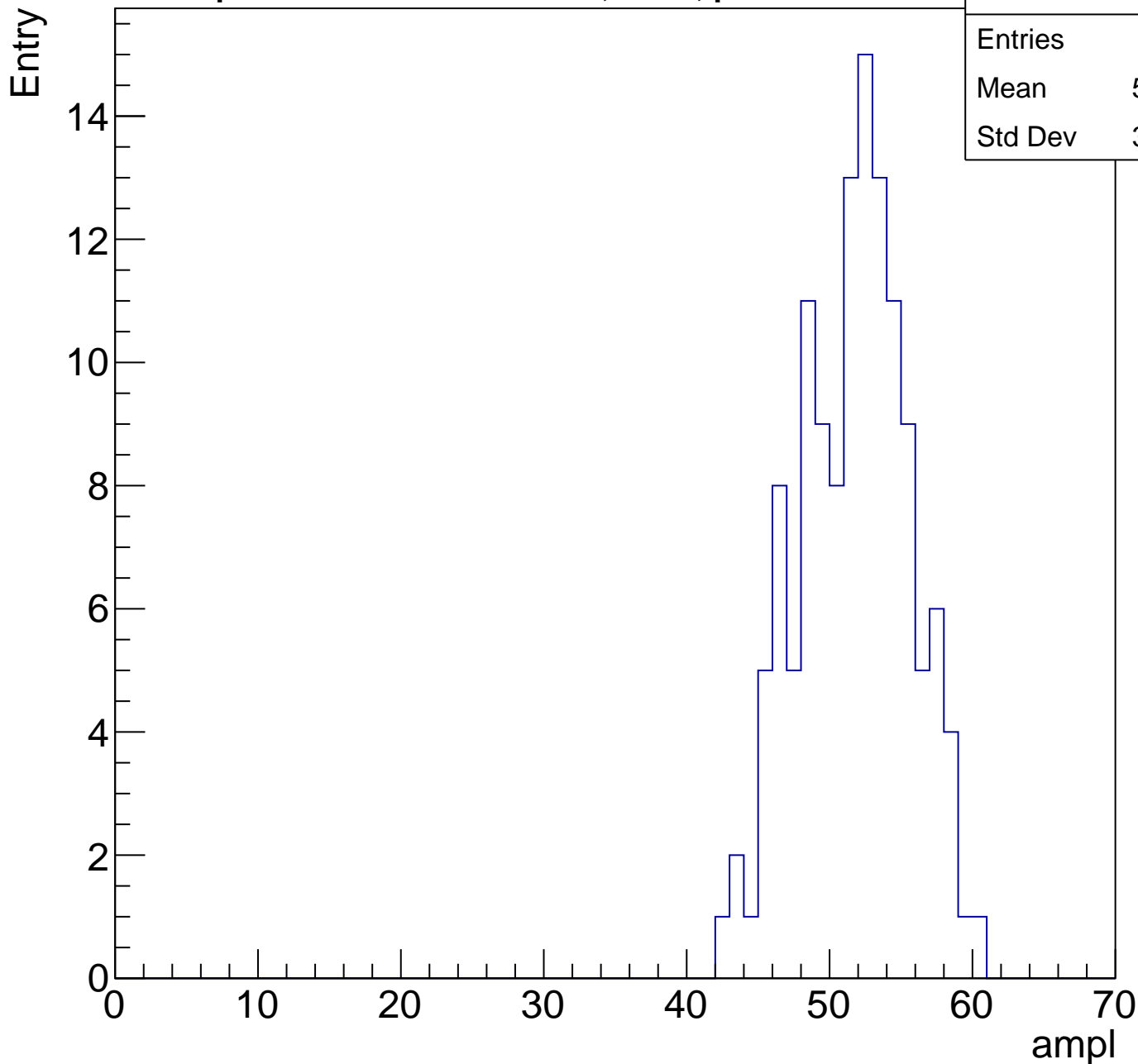
Gaus Width: 3.3299



B0L101S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	51.27
Std Dev	3.795



B0L101S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	79
Mean	56.04
Std Dev	3.16

Entry

10

8

6

4

2

0

0

10

20

30

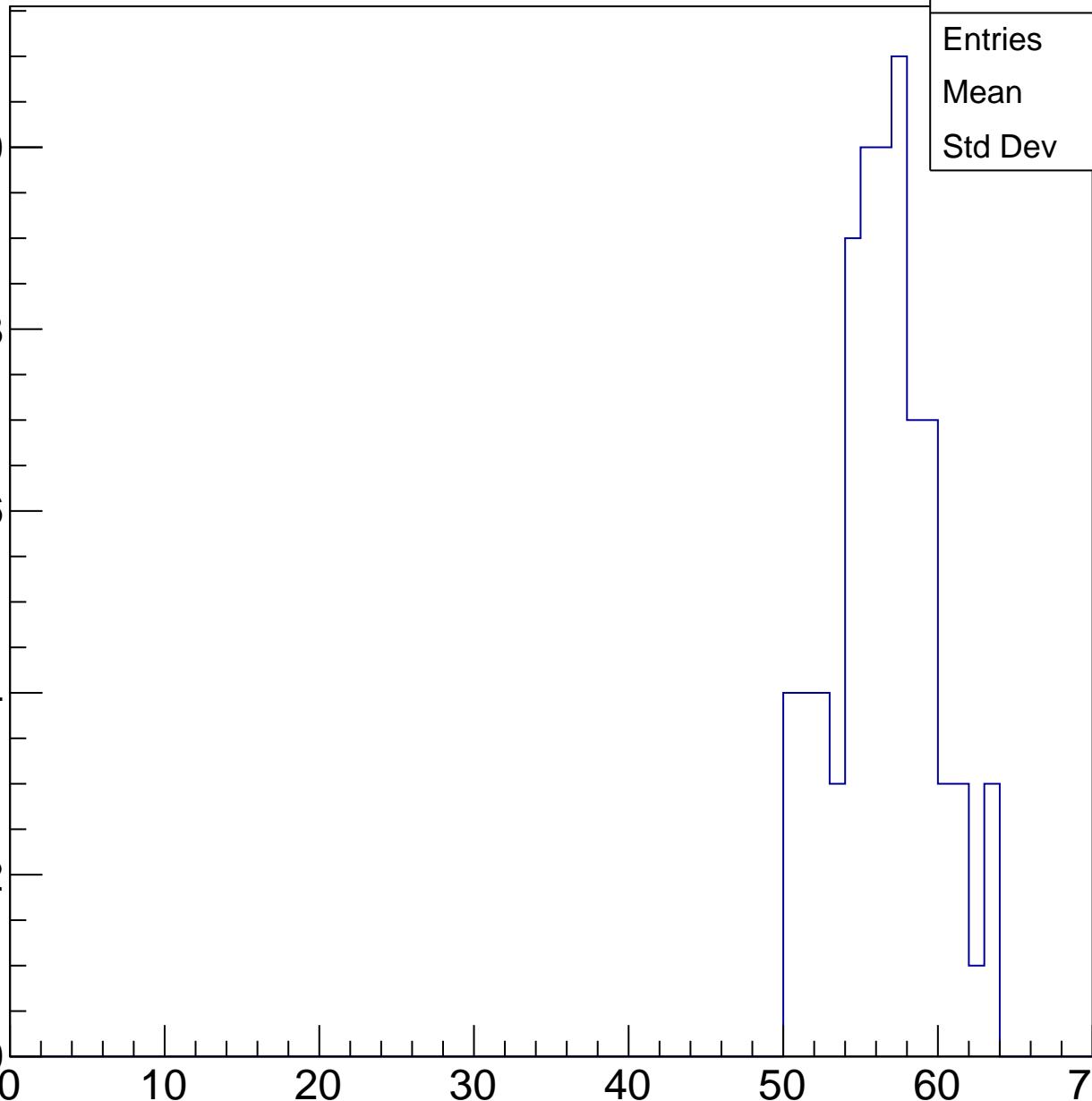
40

50

ampl

60

70



B0L101S, U14-ch36, adc5

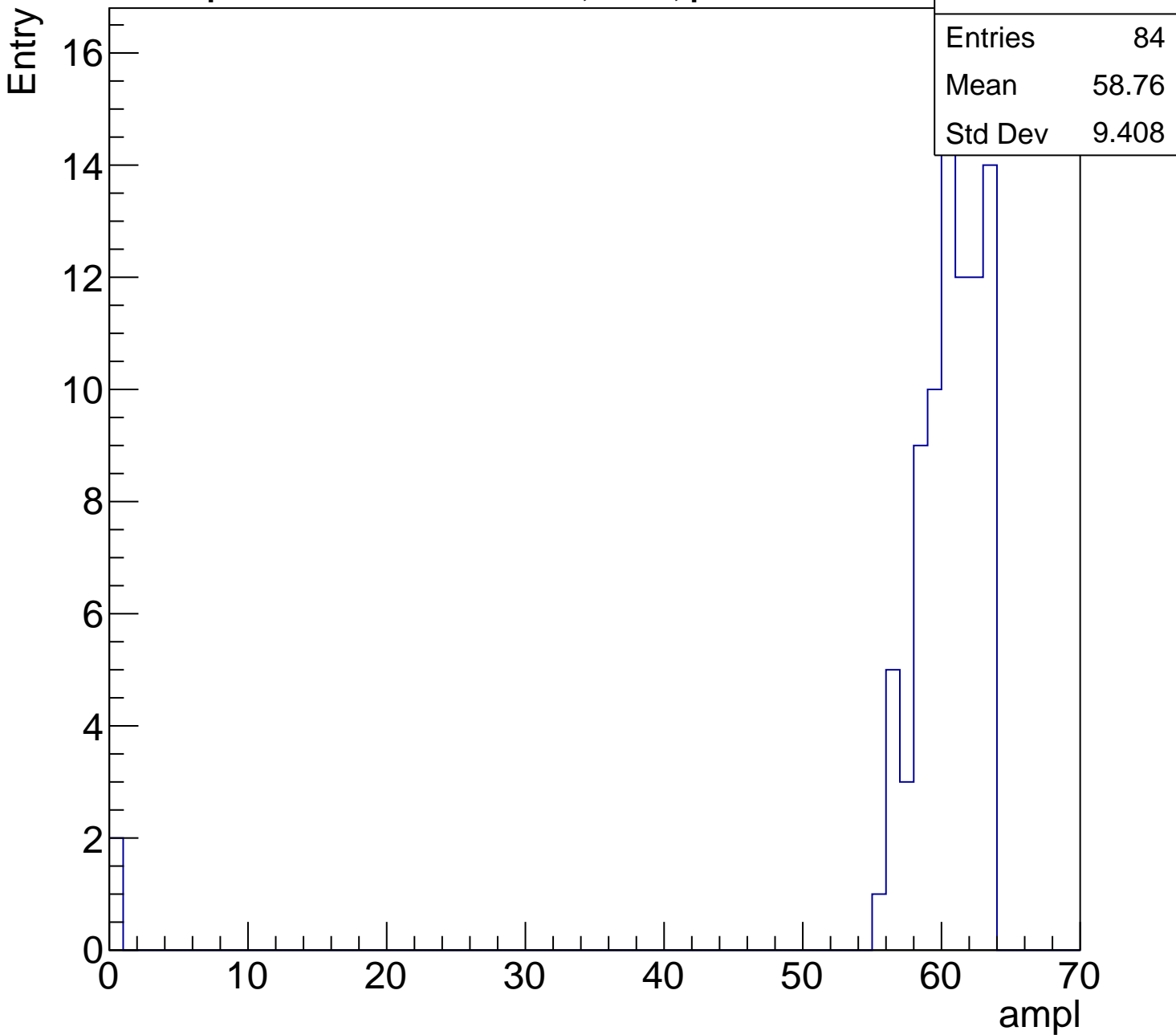
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.76
Std Dev	9.408

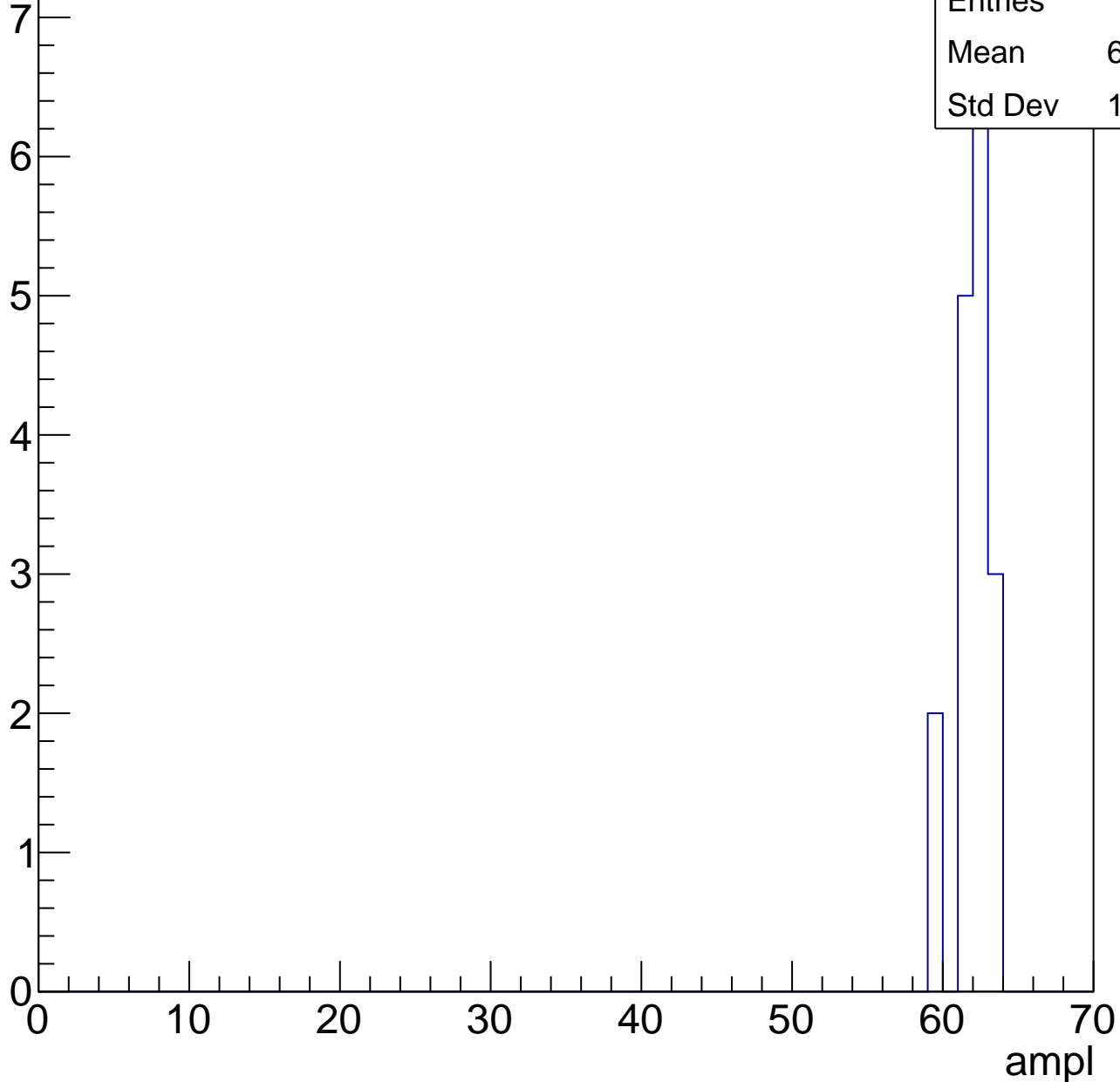
ampl



B0L101S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

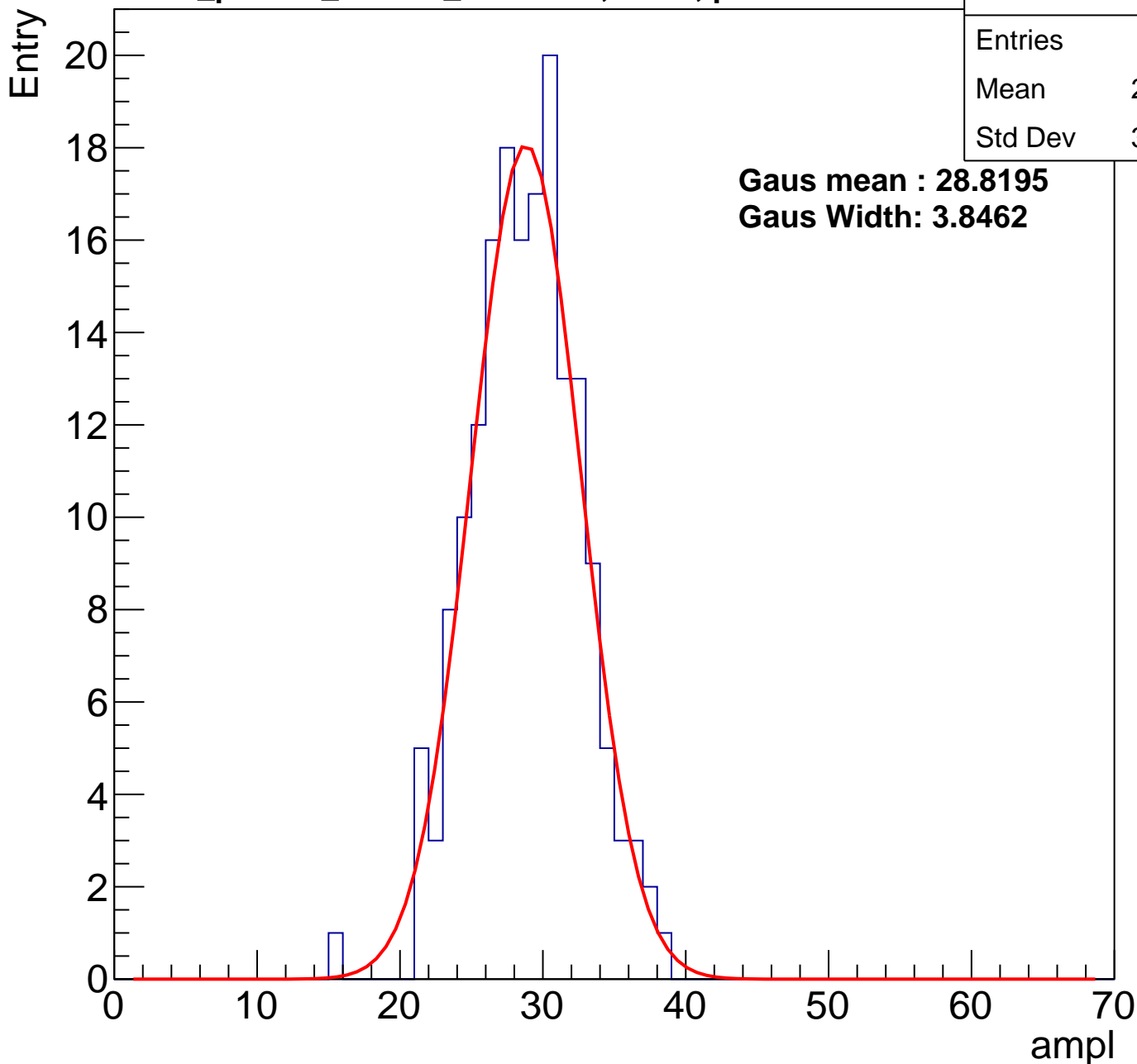
B0L101S, U14-ch37, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	28.37
Std Dev	3.762

Gaus mean : 28.8195

Gaus Width: 3.8462



B0L101S, U14-ch37, adc1

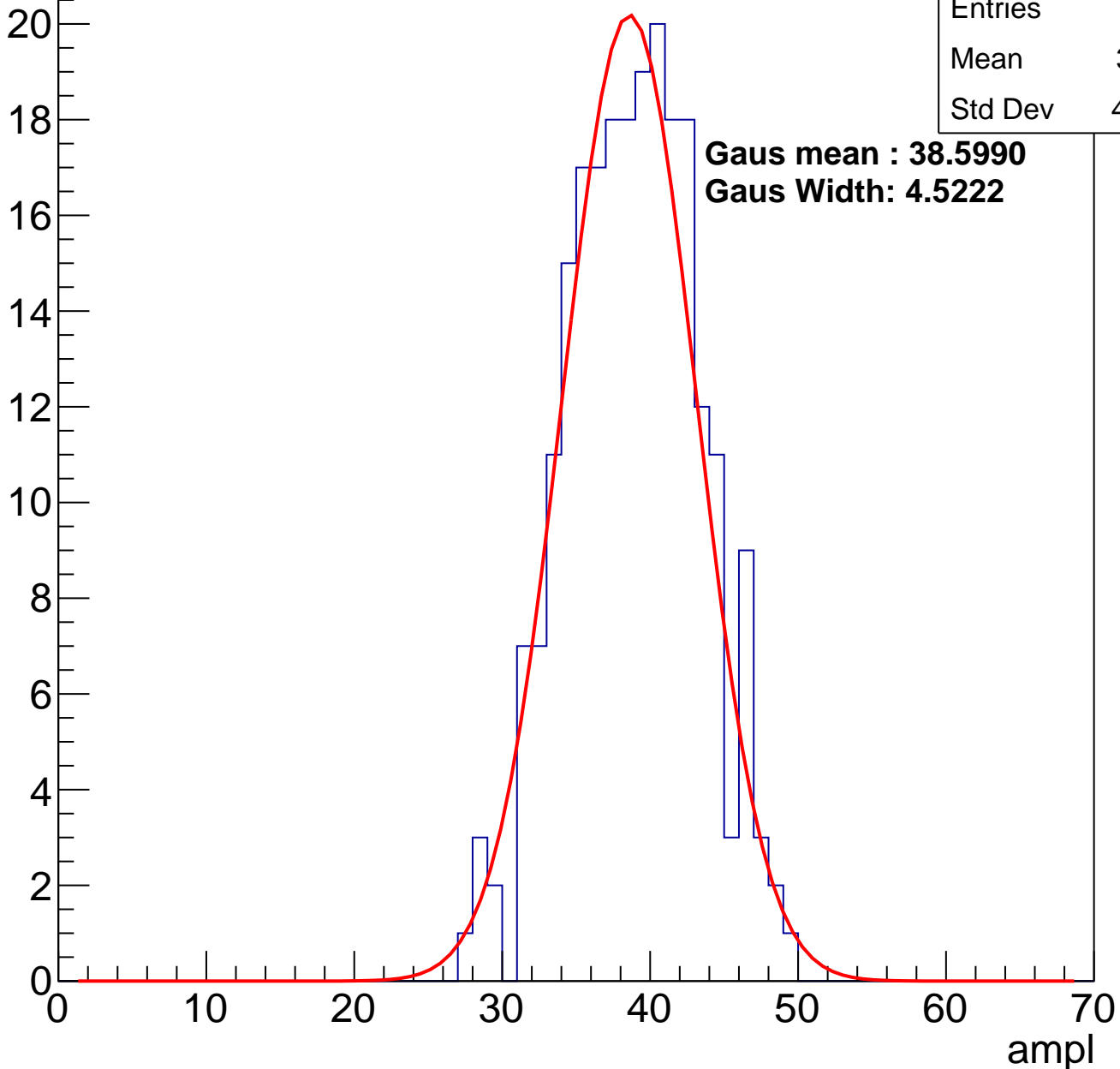
calib_packv5_042523_0143.root, FC#1, port C1

Entries	232
Mean	38.41
Std Dev	4.368

Gaus mean : 38.5990

Gaus Width: 4.5222

Entry



B0L101S, U14-ch37, adc2

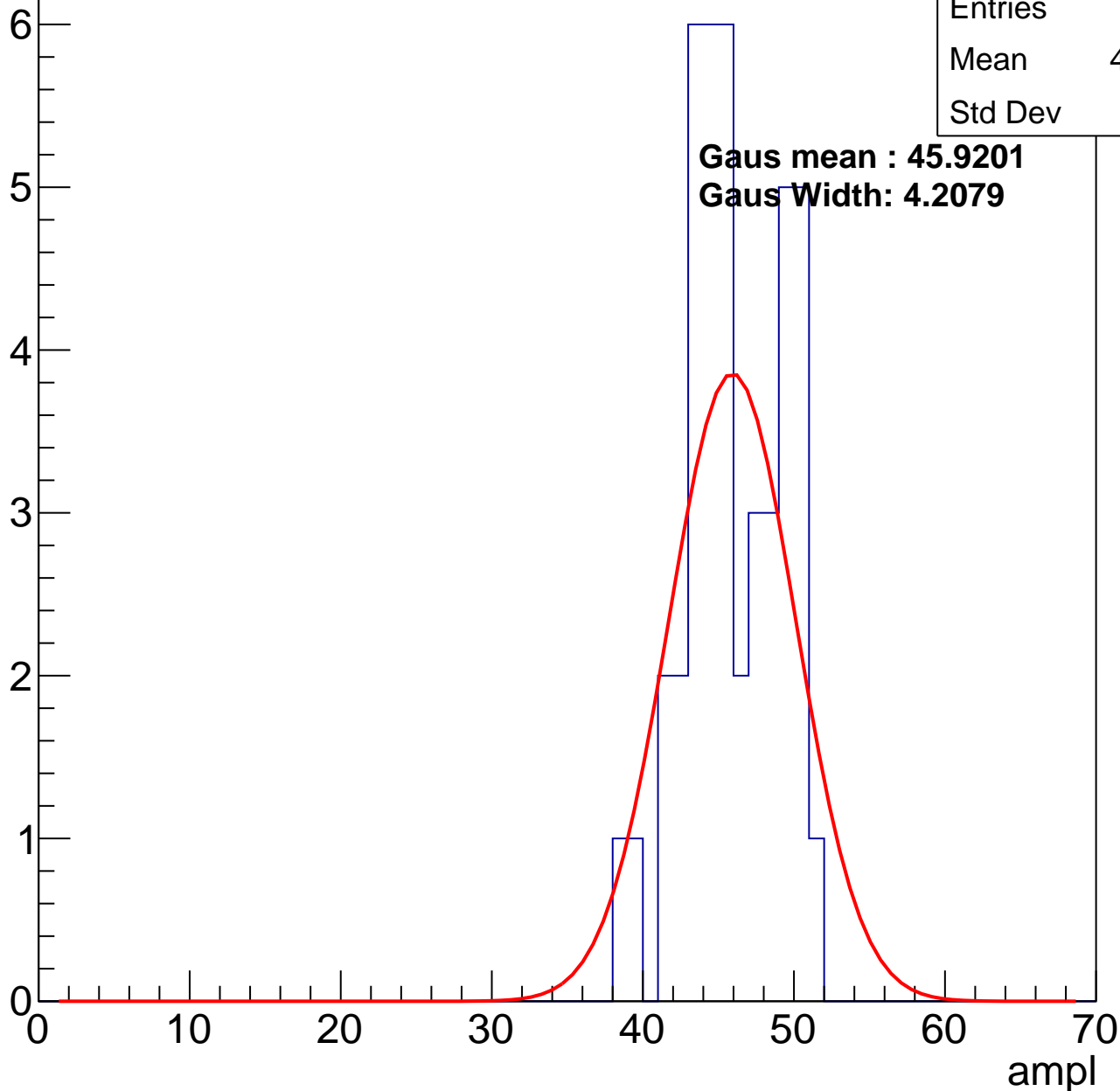
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	43
Mean	45.53
Std Dev	3.18

Gaus mean : 45.9201

Gaus Width: 4.2079



B0L101S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

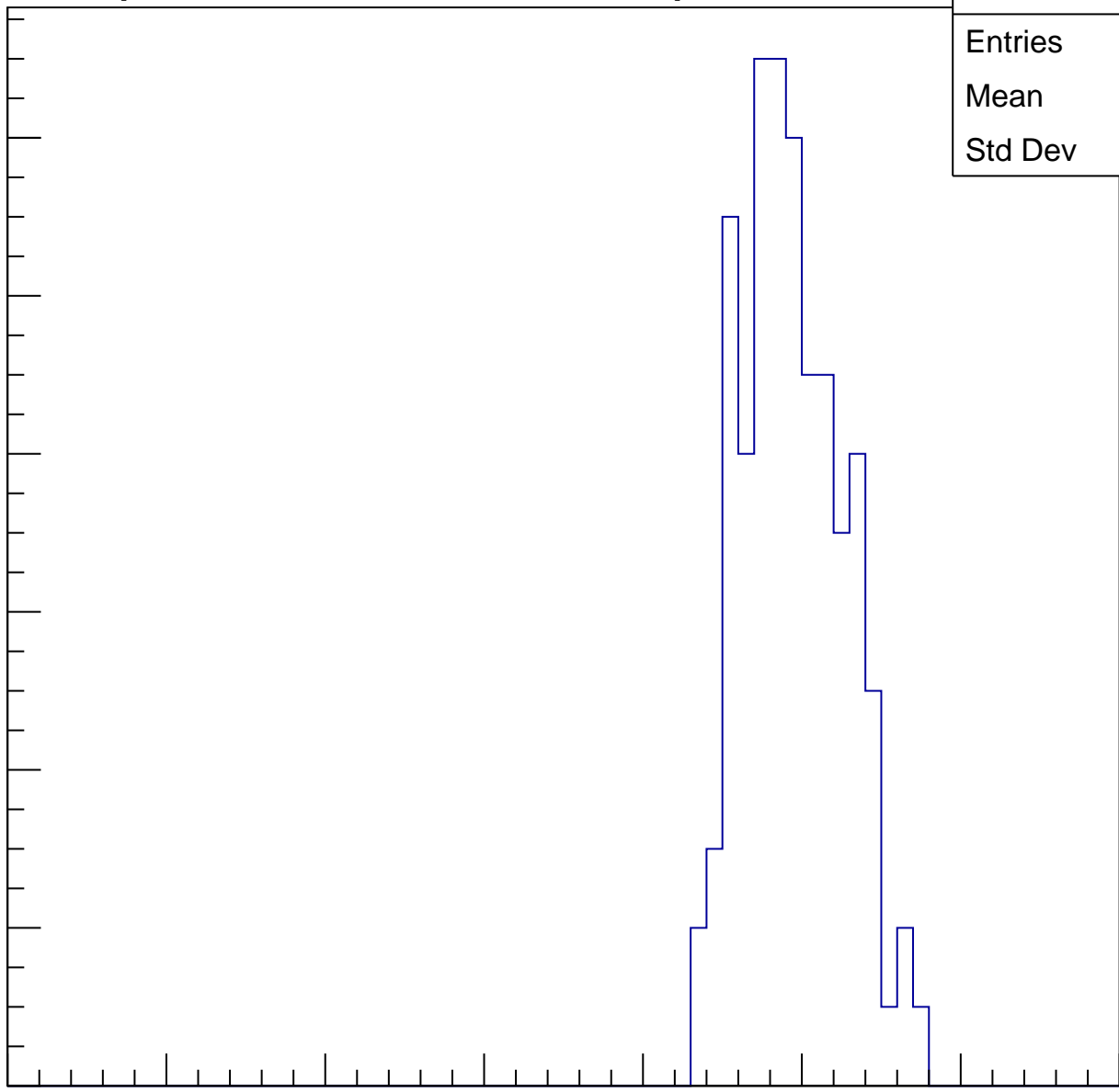
2

0

0 10 20 30 40 50 60 70

ampl

Entries	104
Mean	48.99
Std Dev	3.13



B0L101S, U14-ch37, adc4

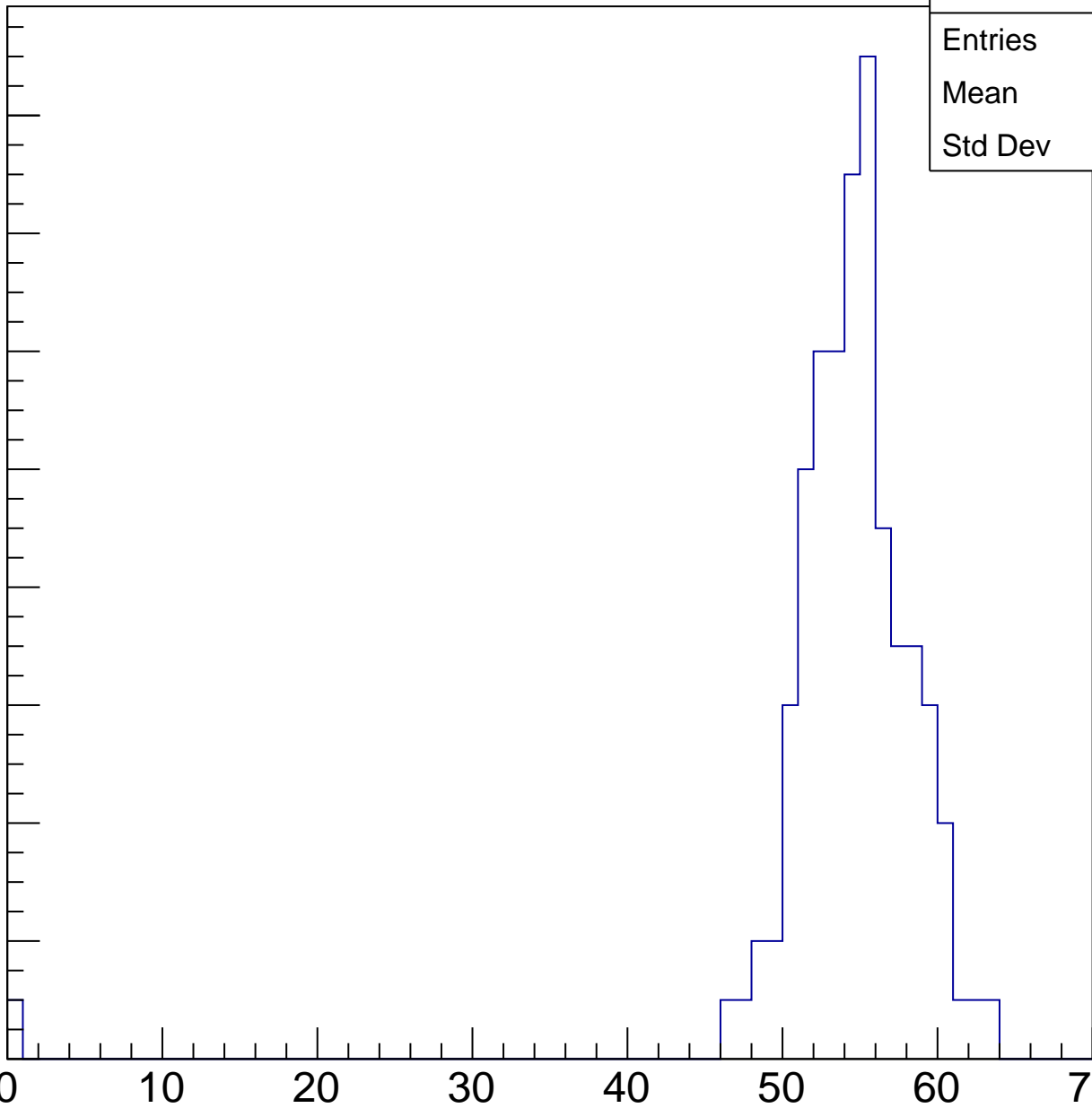
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	53.83
Std Dev	5.978

ampl



B0L101S, U14-ch37, adc5

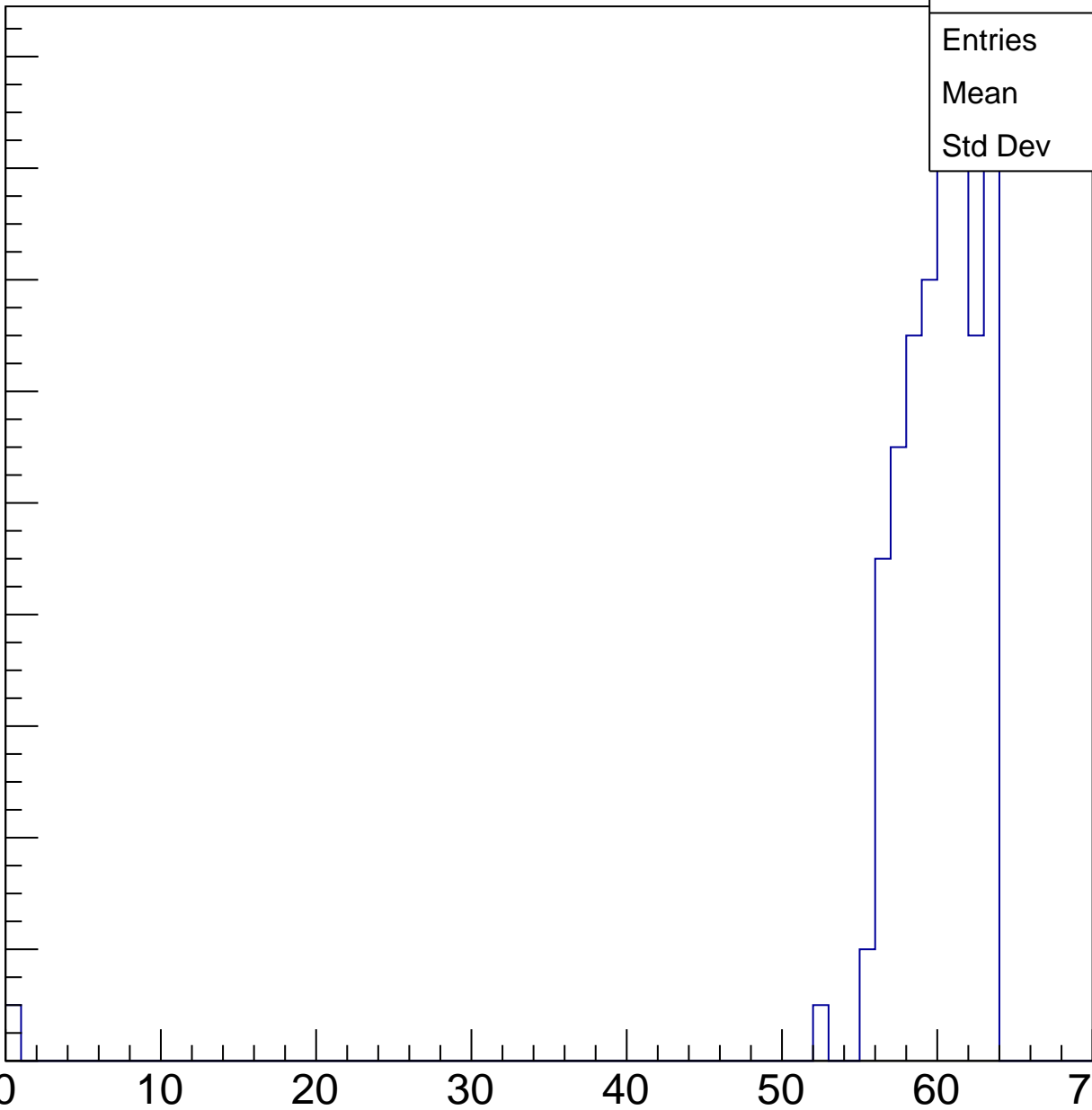
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	115
Mean	59.2
Std Dev	6.023

ampl

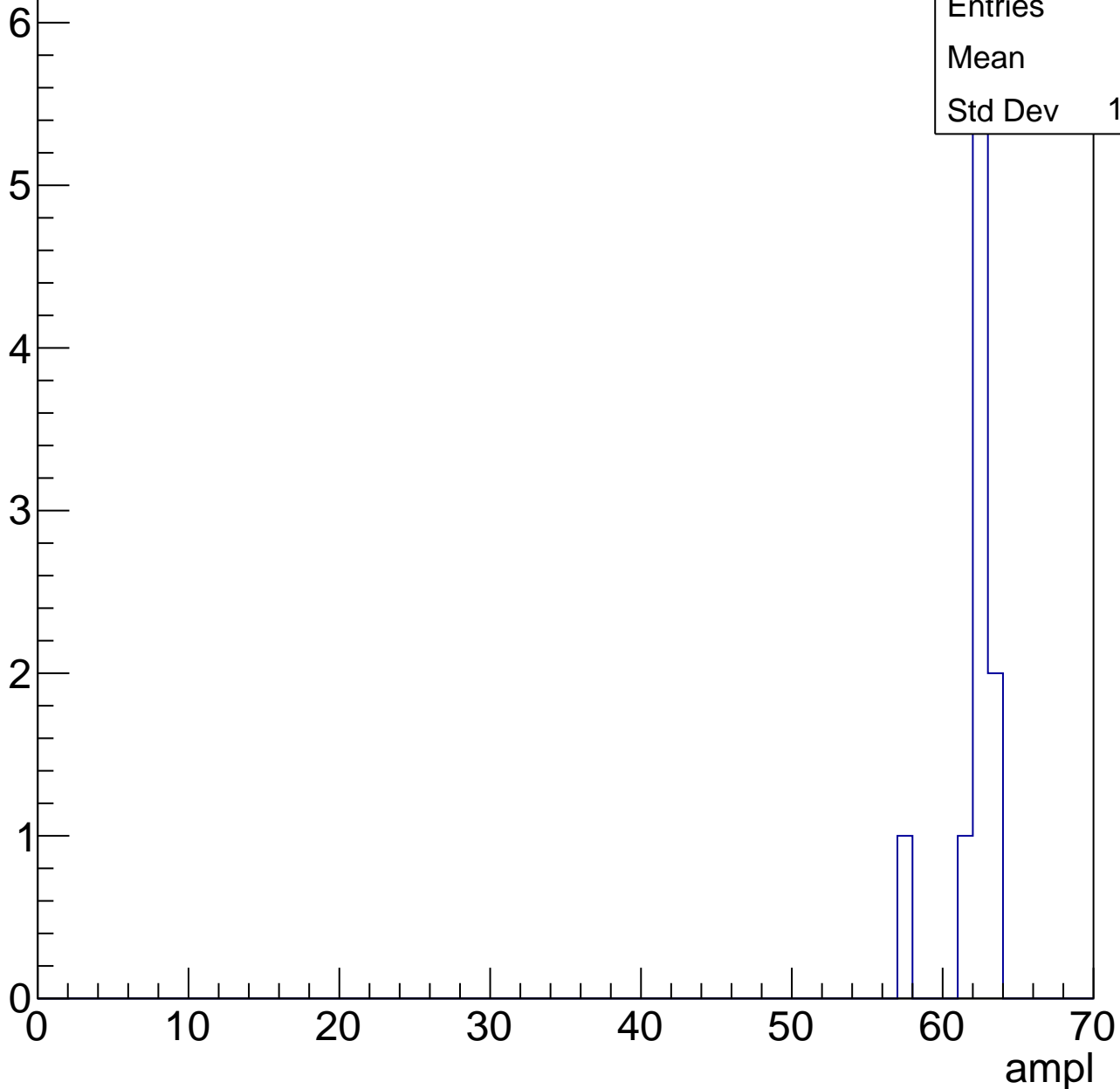


B0L101S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	61.6
Std Dev	1.625



B0L101S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch38, adc0

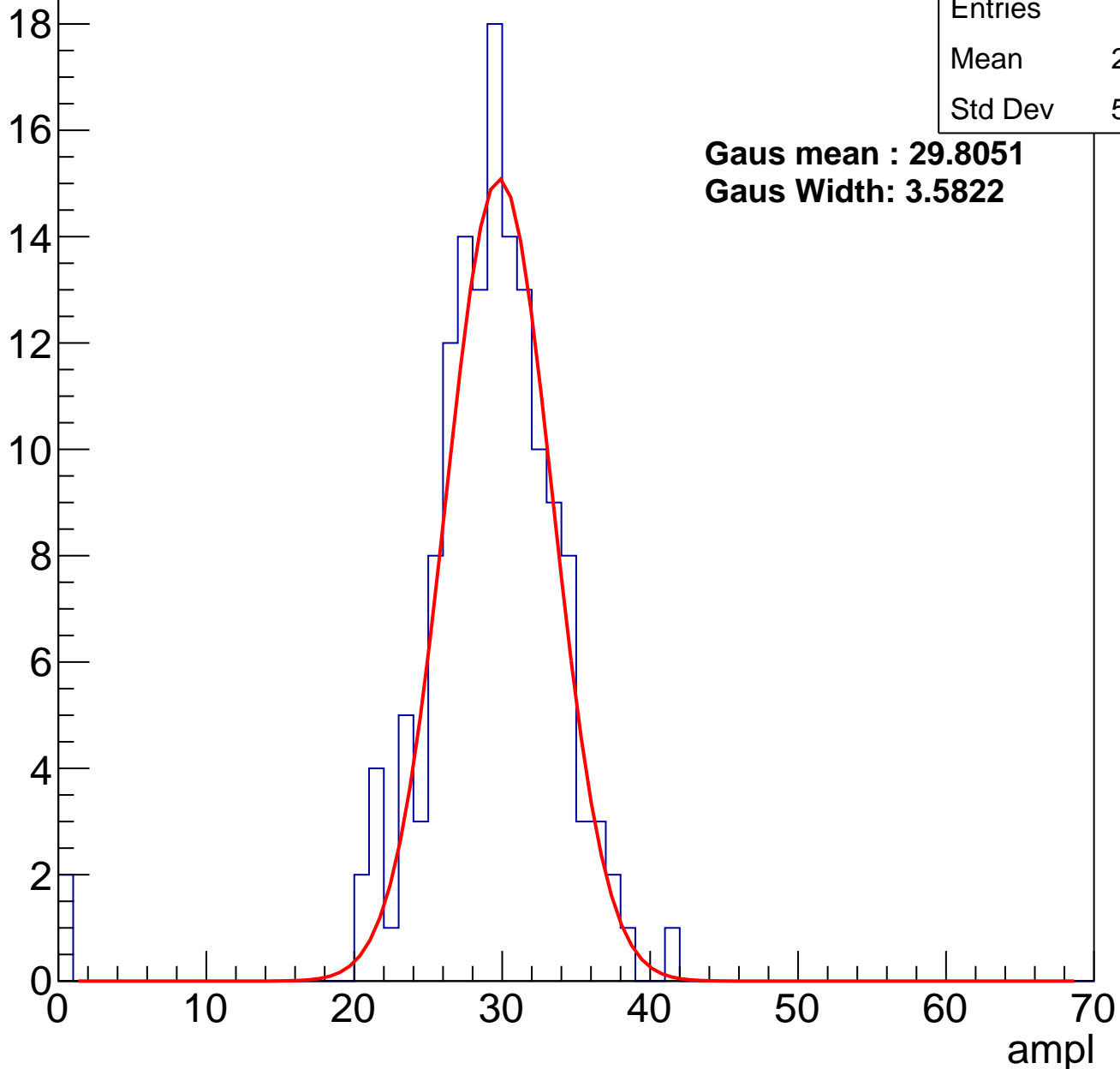
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	28.68
Std Dev	5.103

Gaus mean : 29.8051

Gaus Width: 3.5822

Entry



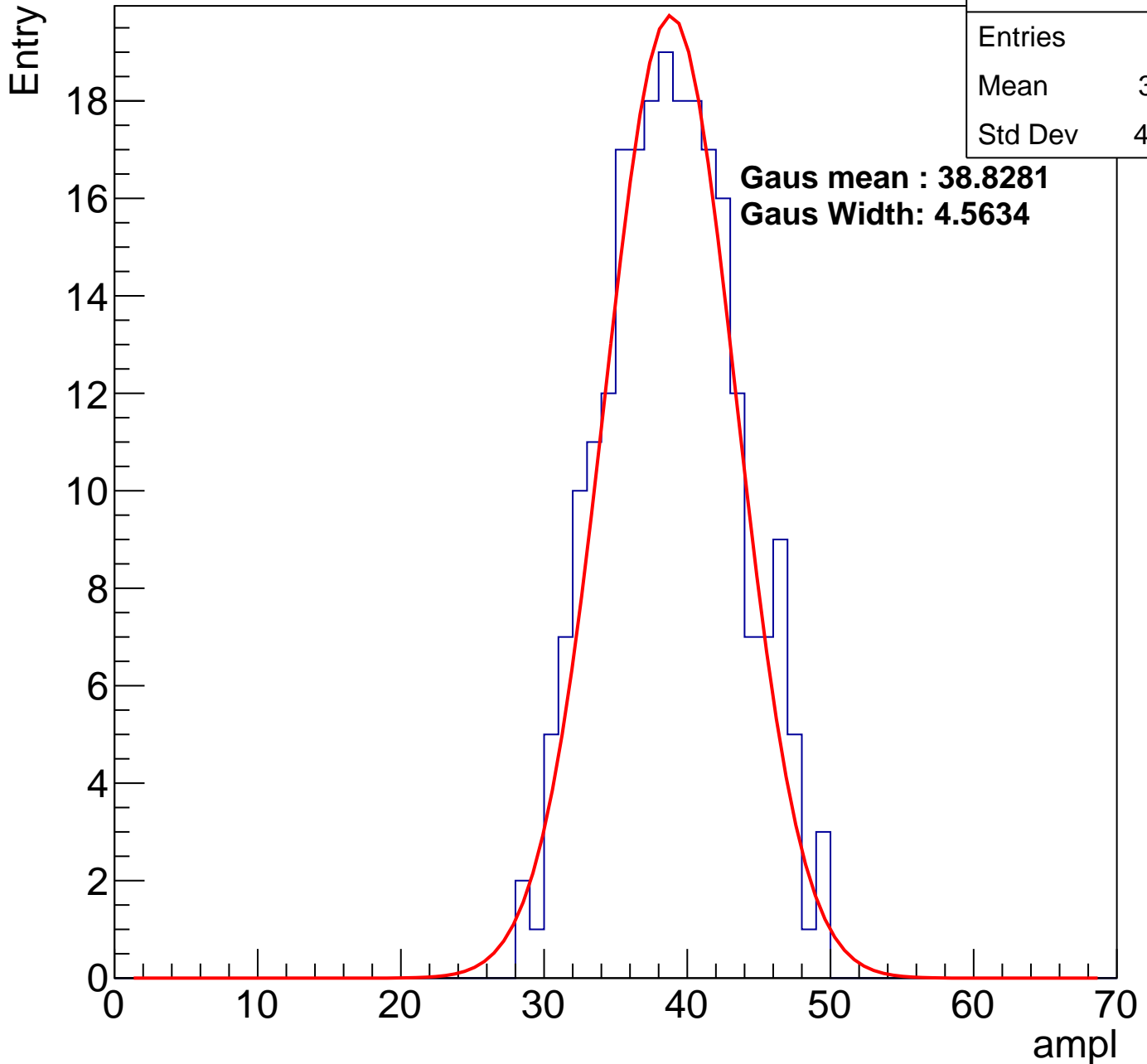
B0L101S, U14-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	232
Mean	38.41
Std Dev	4.553

Gaus mean : 38.8281

Gaus Width: 4.5634



B0L101S, U14-ch38, adc2

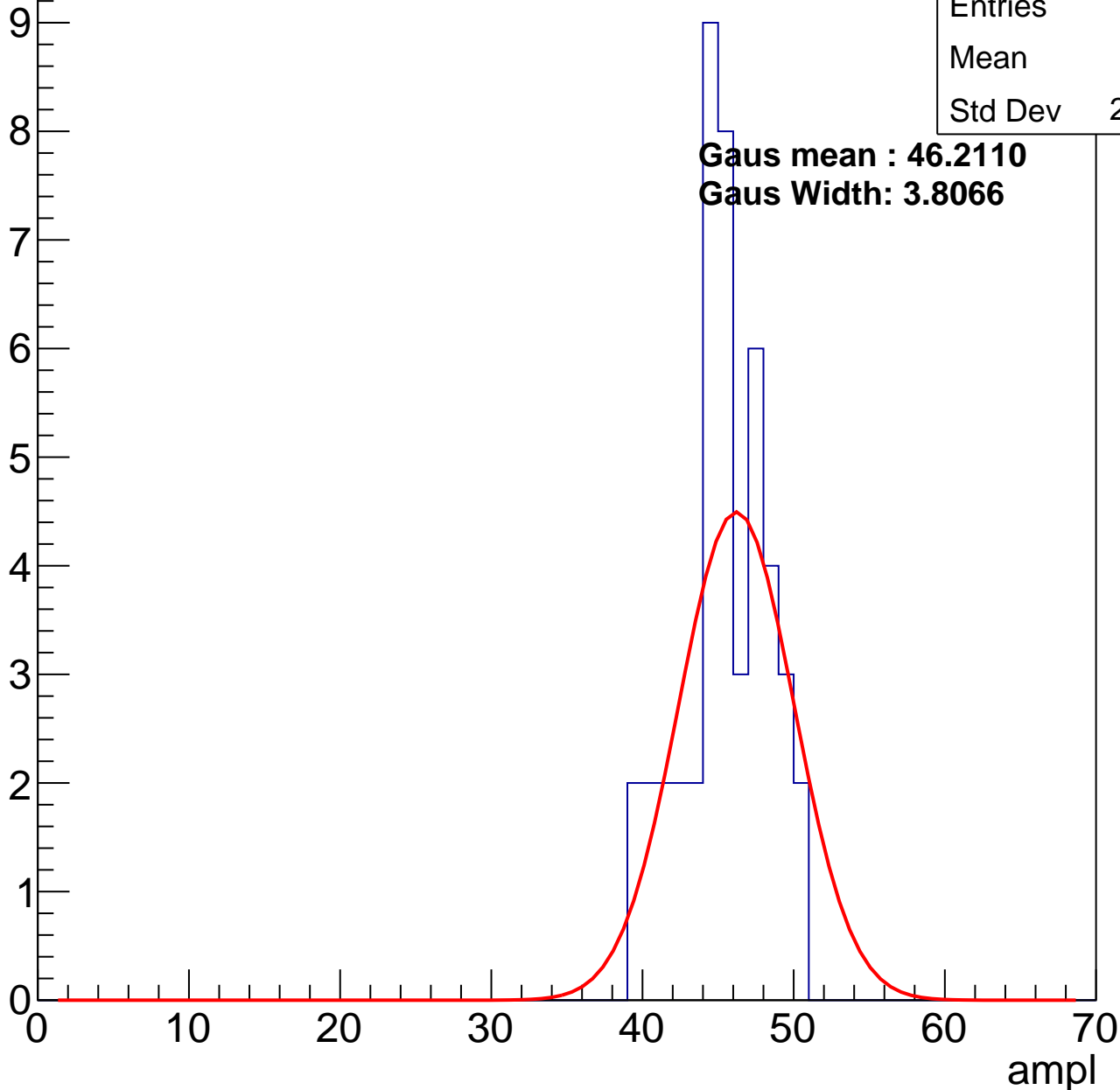
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	45
Mean	45
Std Dev	2.789

Gaus mean : 46.2110

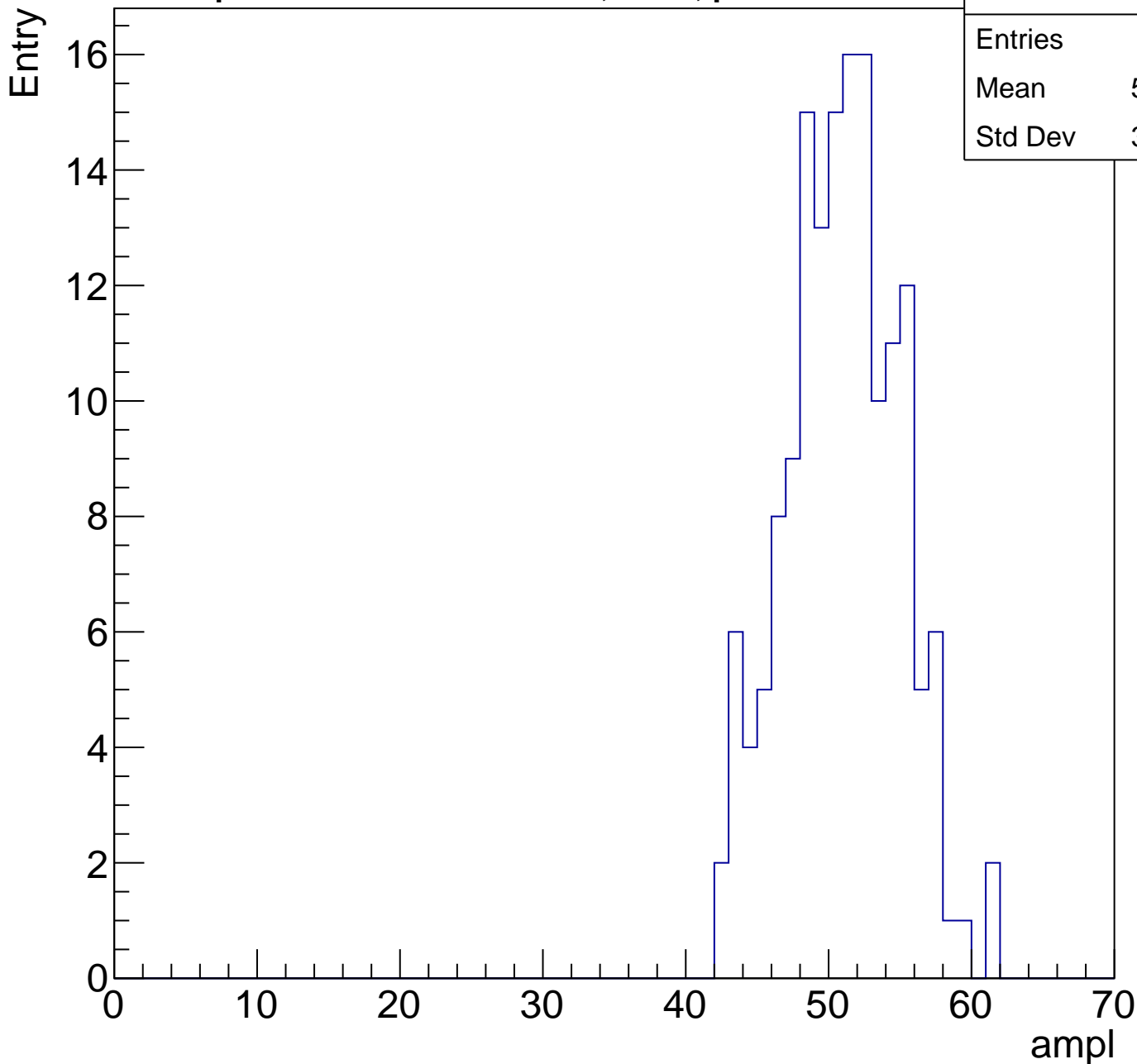
Gaus Width: 3.8066



B0L101S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	50.54
Std Dev	3.938



B0L101S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	73
Mean	56.11
Std Dev	3.259

Entry

12

10

8

6

4

2

0

0

10

20

30

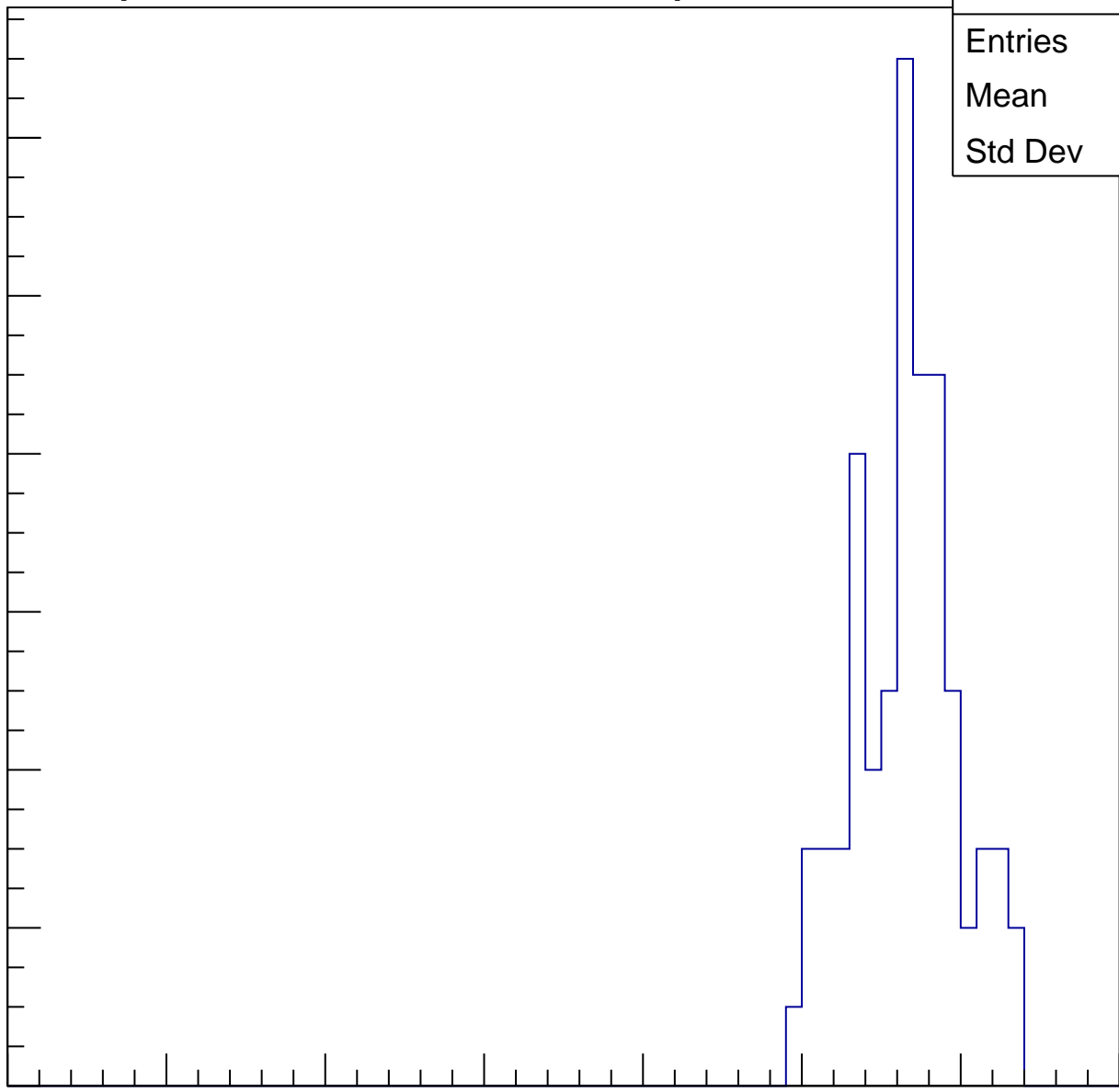
40

50

60

70

ampl



B0L101S, U14-ch38, adc5

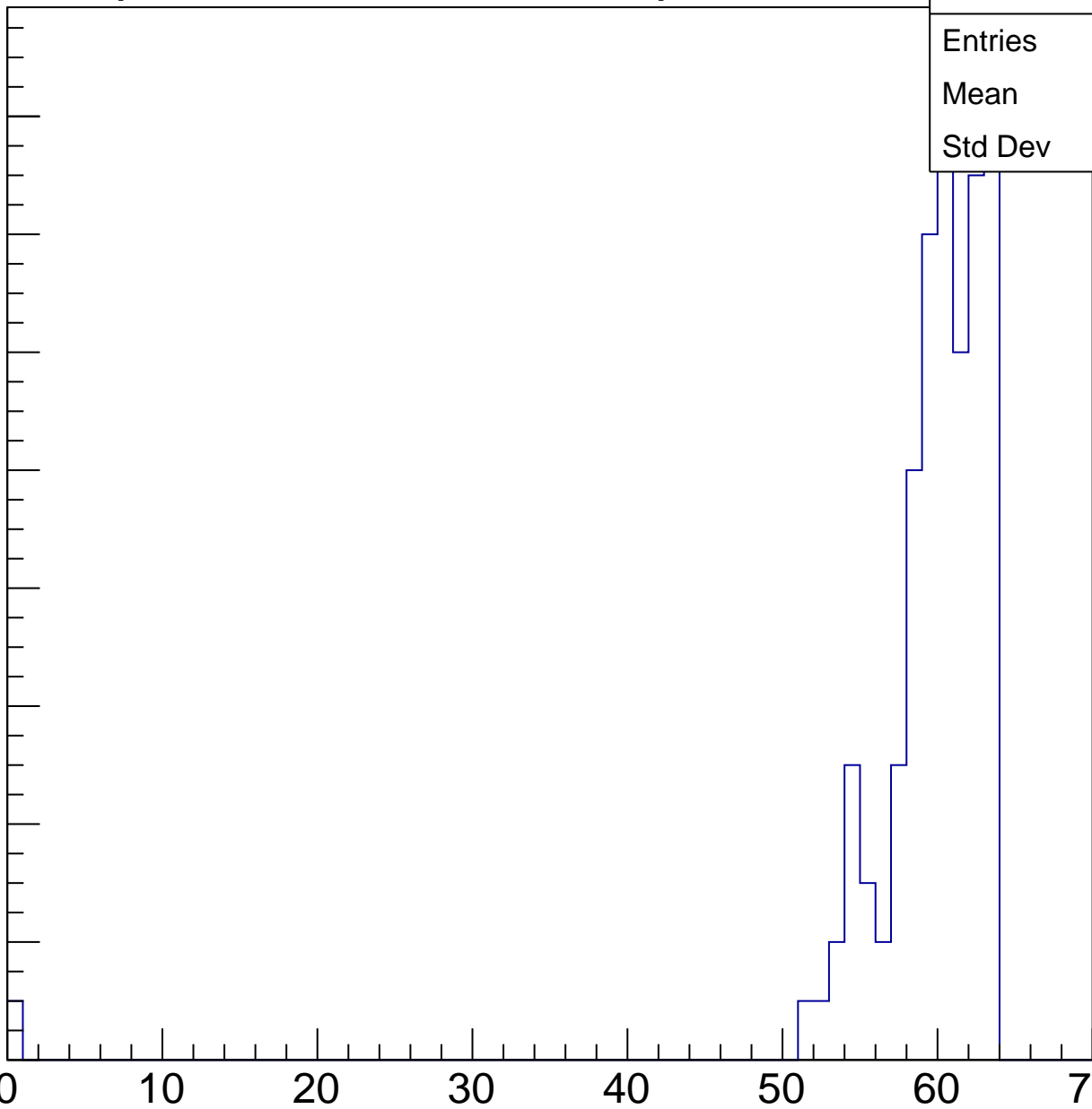
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	59.01
Std Dev	6.462

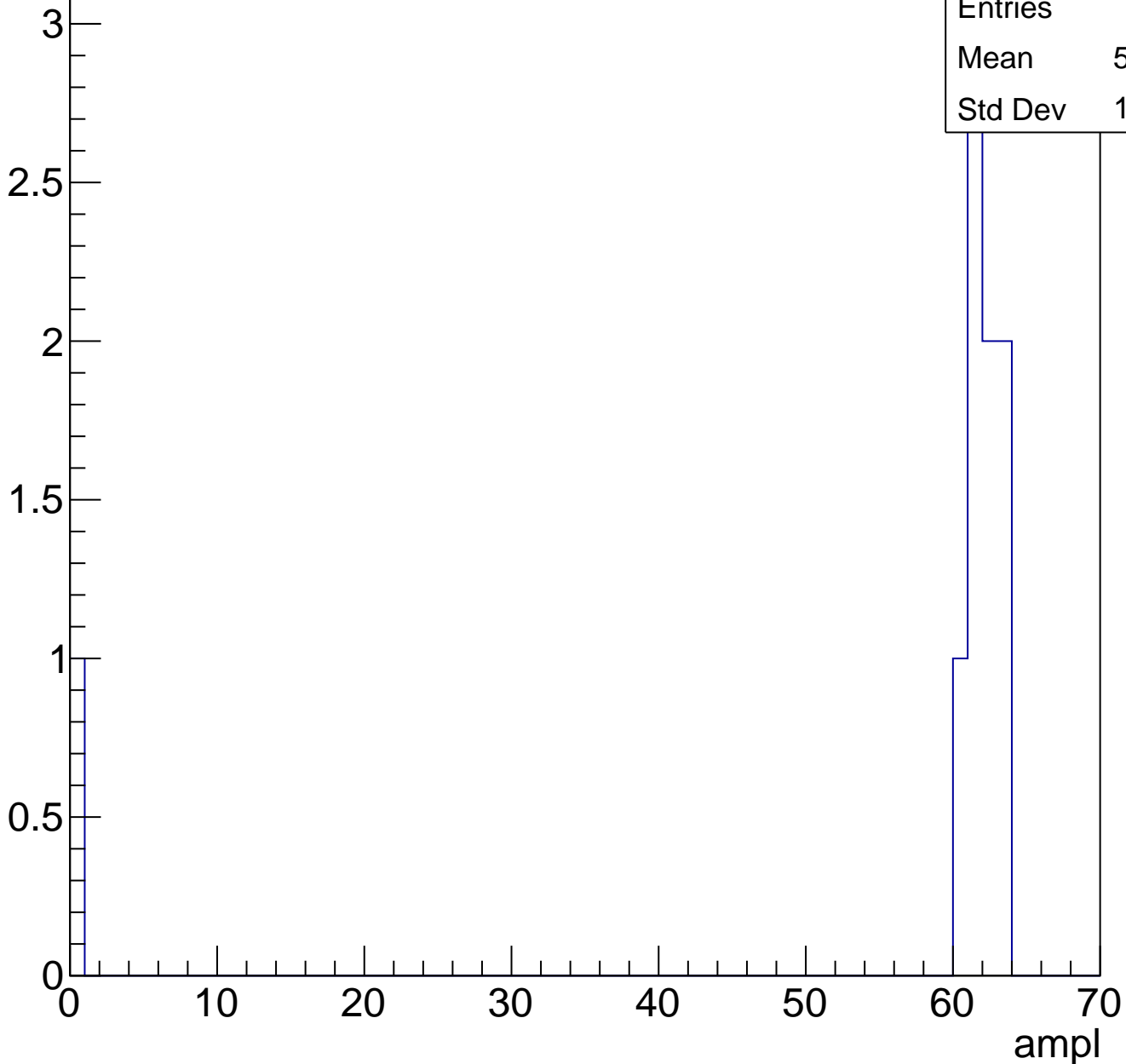
ampl



B0L101S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch39, adc0

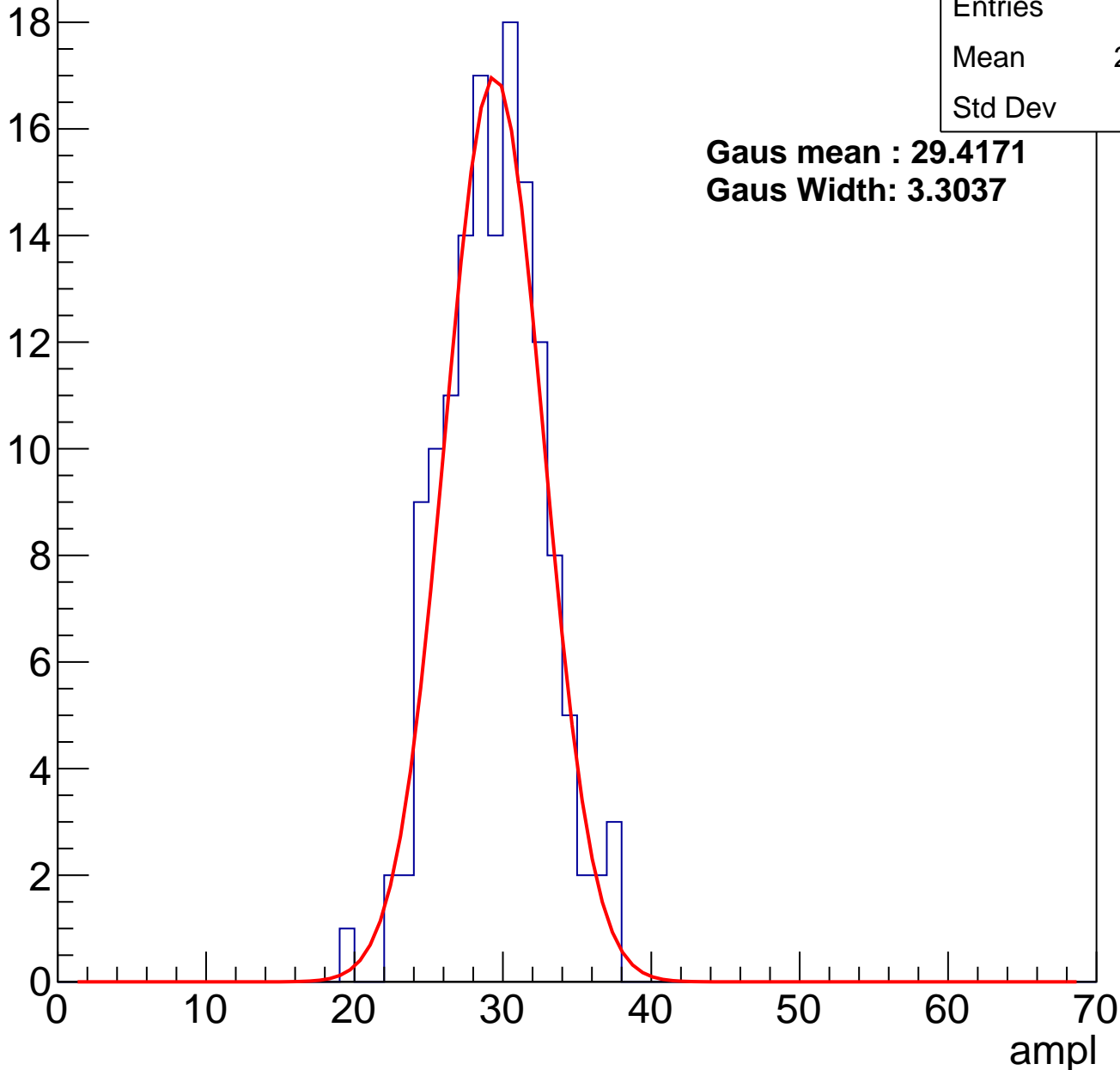
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.94
Std Dev	3.35

Gaus mean : 29.4171

Gaus Width: 3.3037

Entry



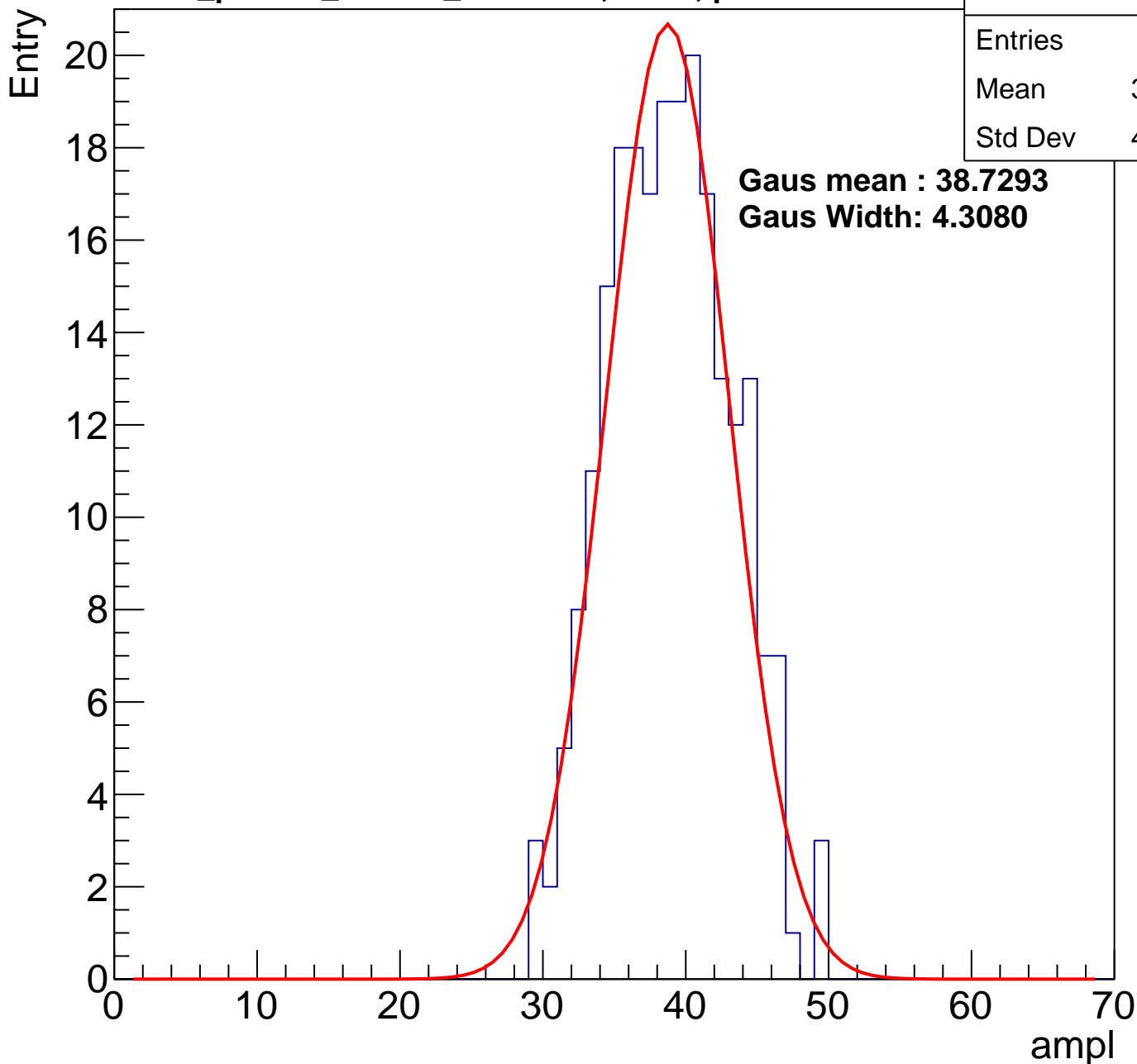
B0L101S, U14-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	228
Mean	38.43
Std Dev	4.235

Gaus mean : 38.7293

Gaus Width: 4.3080



B0L101S, U14-ch39, adc2

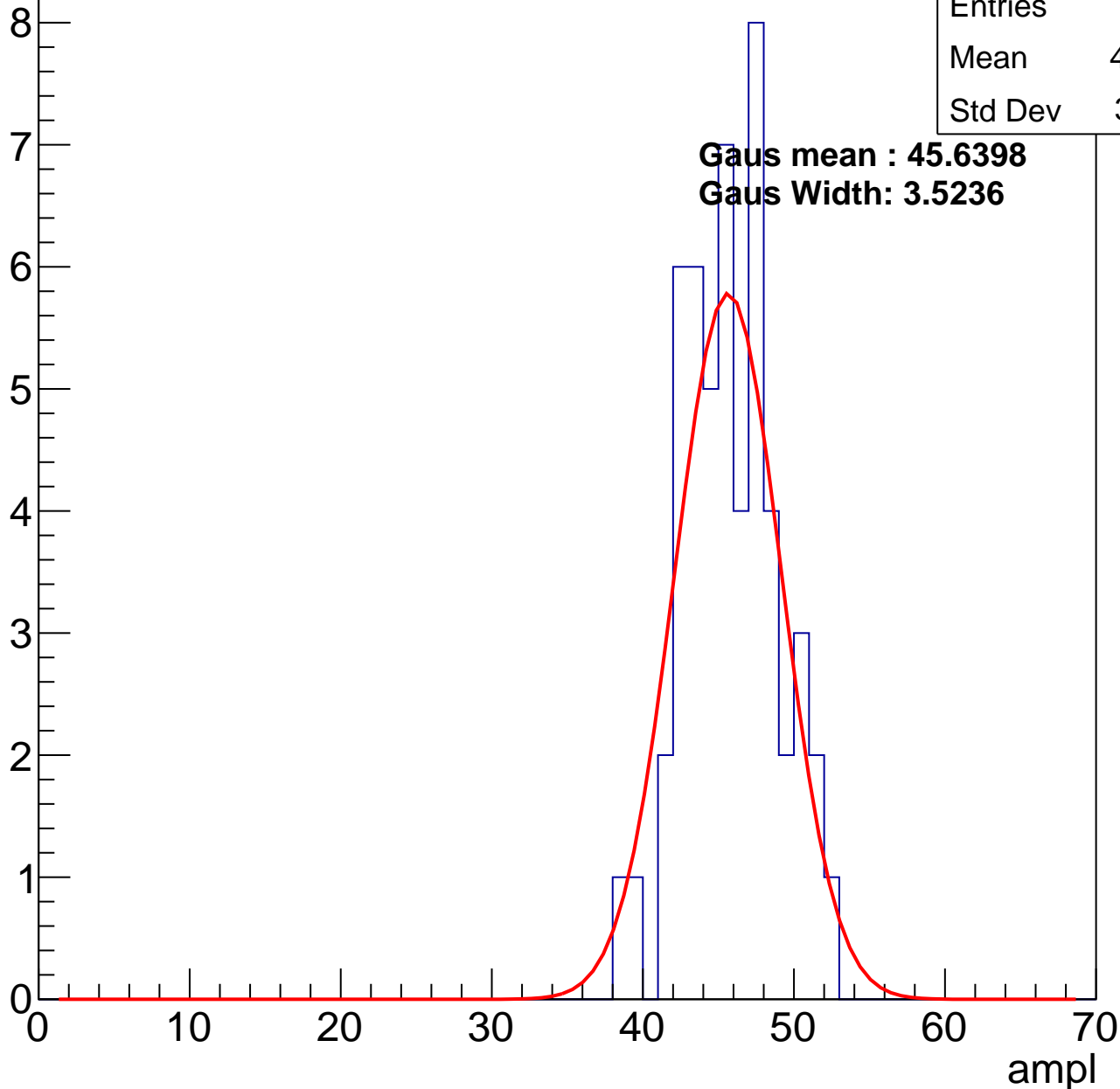
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	52
Mean	45.35
Std Dev	3.081

Gaus mean : 45.6398

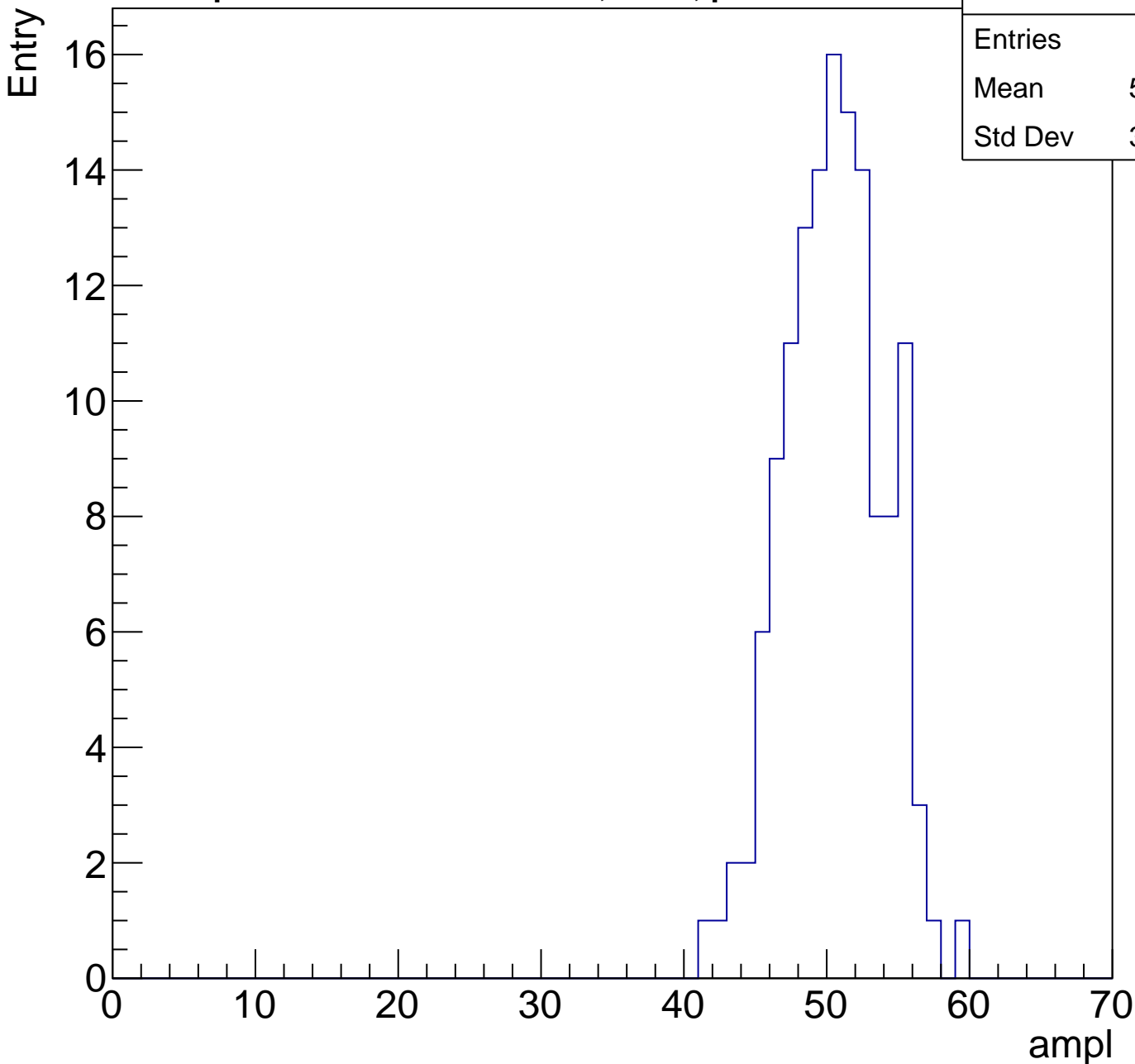
Gaus Width: 3.5236



B0L101S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	50.04
Std Dev	3.384

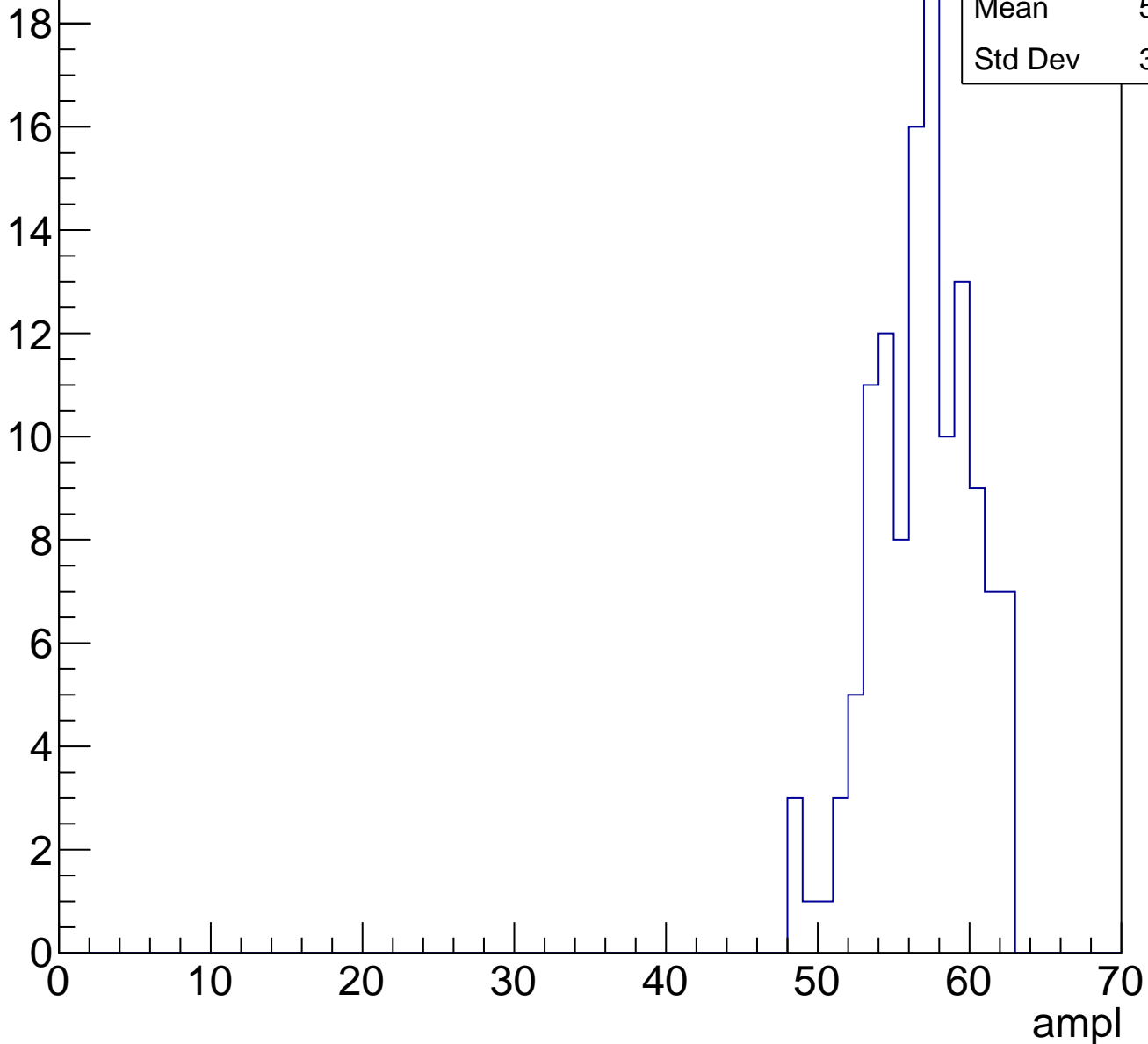


B0L101S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	56.43
Std Dev	3.238

Entry



B0L101S, U14-ch39, adc5

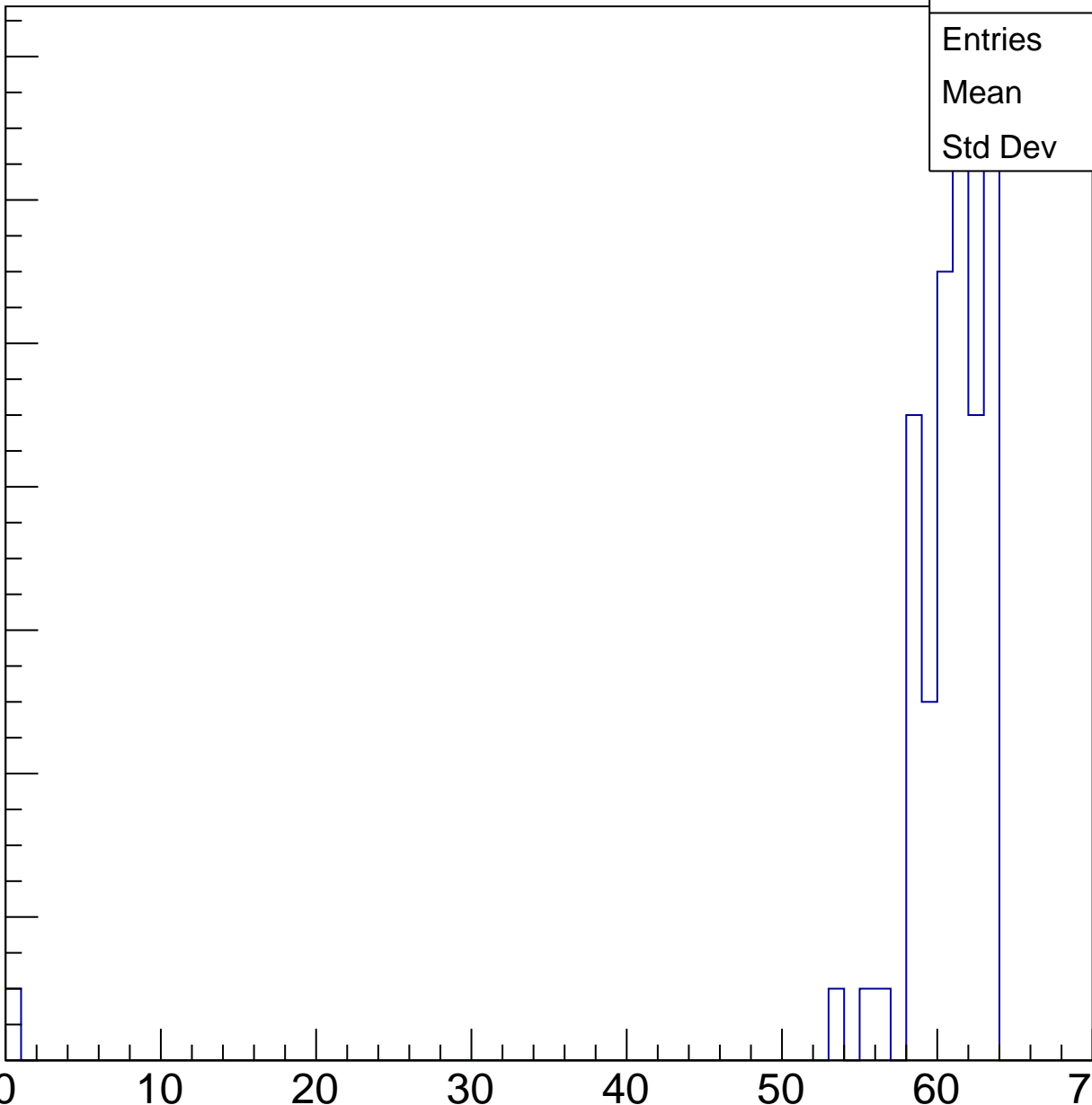
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.6
Std Dev	7.742

ampl

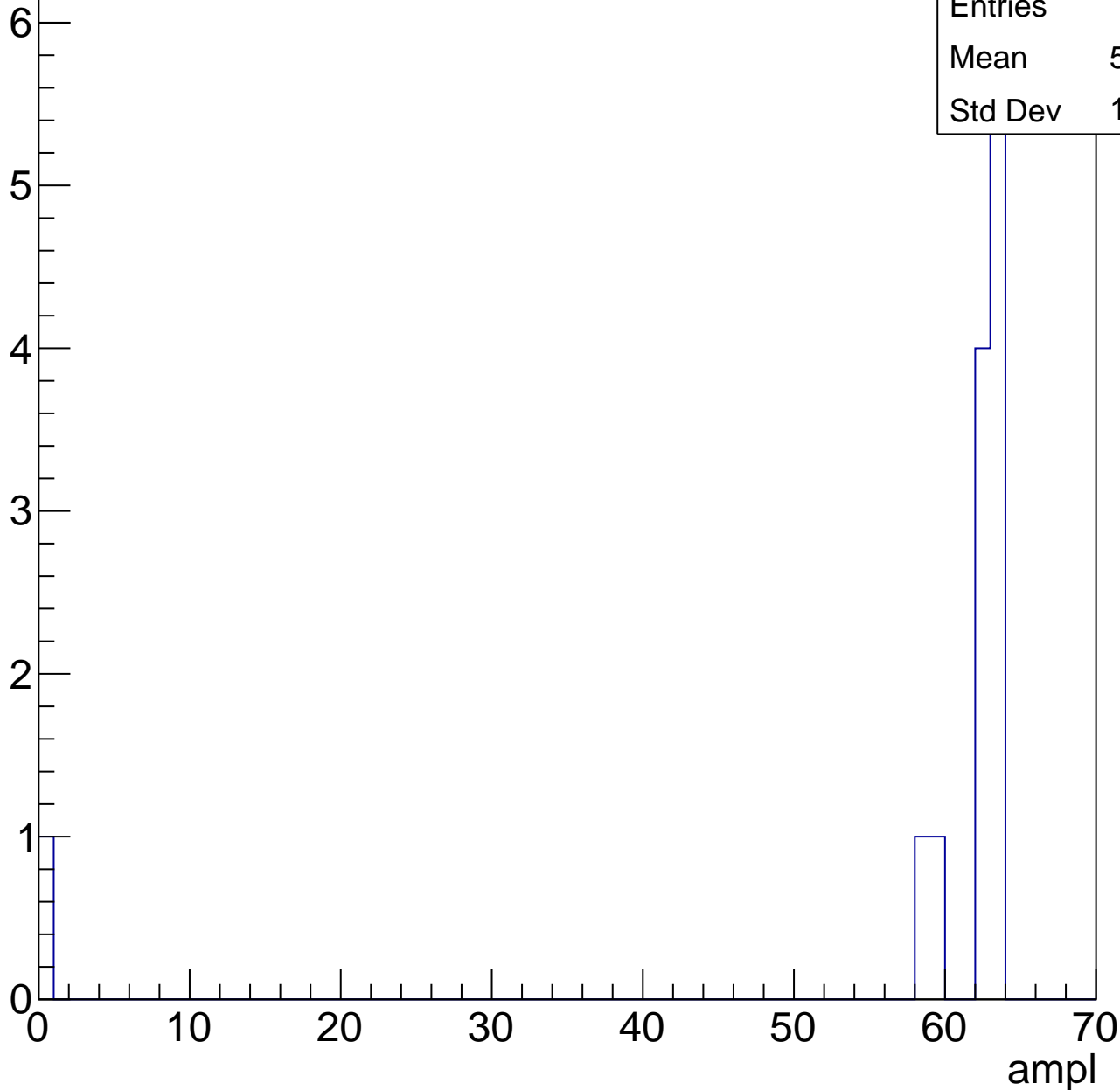


B0L101S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.15
Std Dev	16.57



B0L101S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L101S, U14-ch40, adc0

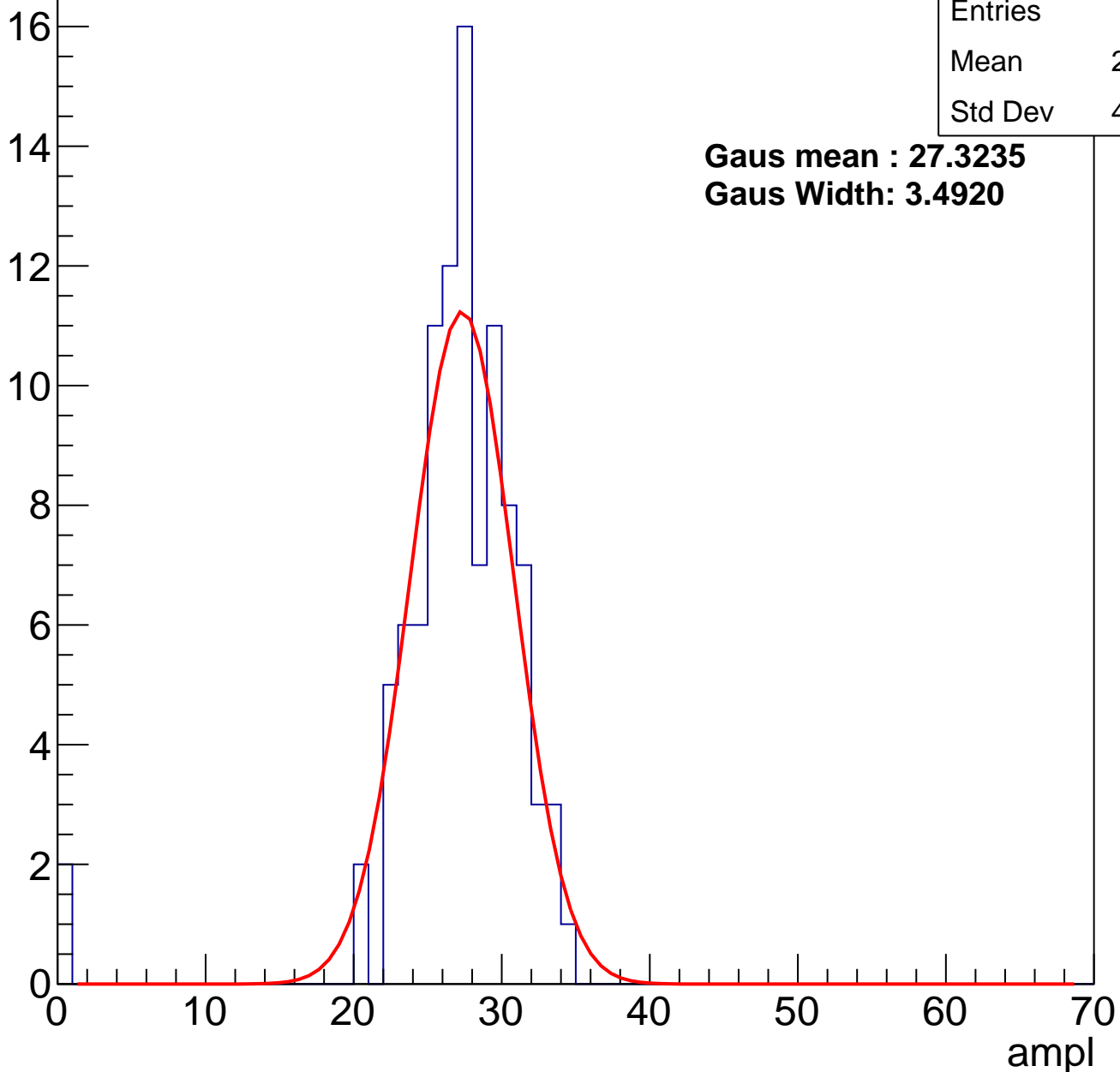
calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	26.52
Std Dev	4.828

Gaus mean : 27.3235

Gaus Width: 3.4920

Entry



B0L101S, U14-ch40, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	264
Mean	35.92
Std Dev	4.824

Gaus mean : 36.7184

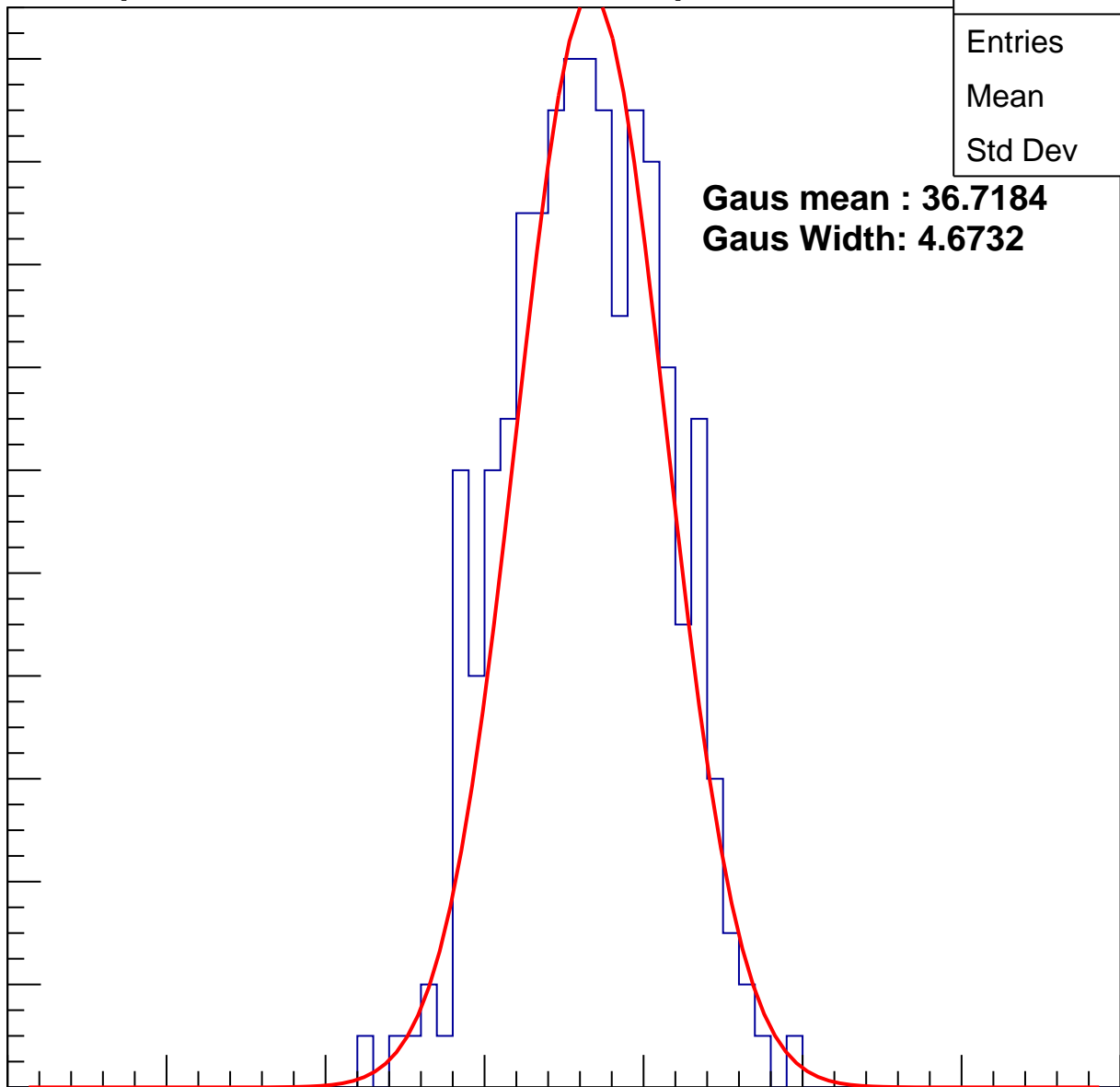
Gaus Width: 4.6732

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch40, adc2

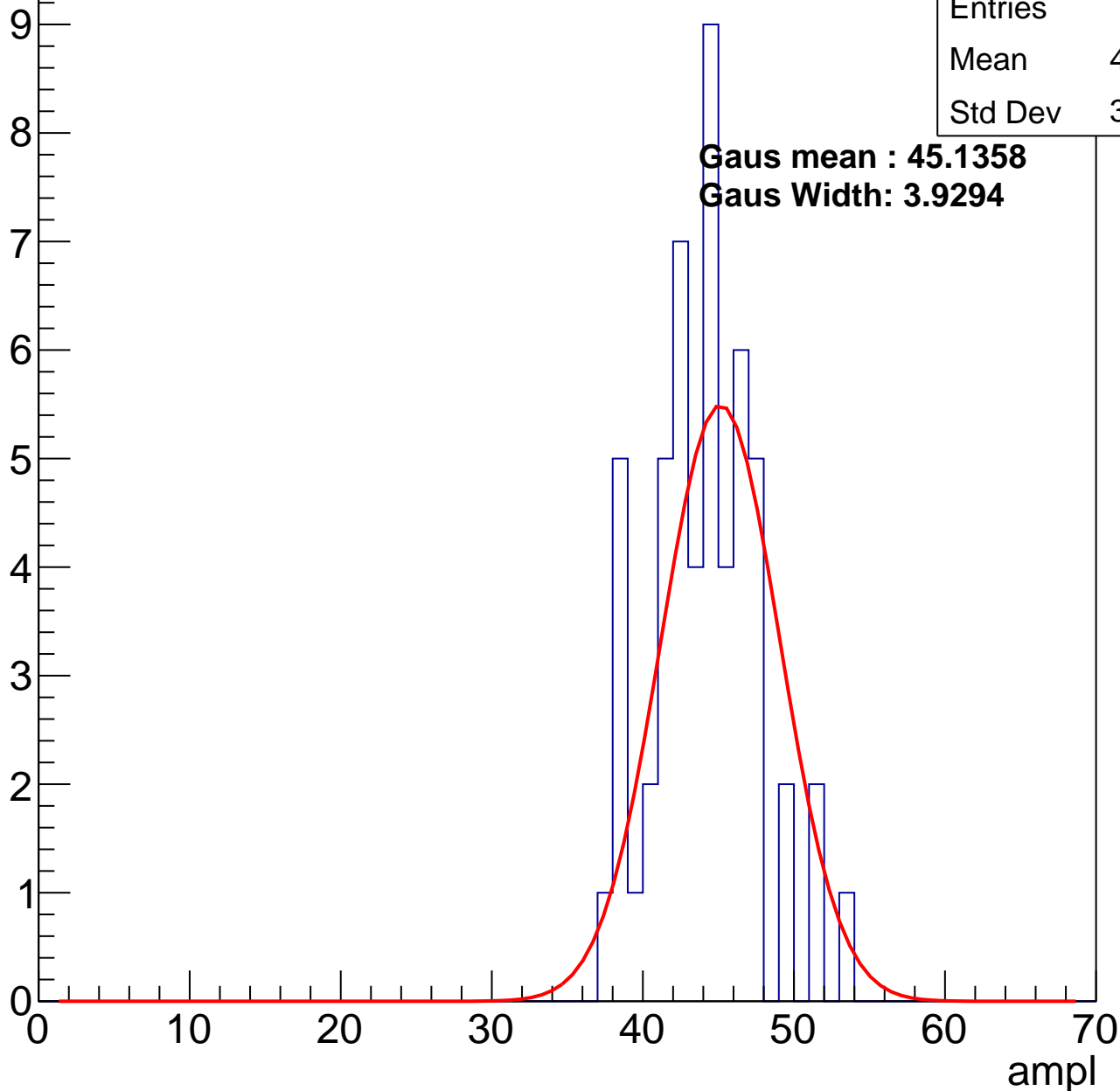
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	43.65
Std Dev	3.492

Gaus mean : 45.1358

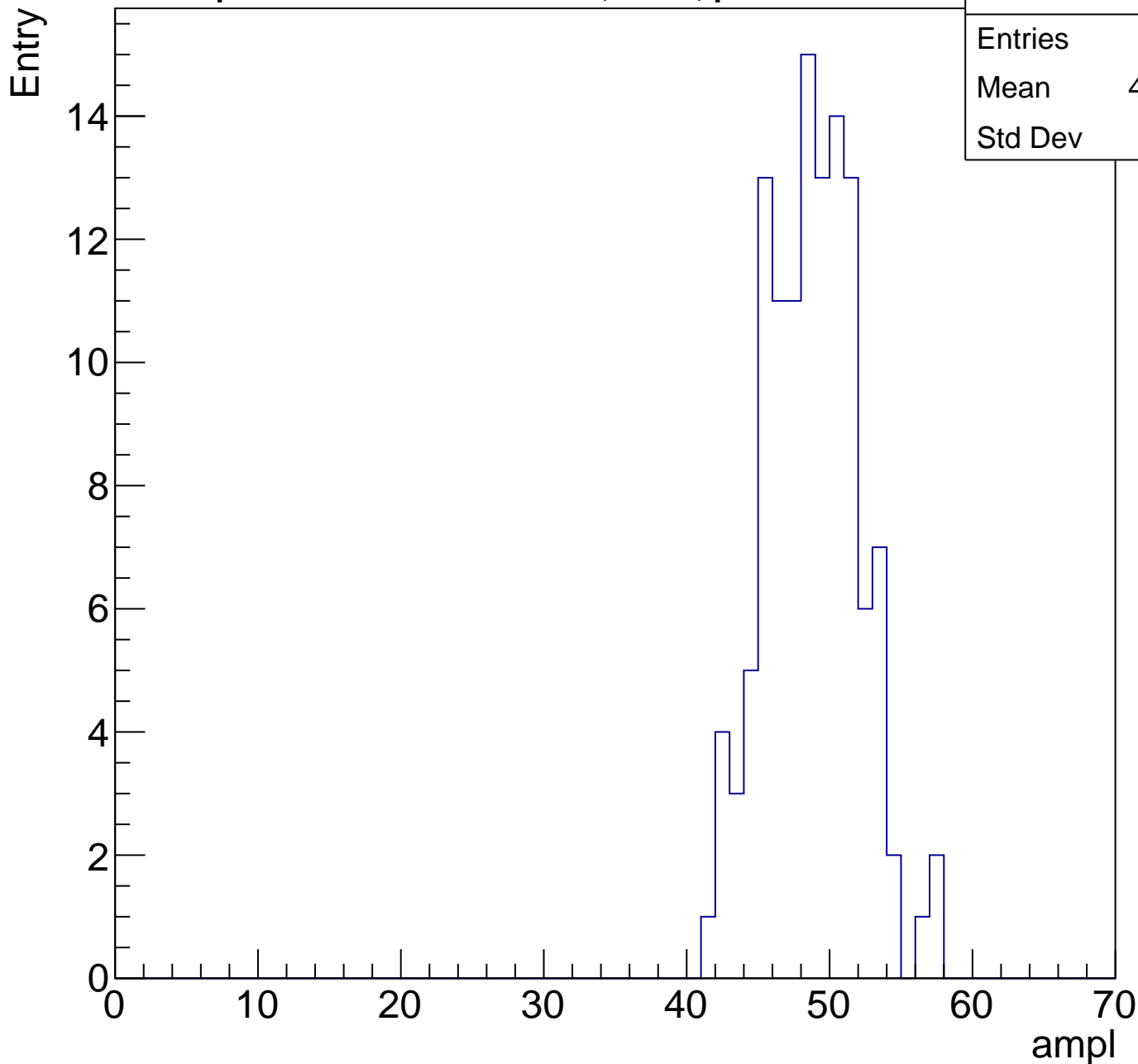
Gaus Width: 3.9294



B0L101S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

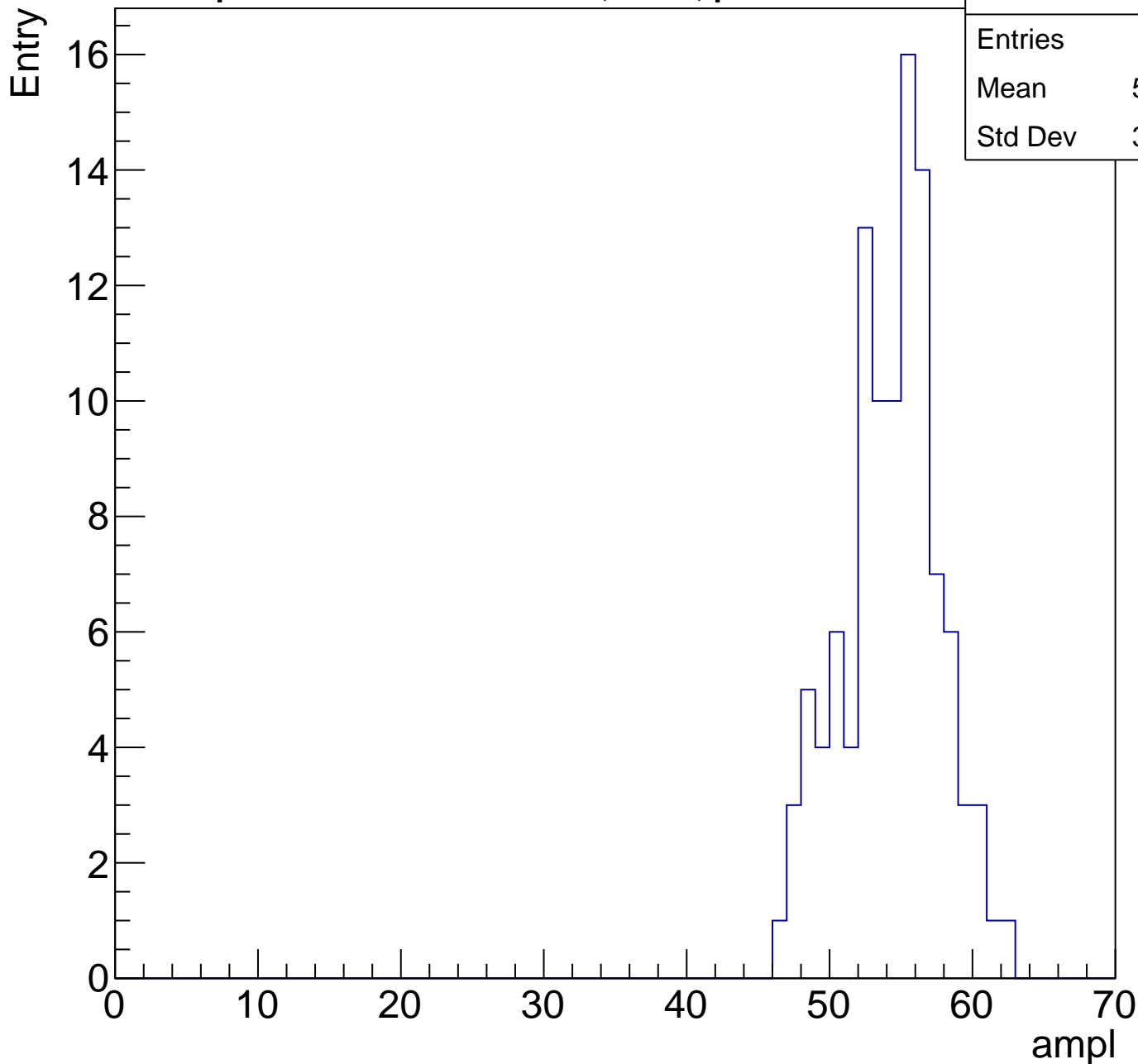
Entries	121
Mean	48.32
Std Dev	3.23



B0L101S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	53.87
Std Dev	3.363



B0L101S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#1, port C1

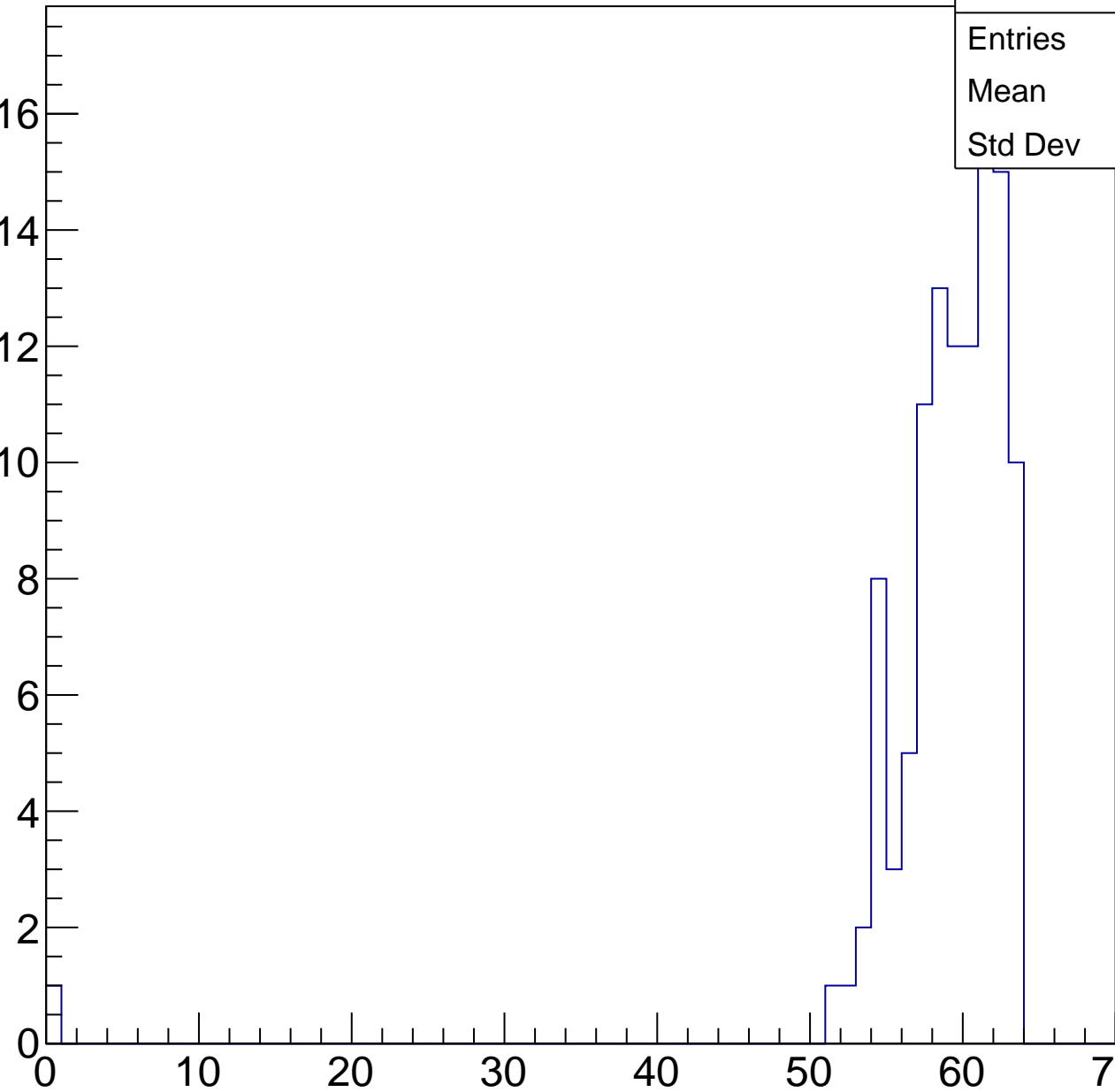
Entries	111
Mean	58.49
Std Dev	6.269

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

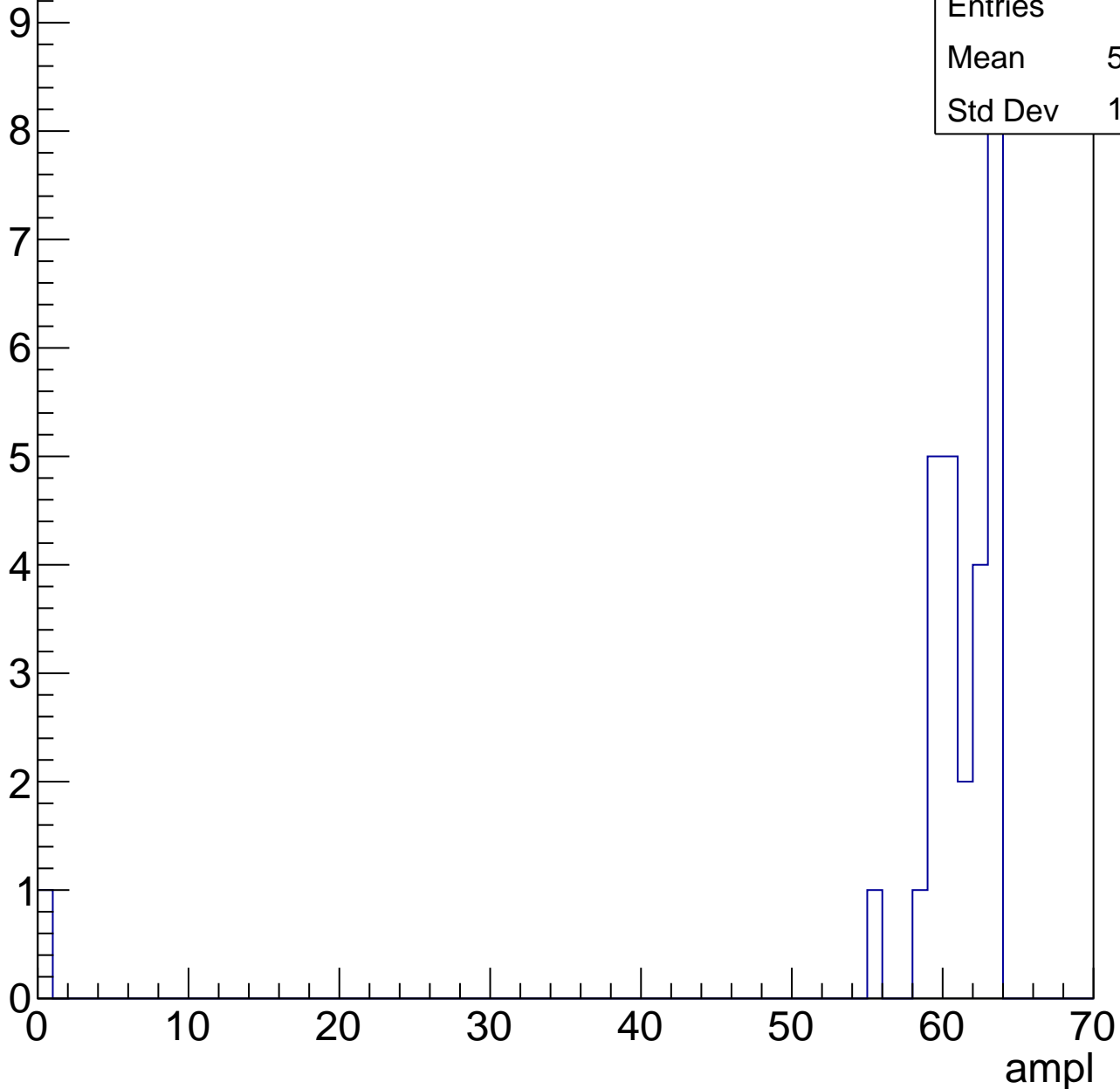


B0L101S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	28
Mean	58.75
Std Dev	11.48



B0L101S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U14-ch41, adc0

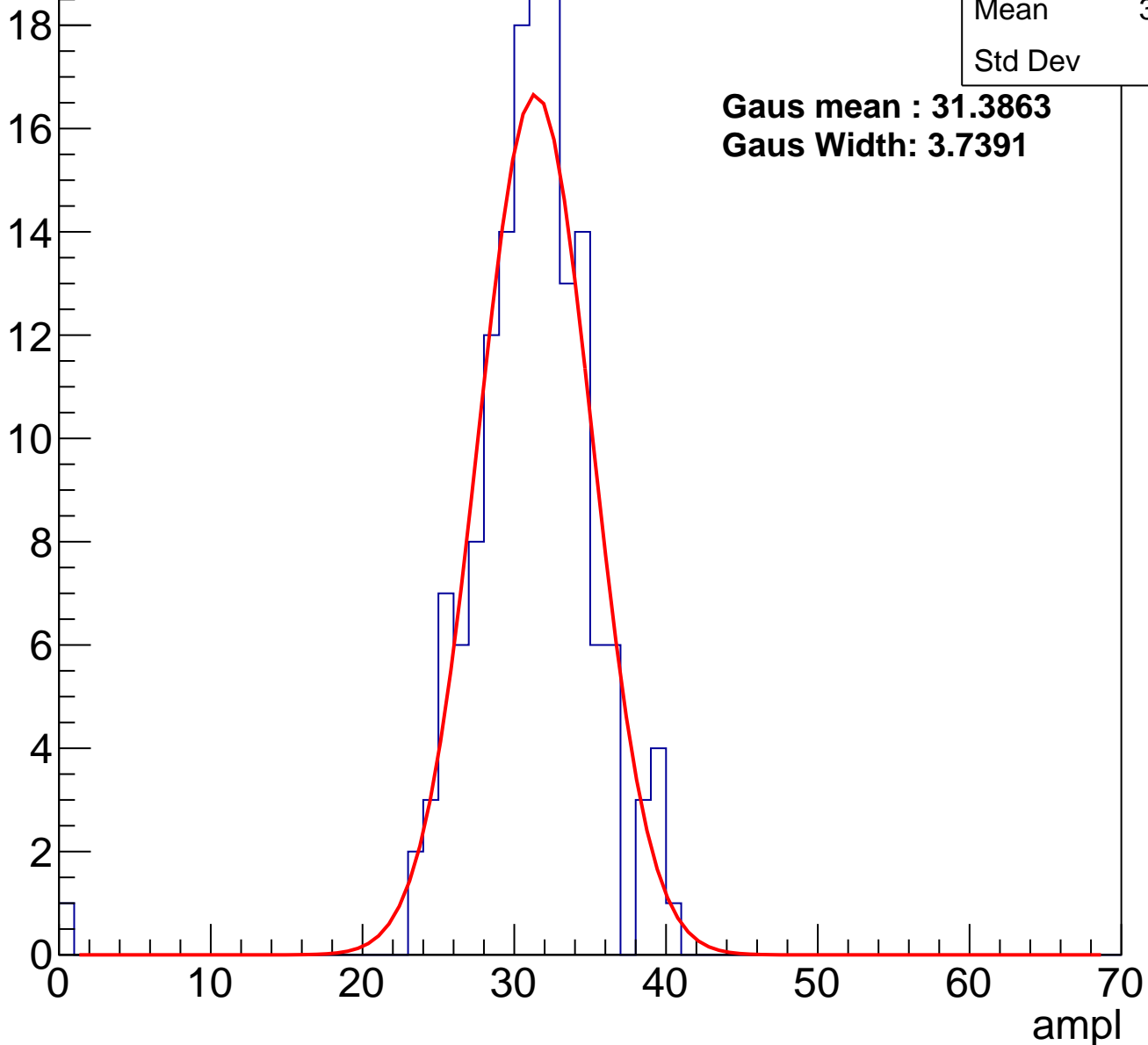
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	30.67
Std Dev	4.28

Gaus mean : 31.3863

Gaus Width: 3.7391

Entry



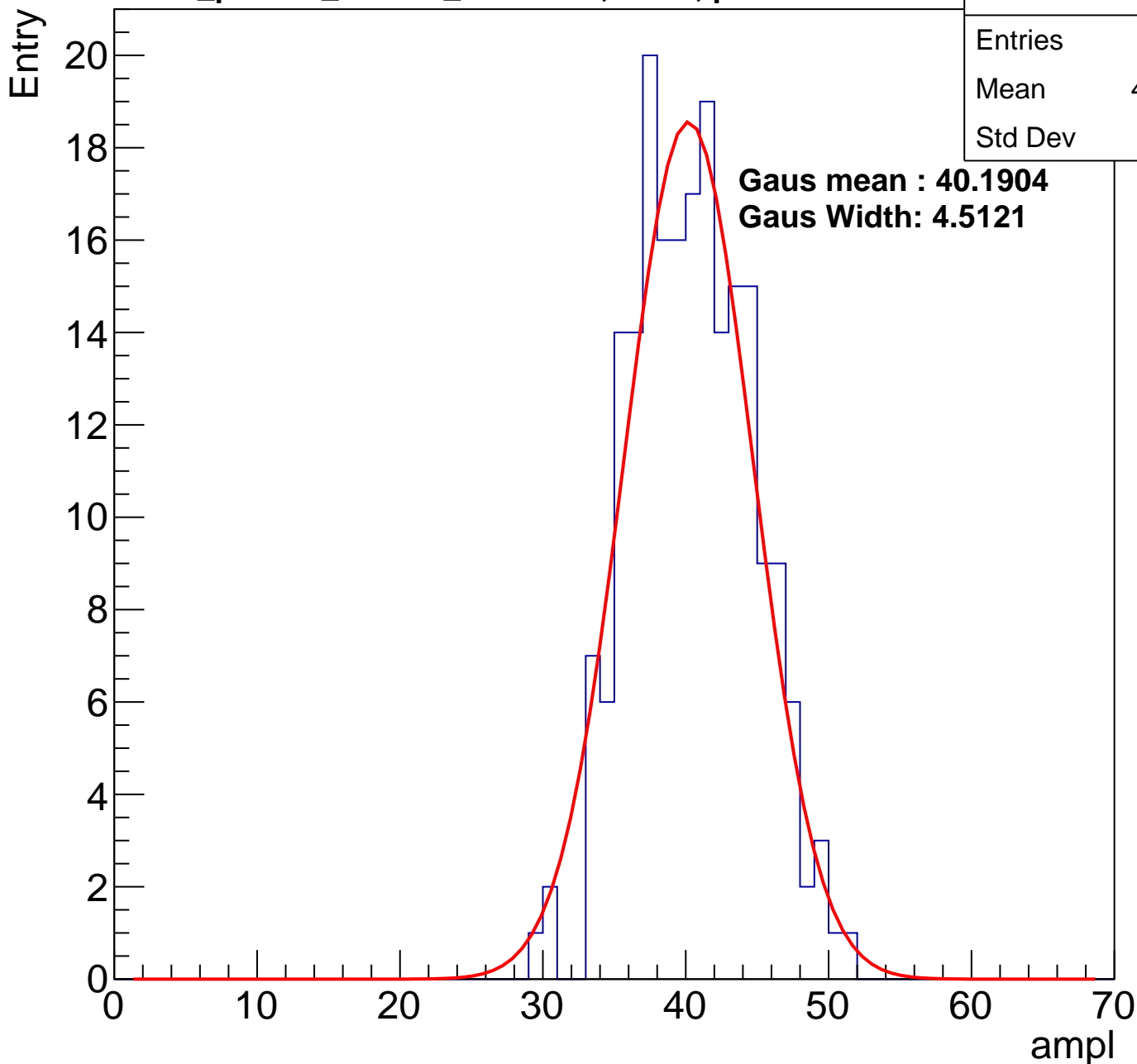
B0L101S, U14-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	207
Mean	40.03
Std Dev	4.17

Gaus mean : 40.1904

Gaus Width: 4.5121



B0L101S, U14-ch41, adc2

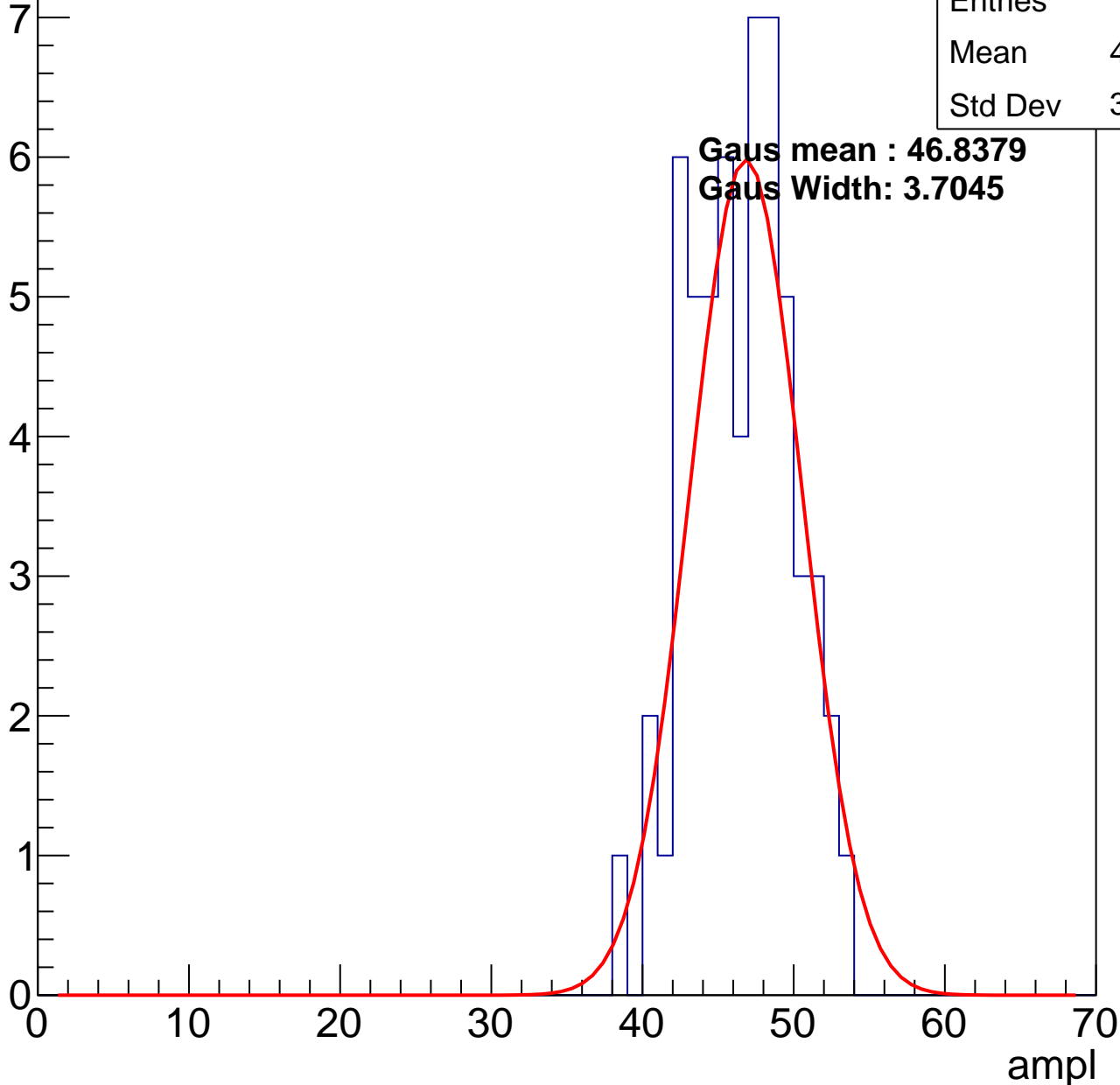
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	46.03
Std Dev	3.353

Gaus mean : 46.8379

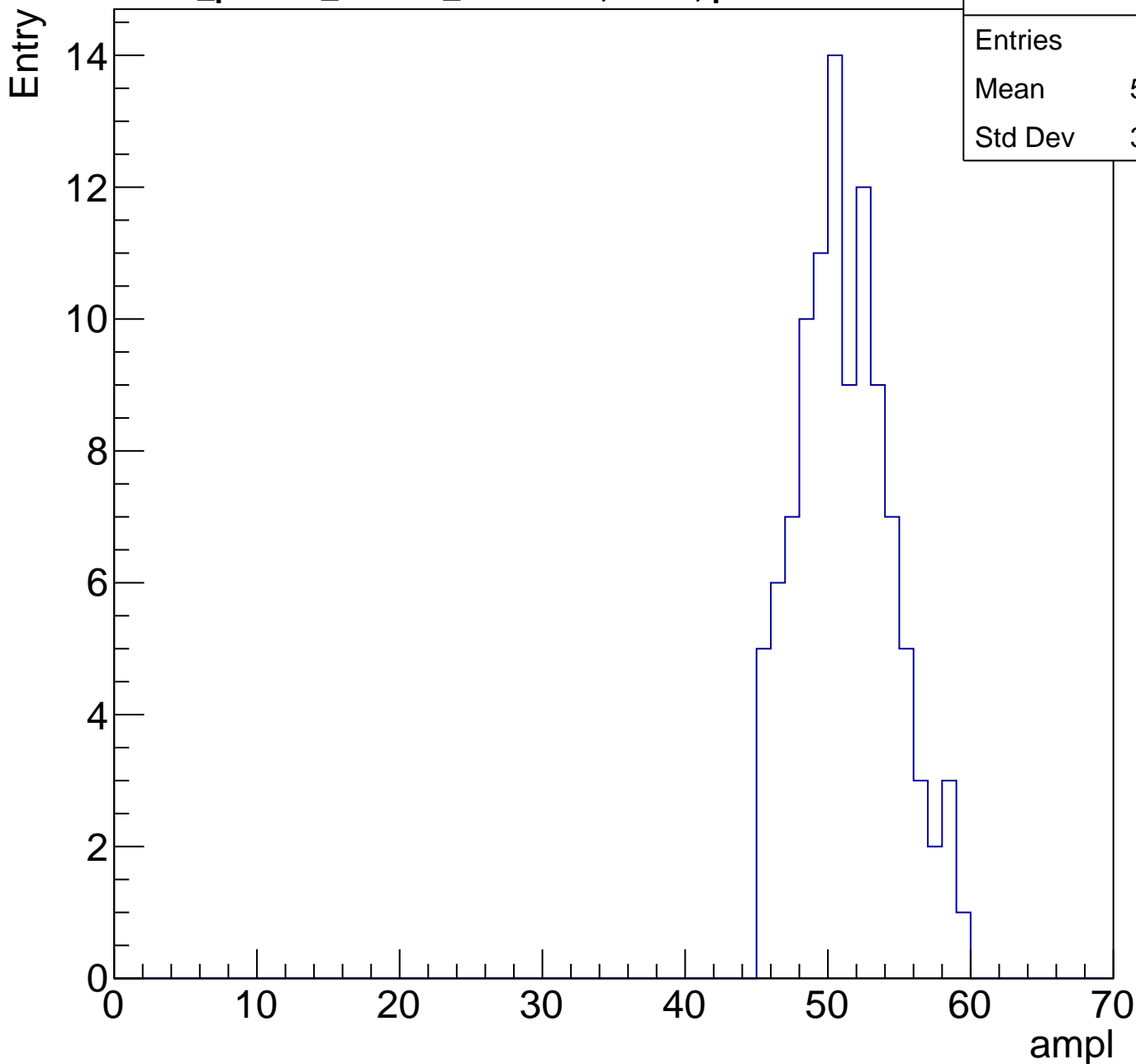
Gaus Width: 3.7045



B0L101S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	50.74
Std Dev	3.299



B0L101S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	56.34
Std Dev	3.423

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

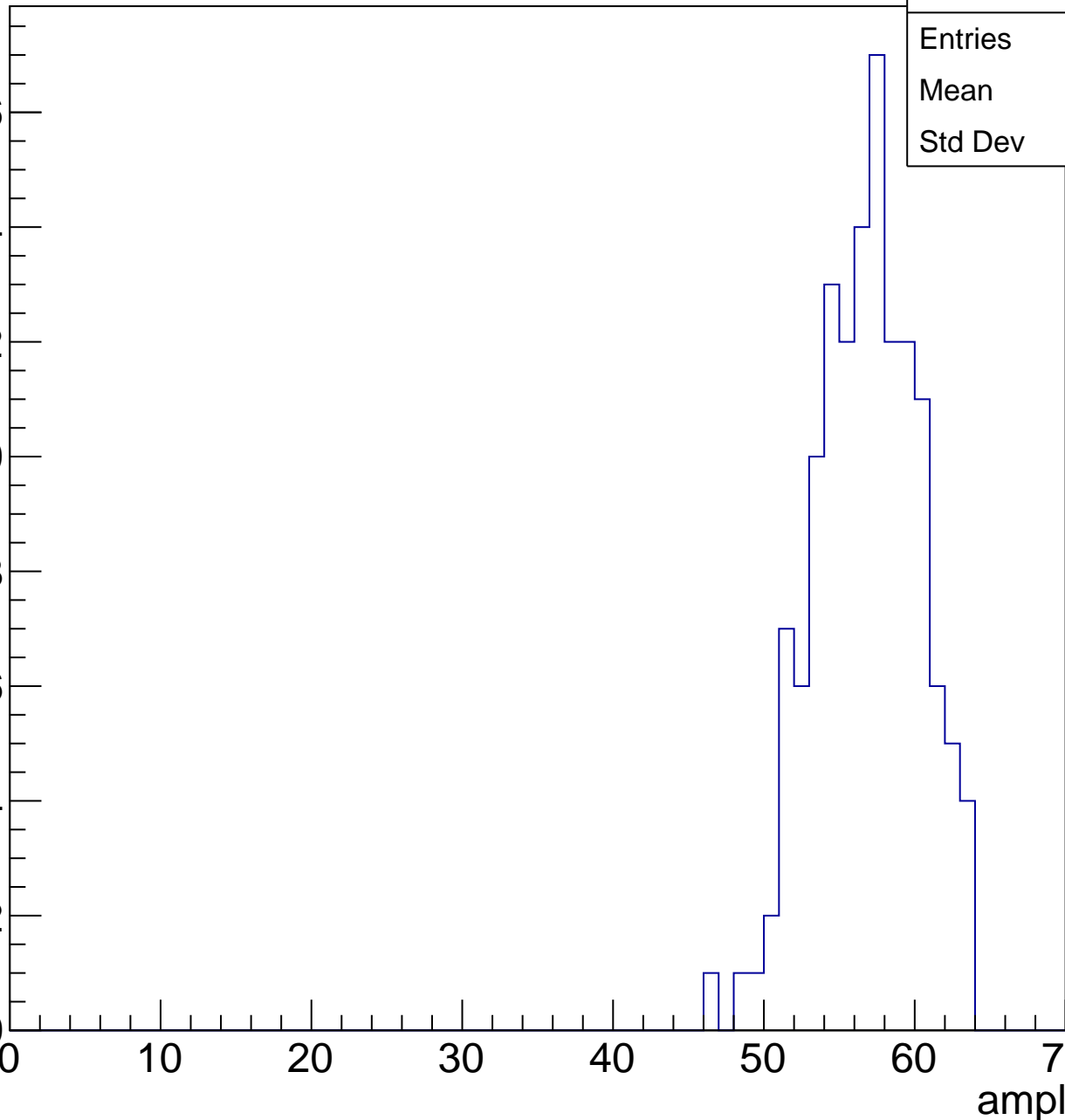
40

50

60

70

ampl



B0L101S, U14-ch41, adc5

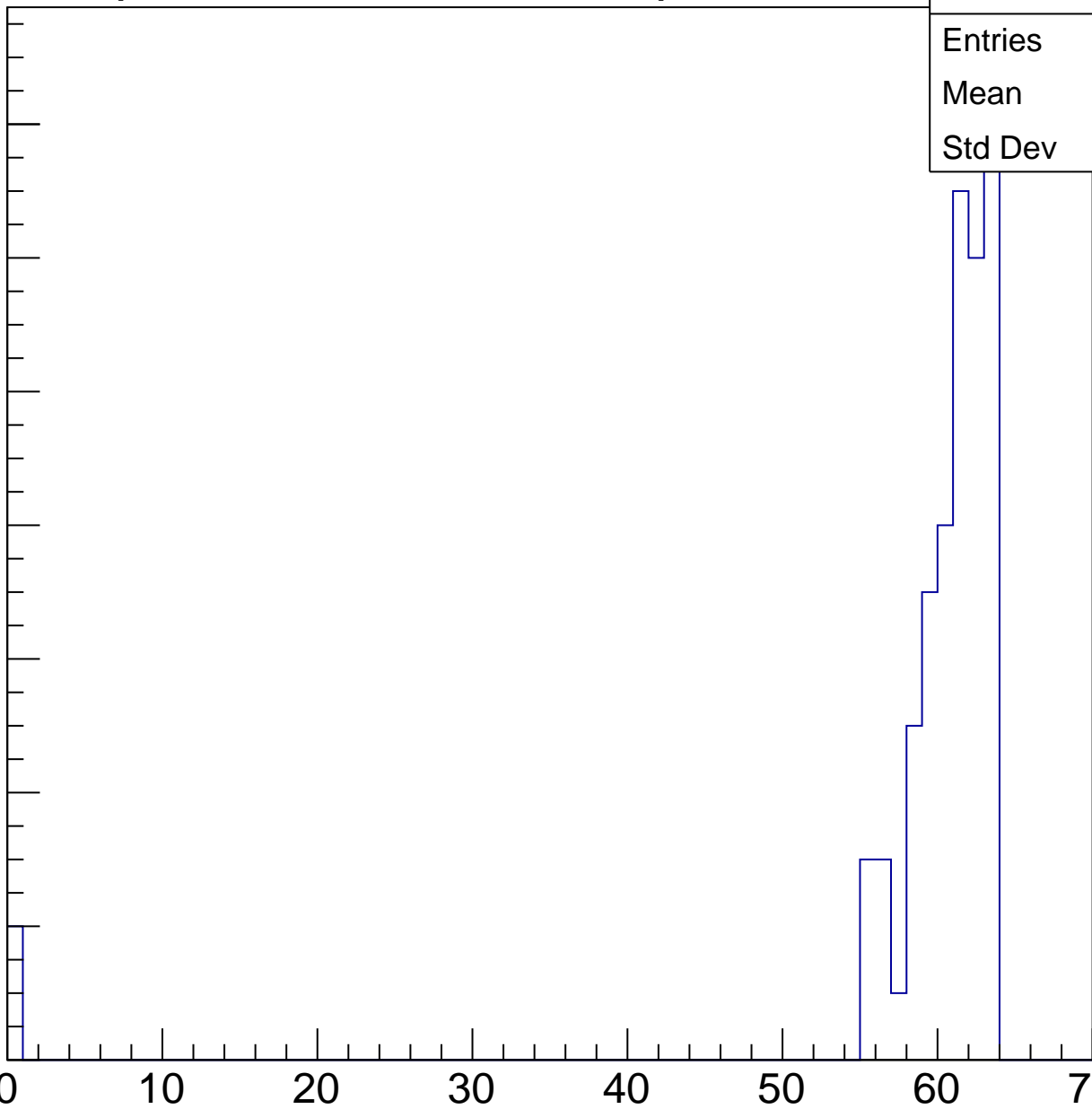
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.77
Std Dev	10.39

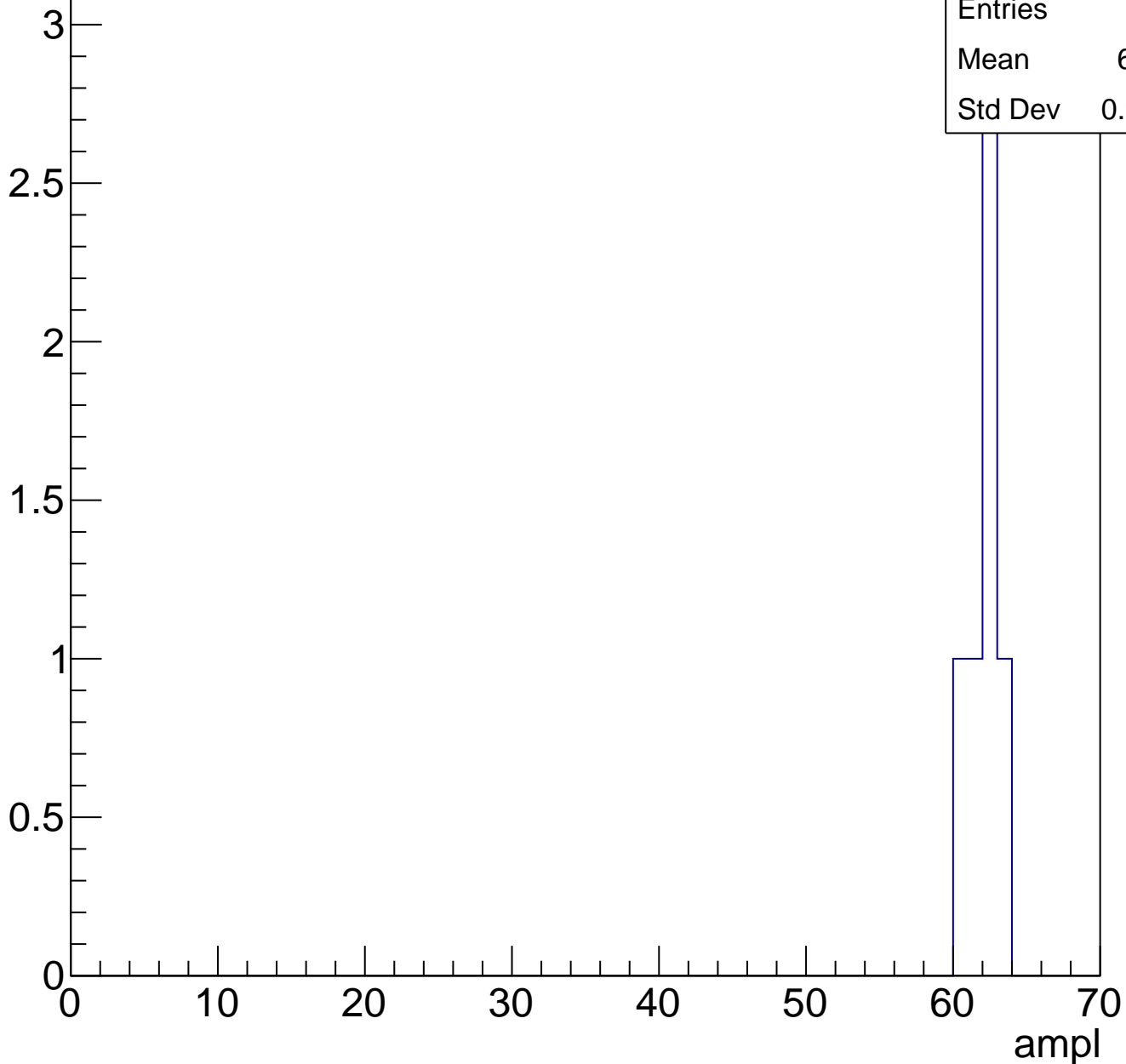
ampl



B0L101S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



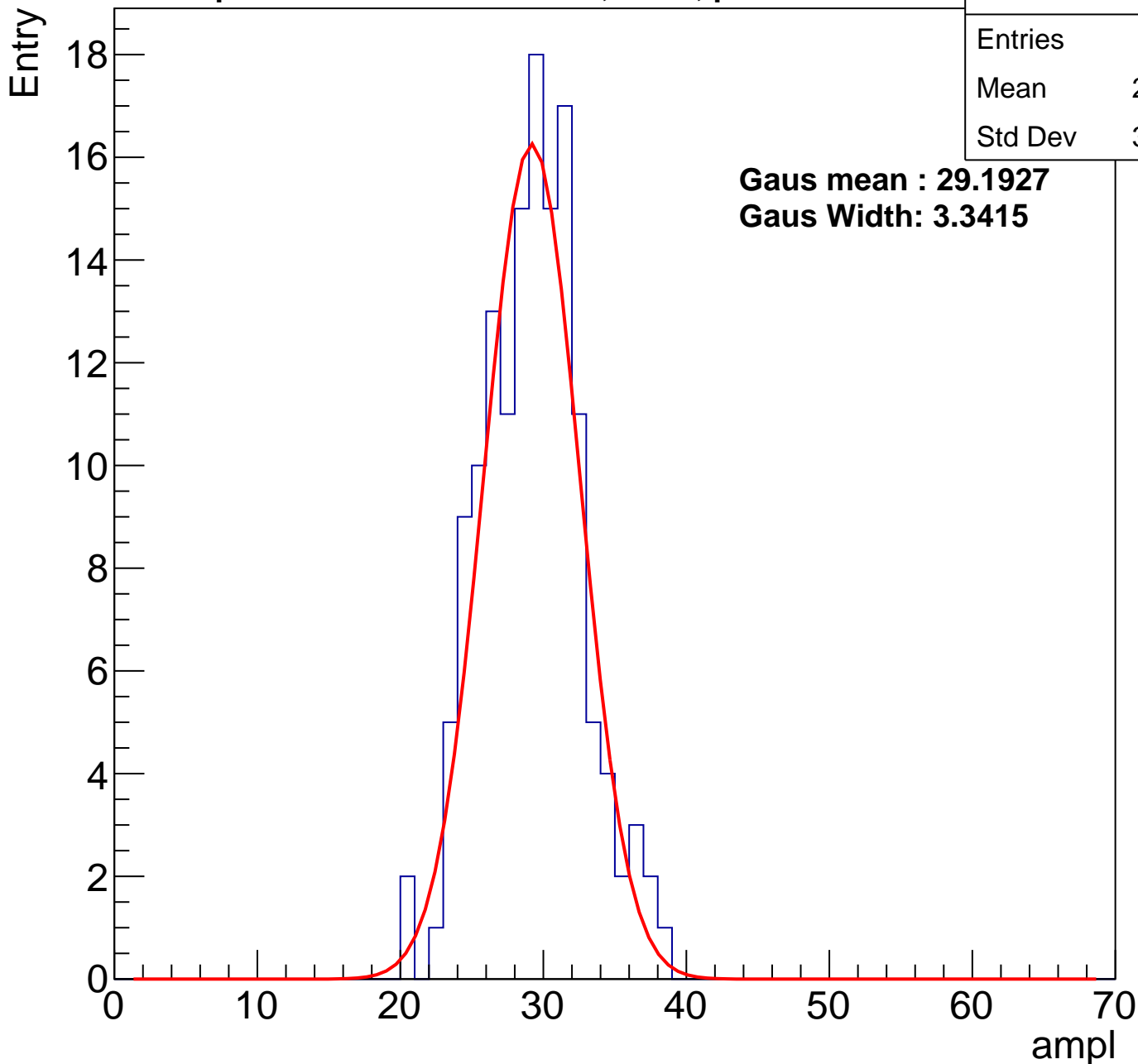
B0L101S, U14-ch42, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	28.75
Std Dev	3.469

Gaus mean : 29.1927

Gaus Width: 3.3415



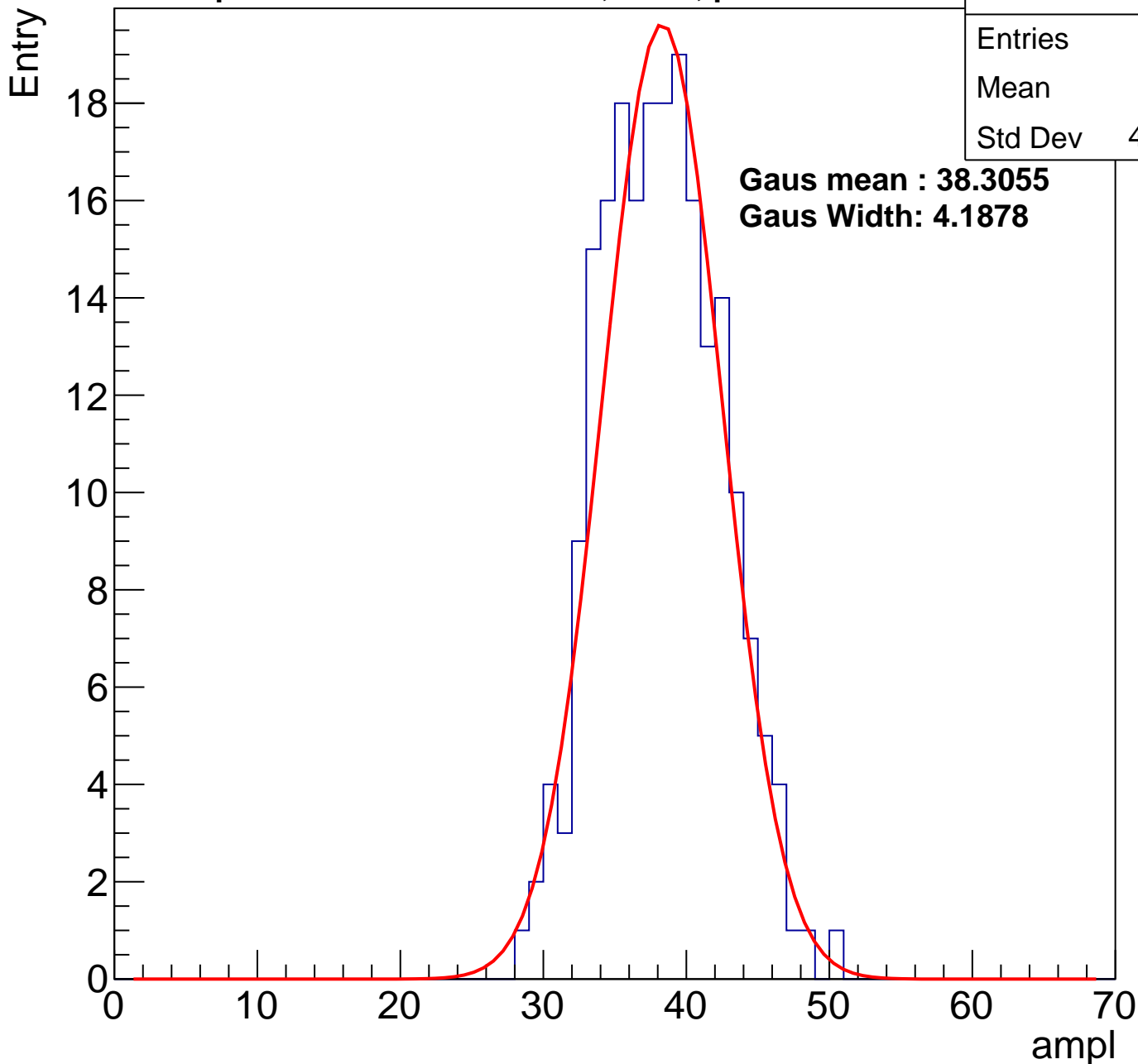
B0L101S, U14-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	211
Mean	37.8
Std Dev	4.135

Gaus mean : 38.3055

Gaus Width: 4.1878



B0L101S, U14-ch42, adc2

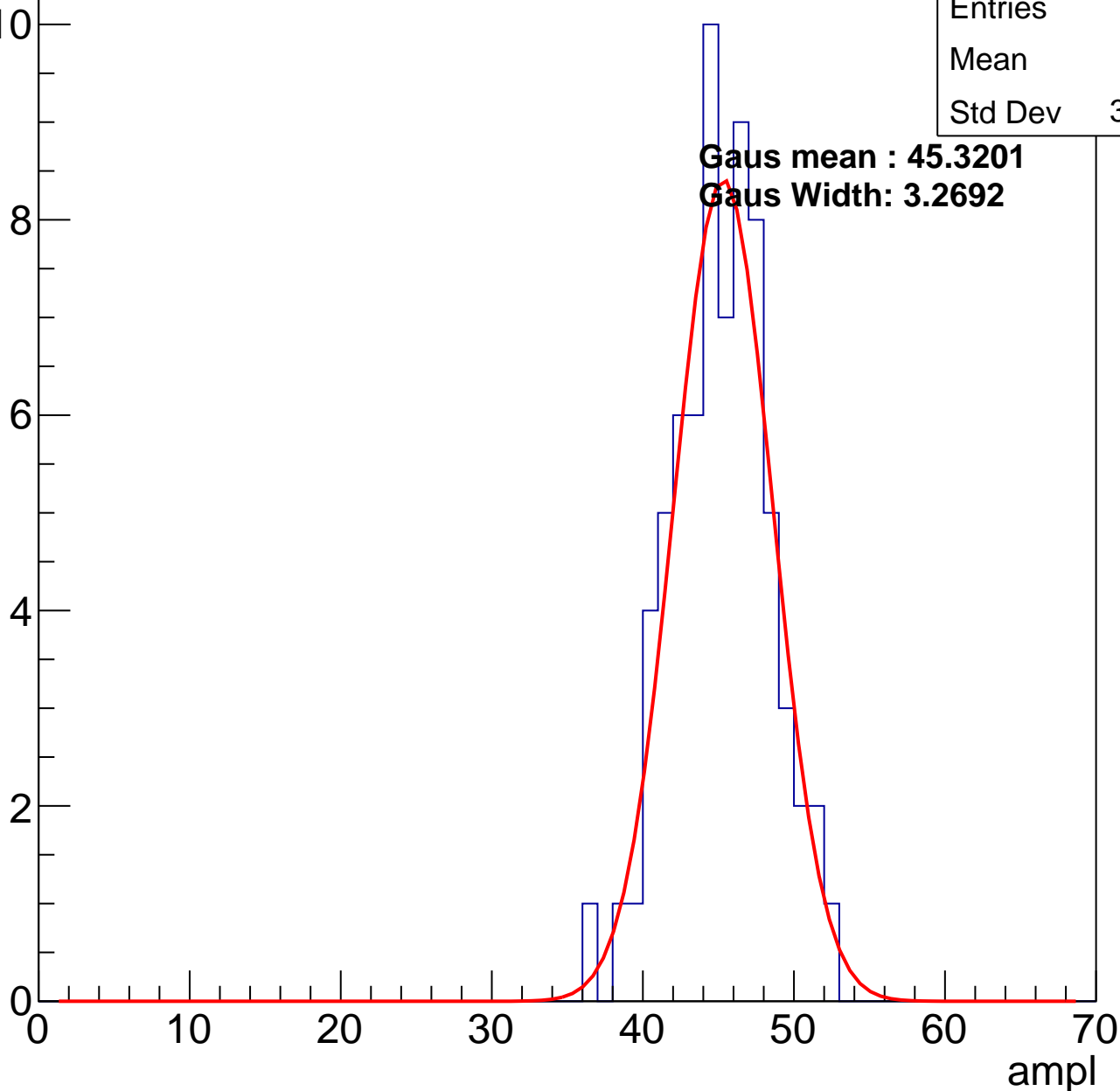
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	71
Mean	44.7
Std Dev	3.213

Gaus mean : 45.3201

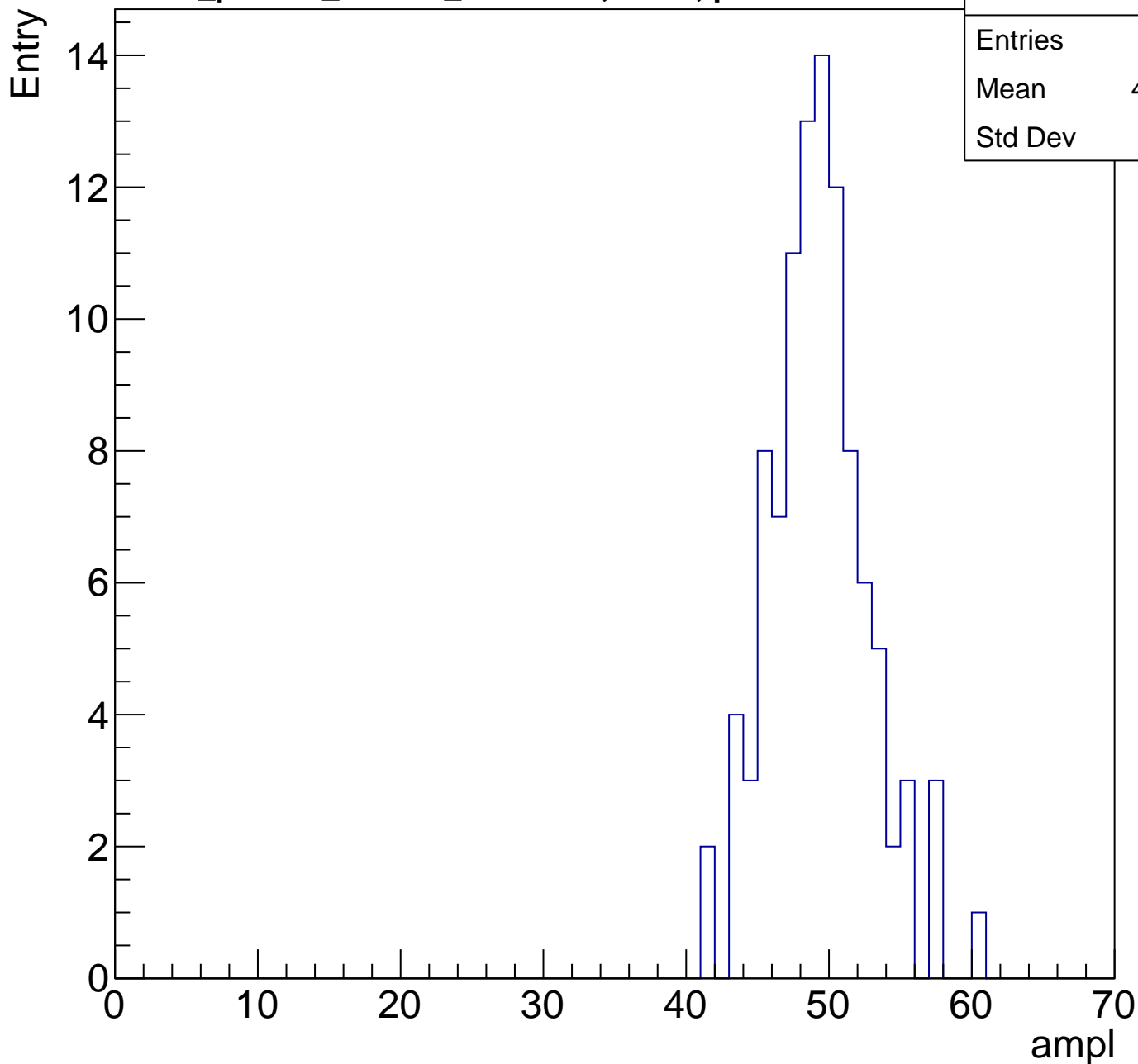
Gaus Width: 3.2692



B0L101S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#1, port C1

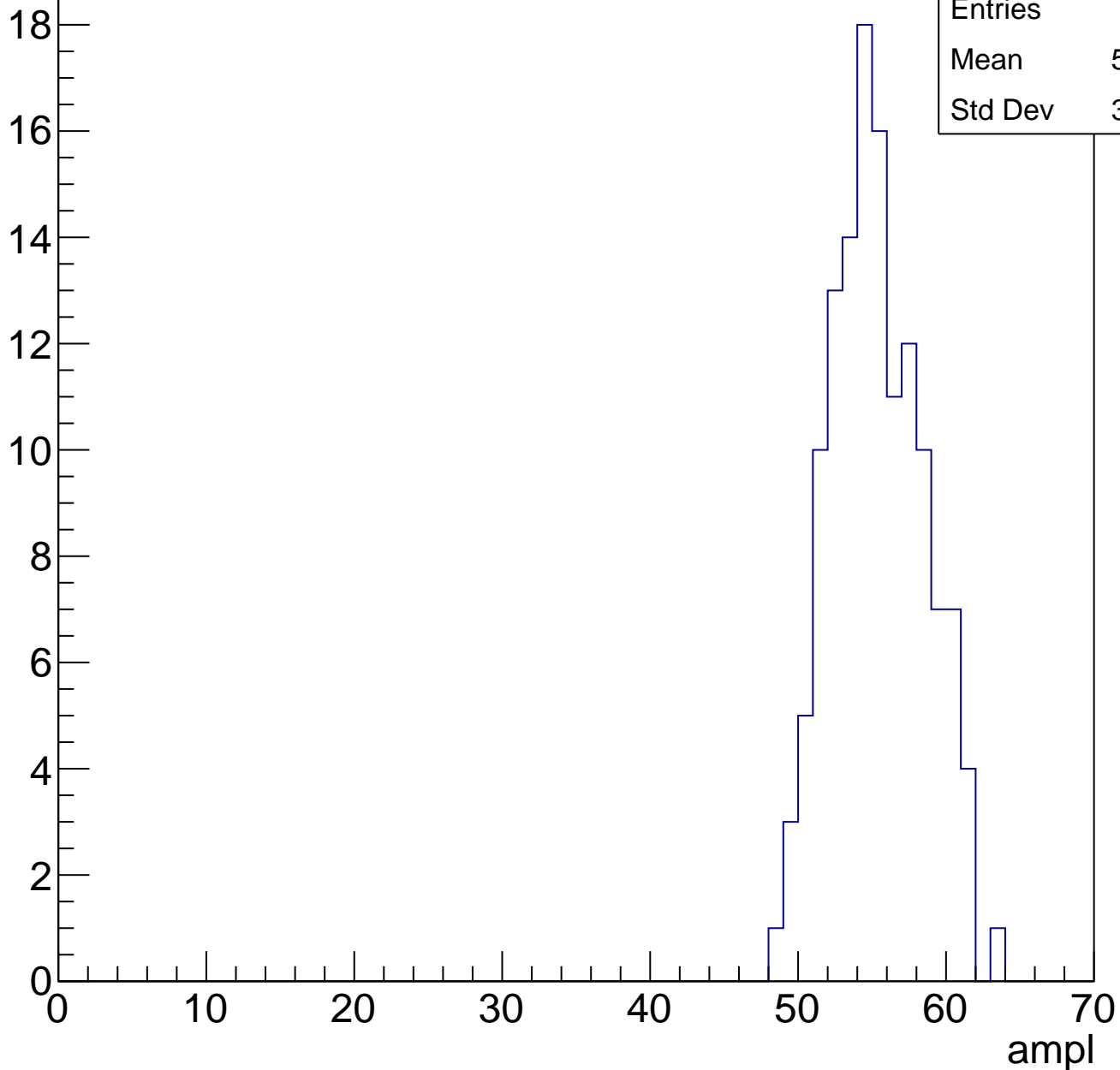
Entries	102
Mean	48.86
Std Dev	3.49



B0L101S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	132
Mean	54.89
Std Dev	3.108

B0L101S, U14-ch42, adc5

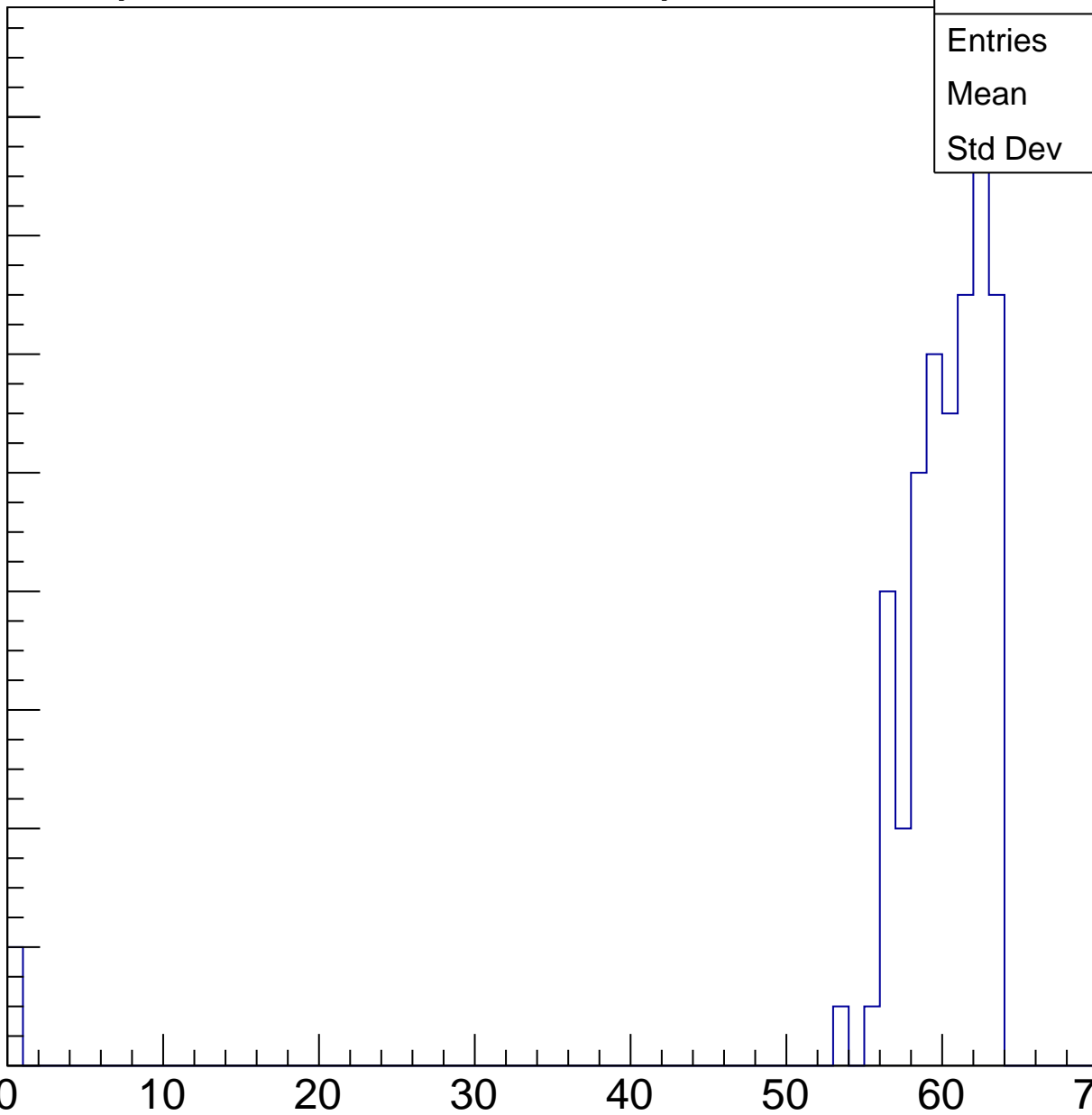
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.67
Std Dev	9.045

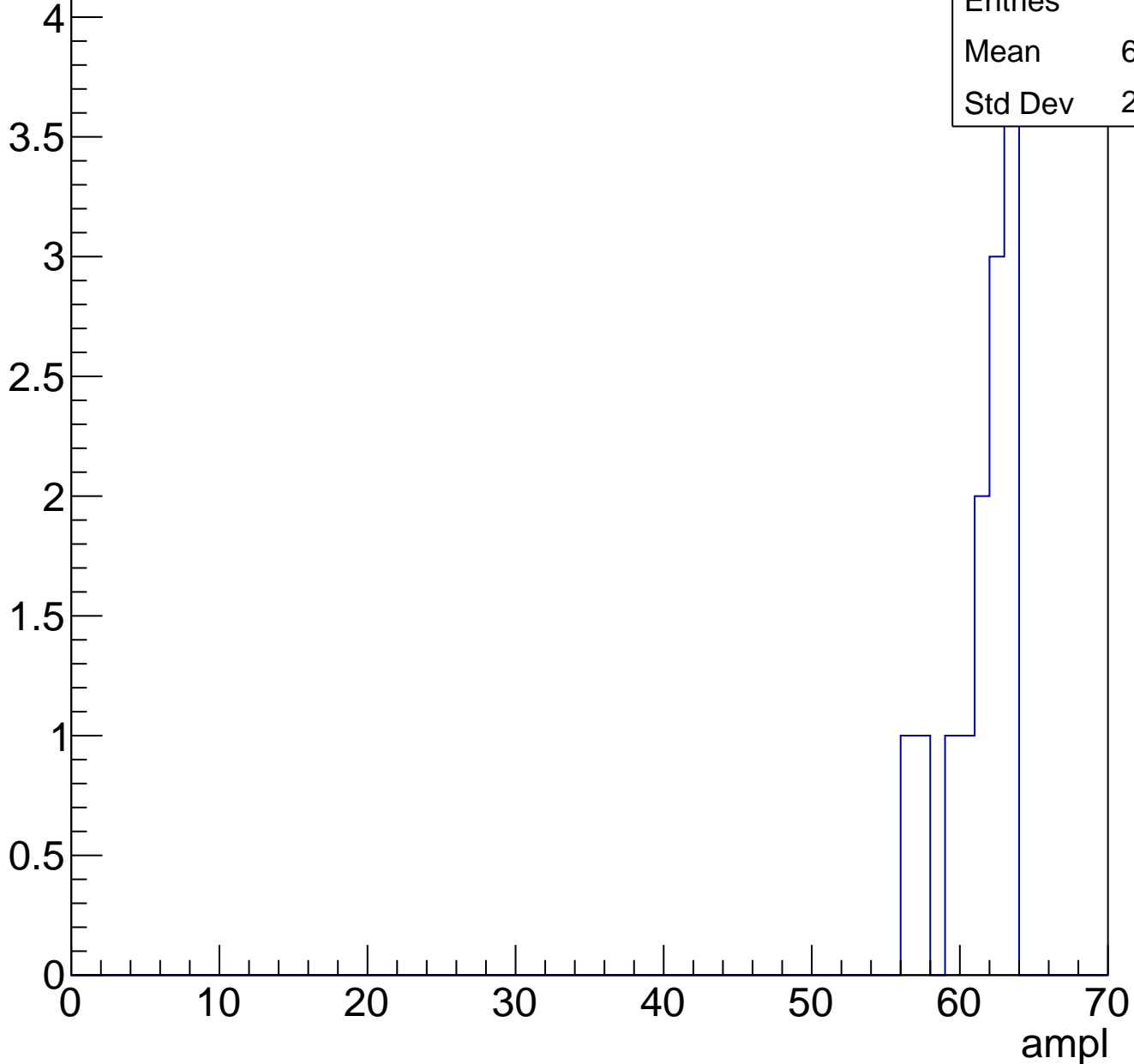
ampl



B0L101S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

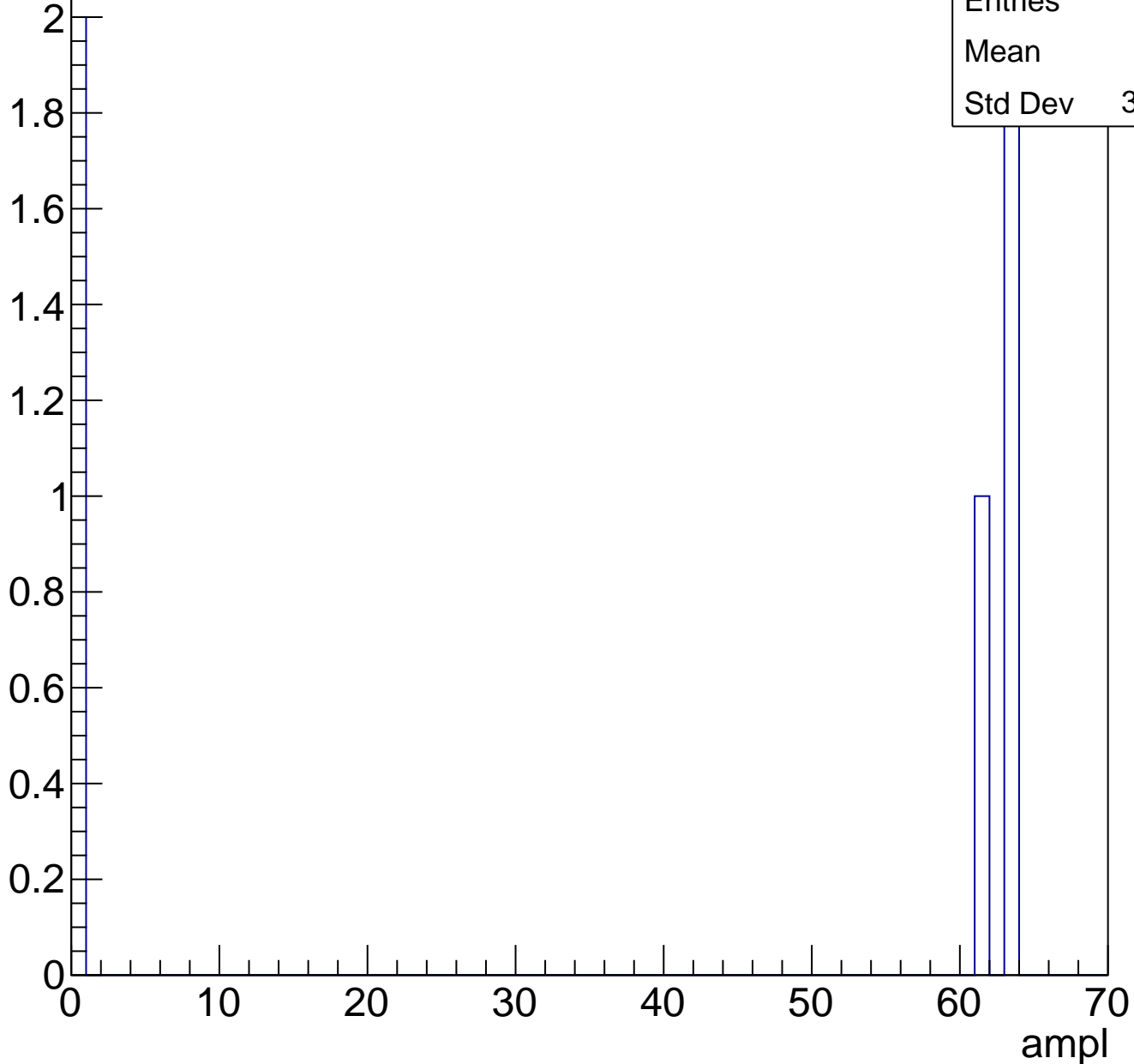
Entry



B0L101S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



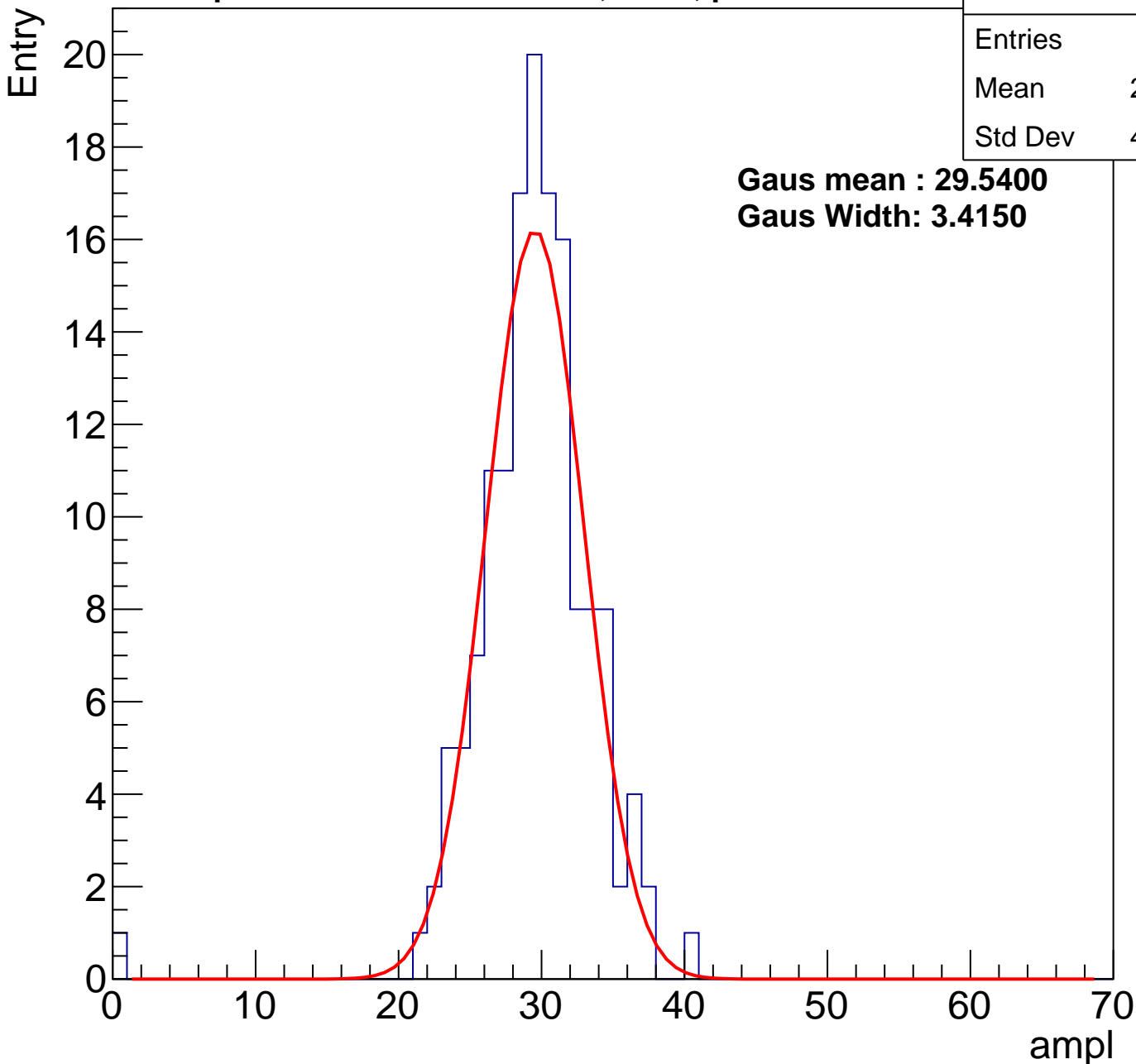
B0L101S, U14-ch43, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	29.04
Std Dev	4.202

Gaus mean : 29.5400

Gaus Width: 3.4150



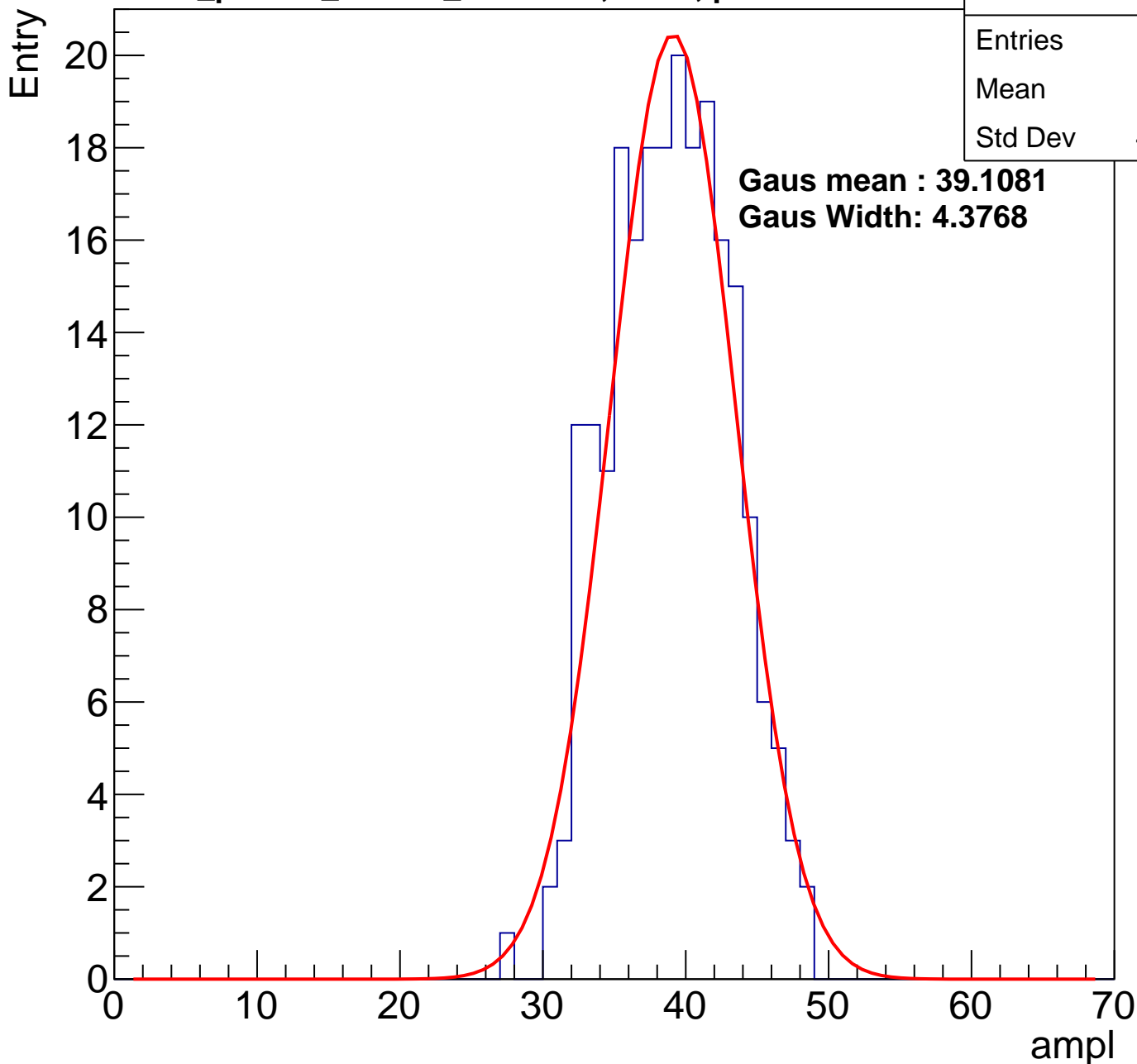
B0L101S, U14-ch43, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	225
Mean	38.5
Std Dev	4.111

Gaus mean : 39.1081

Gaus Width: 4.3768



B0L101S, U14-ch43, adc2

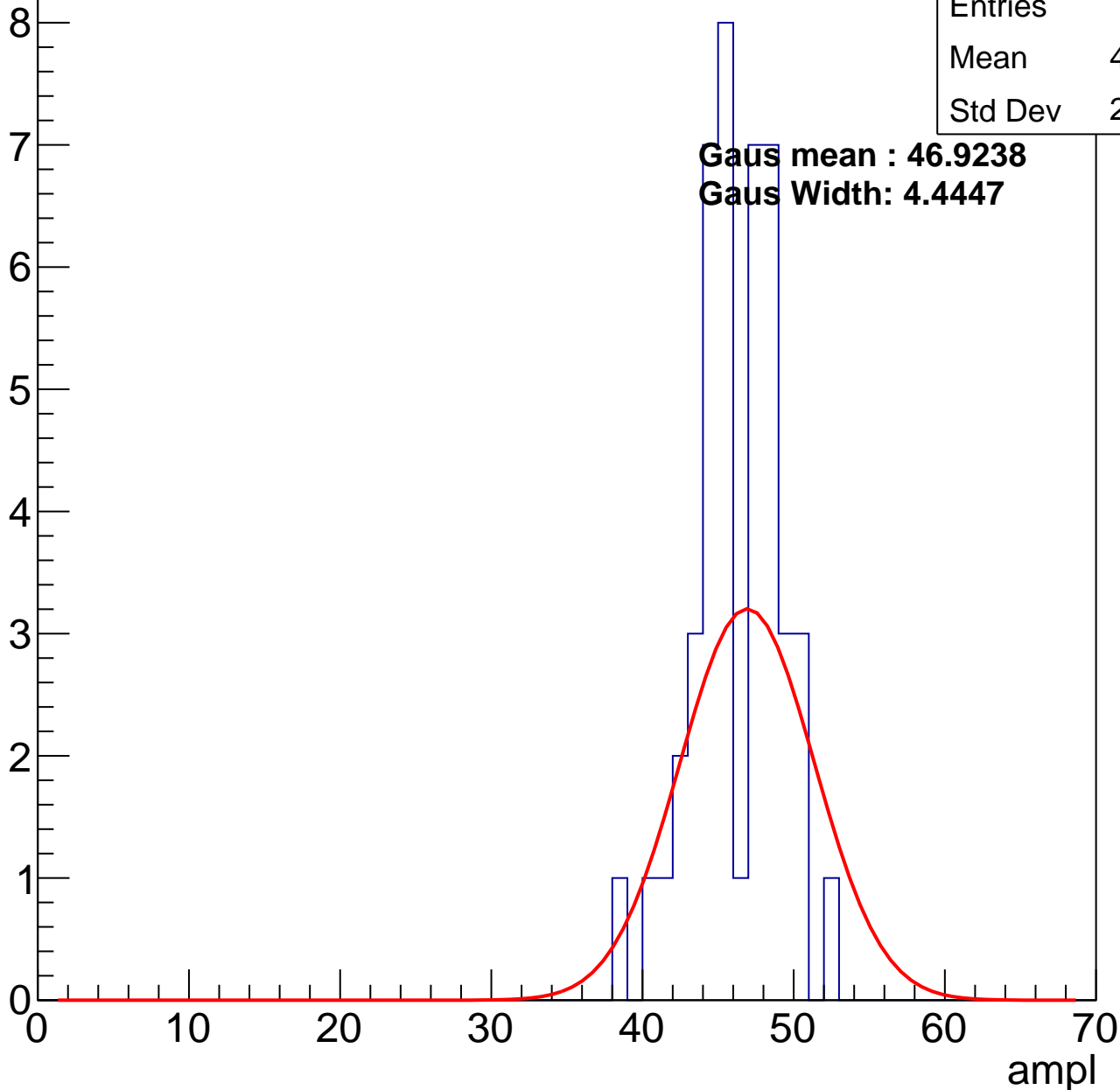
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	45
Mean	45.78
Std Dev	2.843

Gaus mean : 46.9238

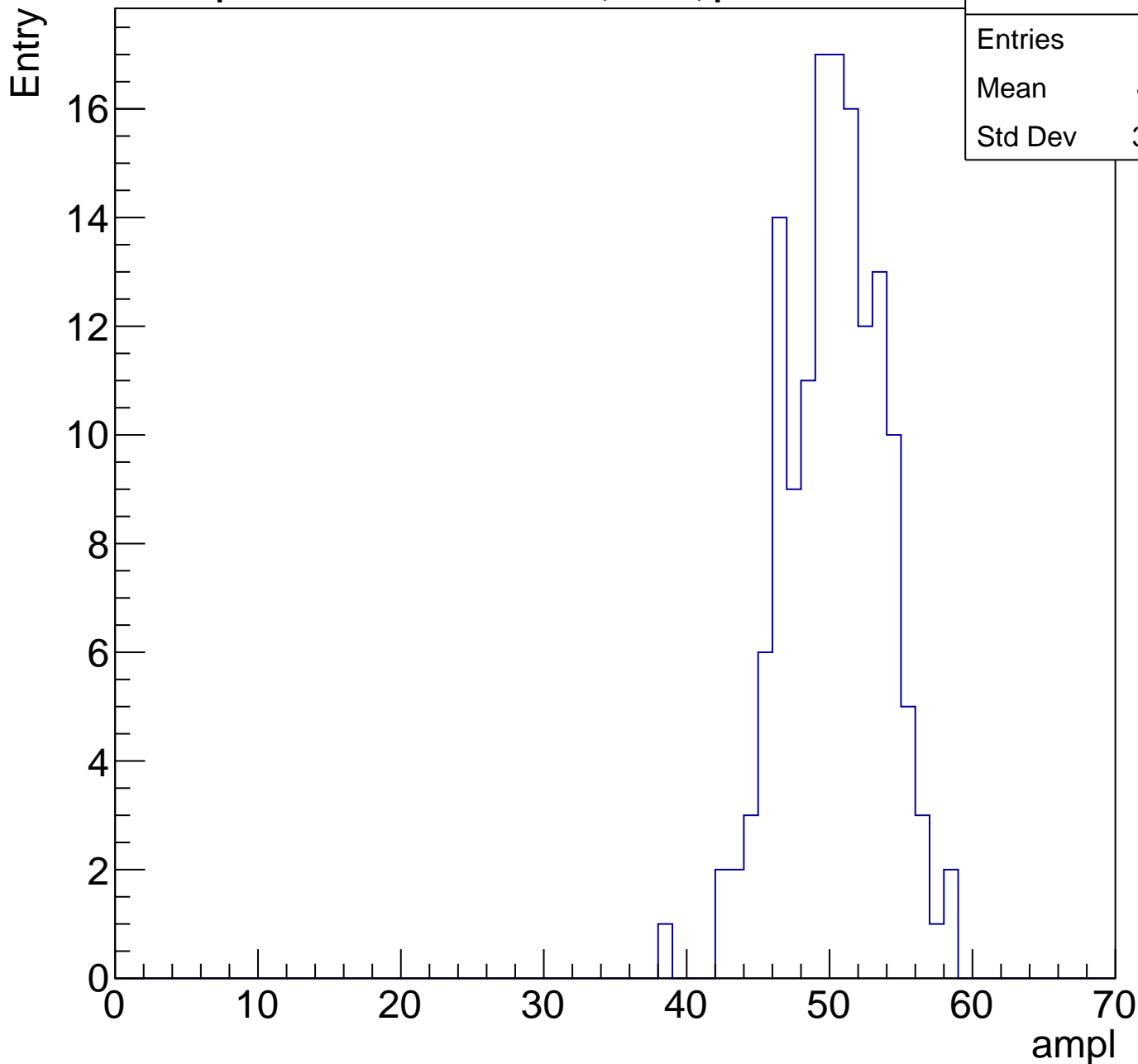
Gaus Width: 4.4447



B0L101S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	49.81
Std Dev	3.476



B0L101S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	102
Mean	56.26
Std Dev	2.907

Entry

14
12
10
8
6
4
2
0

0

10

20

30

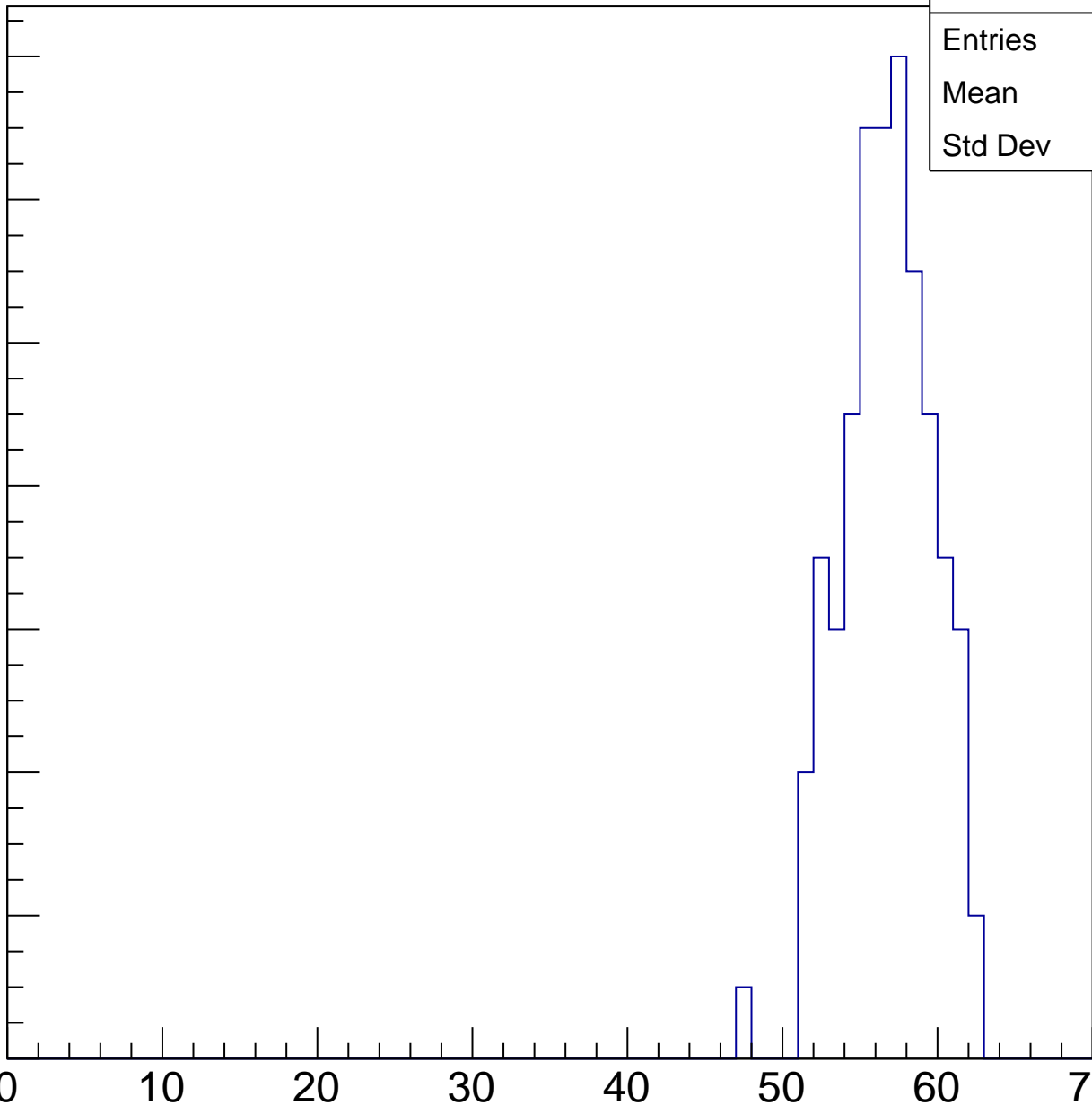
40

50

60

70

ampl



B0L101S, U14-ch43, adc5

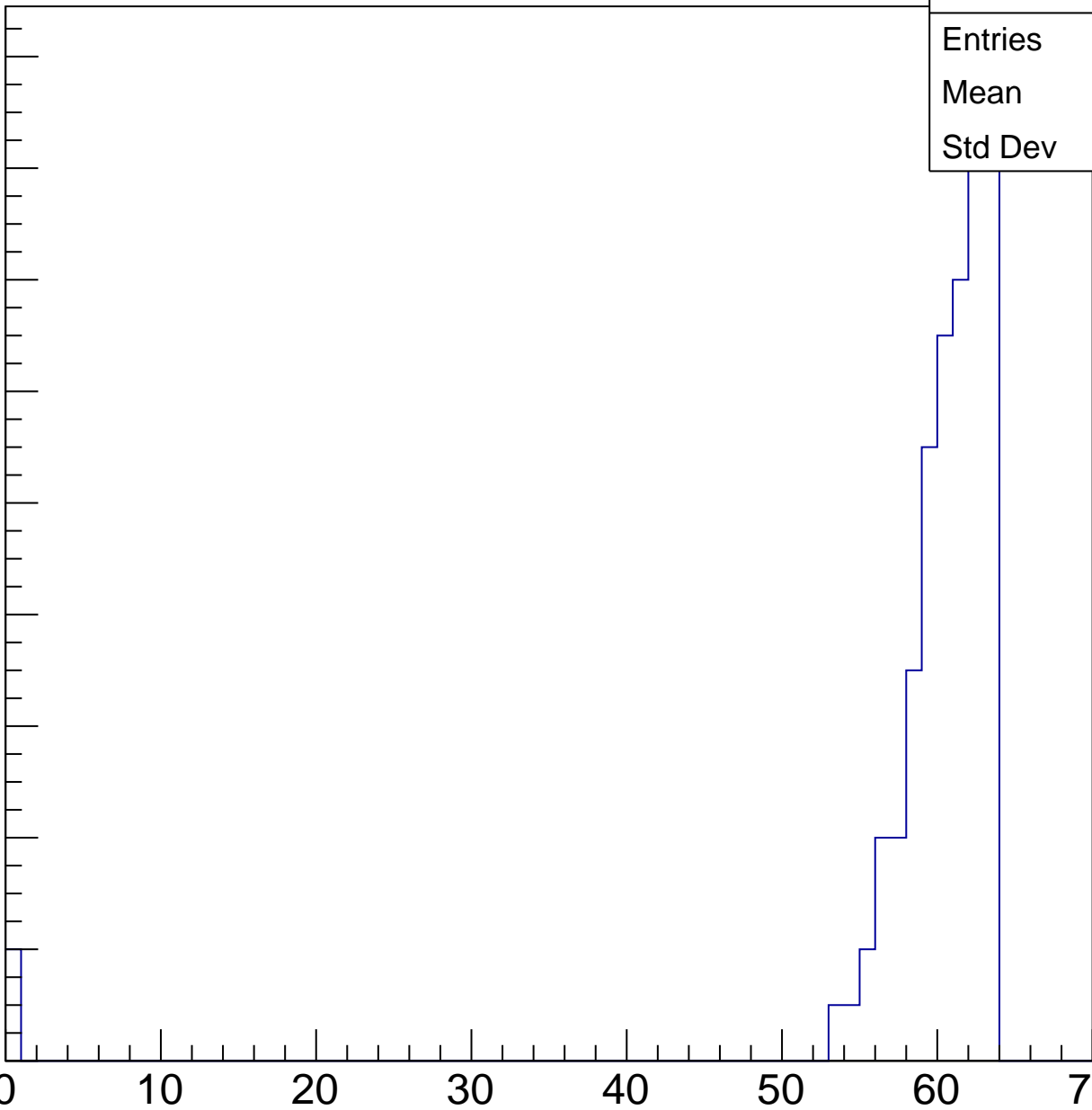
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.97
Std Dev	9.049

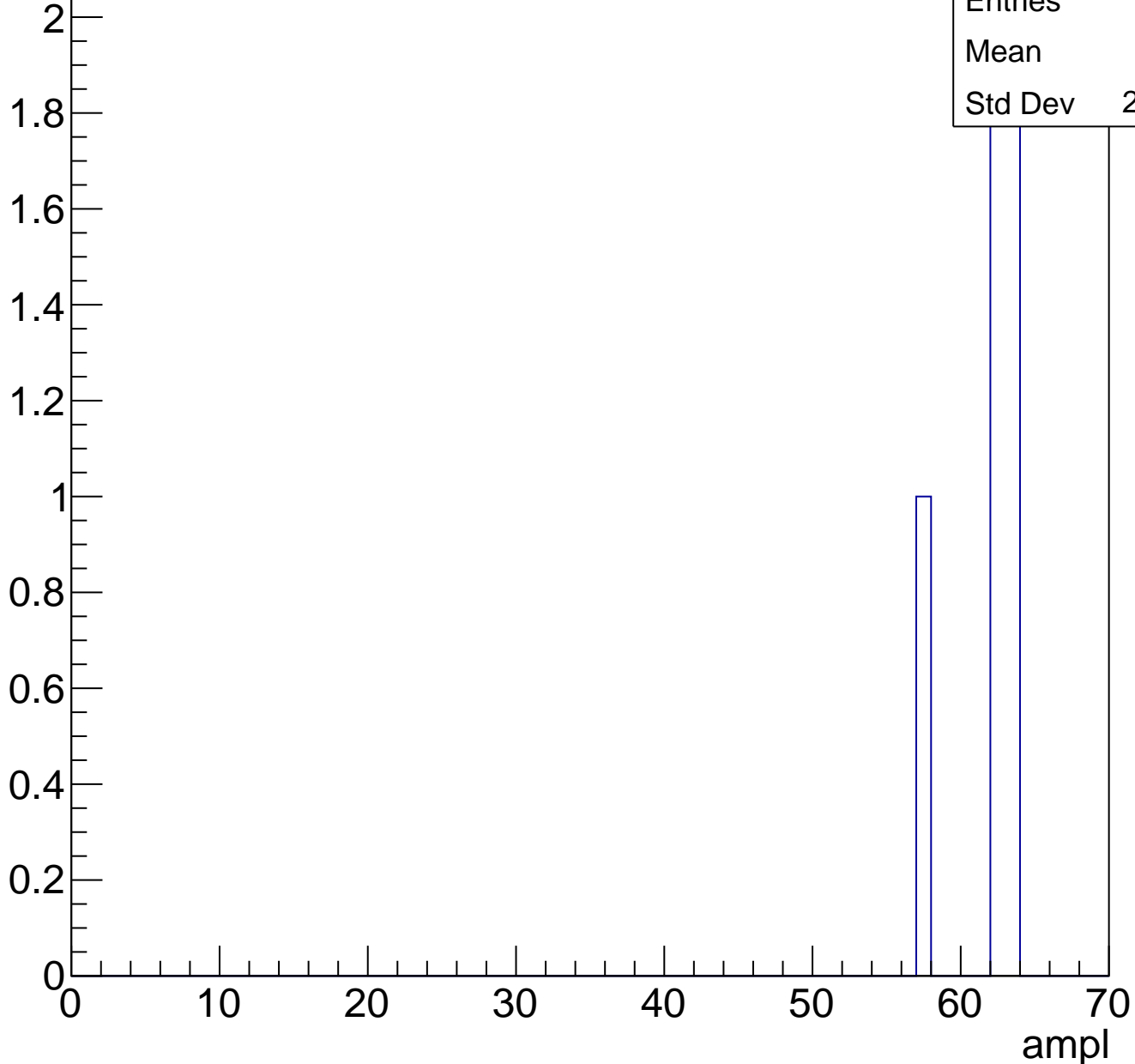
ampl



B0L101S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

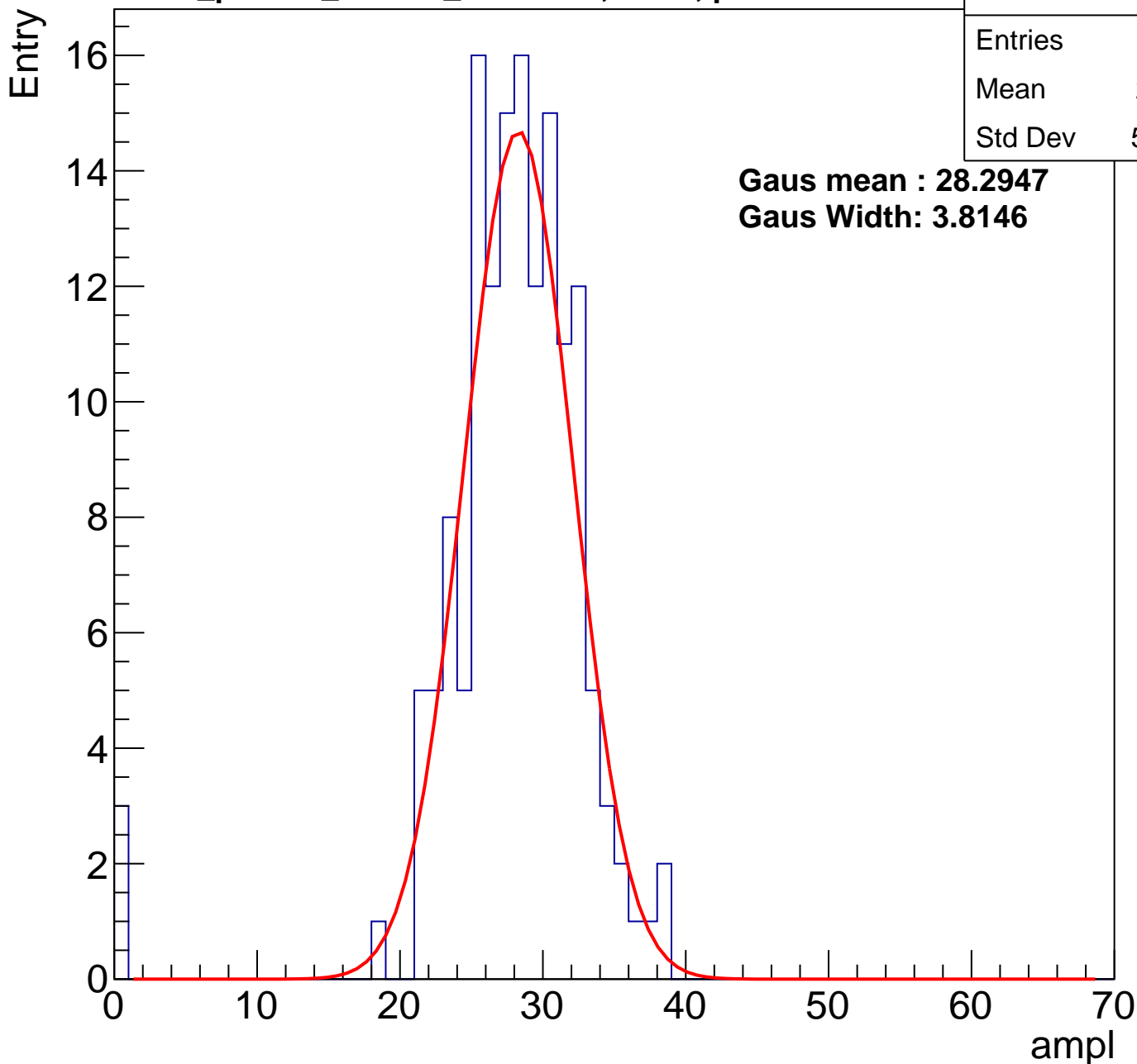
B0L101S, U14-ch44, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	27.41
Std Dev	5.385

Gaus mean : 28.2947

Gaus Width: 3.8146



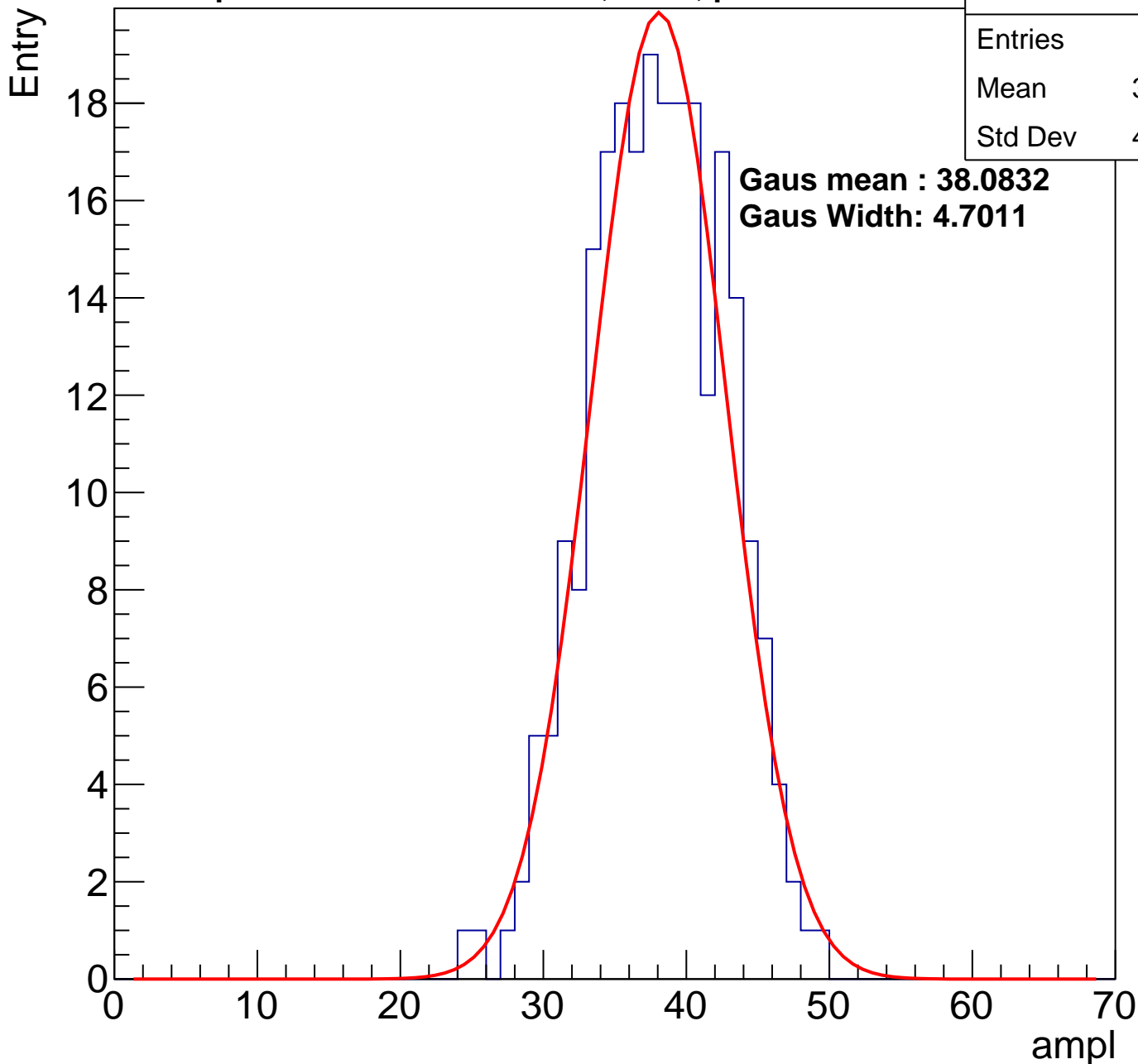
B0L101S, U14-ch44, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	239
Mean	37.57
Std Dev	4.628

Gaus mean : 38.0832

Gaus Width: 4.7011



B0L101S, U14-ch44, adc2

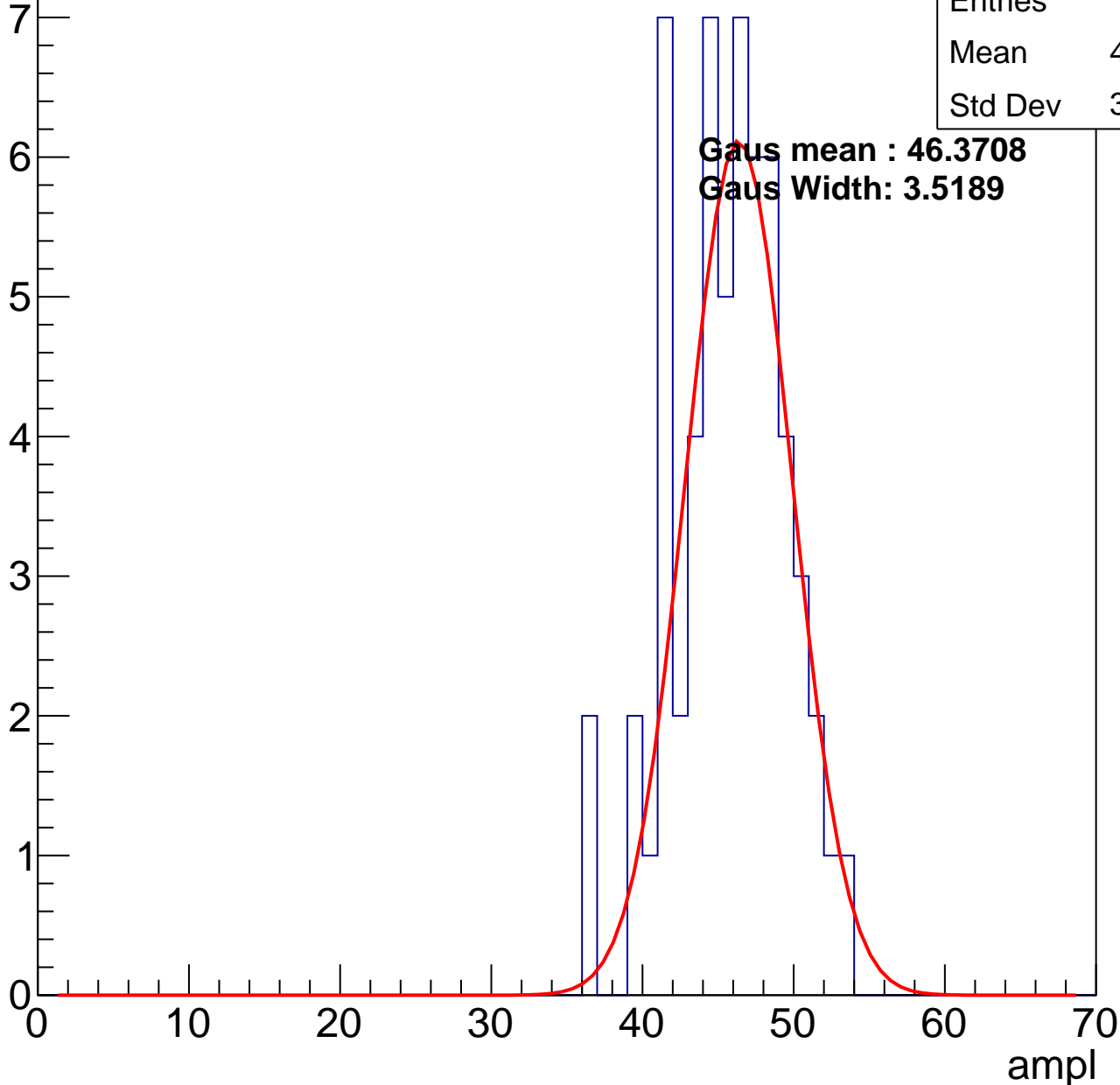
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	60
Mean	45.18
Std Dev	3.695

Gaus mean : 46.3708

Gaus Width: 3.5189



B0L101S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	105
Mean	48.96
Std Dev	3.366

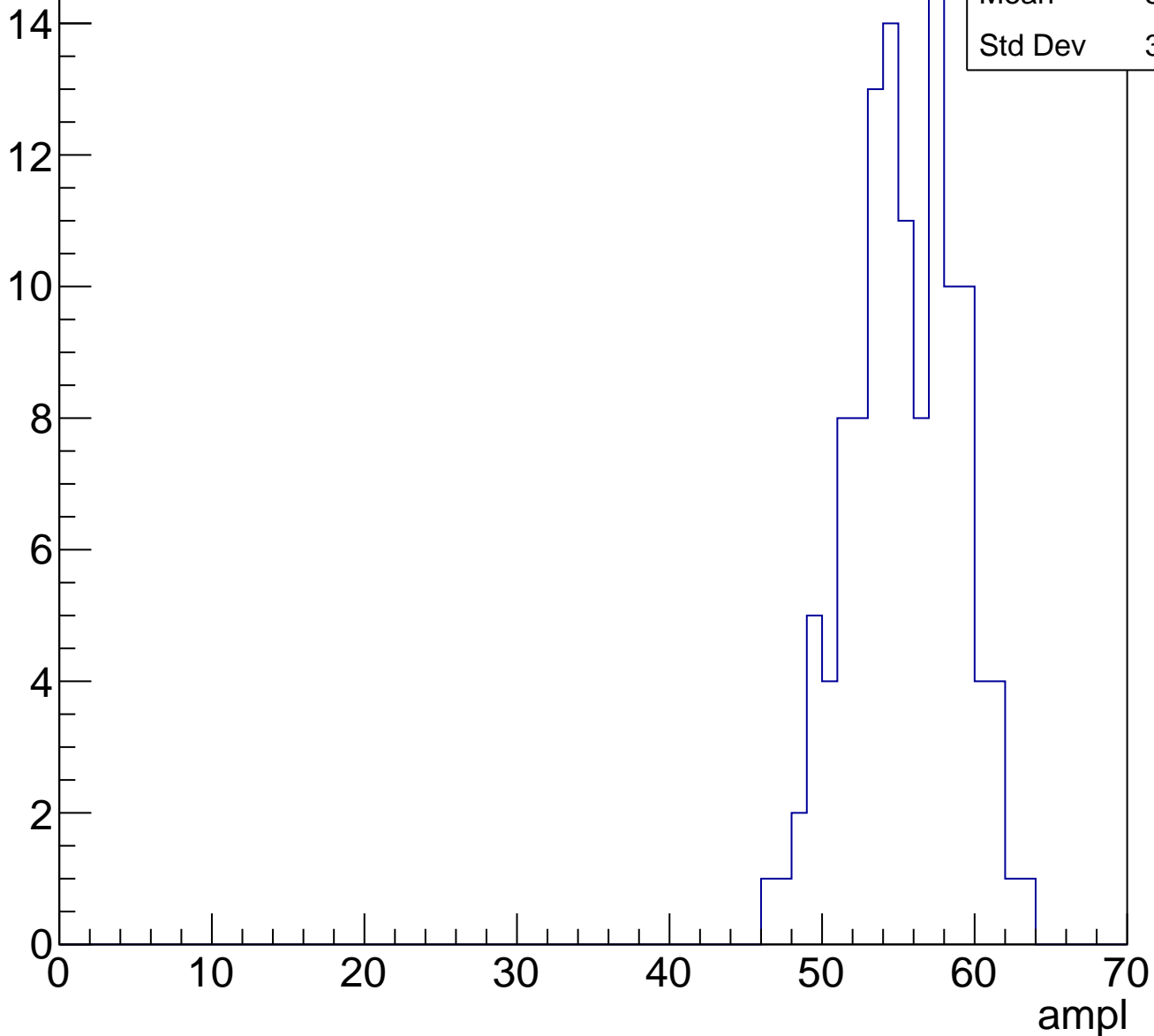
ampl

0 10 20 30 40 50 60 70

B0L101S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch44, adc5

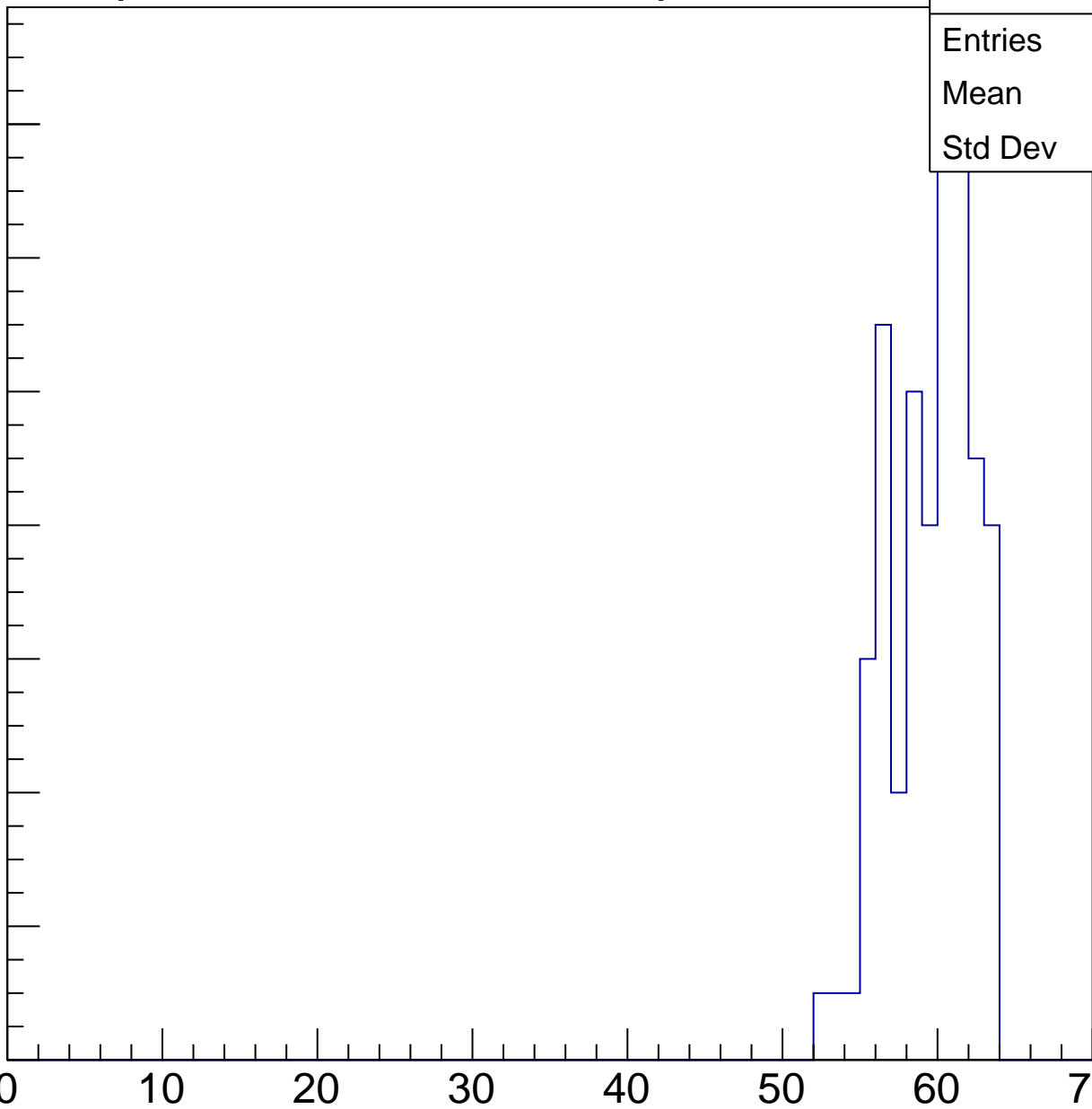
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	88
Mean	59.11
Std Dev	2.639

ampl

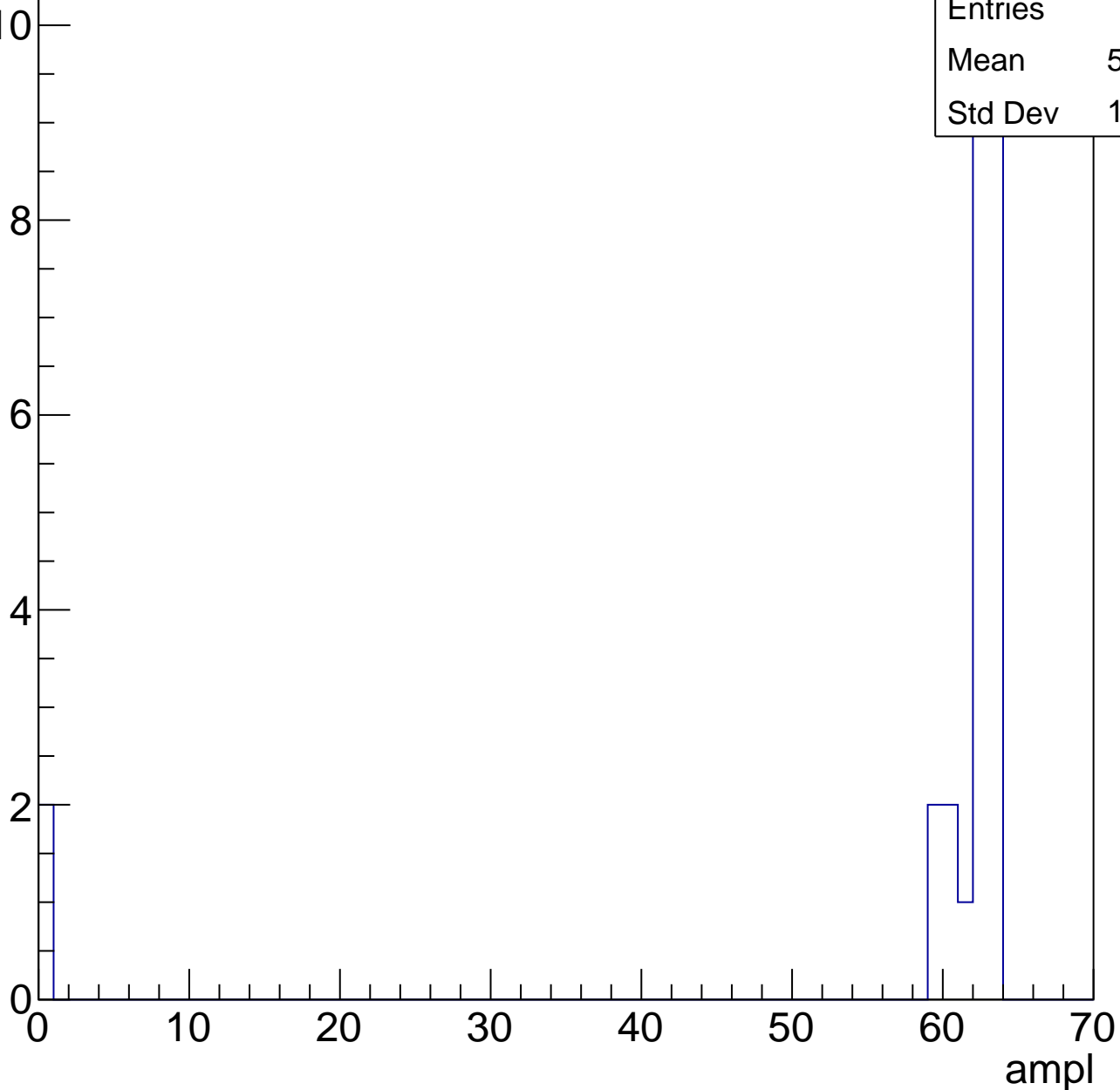


B0L101S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

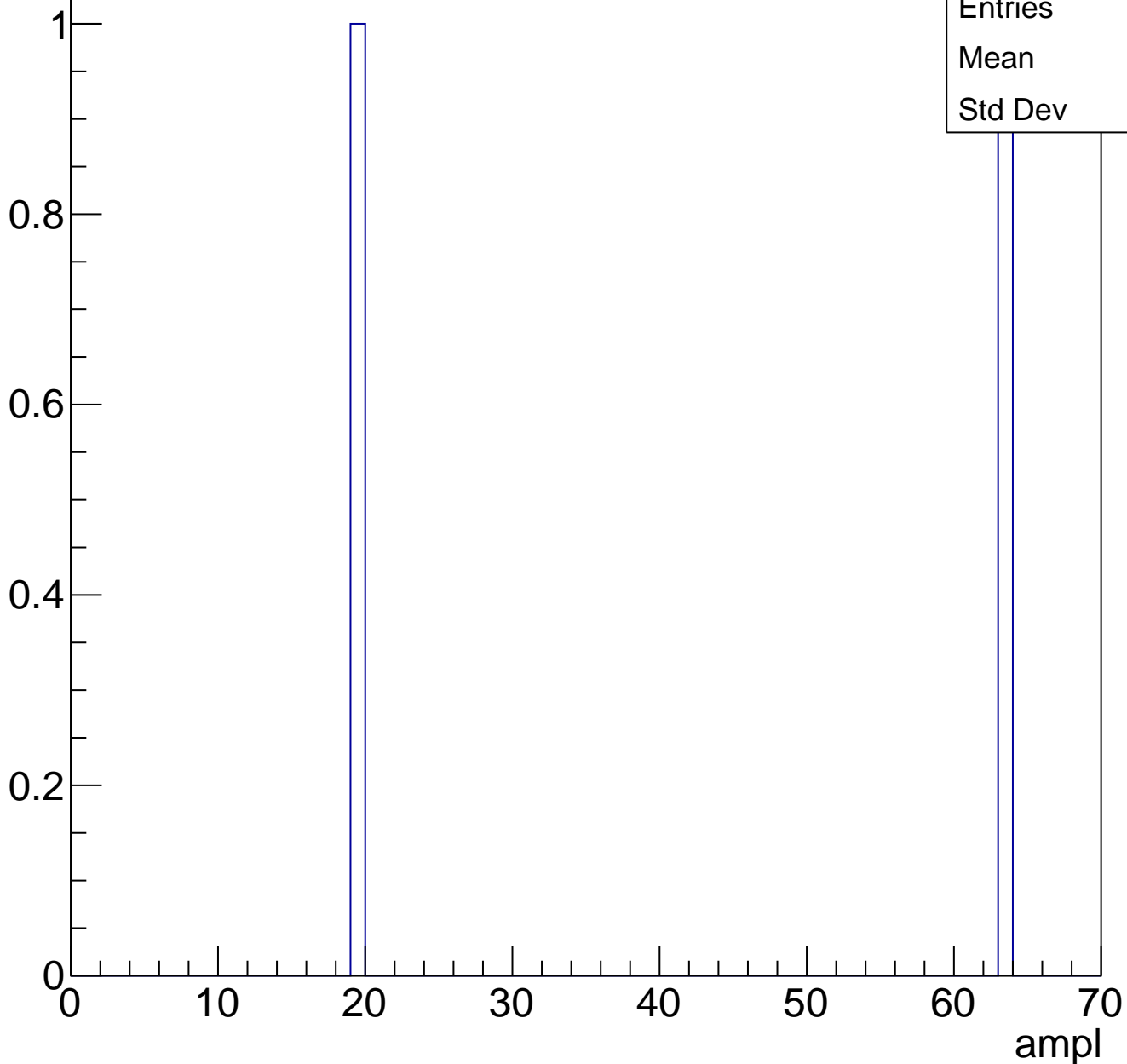
Entries	27
Mean	57.37
Std Dev	16.27



B0L101S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch45, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	31.02
Std Dev	3.544

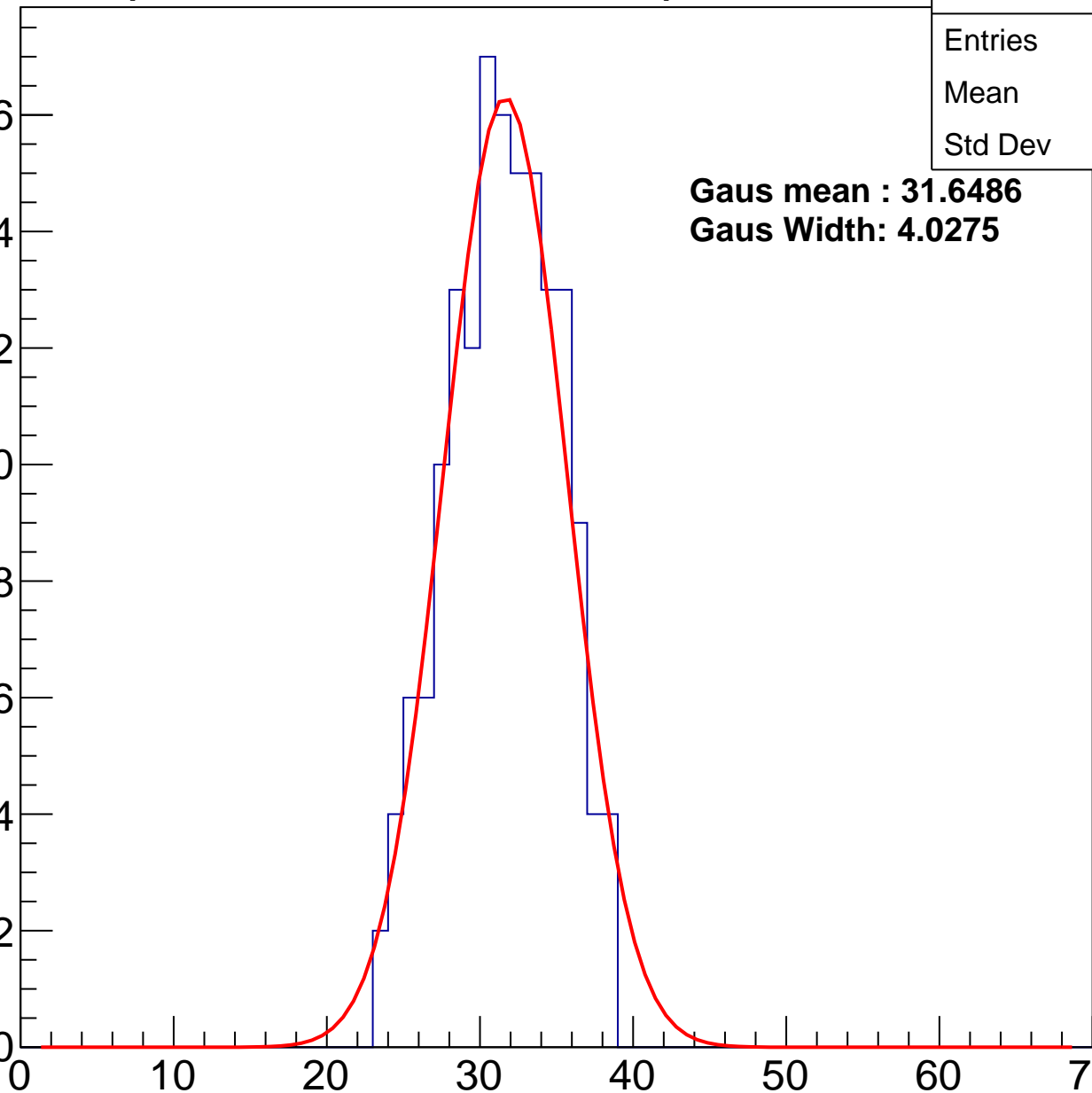
Gaus mean : 31.6486

Gaus Width: 4.0275

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch45, adc1

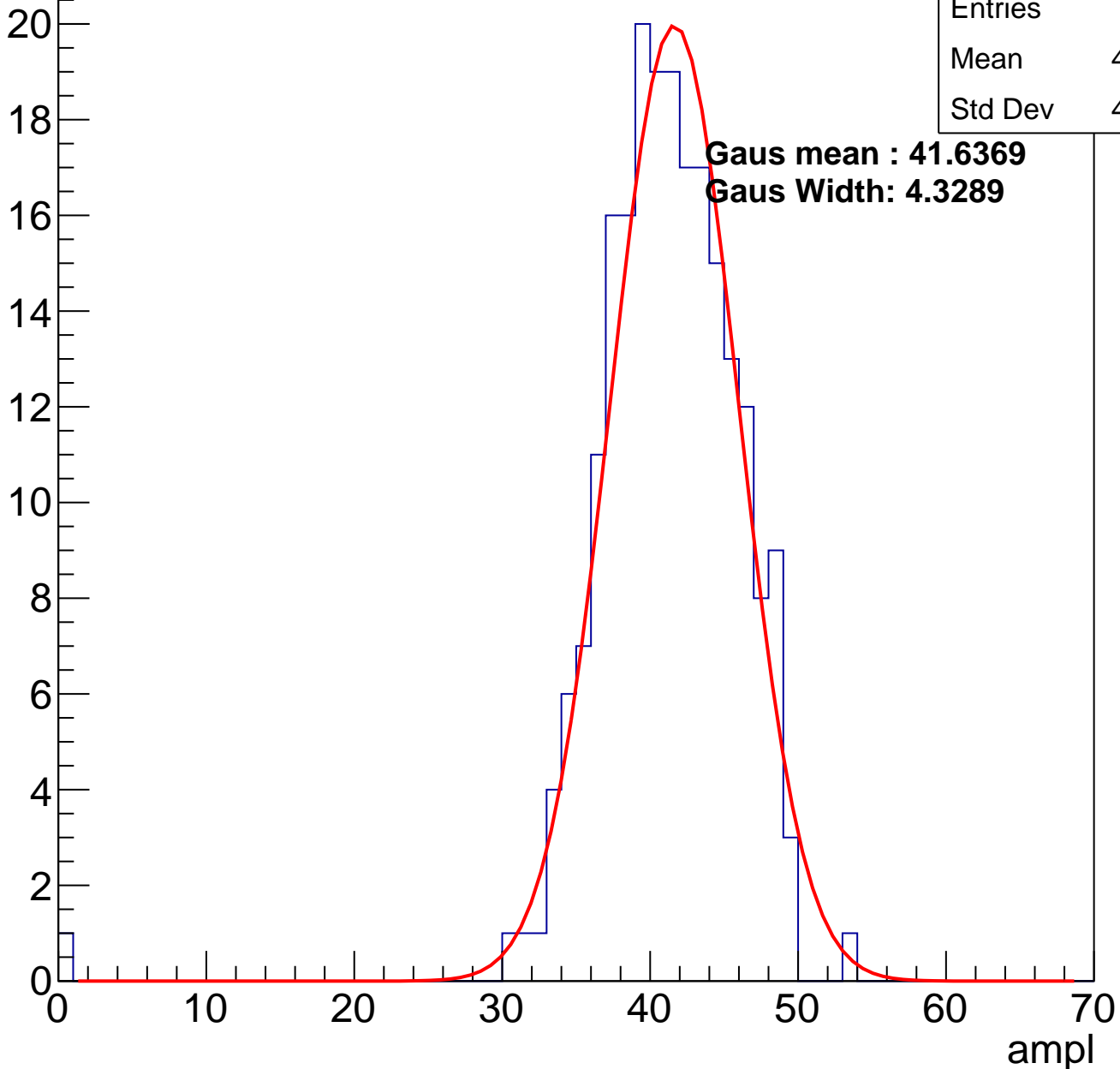
calib_packv5_042523_0143.root, FC#1, port C1

Entries	217
Mean	40.73
Std Dev	4.959

Gaus mean : 41.6369

Gaus Width: 4.3289

Entry



B0L101S, U14-ch45, adc2

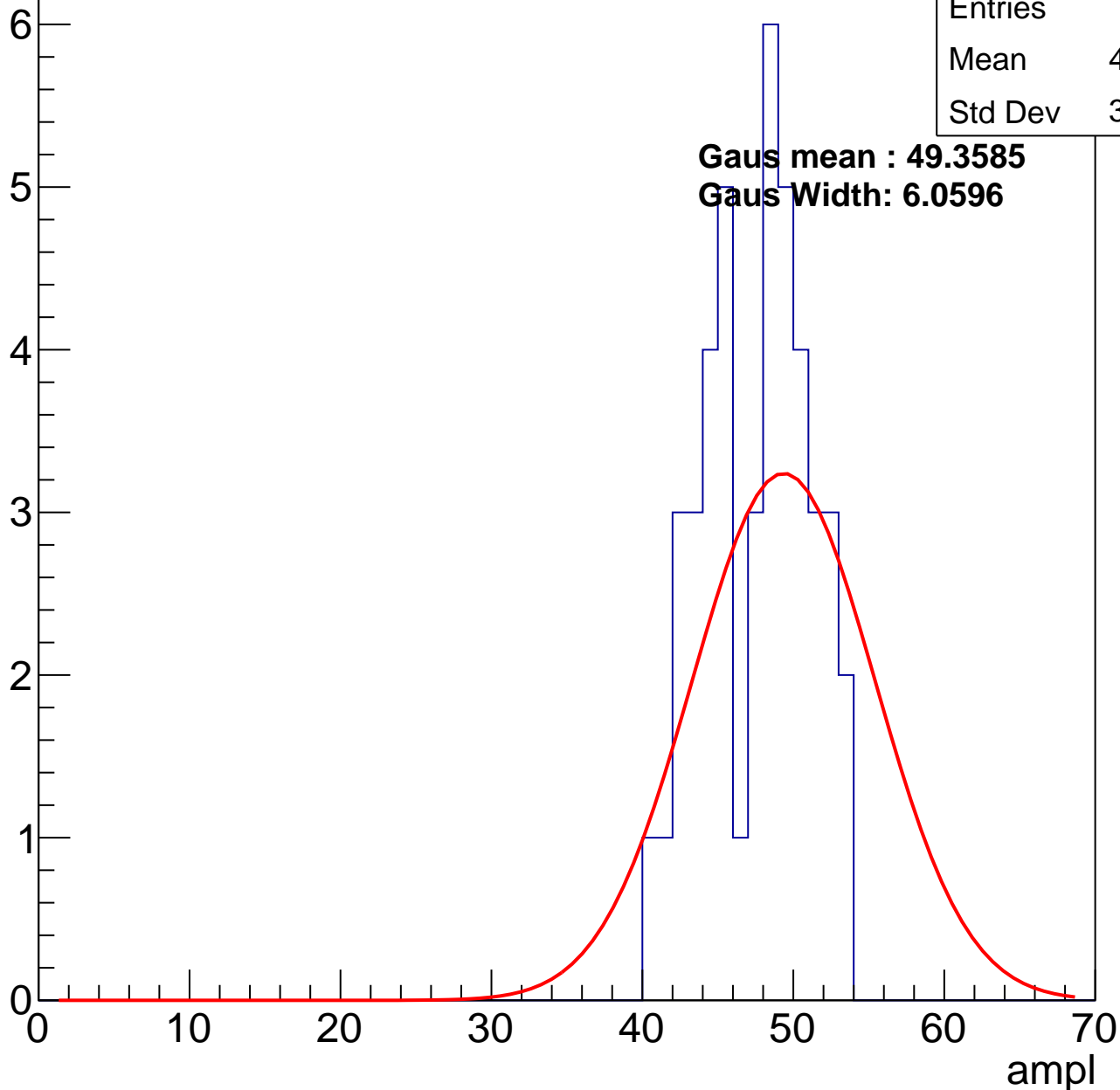
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	44
Mean	47.09
Std Dev	3.443

Gaus mean : 49.3585

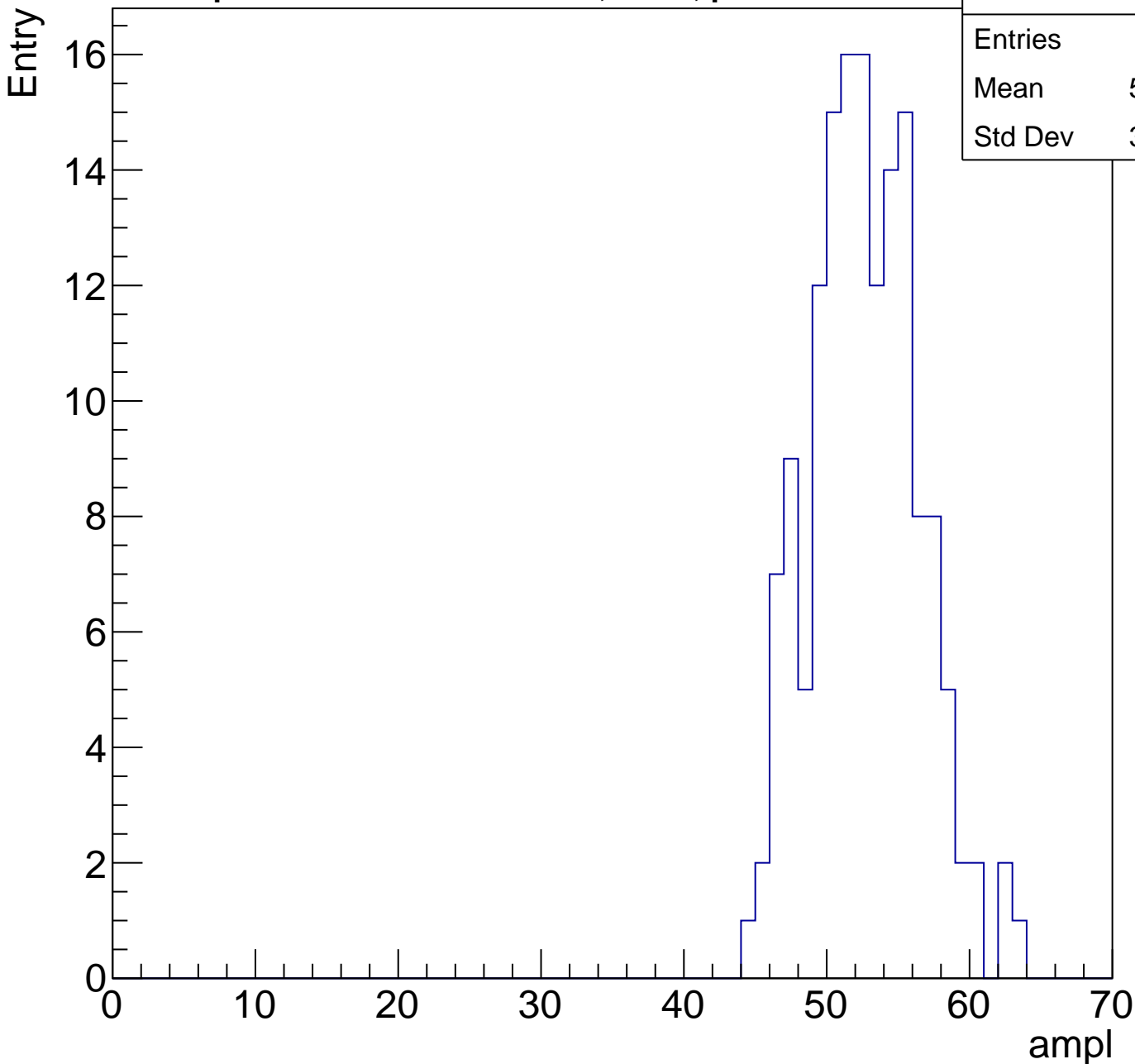
Gaus Width: 6.0596



B0L101S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	52.24
Std Dev	3.763



B0L101S, U14-ch45, adc4

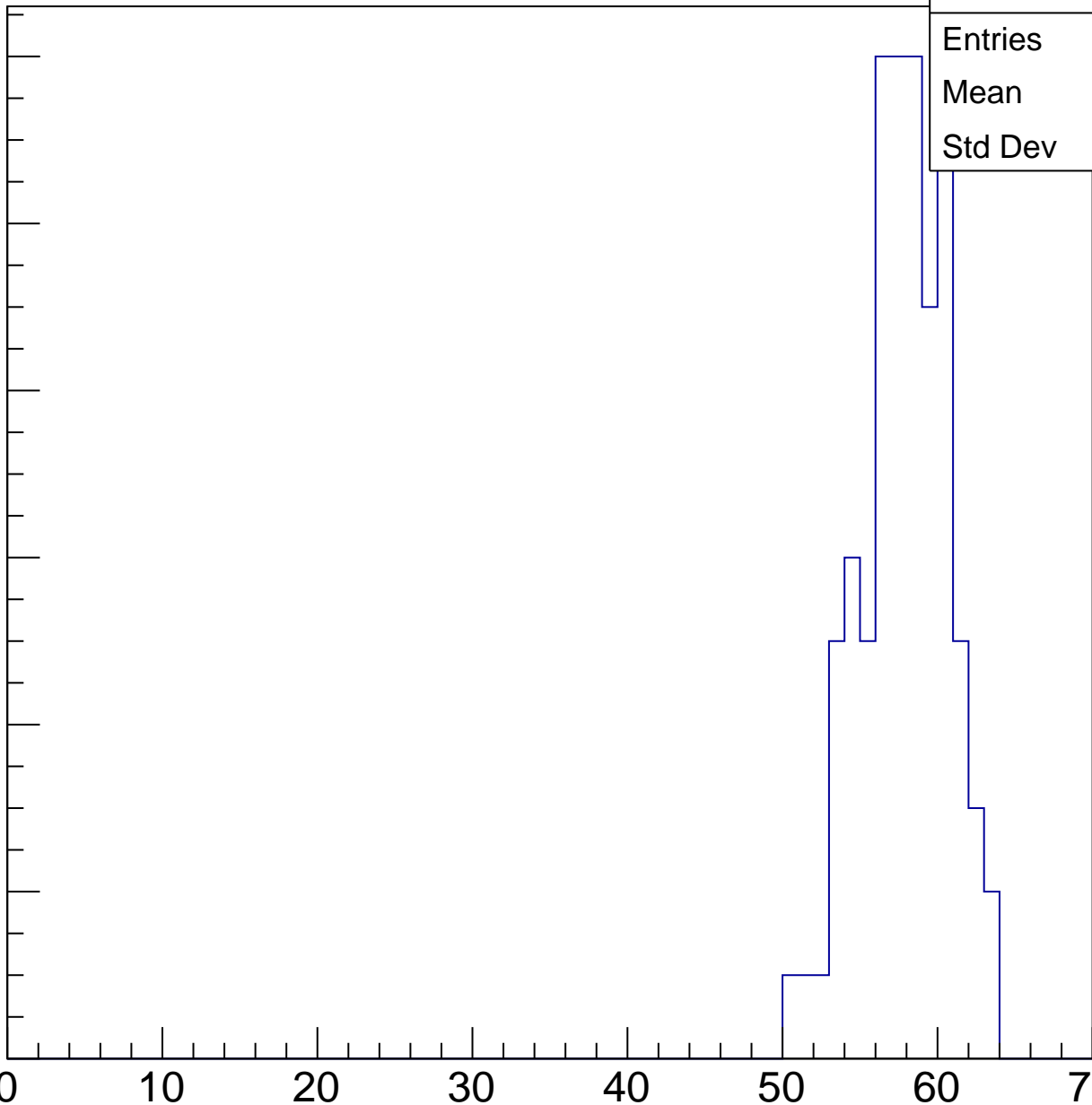
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	85
Mean	57.38
Std Dev	2.74

ampl



B0L101S, U14-ch45, adc5

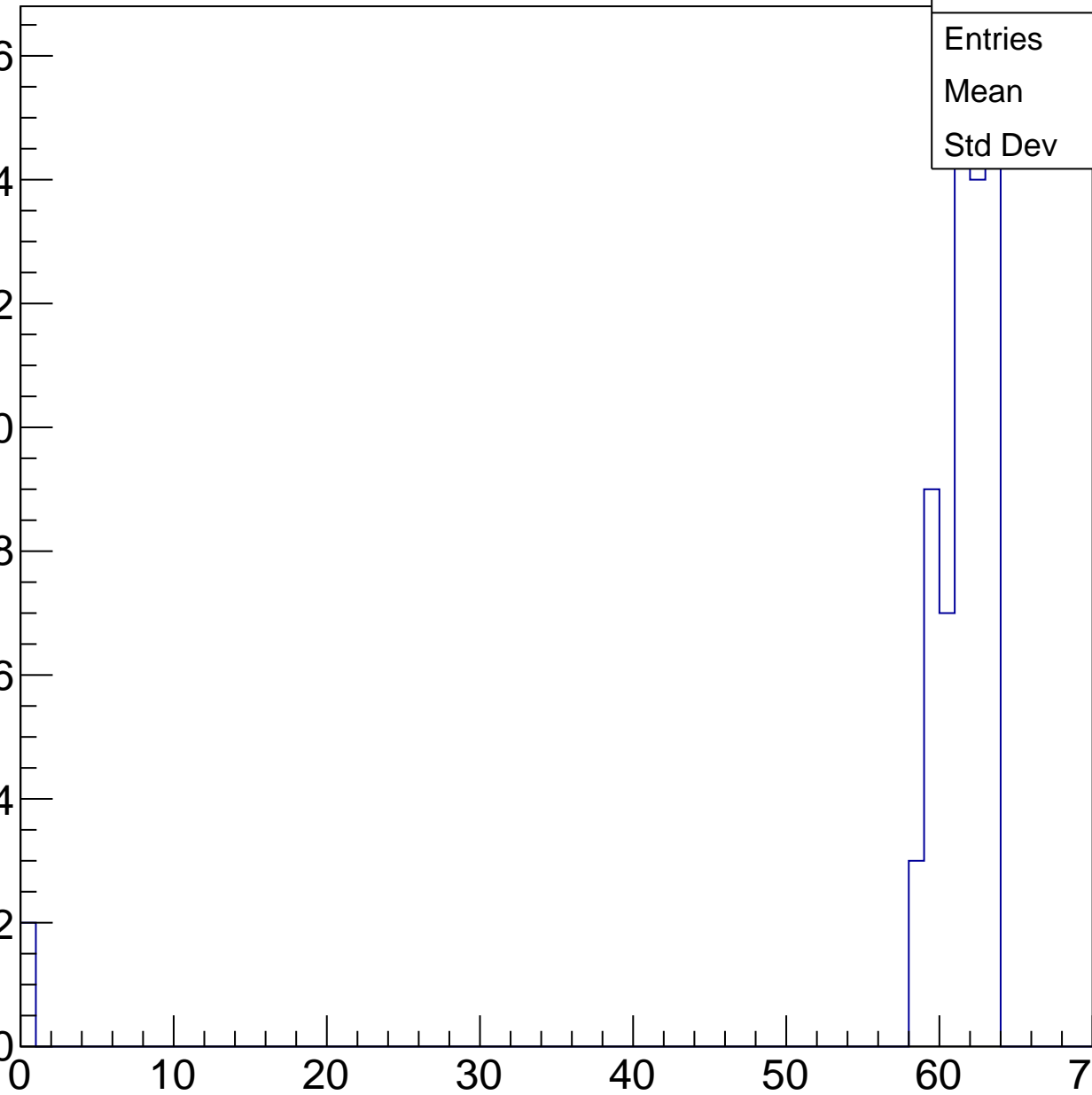
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	66
Mean	59.33
Std Dev	10.59

ampl



B0L101S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch46, adc0

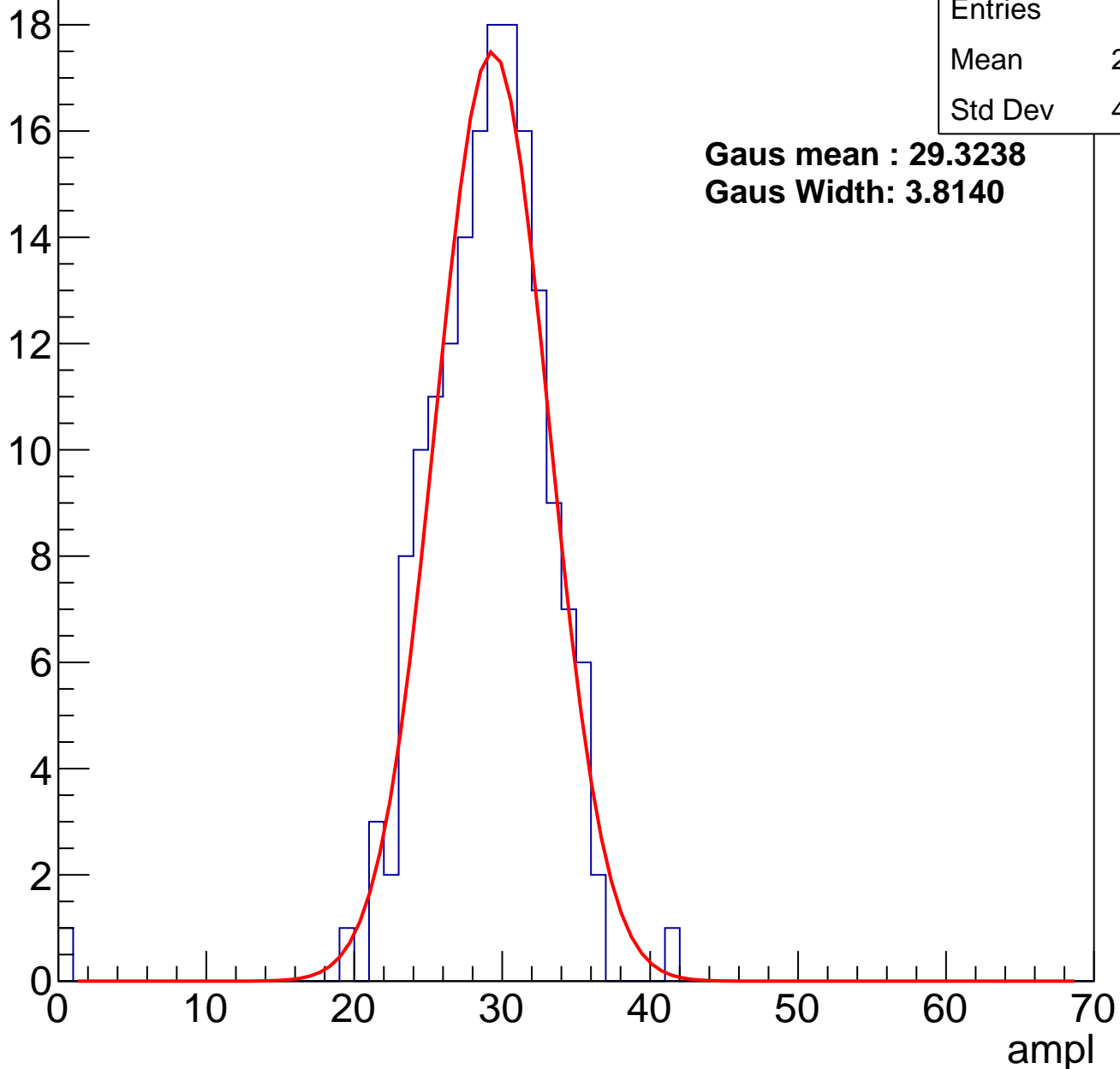
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	28.54
Std Dev	4.272

Gaus mean : 29.3238

Gaus Width: 3.8140

Entry



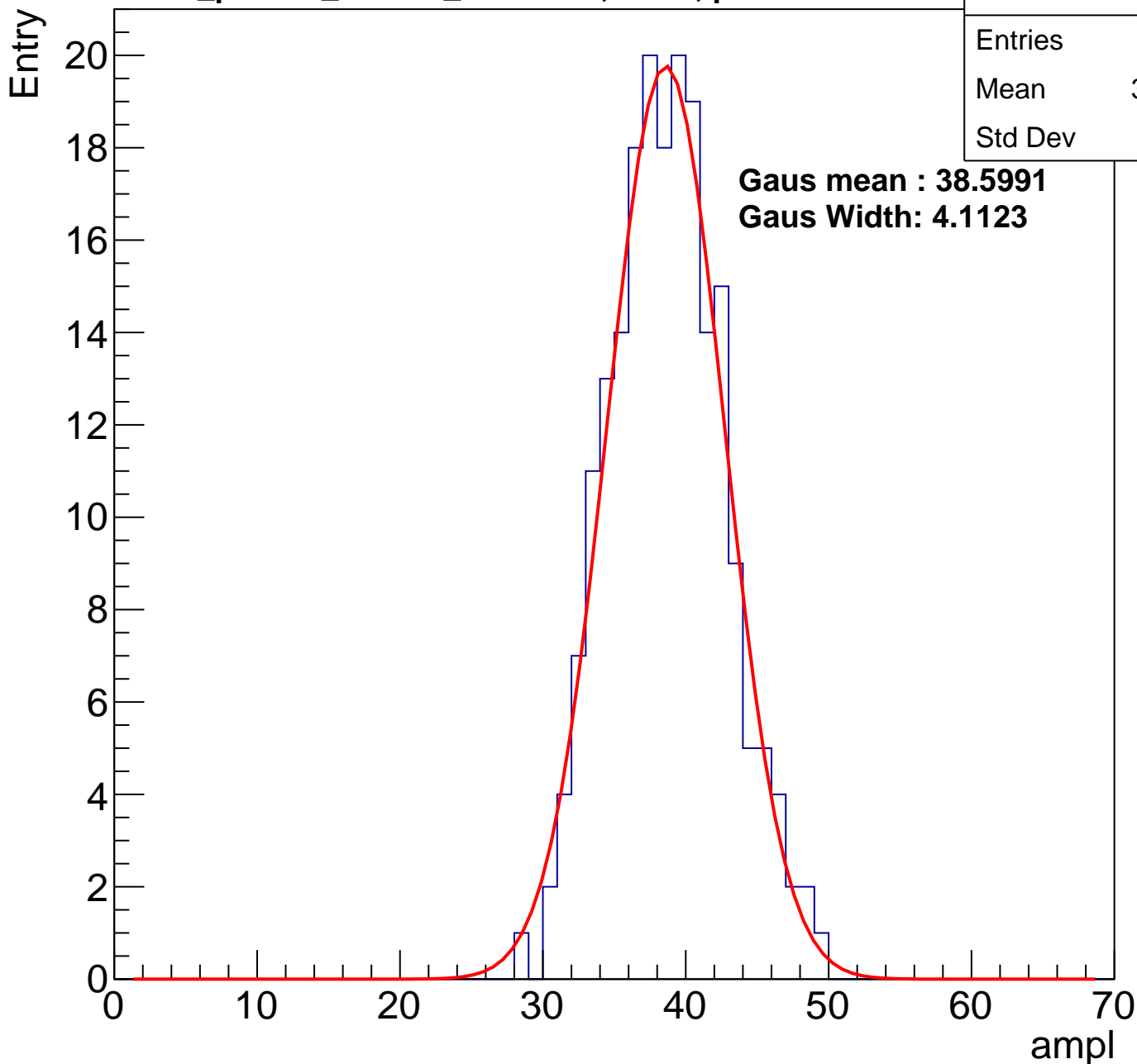
B0L101S, U14-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	204
Mean	38.25
Std Dev	3.97

Gaus mean : 38.5991

Gaus Width: 4.1123



B0L101S, U14-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	53
Mean	44.83
Std Dev	2.925

Gaus mean : 44.9135

Gaus Width: 2.1763

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

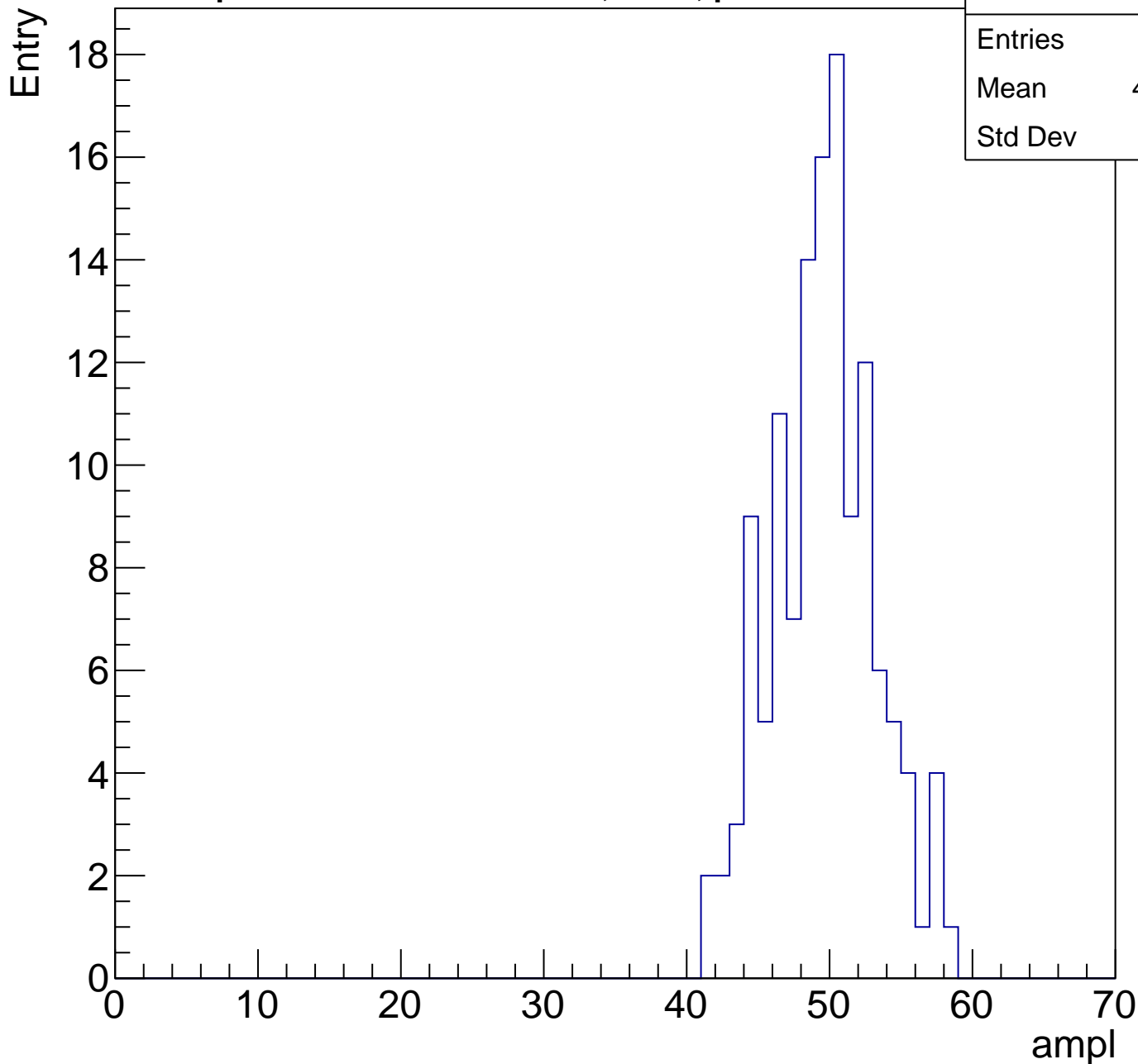
8

10

B0L101S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	49.15
Std Dev	3.62



B0L101S, U14-ch46, adc4

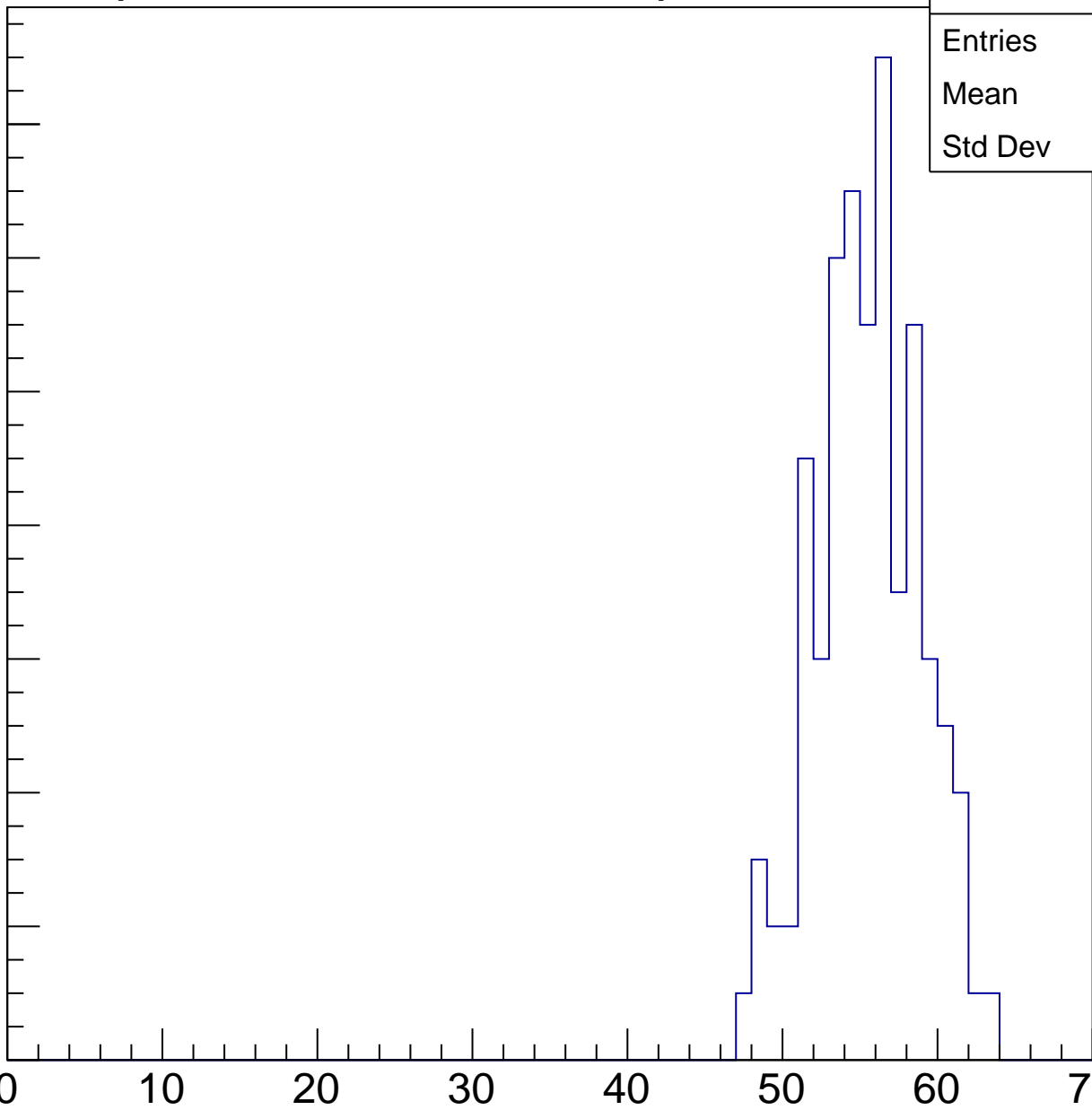
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	55.07
Std Dev	3.334

ampl



B0L101S, U14-ch46, adc5

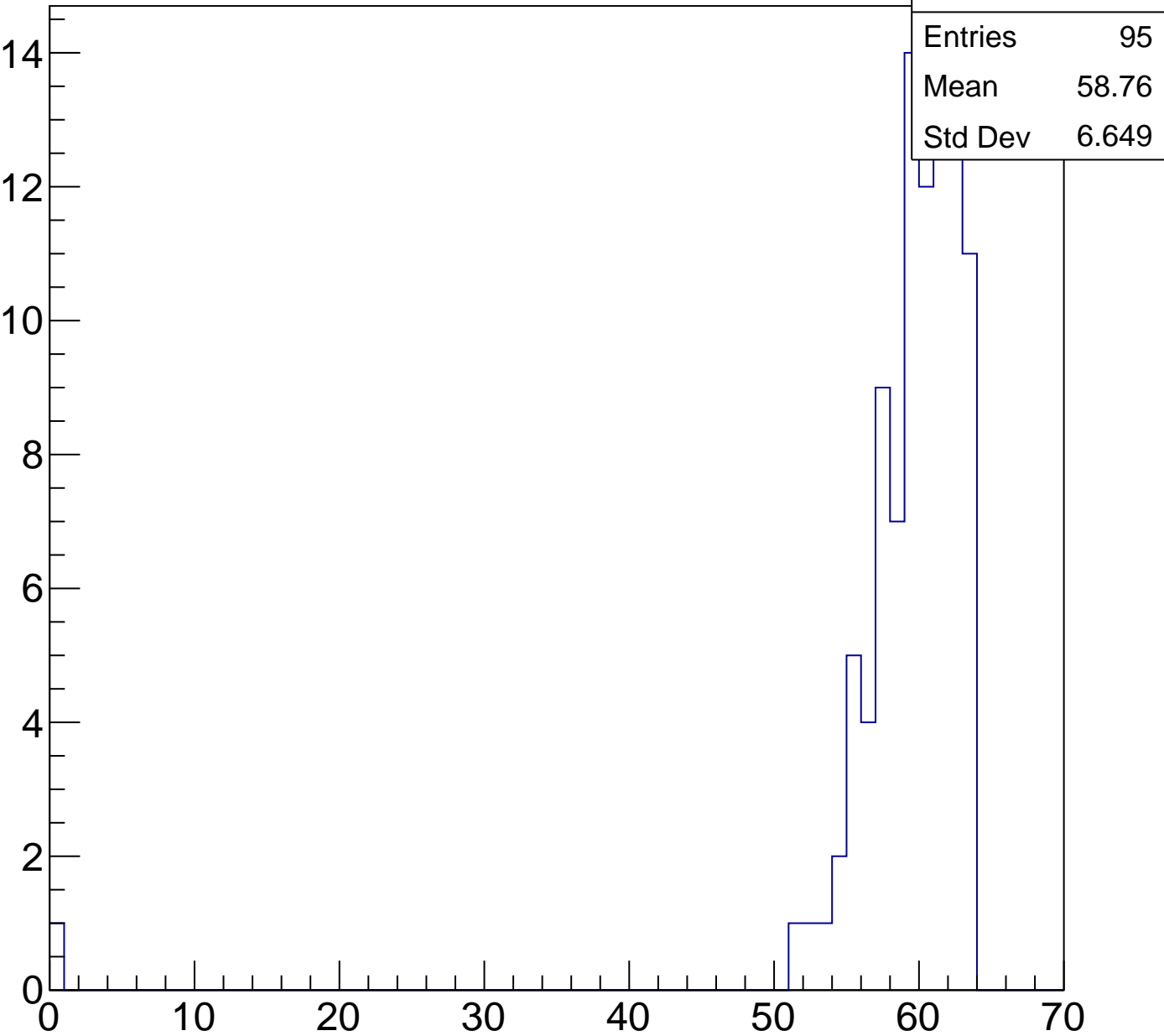
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.76
Std Dev	6.649

ampl

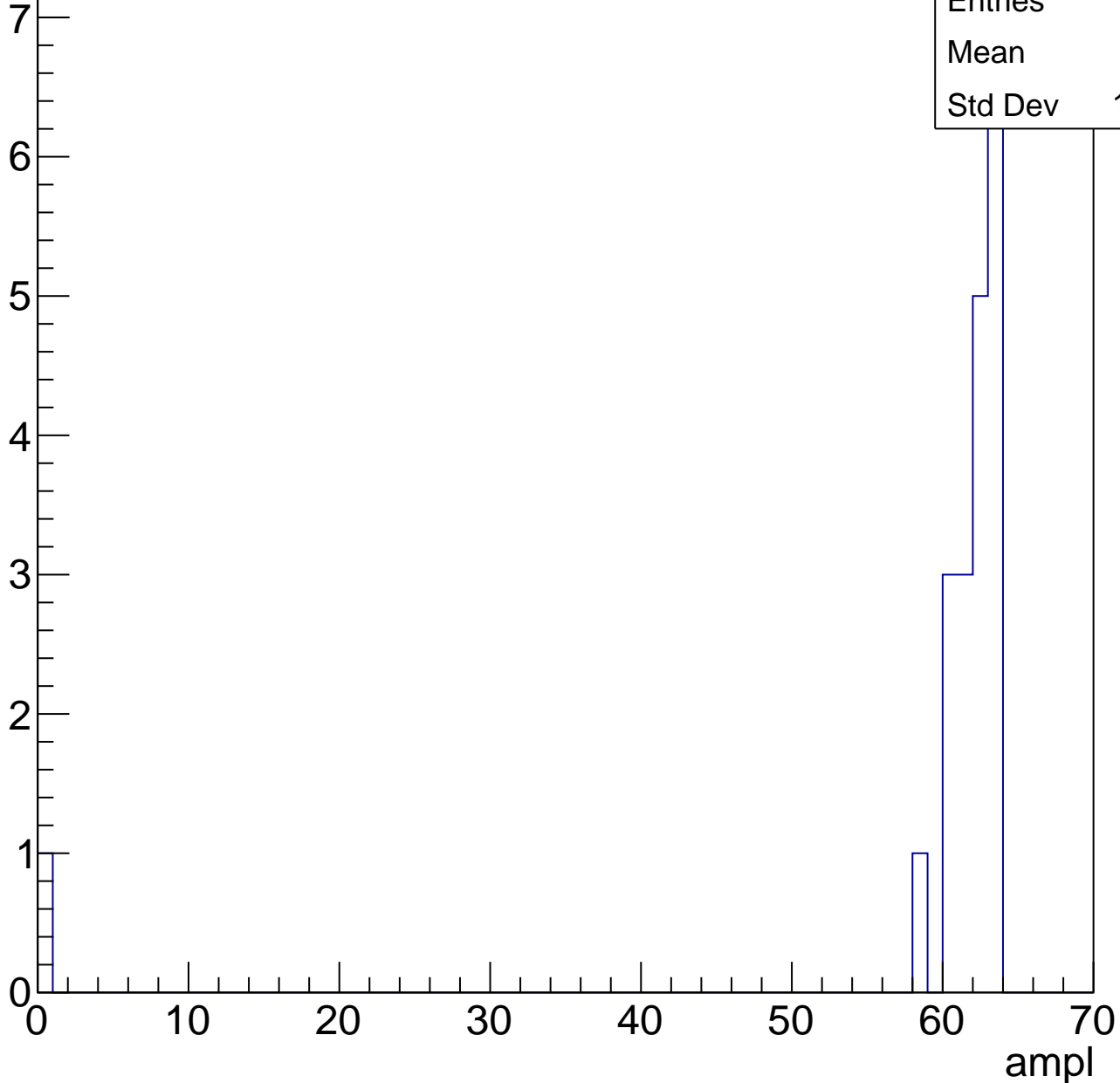


B0L101S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

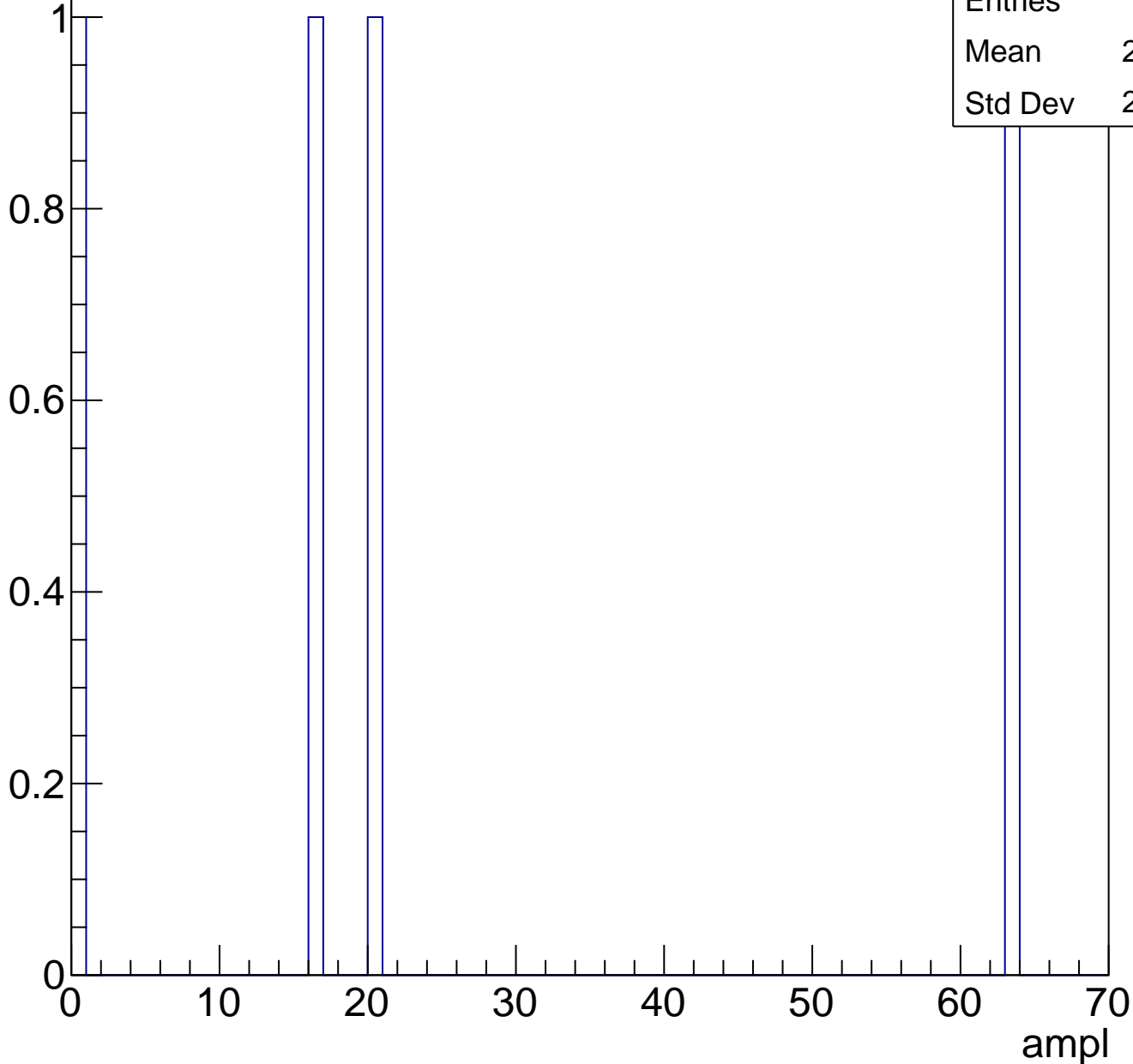
Entries	20
Mean	58.6
Std Dev	13.51



B0L101S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch47, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	29.06
Std Dev	4.204

Gaus mean : 29.6531

Gaus Width: 3.1011

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

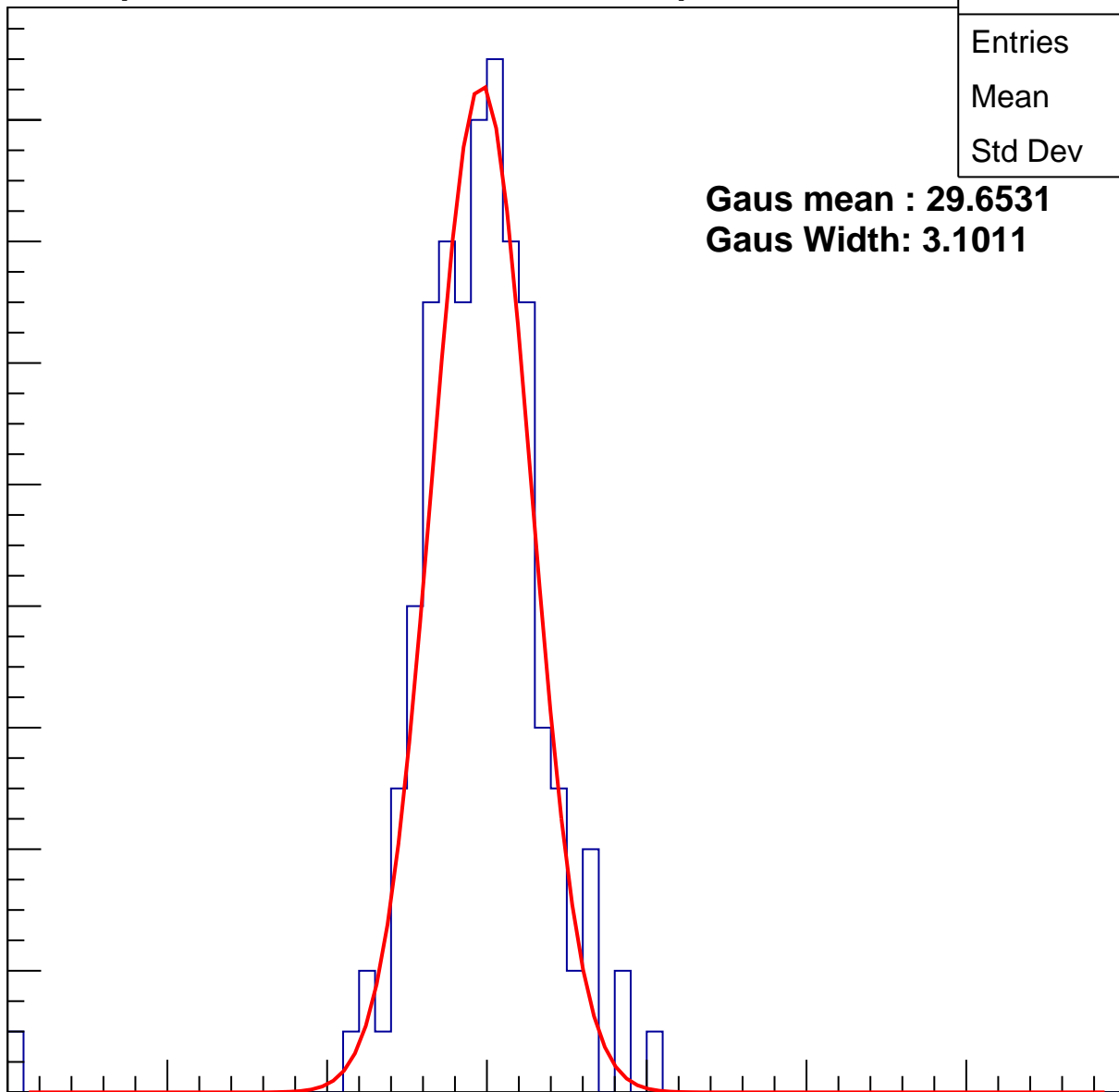
40

50

60

70

ampl



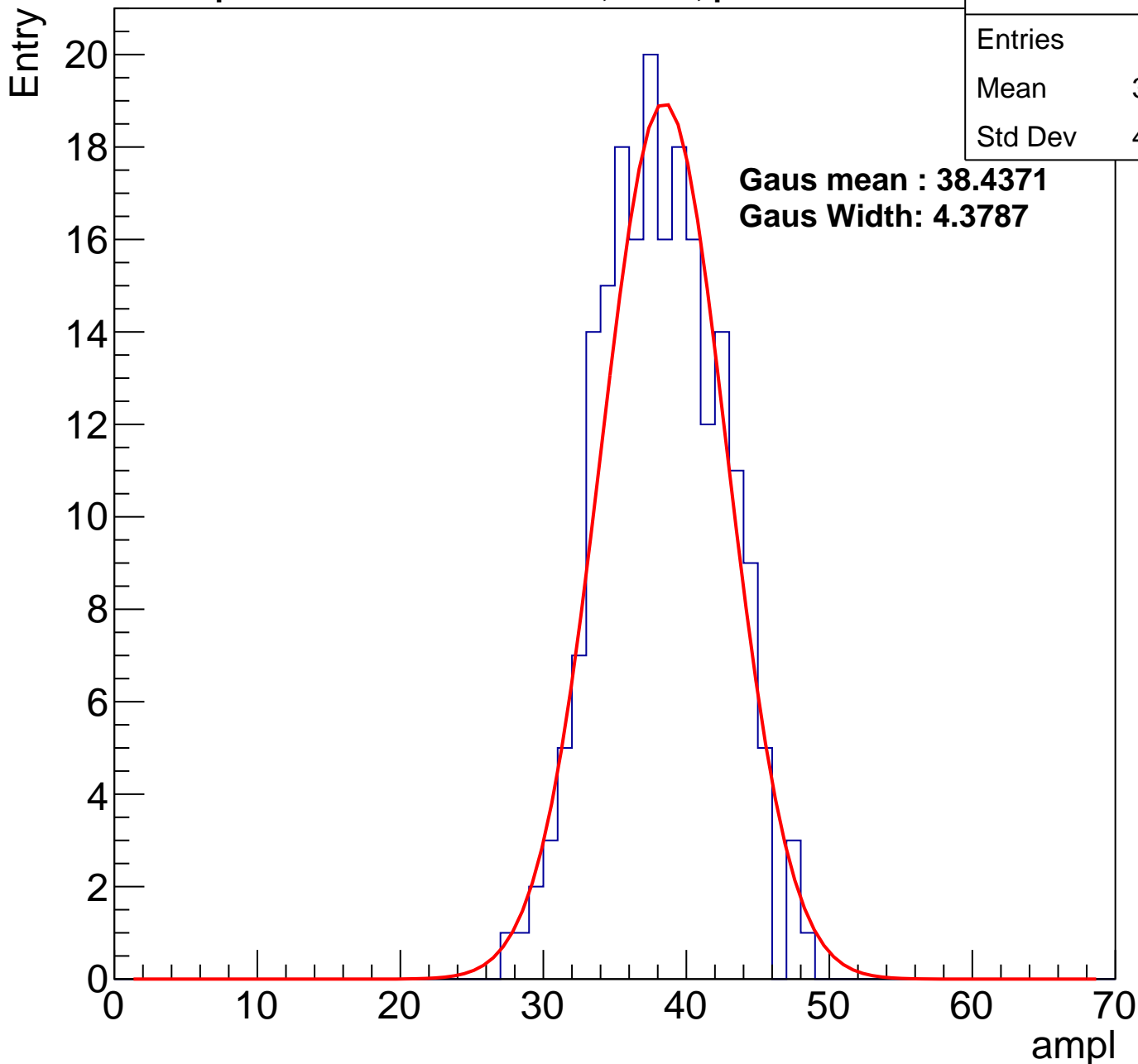
B0L101S, U14-ch47, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	207
Mean	37.74
Std Dev	4.109

Gaus mean : 38.4371

Gaus Width: 4.3787



B0L101S, U14-ch47, adc2

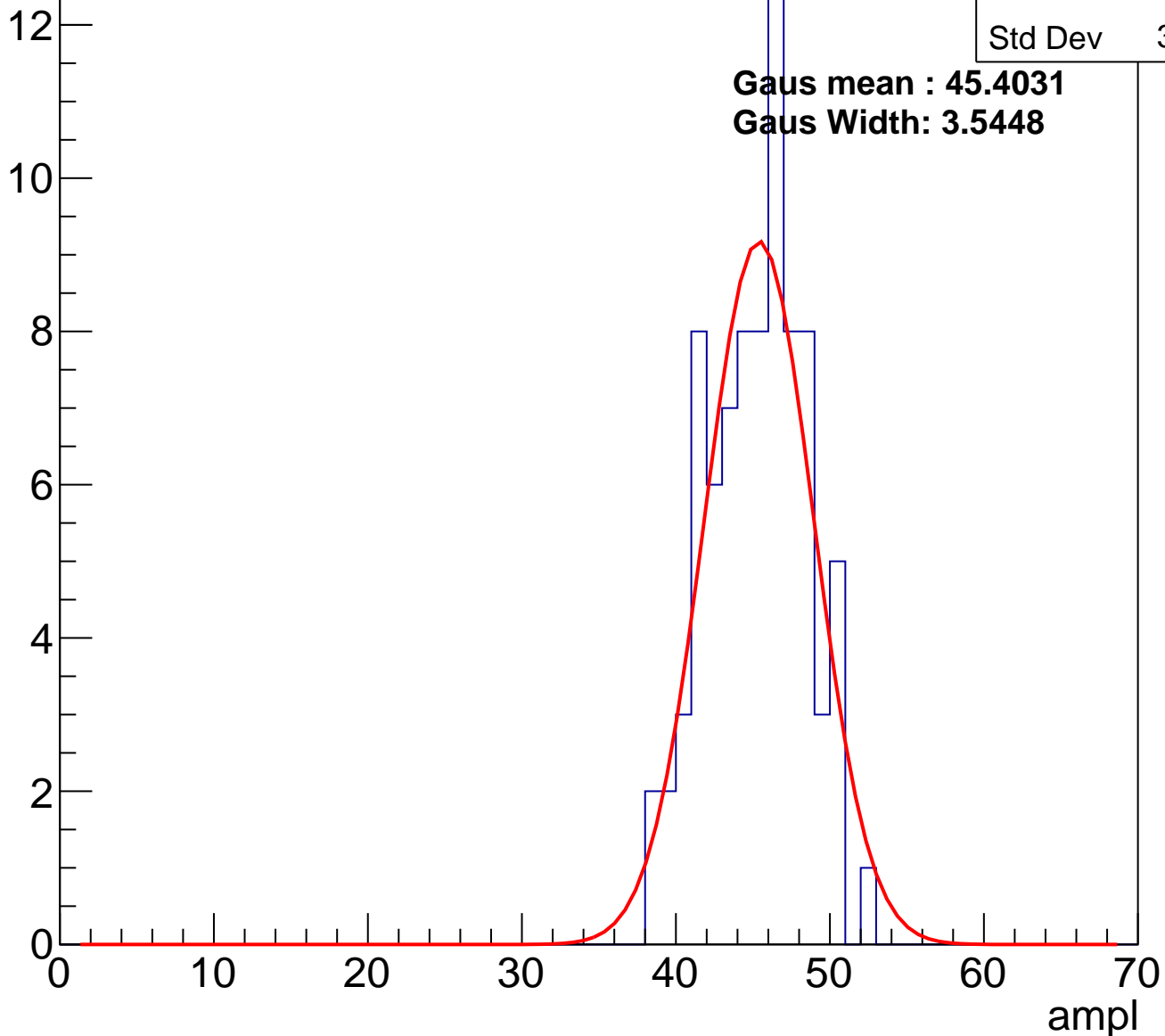
calib_packv5_042523_0143.root, FC#1, port C1

Entries	82
Mean	44.8
Std Dev	3.121

Gaus mean : 45.4031

Gaus Width: 3.5448

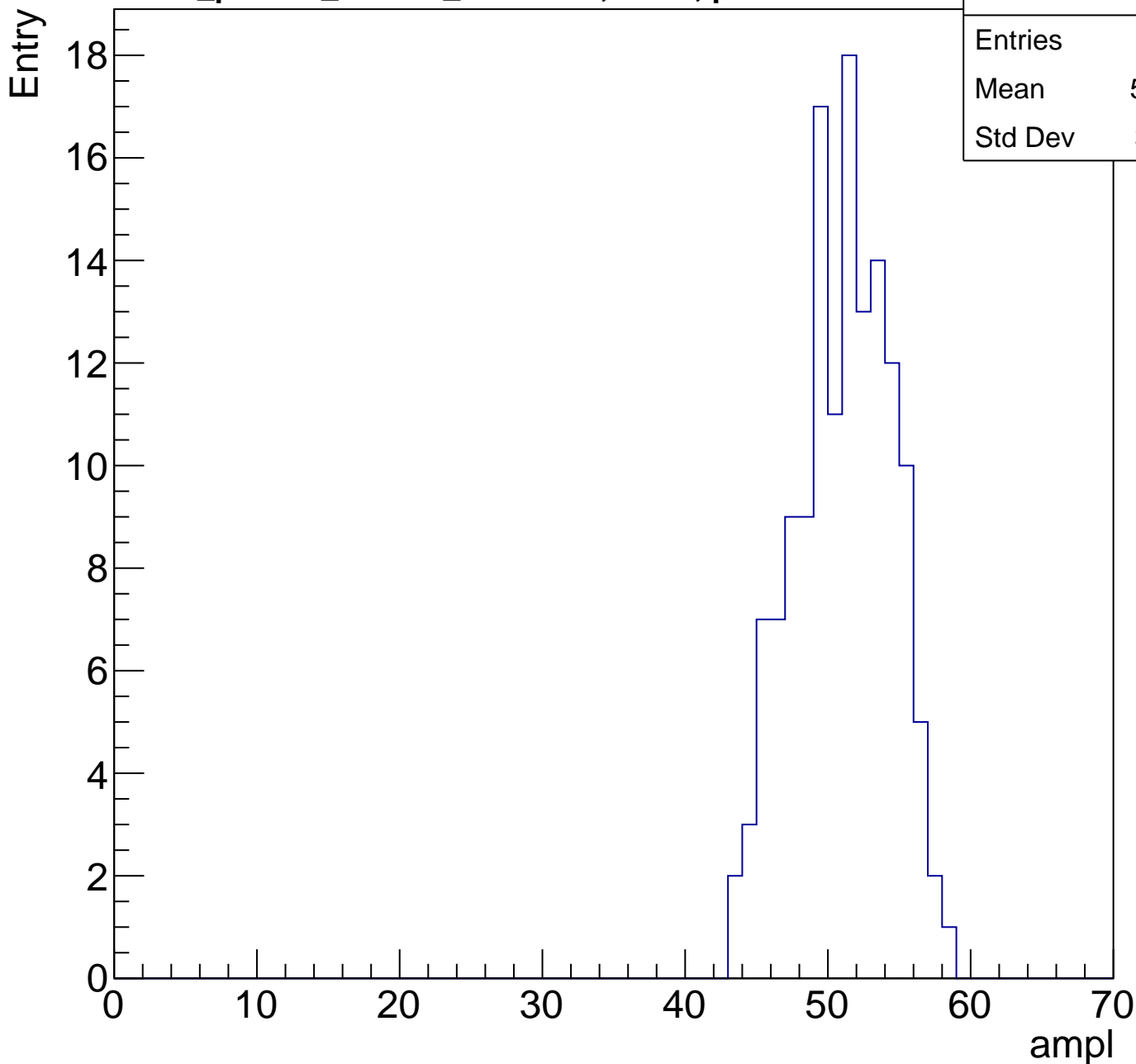
Entry



B0L101S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

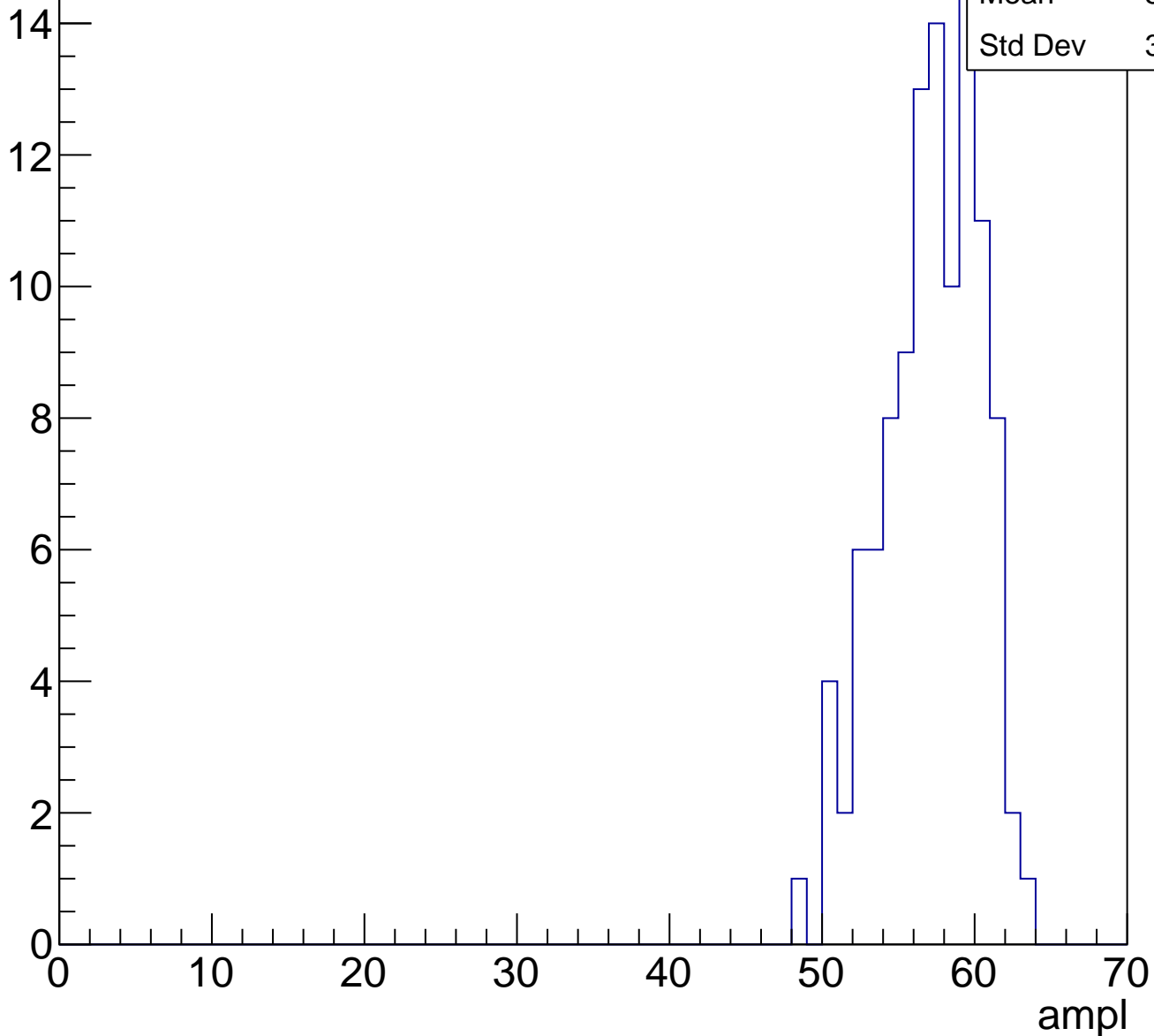
Entries	140
Mean	50.56
Std Dev	3.341



B0L101S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch47, adc5

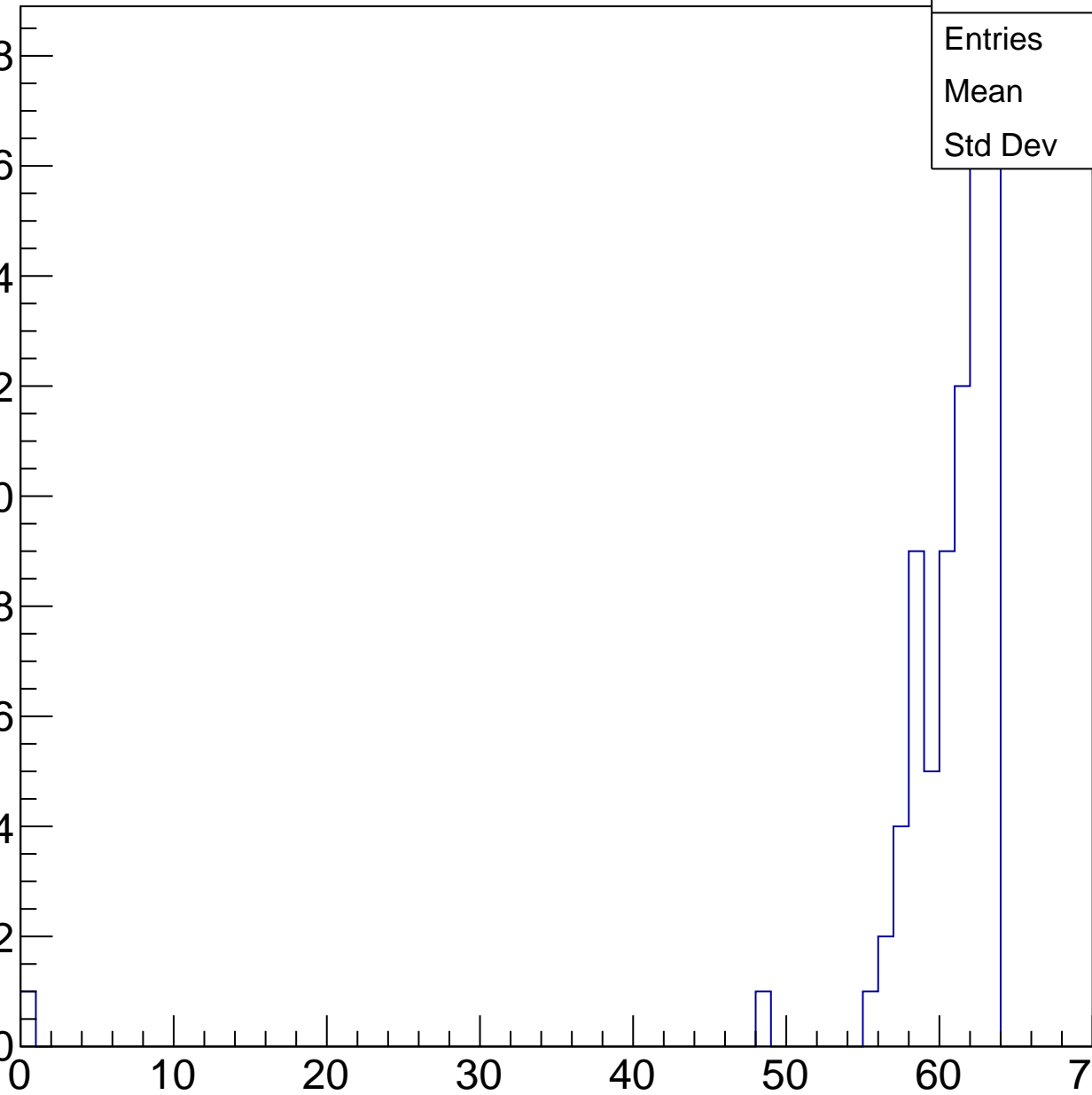
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.69
Std Dev	7.249

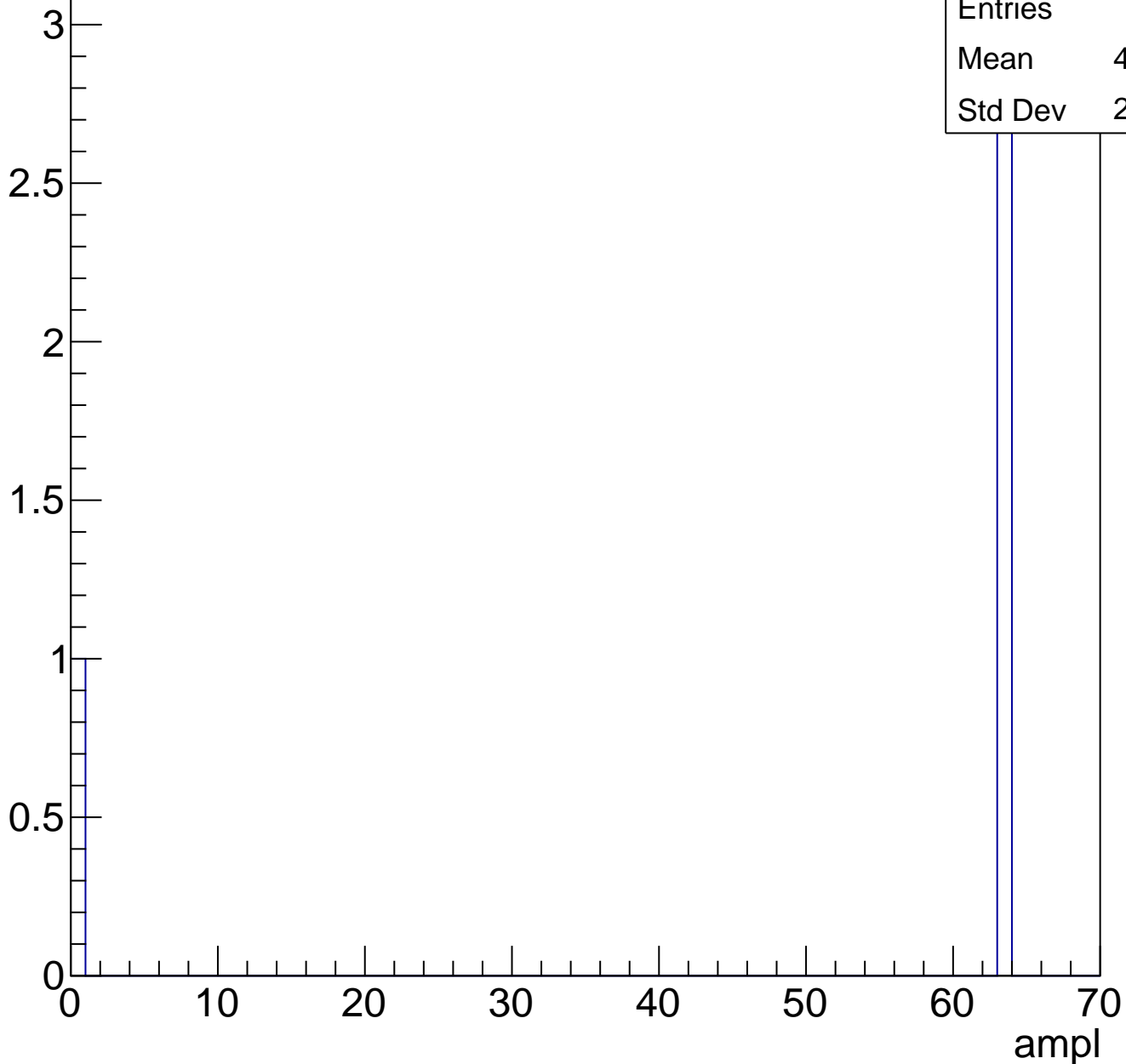
ampl



B0L101S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch48, adc0

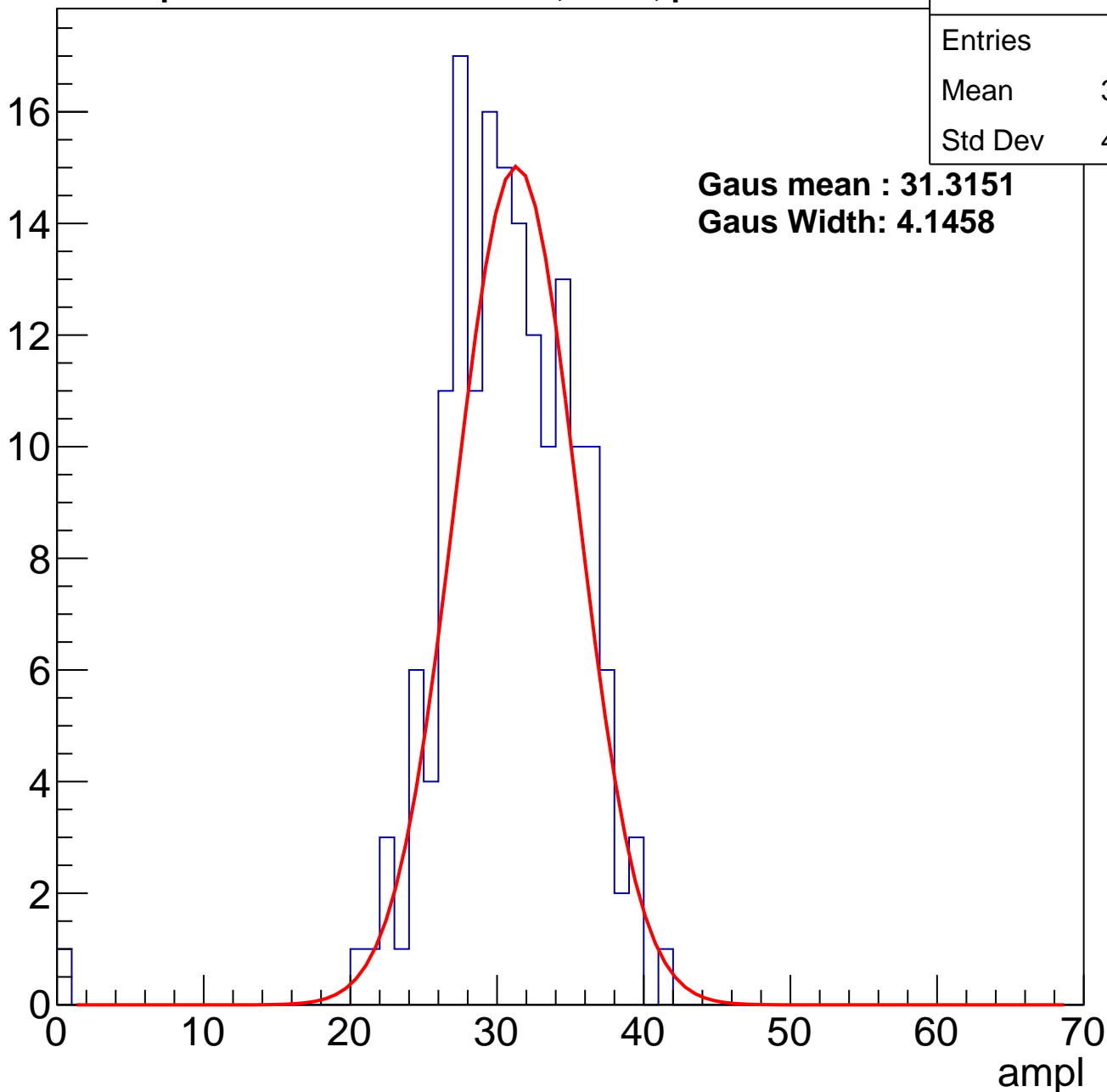
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	30.34
Std Dev	4.733

Gaus mean : 31.3151

Gaus Width: 4.1458

Entry



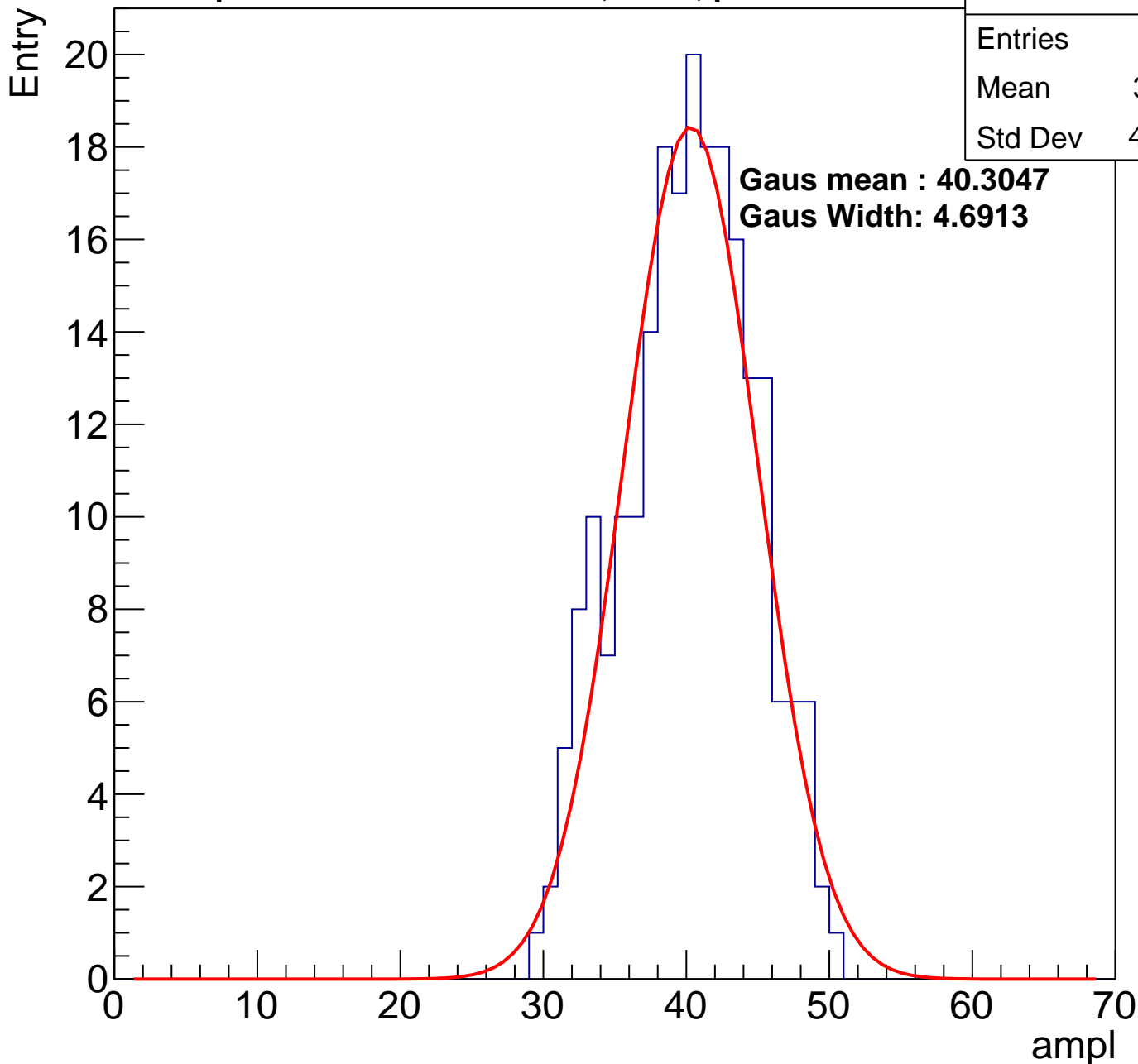
B0L101S, U14-ch48, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	221
Mean	39.71
Std Dev	4.517

Gaus mean : 40.3047

Gaus Width: 4.6913



B0L101S, U14-ch48, adc2

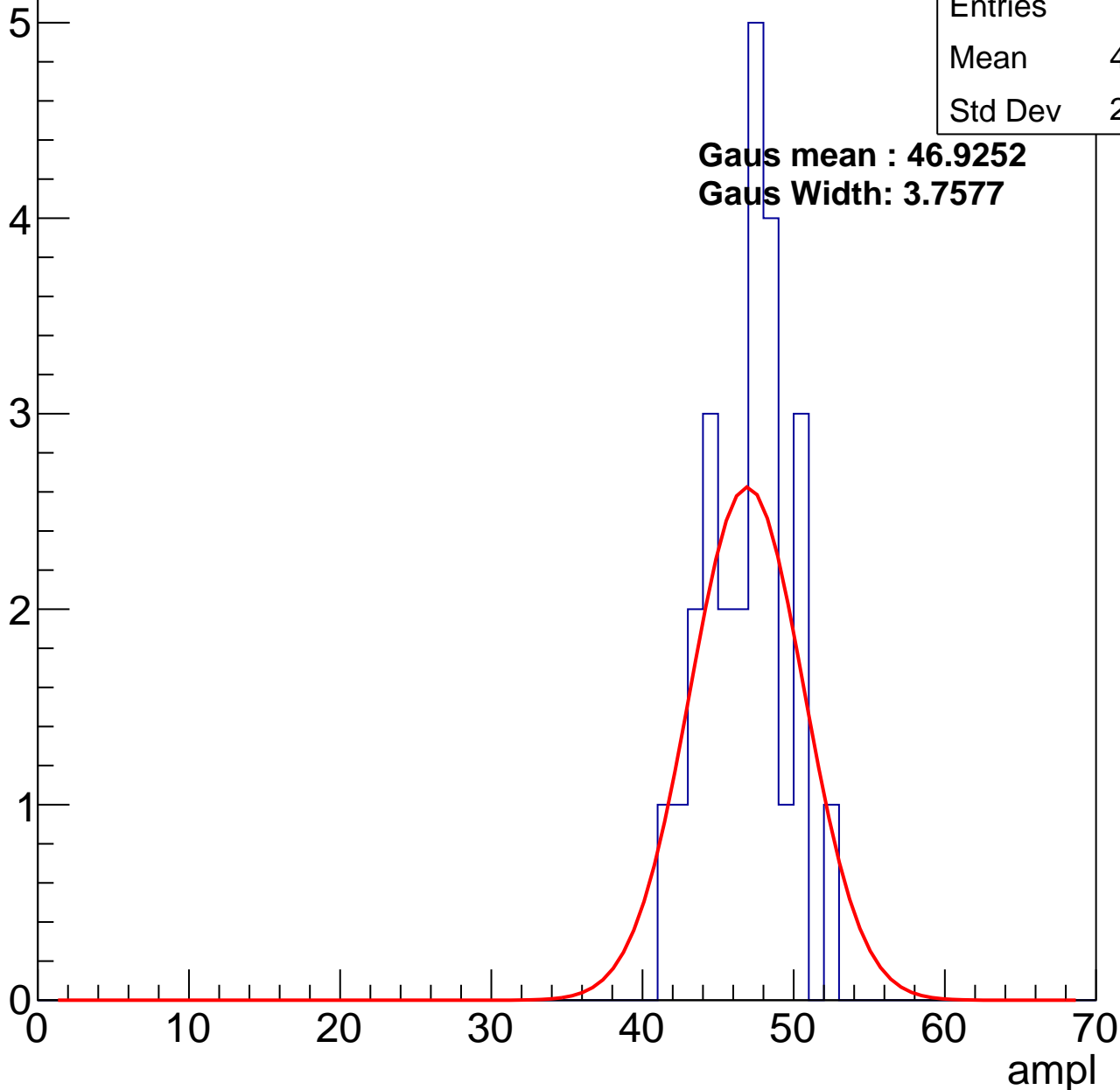
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	25
Mean	46.44
Std Dev	2.699

Gaus mean : 46.9252

Gaus Width: 3.7577

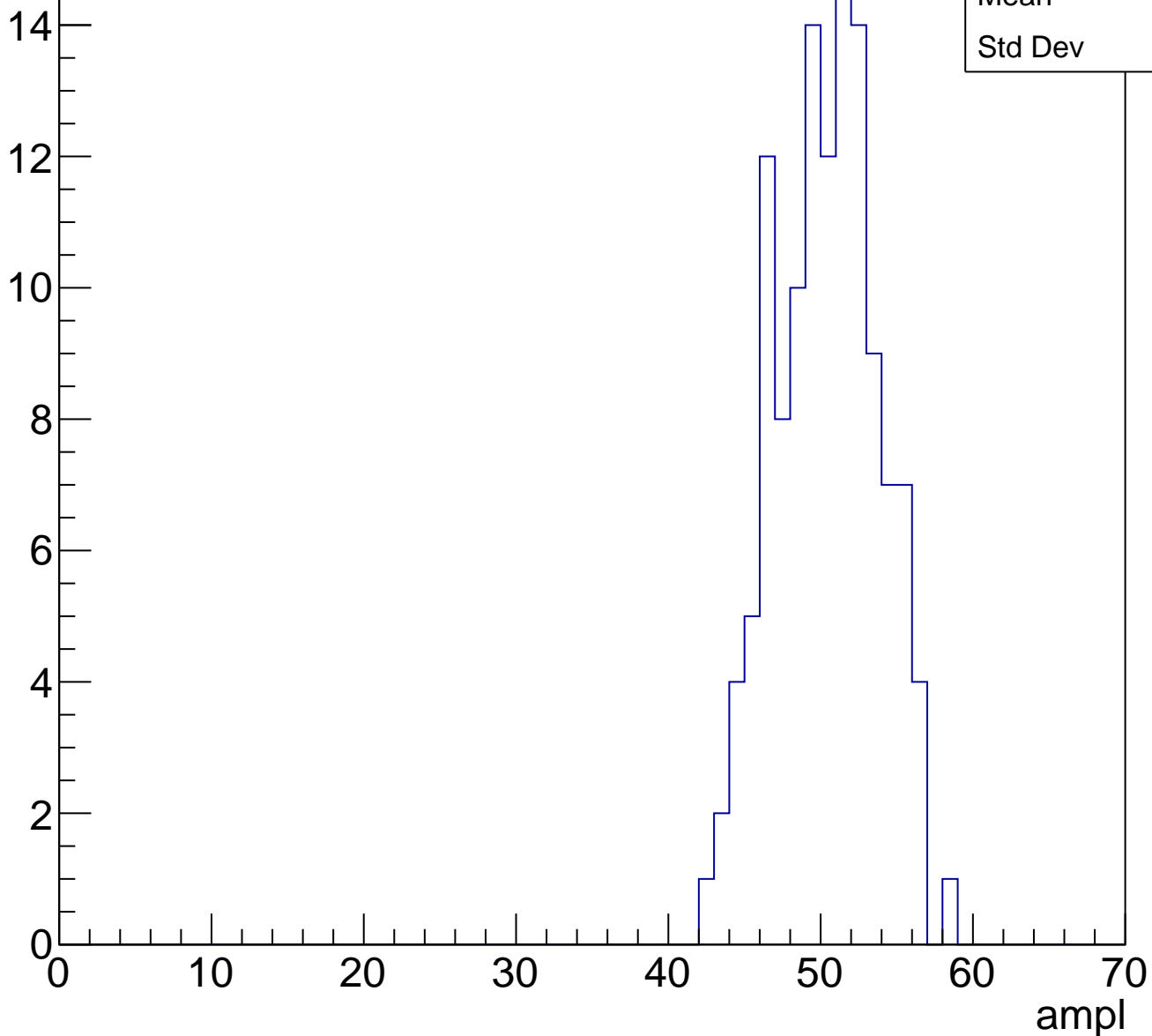


B0L101S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	49.9
Std Dev	3.34

Entry

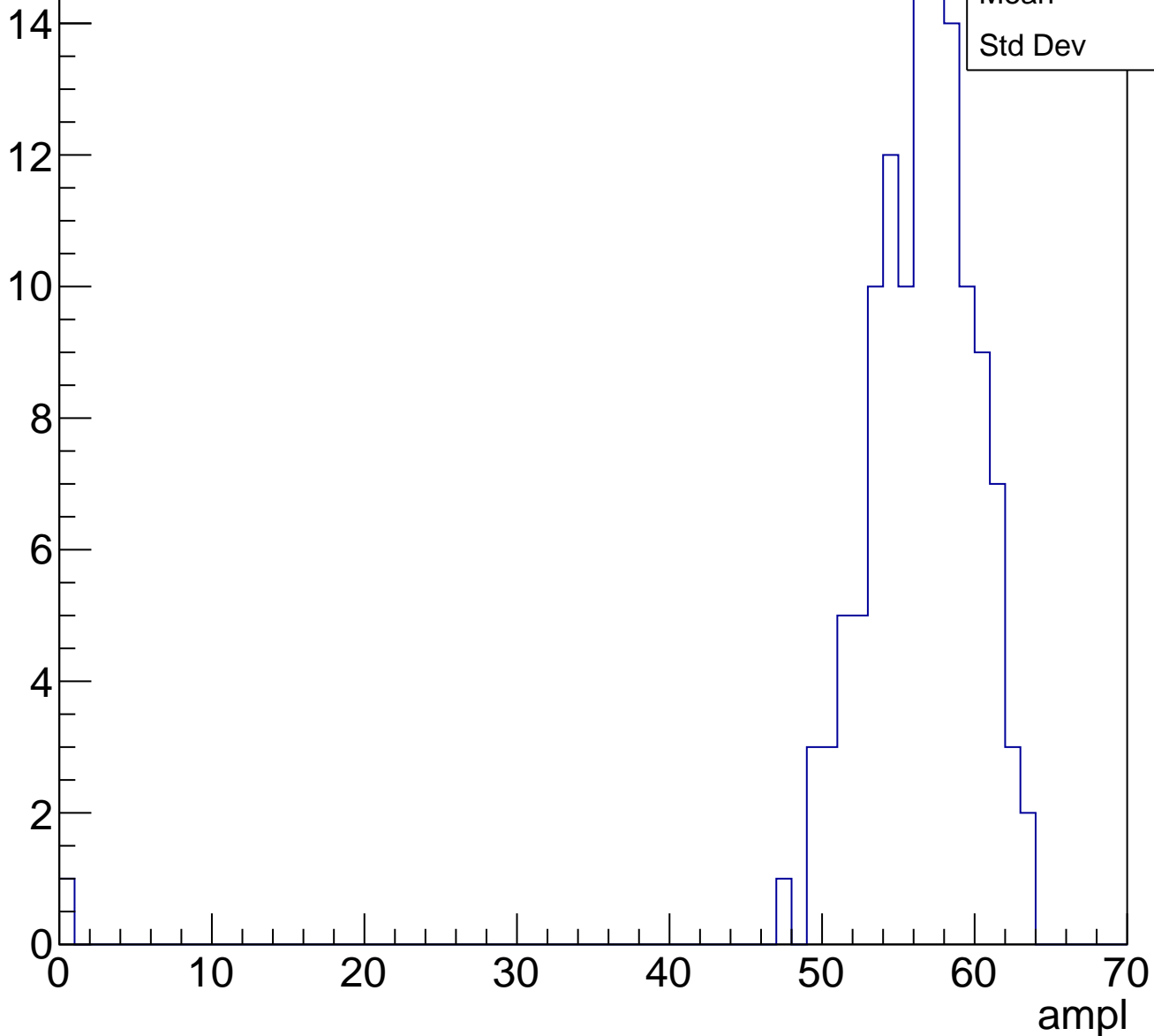


B0L101S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.7
Std Dev	6

Entry



B0L101S, U14-ch48, adc5

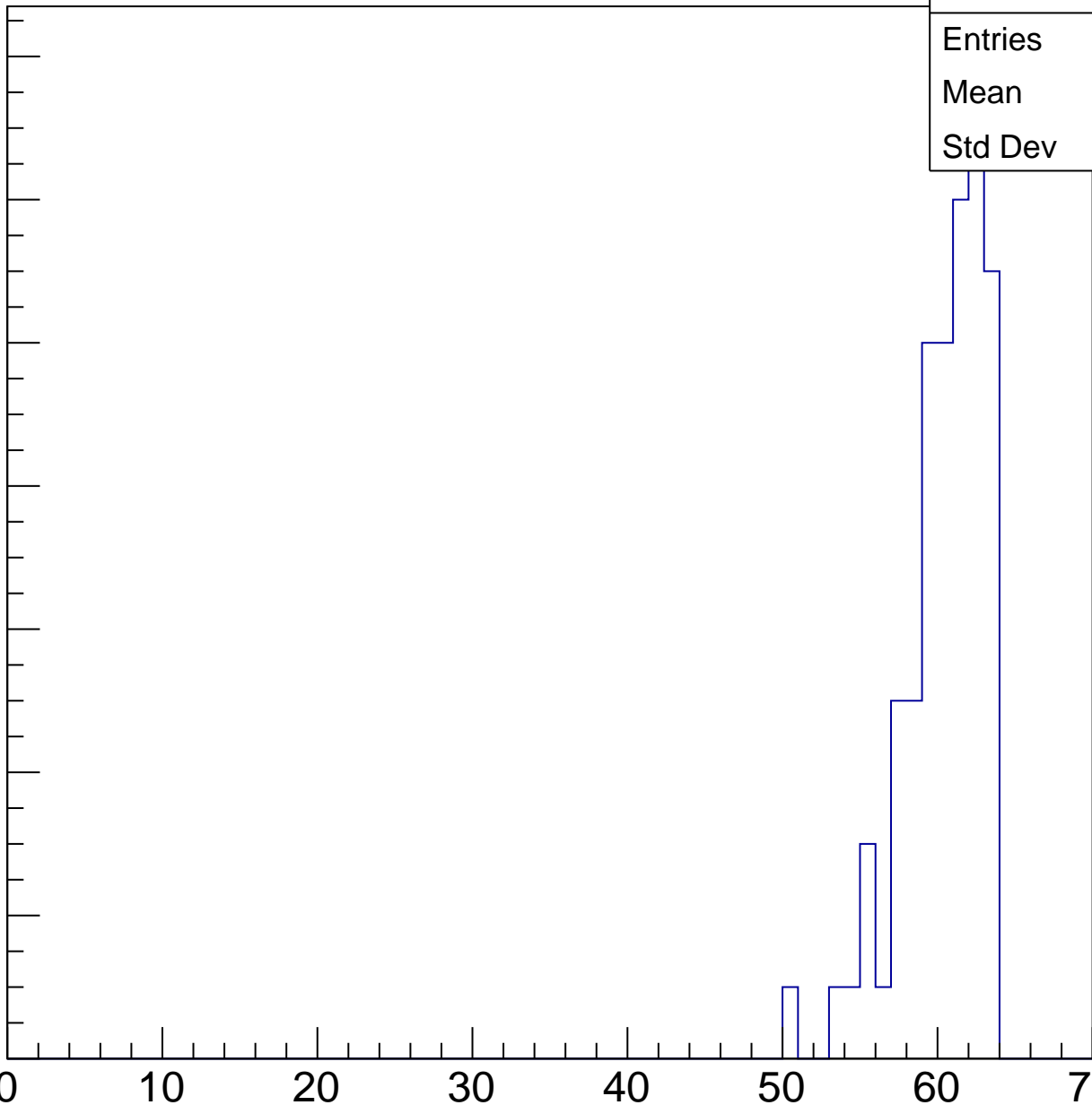
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	59.95
Std Dev	2.645

ampl

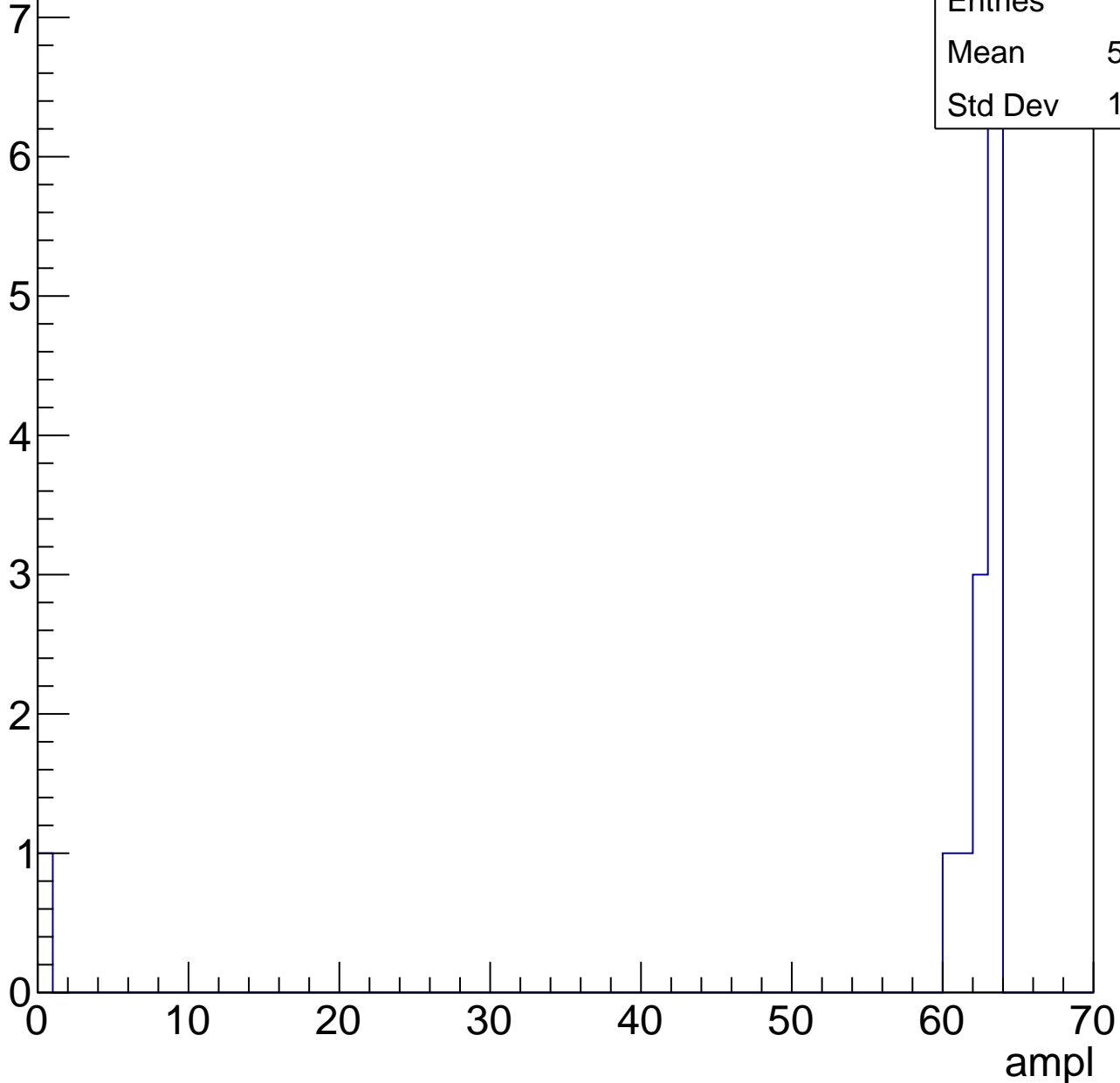


B0L101S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.54
Std Dev	16.63



B0L101S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch49, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	29.48
Std Dev	4.364

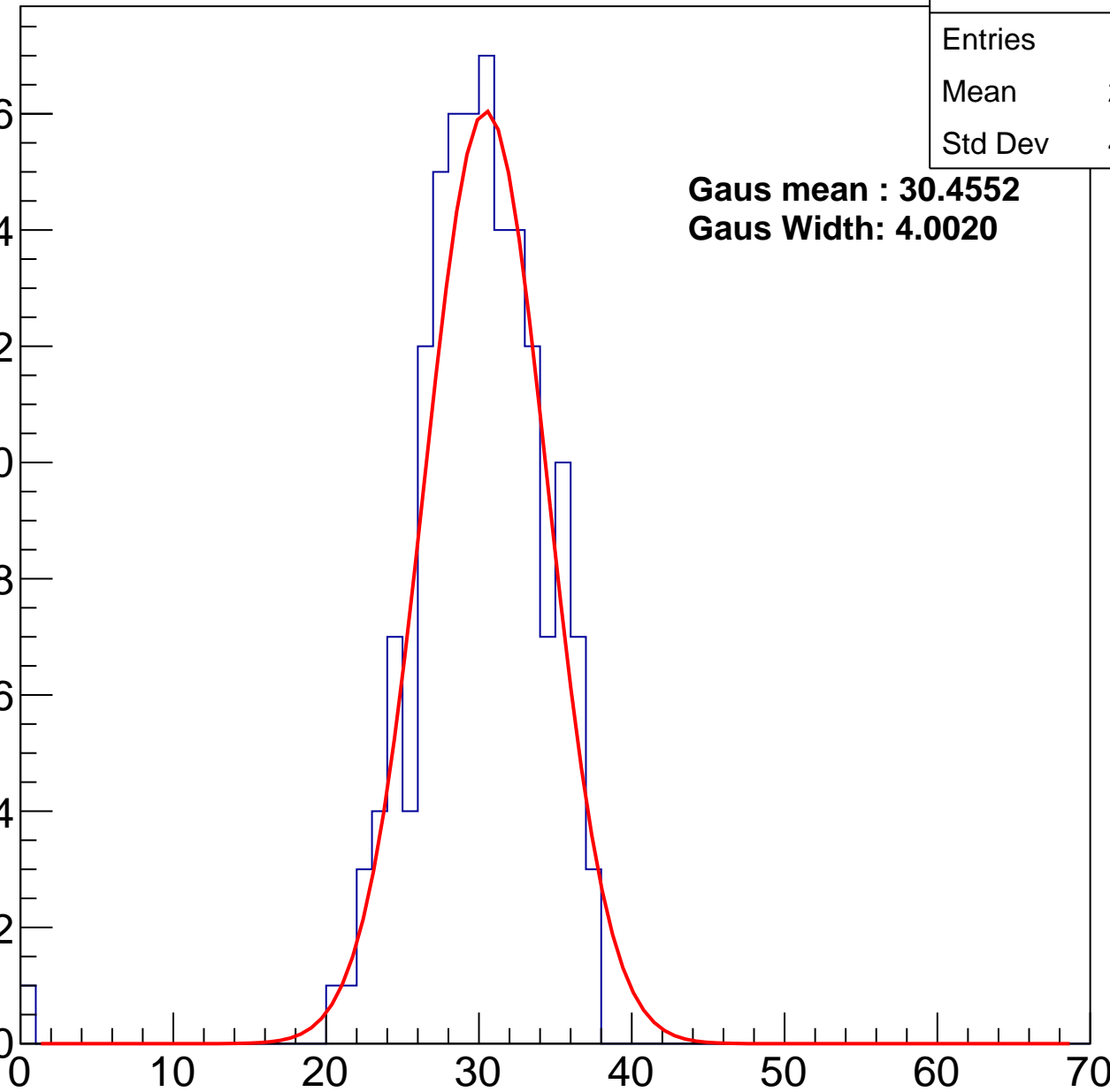
Gaus mean : 30.4552

Gaus Width: 4.0020

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch49, adc1

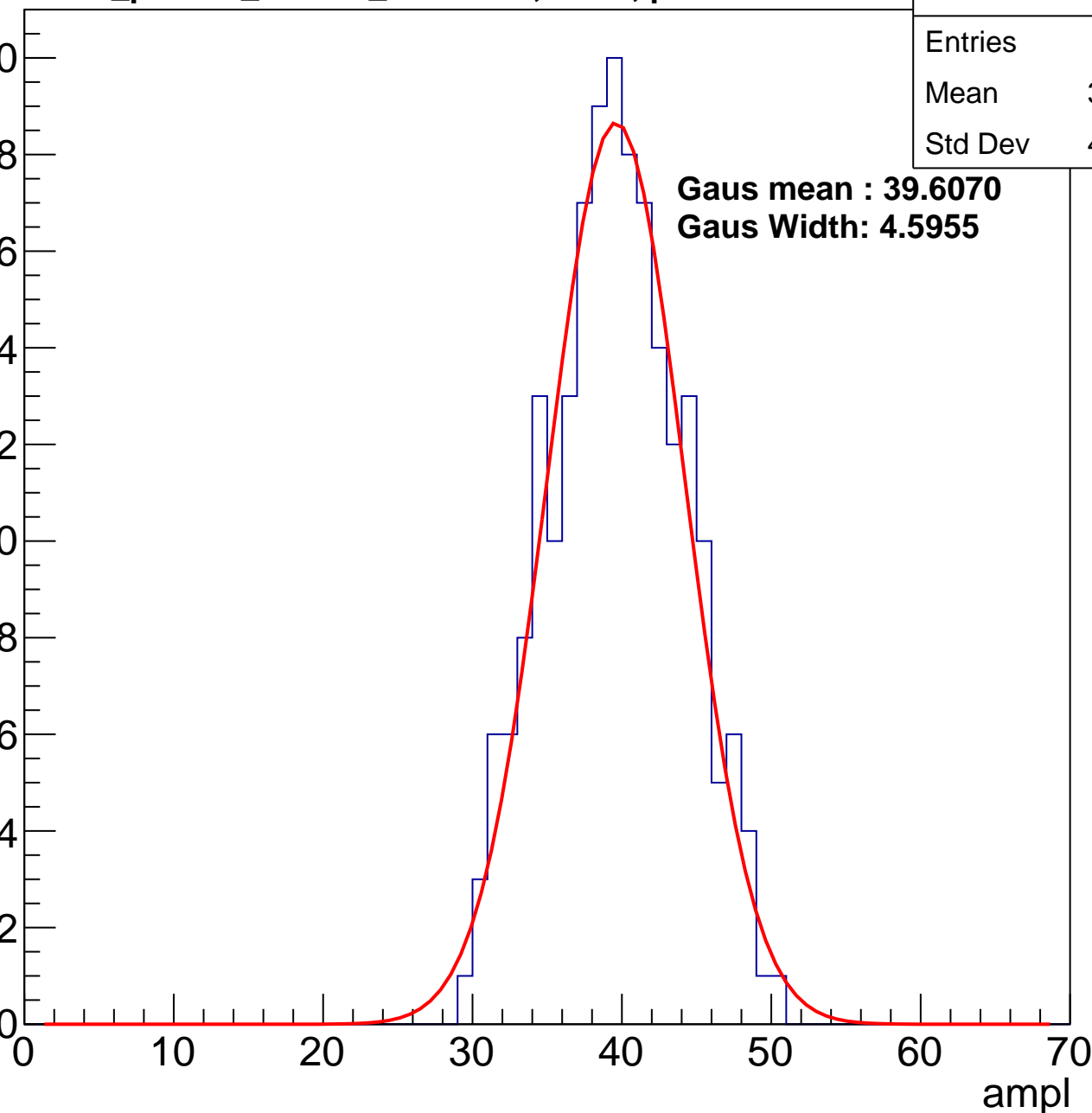
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	217
Mean	39.16
Std Dev	4.428

Gaus mean : 39.6070
Gaus Width: 4.5955



B0L101S, U14-ch49, adc2

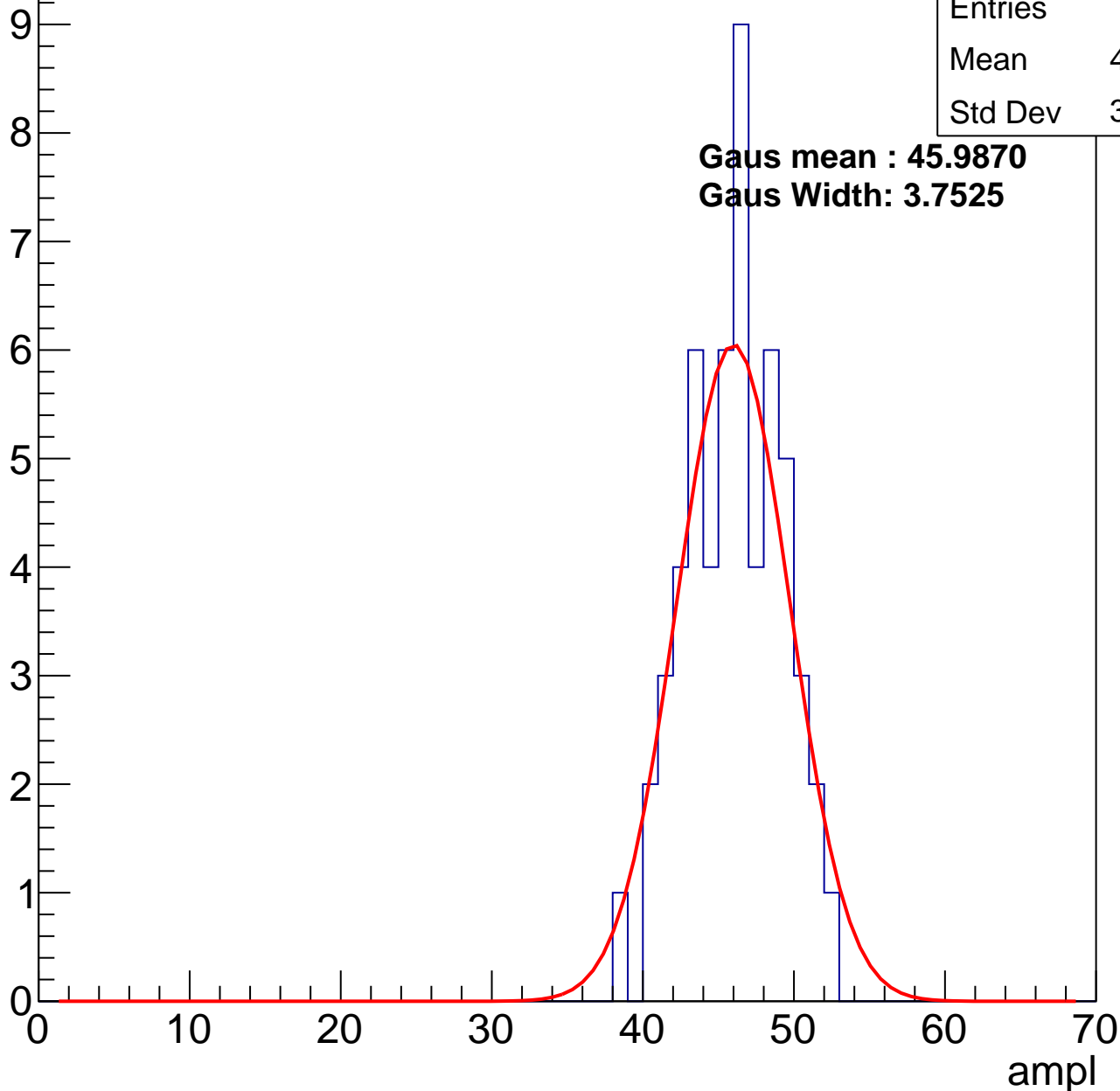
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	56
Mean	45.57
Std Dev	3.127

Gaus mean : 45.9870

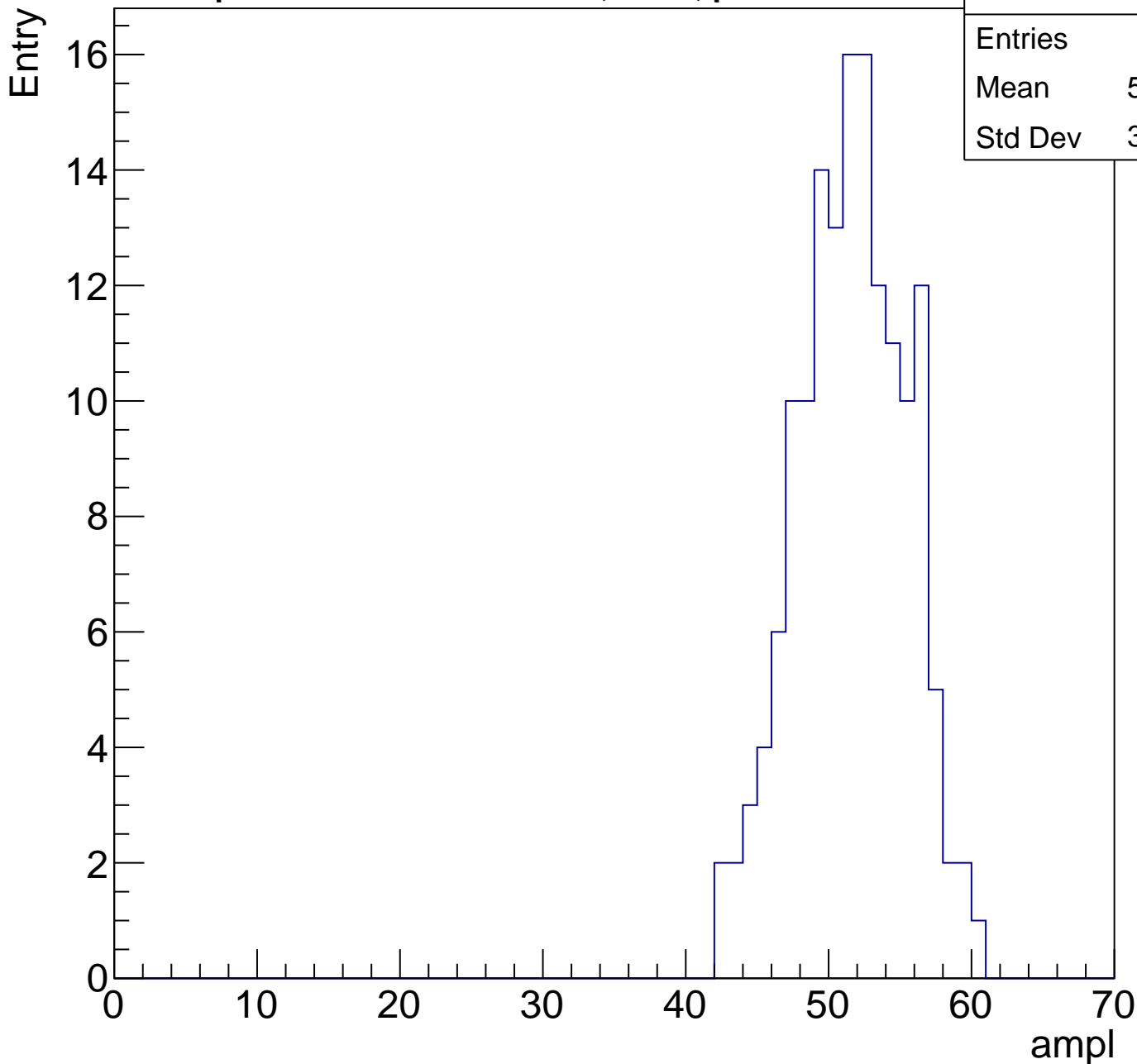
Gaus Width: 3.7525



B0L101S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	51.15
Std Dev	3.779



B0L101S, U14-ch49, adc4

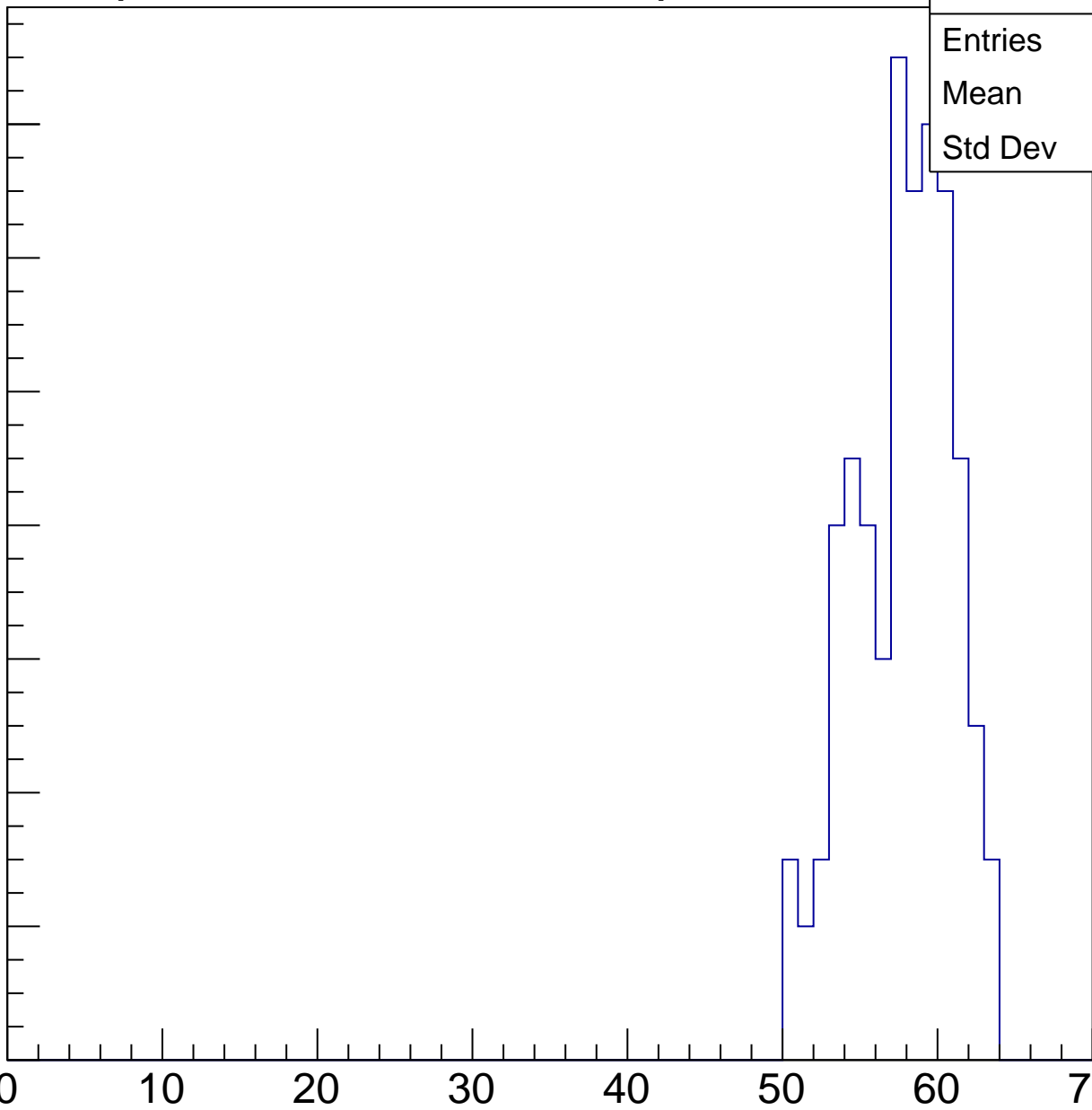
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	57.27
Std Dev	3.142

ampl



B0L101S, U14-ch49, adc5

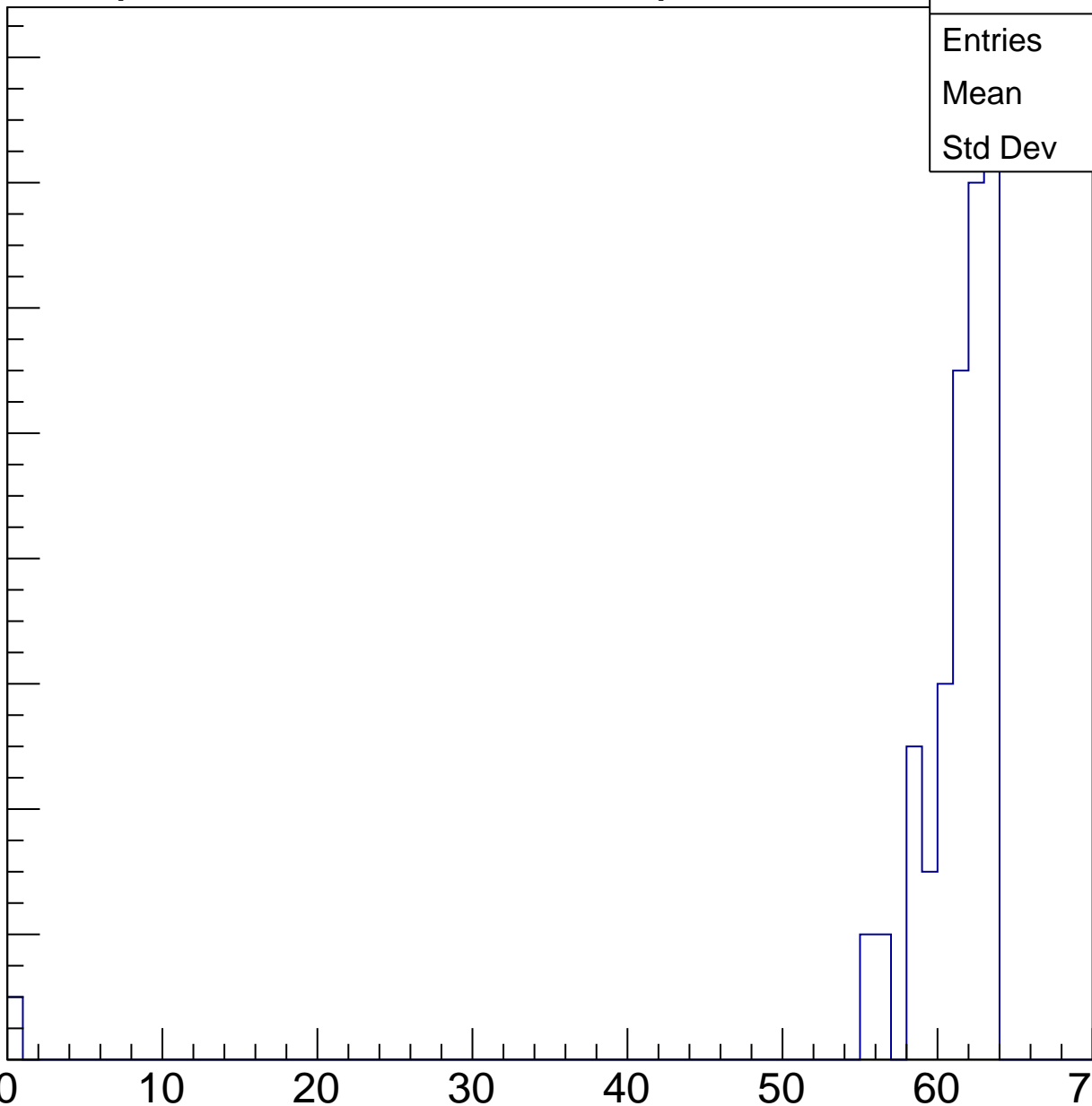
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	60
Mean	59.93
Std Dev	8.079

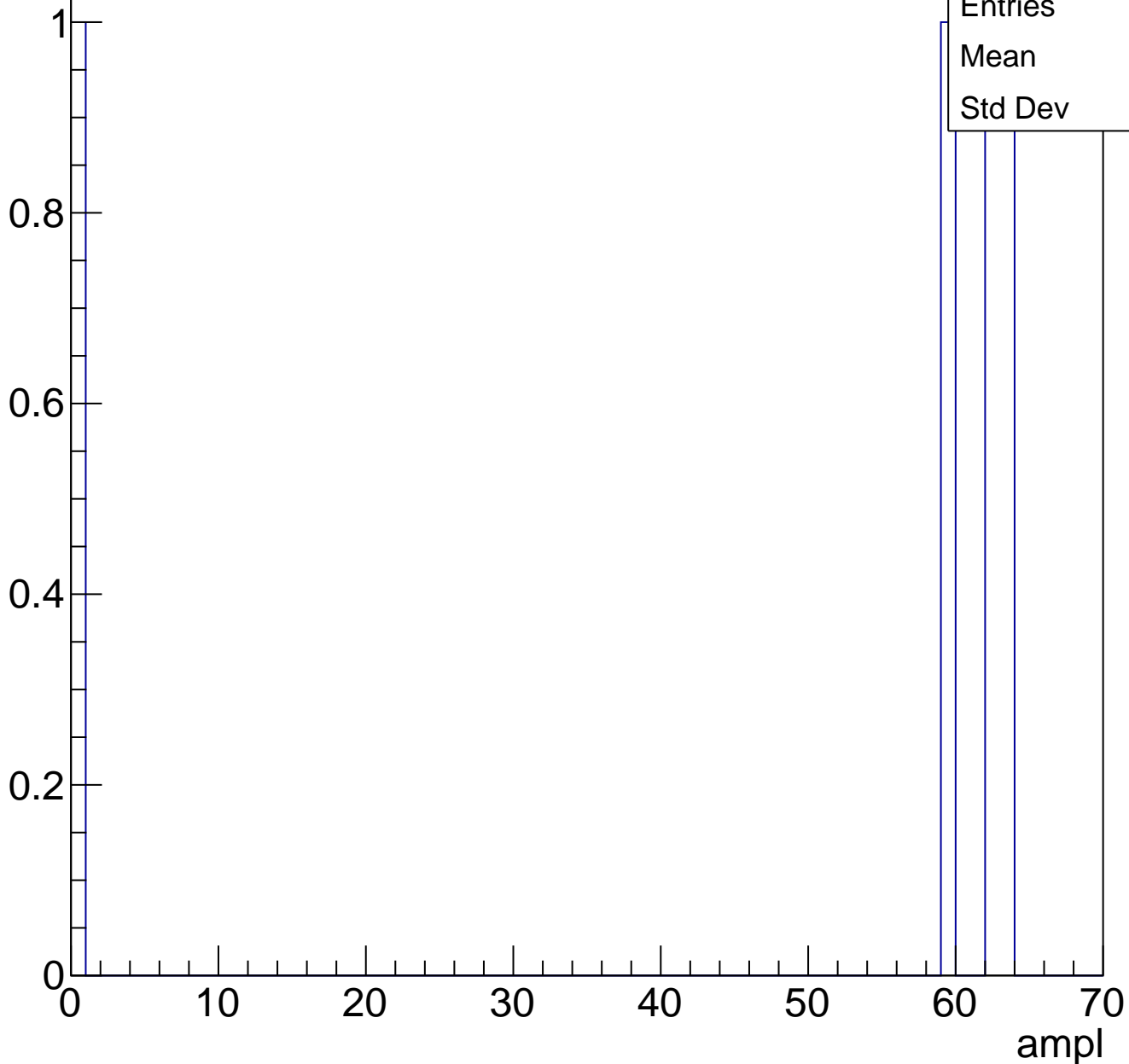
ampl



B0L101S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch50, adc0

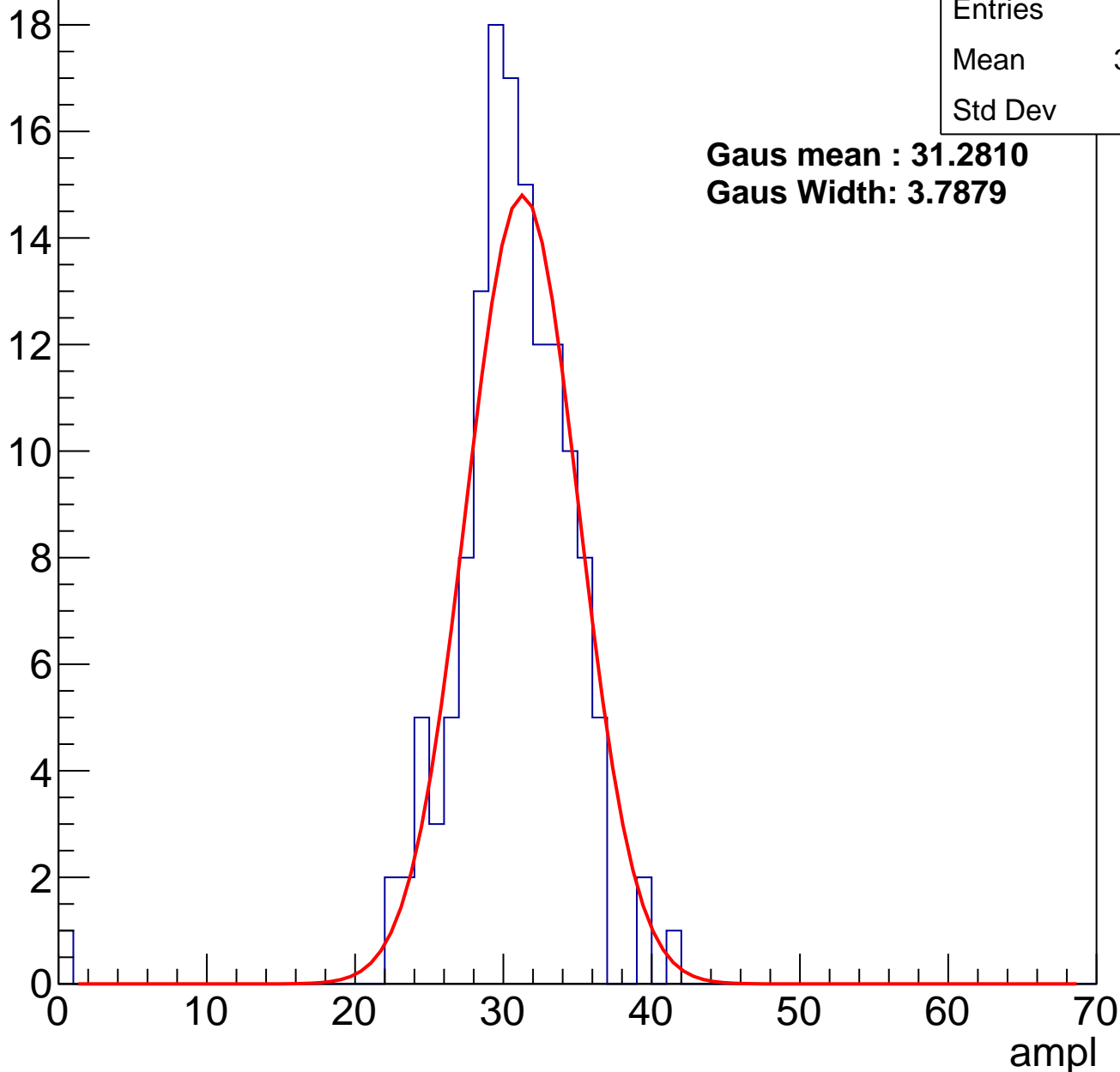
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	30.15
Std Dev	4.32

Gaus mean : 31.2810

Gaus Width: 3.7879

Entry



B0L101S, U14-ch50, adc1

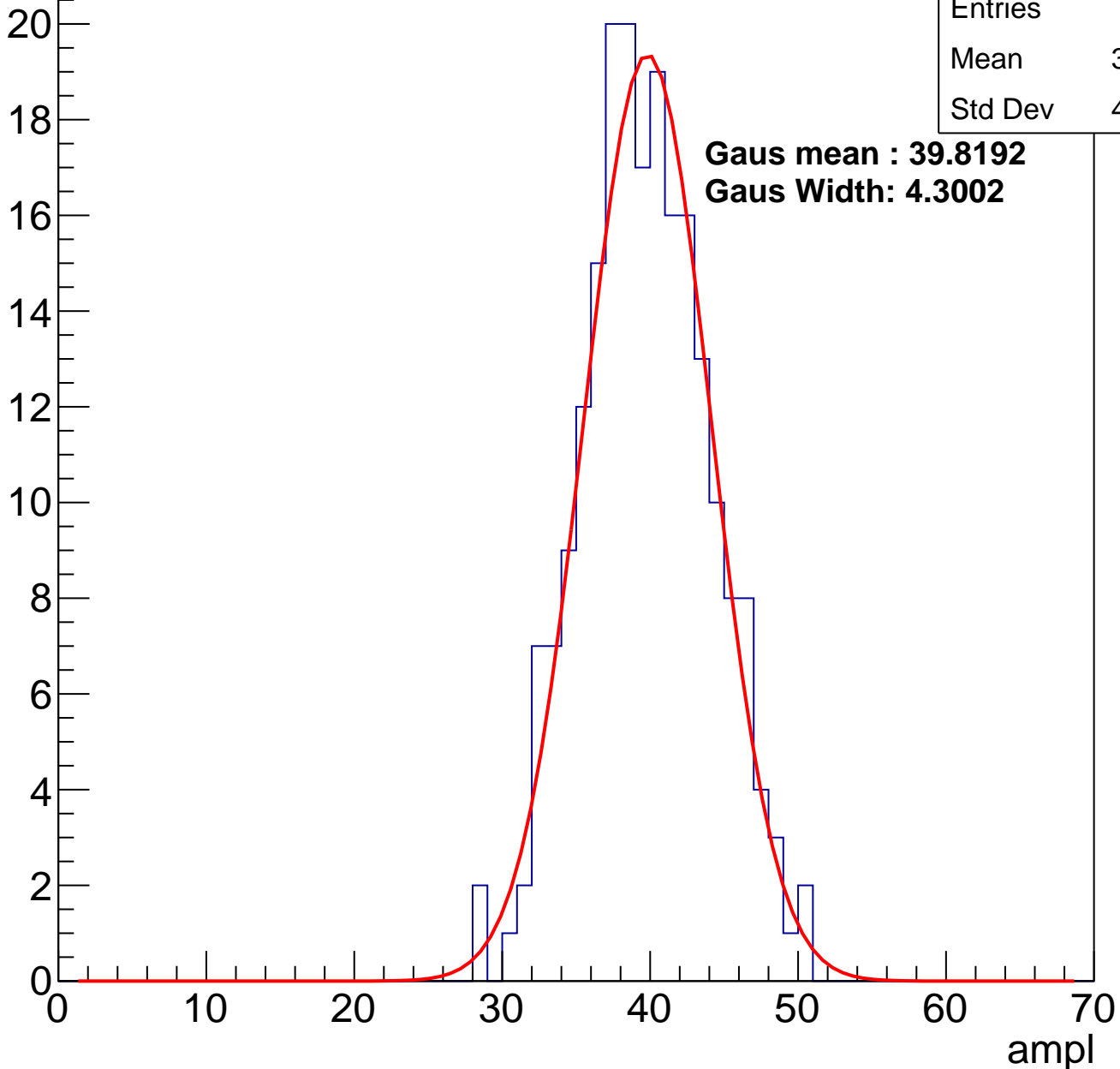
calib_packv5_042523_0143.root, FC#1, port C1

Entries	212
Mean	39.28
Std Dev	4.282

Gaus mean : 39.8192

Gaus Width: 4.3002

Entry



B0L101S, U14-ch50, adc2

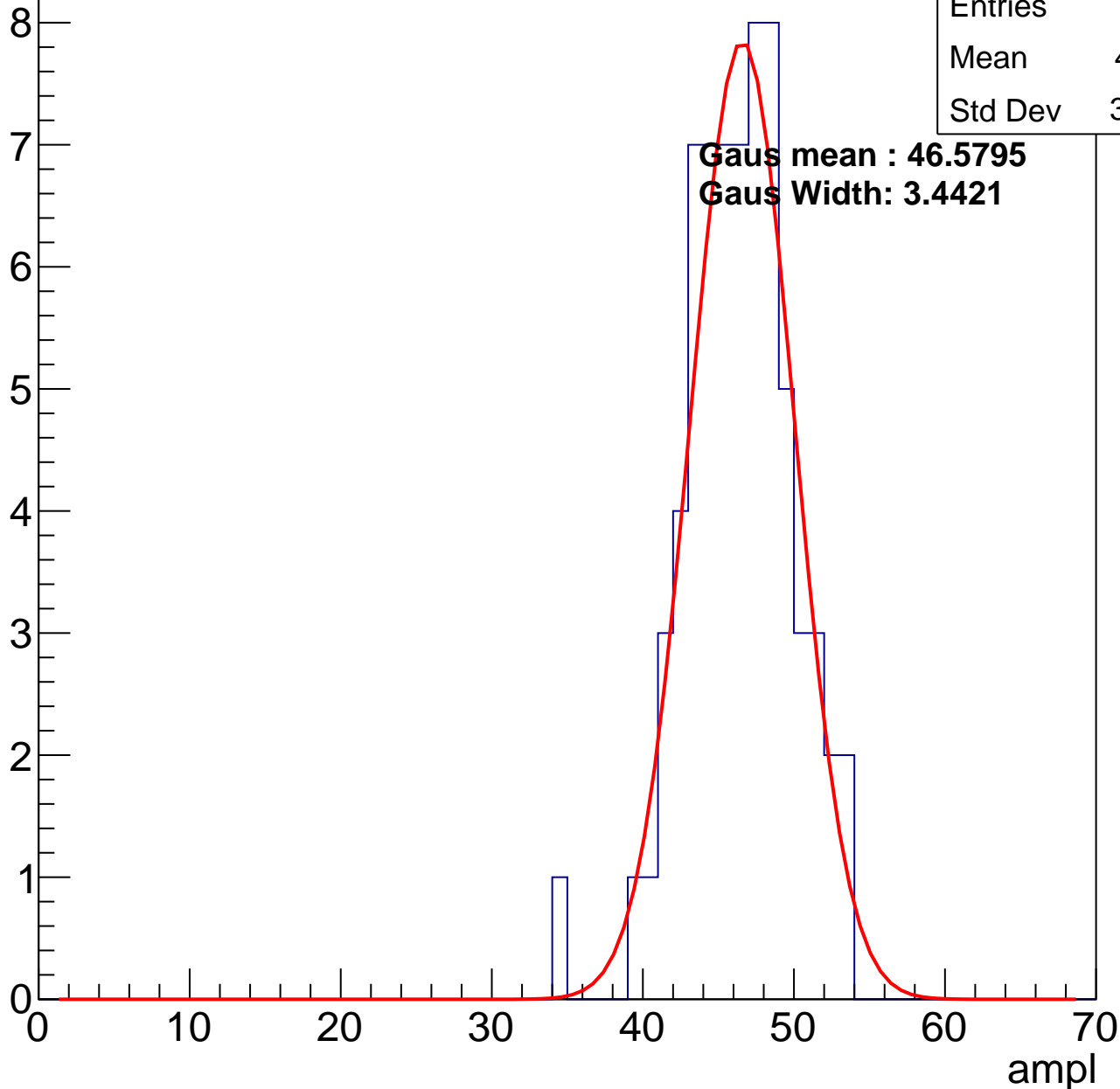
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	69
Mean	45.91
Std Dev	3.496

Gaus mean : 46.5795

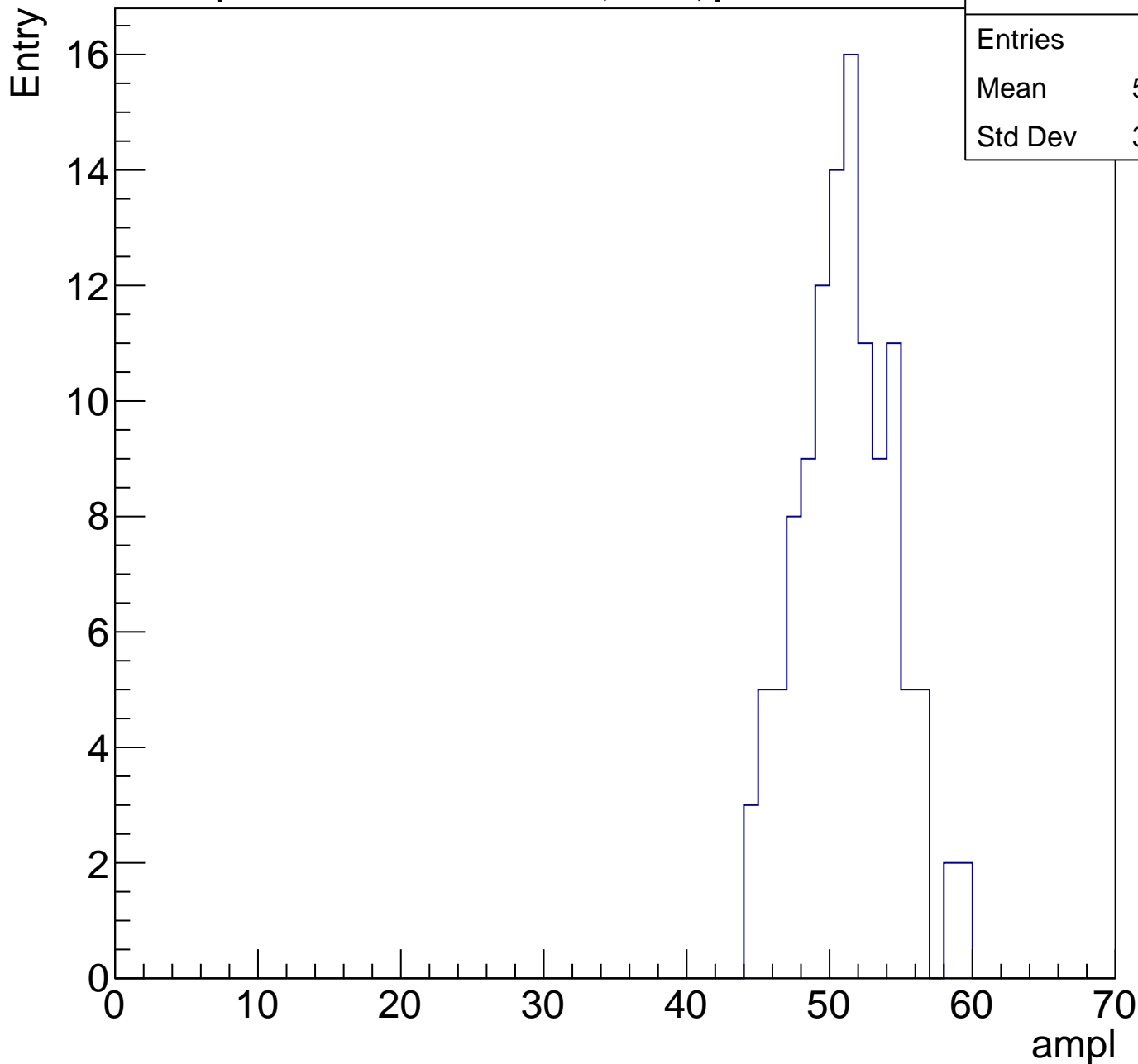
Gaus Width: 3.4421



B0L101S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	50.69
Std Dev	3.319

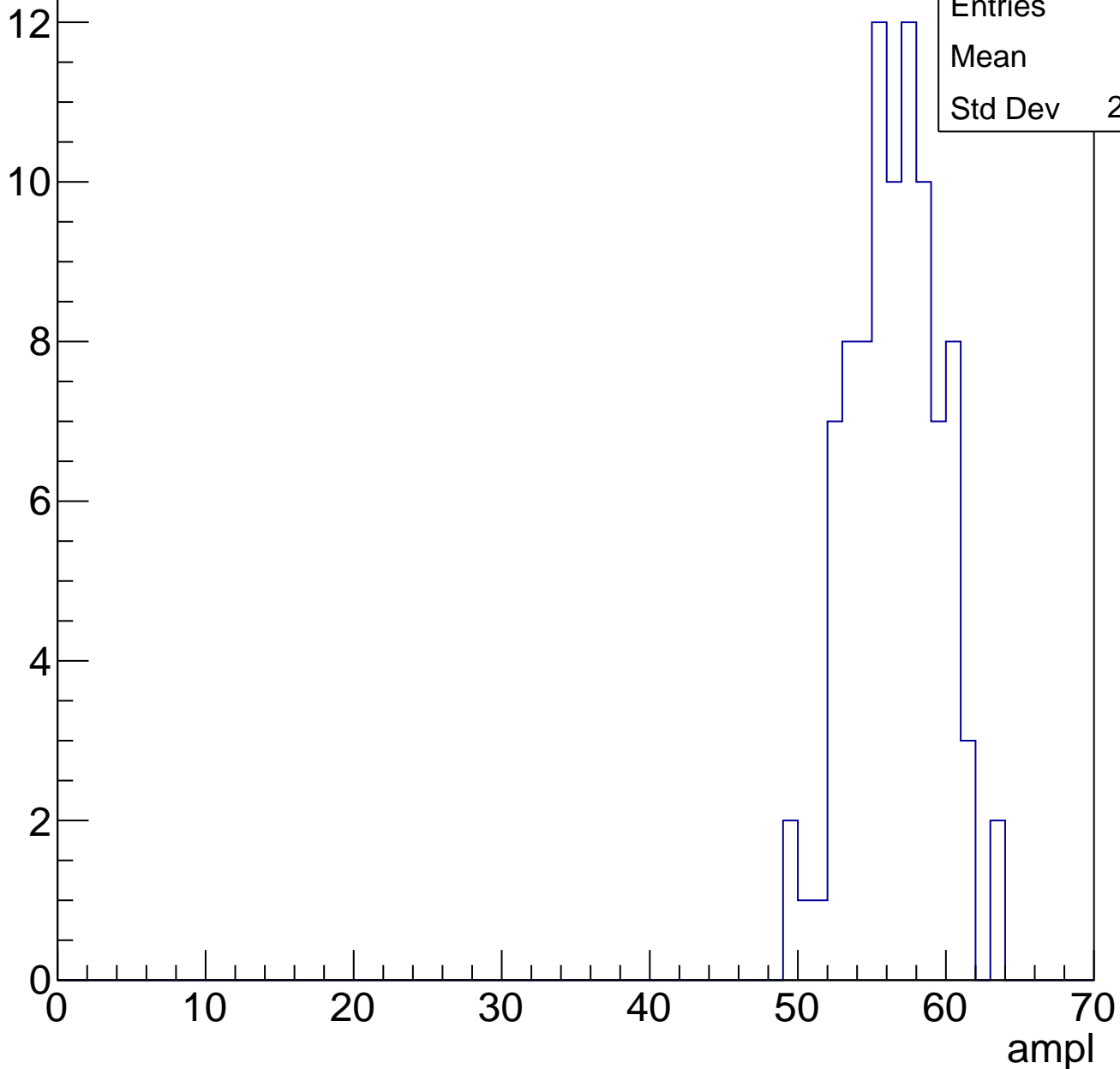


B0L101S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	91
Mean	56.1
Std Dev	2.969

Entry



B0L101S, U14-ch50, adc5

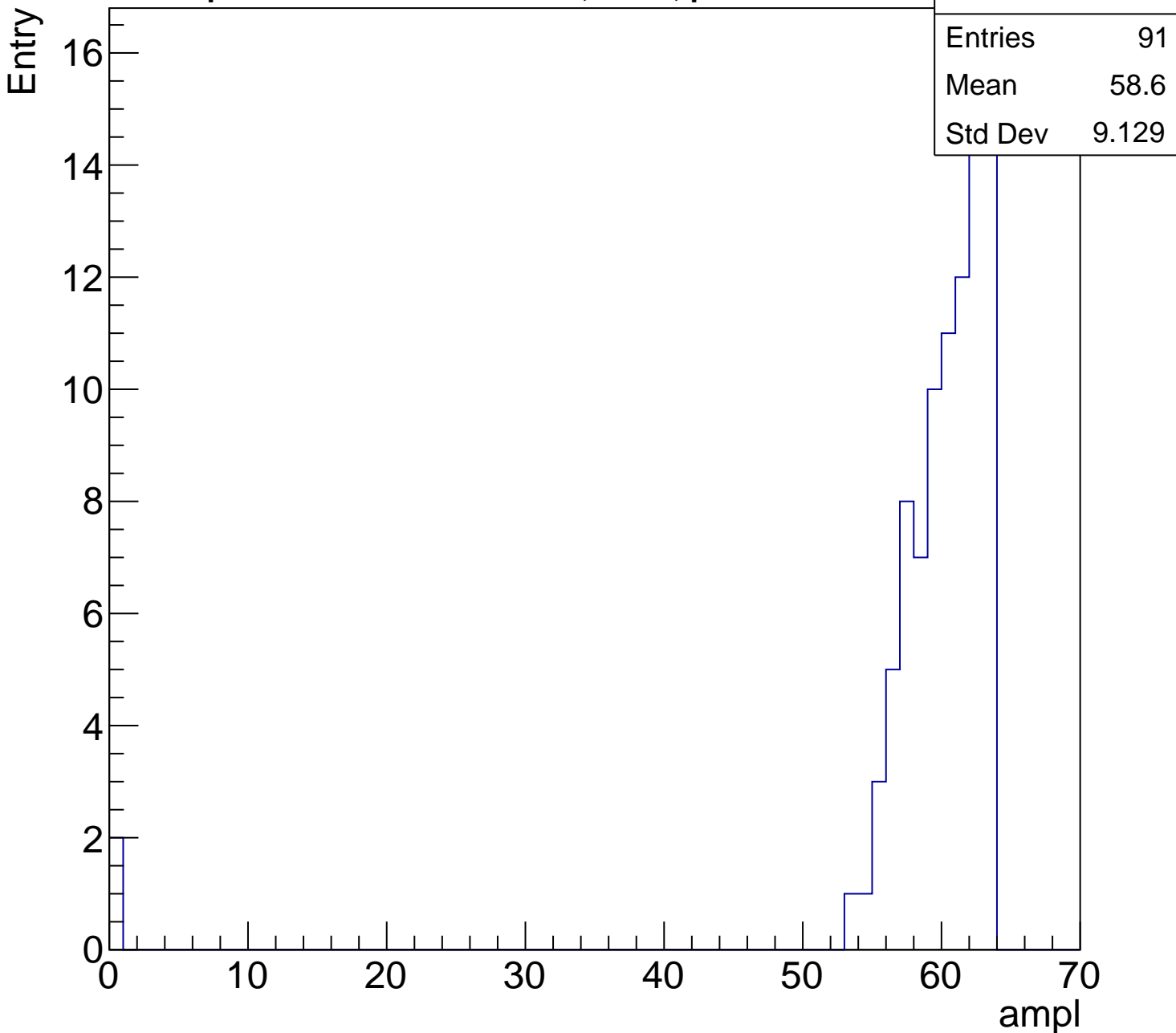
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.6
Std Dev	9.129

ampl

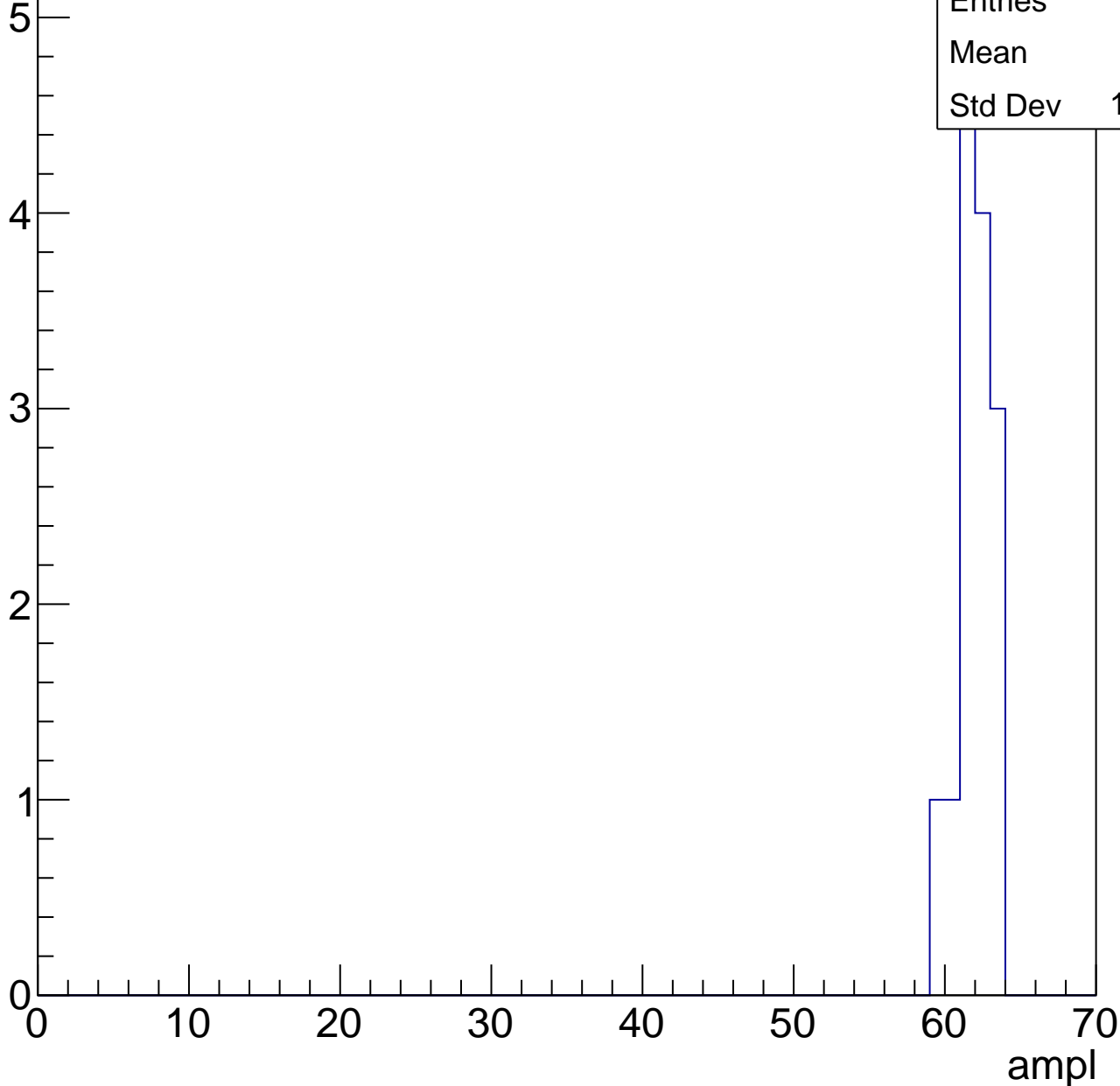


B0L101S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

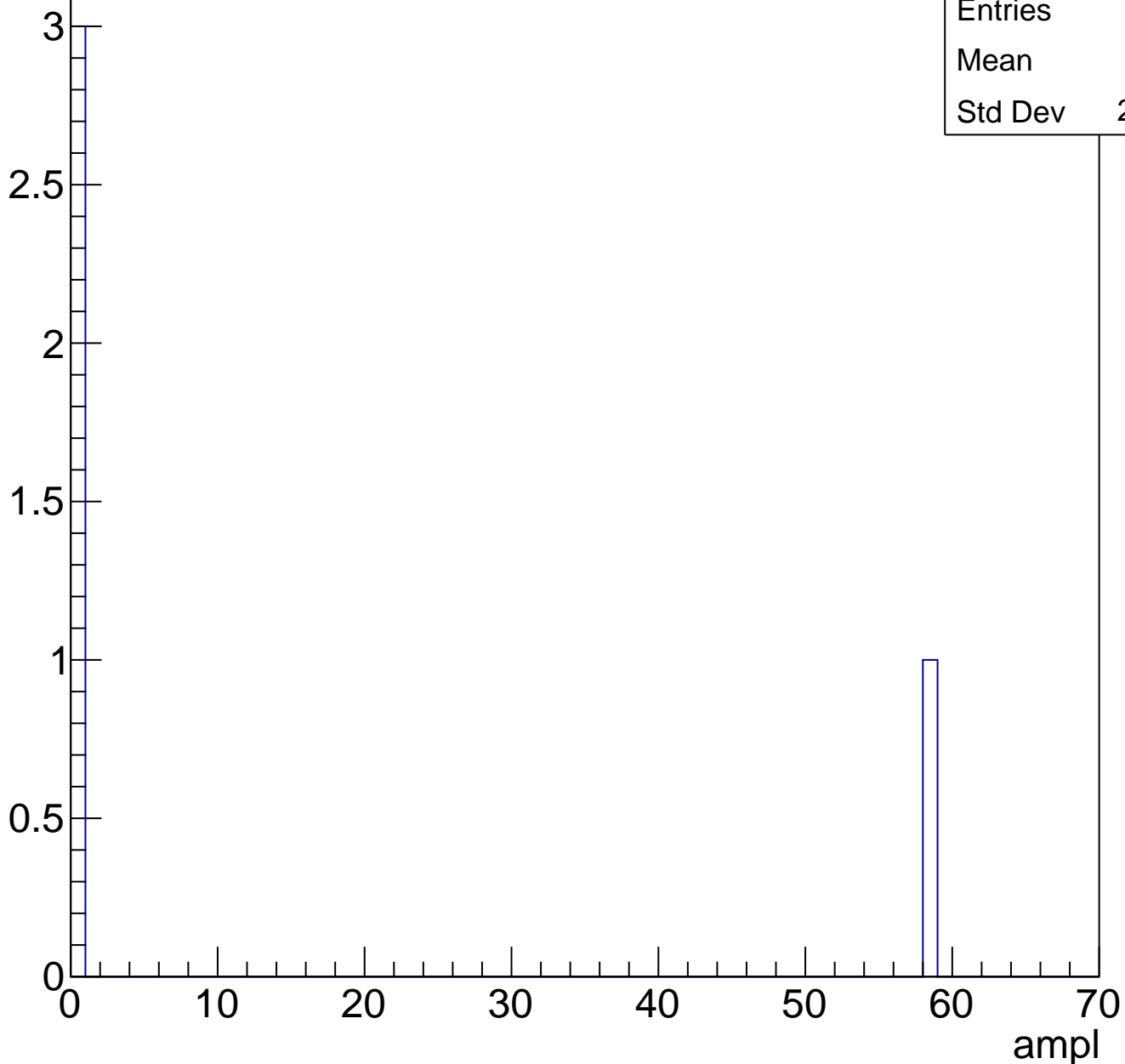
Entries	14
Mean	61.5
Std Dev	1.118



B0L101S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	14.5
Std Dev	25.11

B0L101S, U14-ch51, adc0

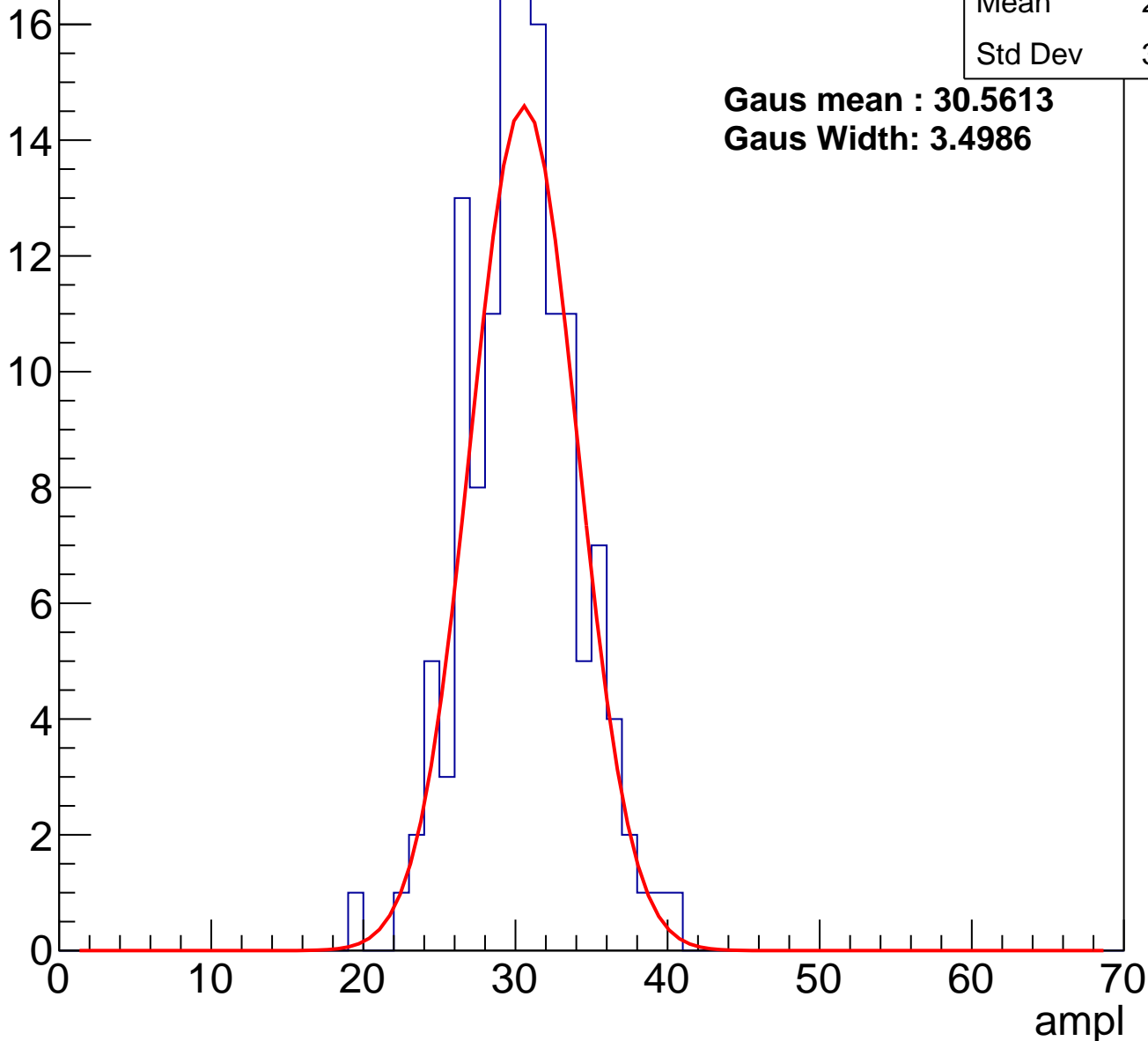
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	137
Mean	29.99
Std Dev	3.605

Gaus mean : 30.5613

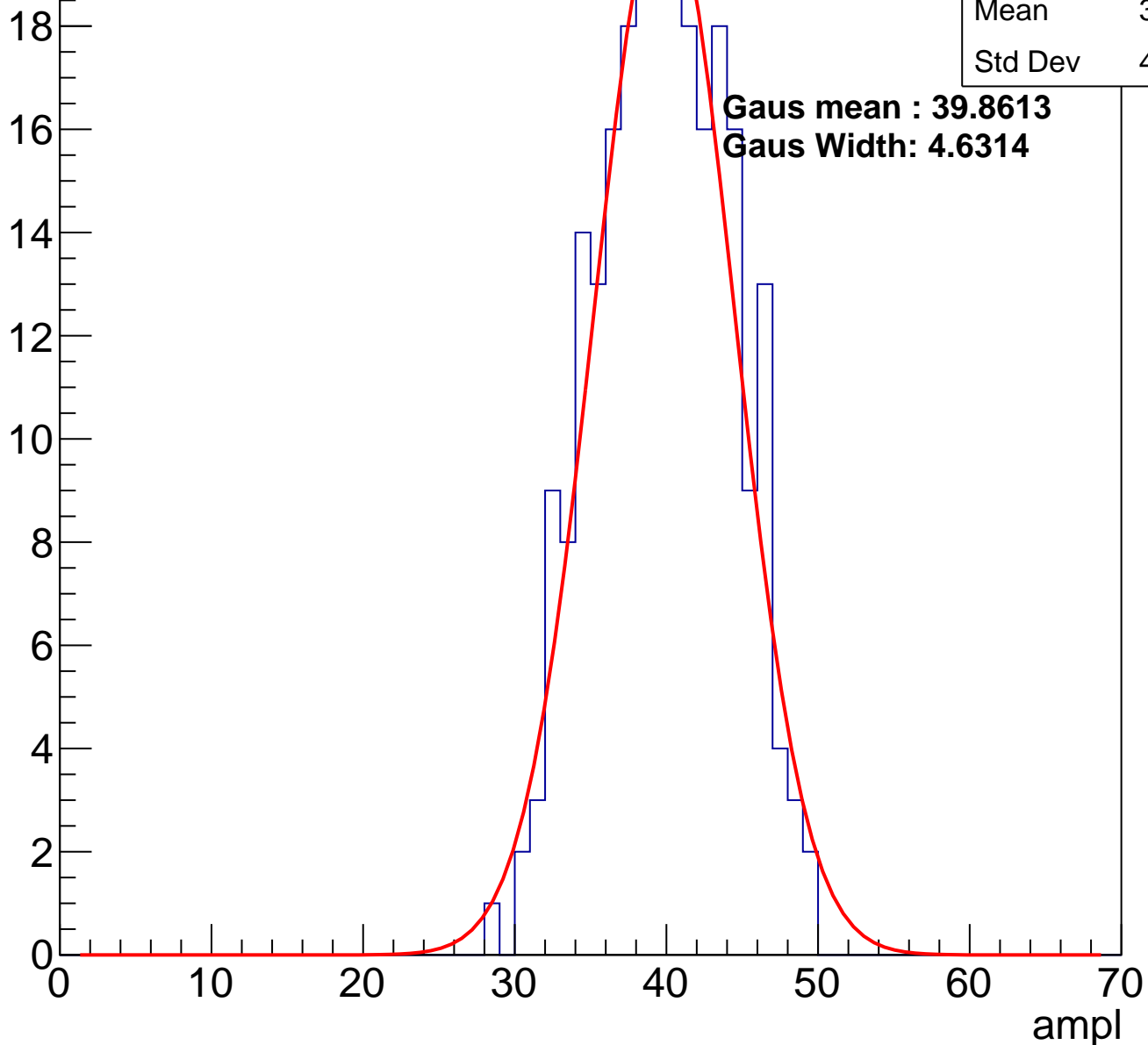
Gaus Width: 3.4986



B0L101S, U14-ch51, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch51, adc2

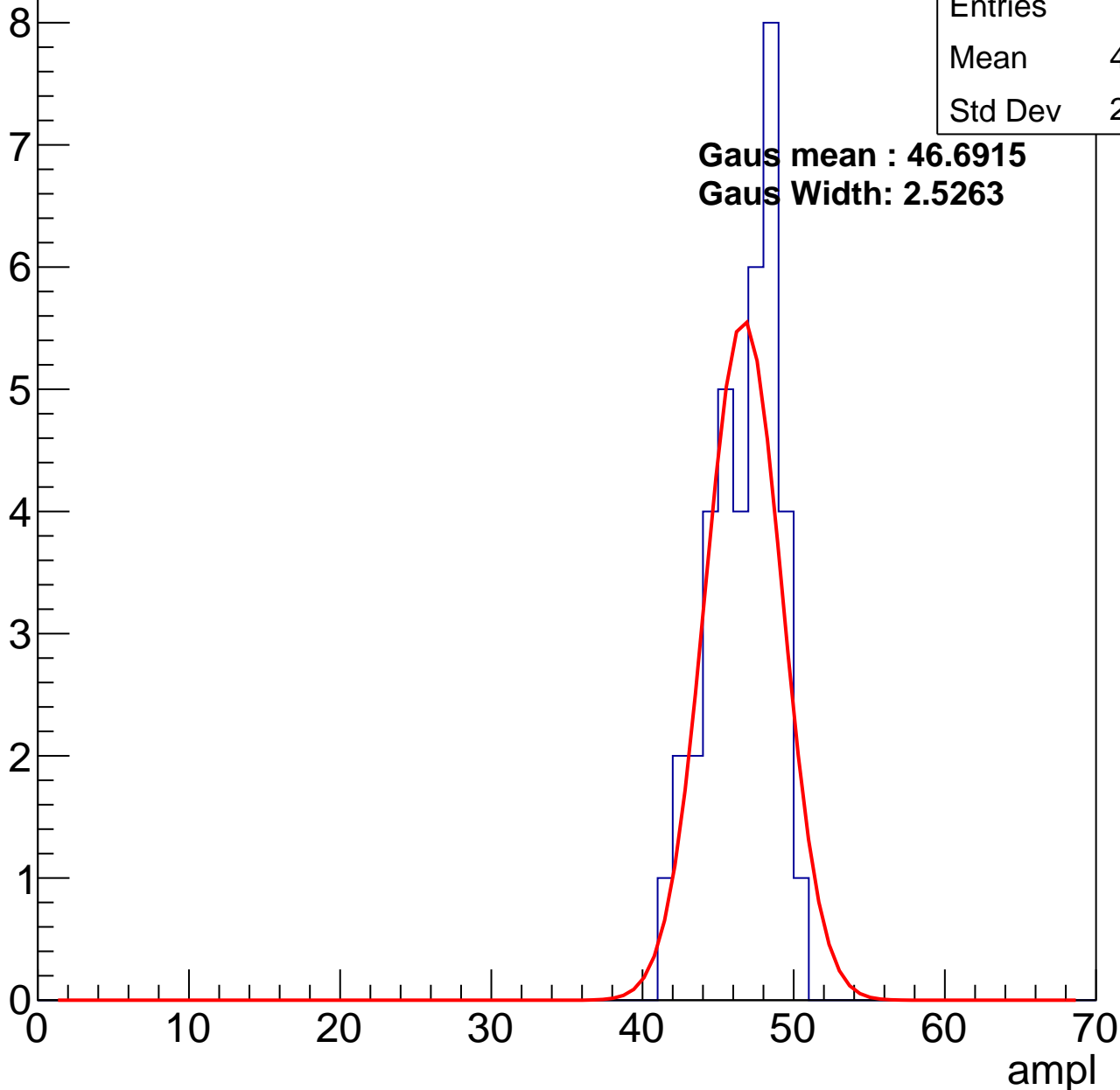
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	37
Mean	46.16
Std Dev	2.236

Gaus mean : 46.6915

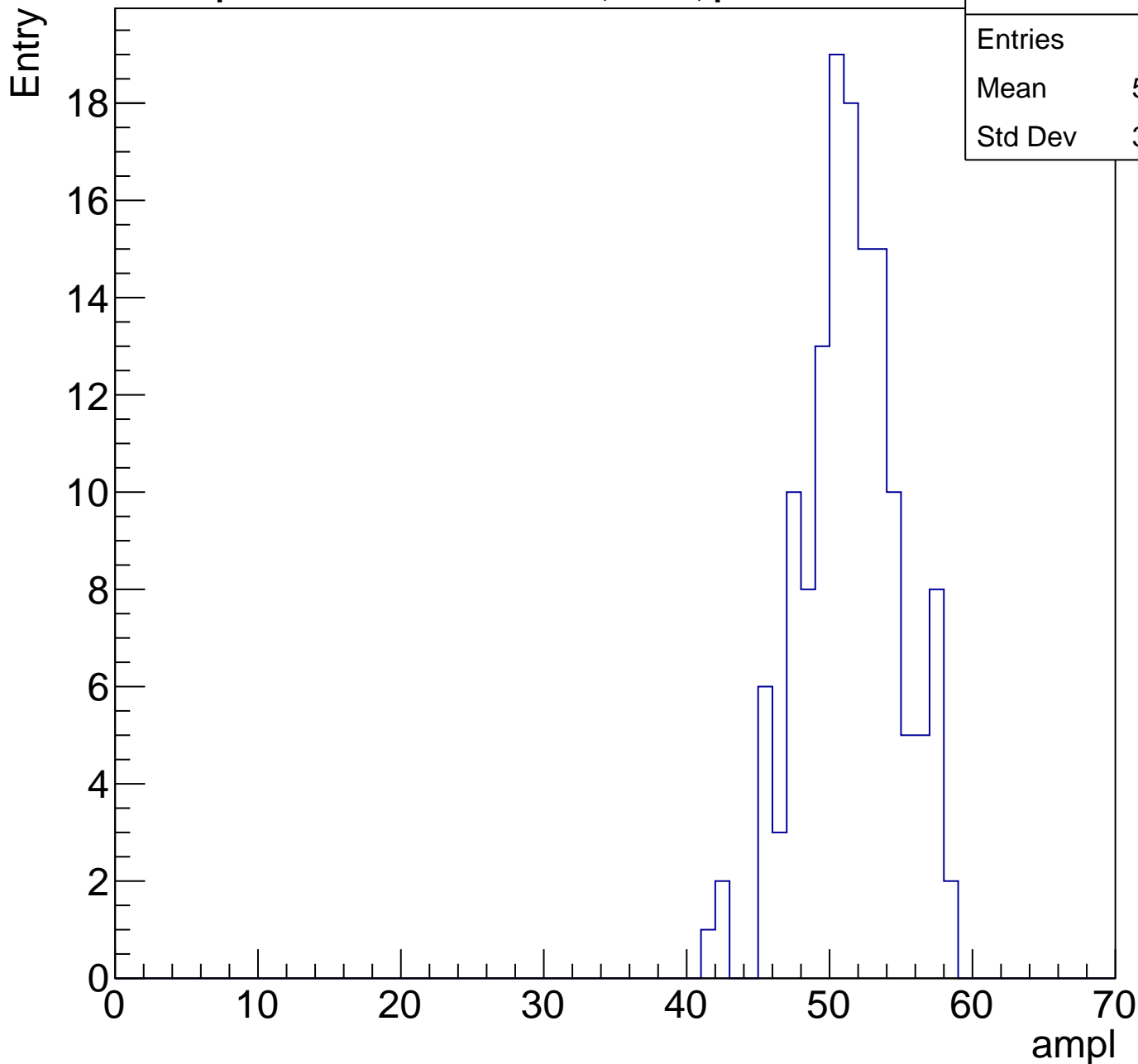
Gaus Width: 2.5263



B0L101S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	50.96
Std Dev	3.393



B0L101S, U14-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

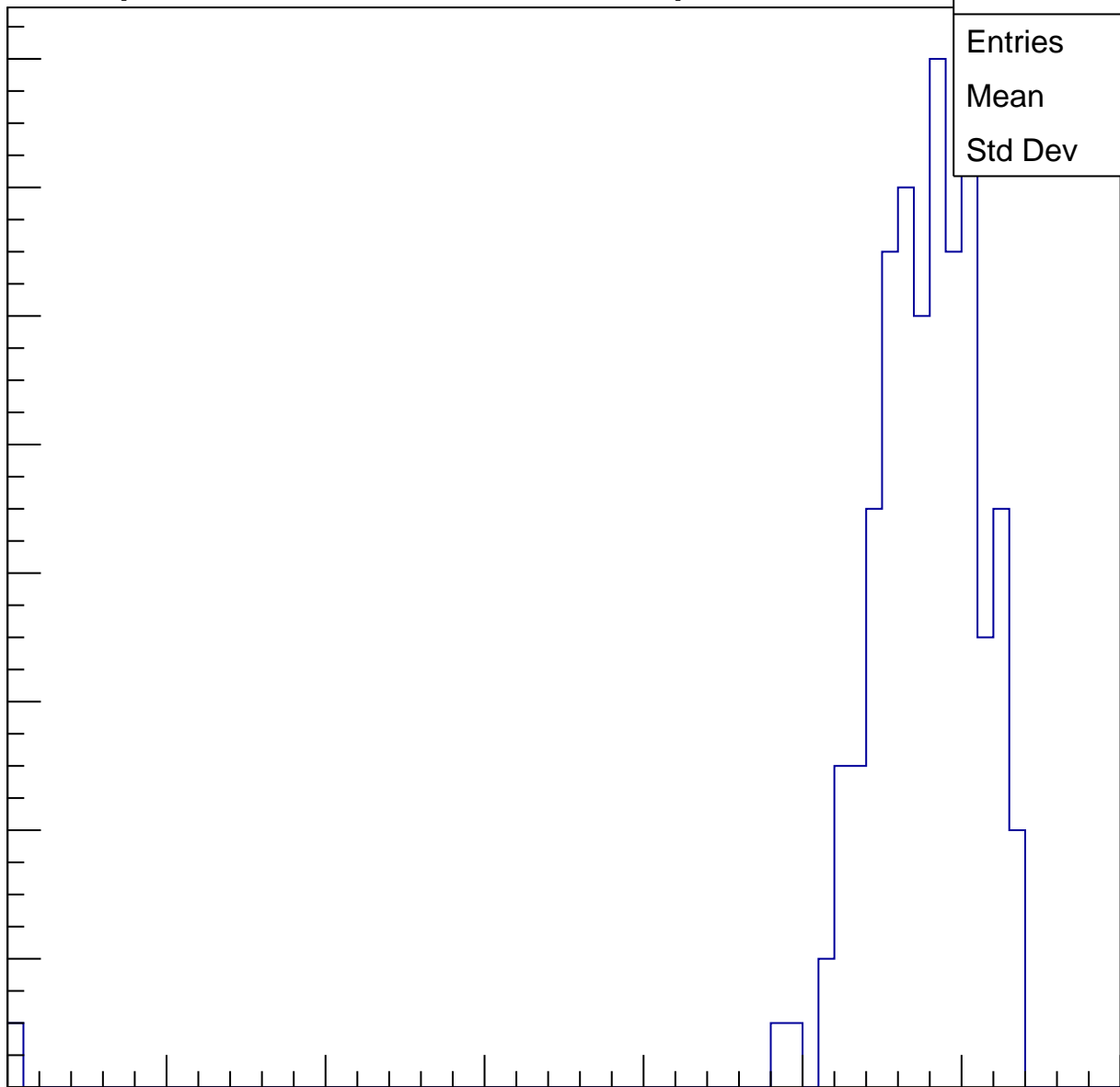
Entries	127
Mean	56.89
Std Dev	5.953

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	52
Mean	59.48
Std Dev	8.606

ampl

0

10

20

30

40

50

60

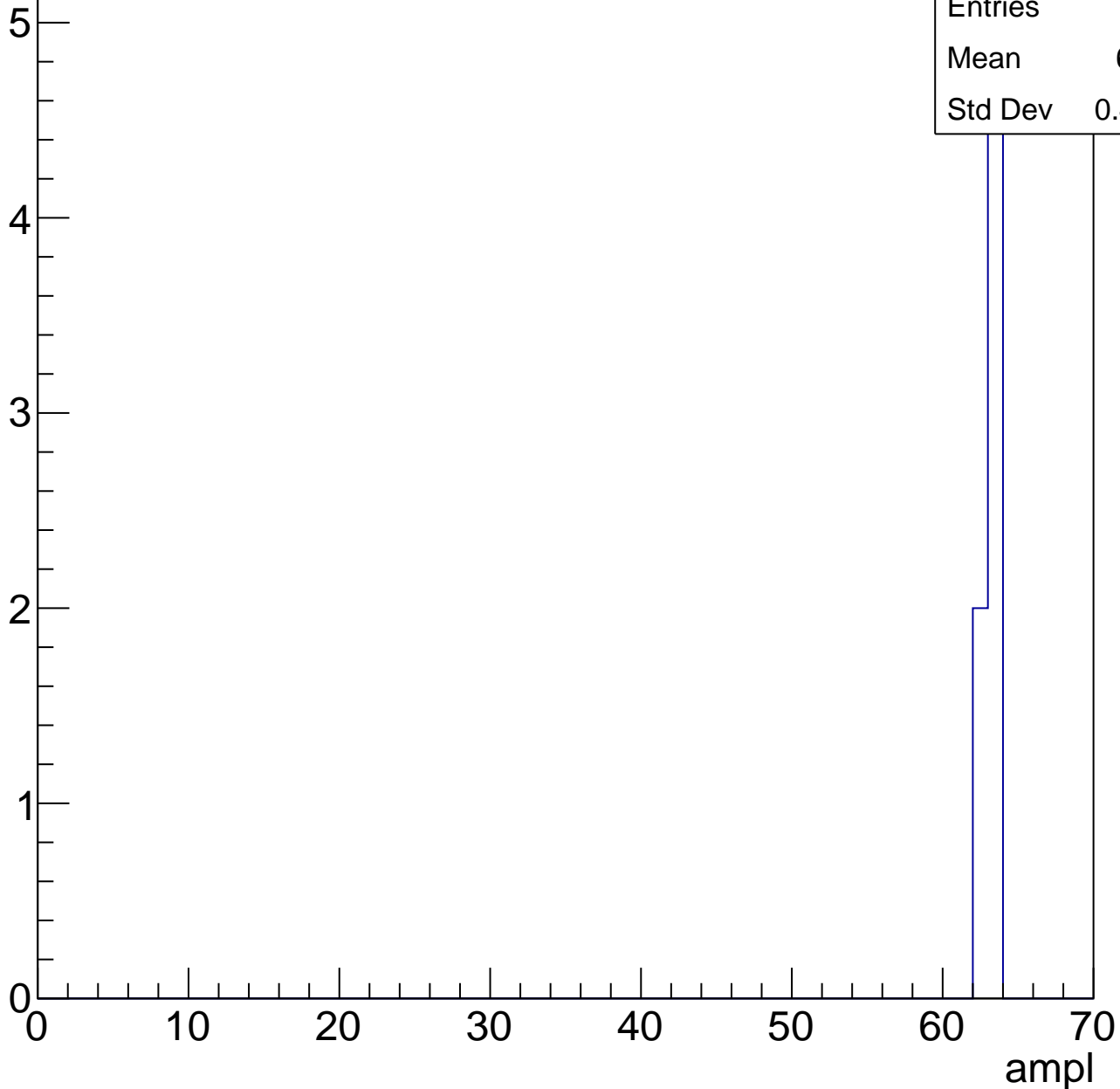
70

B0L101S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B0L101S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch52, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	28.15
Std Dev	3.57

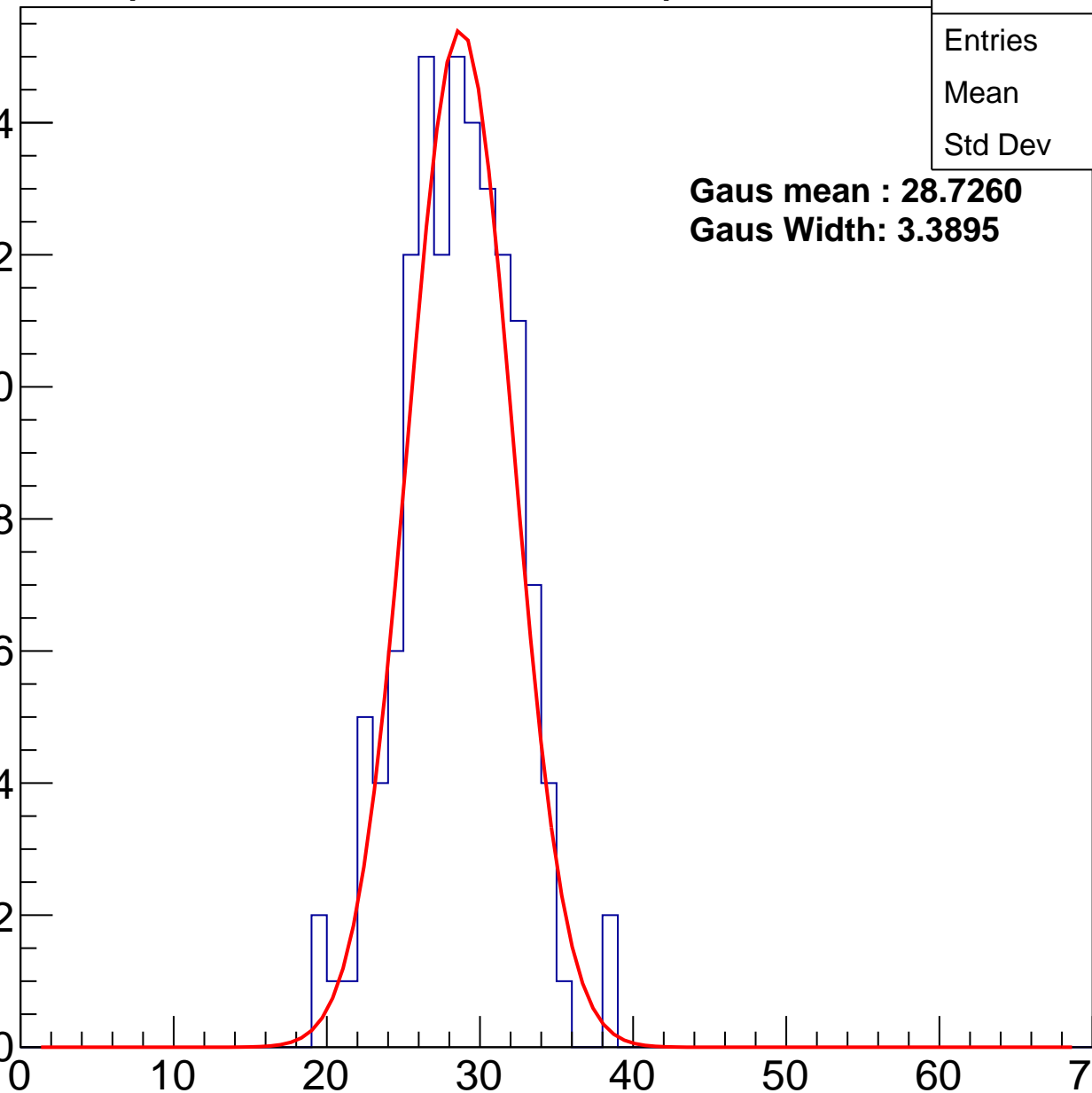
Gaus mean : 28.7260

Gaus Width: 3.3895

Entry

14
12
10
8
6
4
2
0

ampl



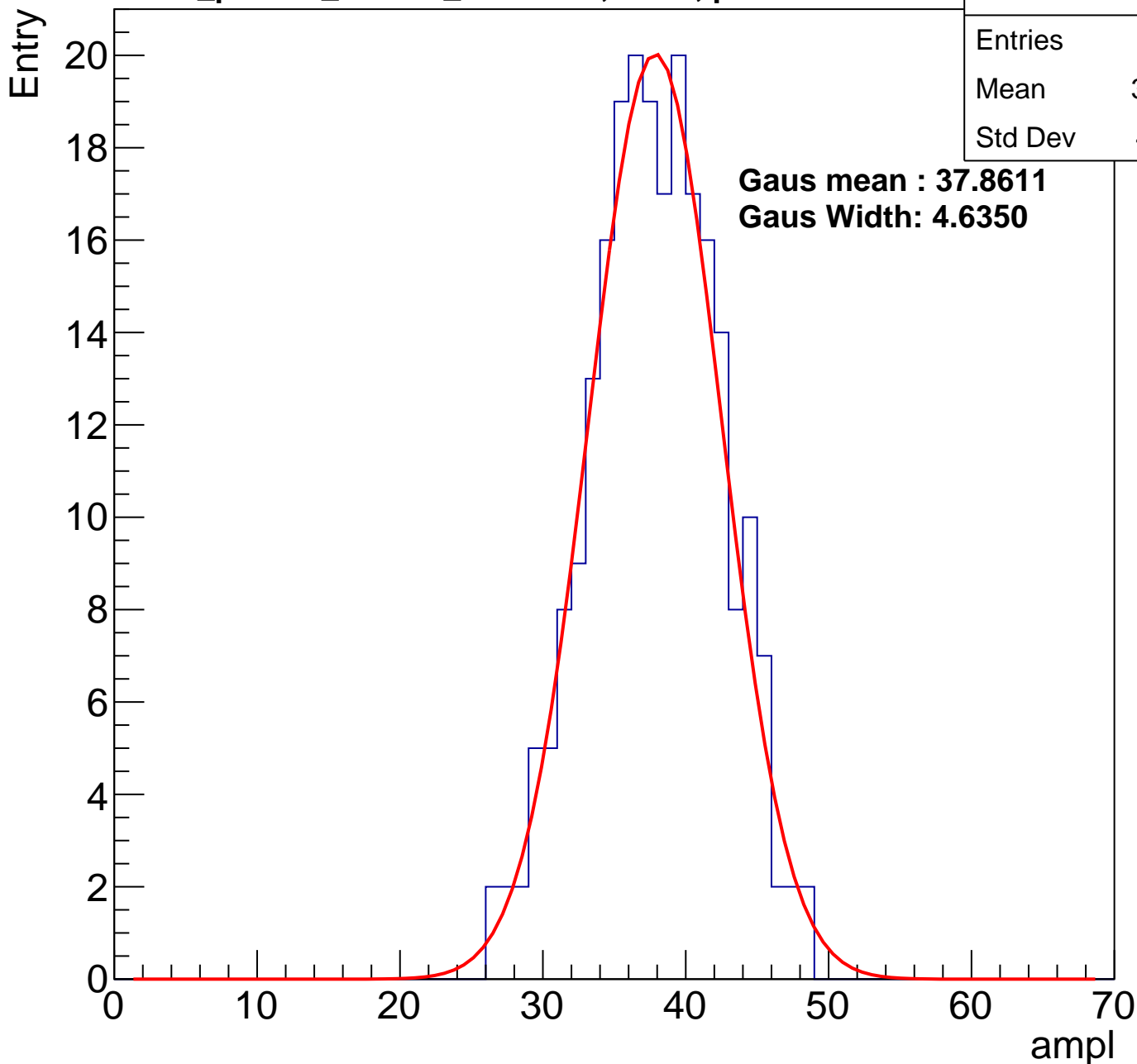
B0L101S, U14-ch52, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	235
Mean	37.38
Std Dev	4.511

Gaus mean : 37.8611

Gaus Width: 4.6350



B0L101S, U14-ch52, adc2

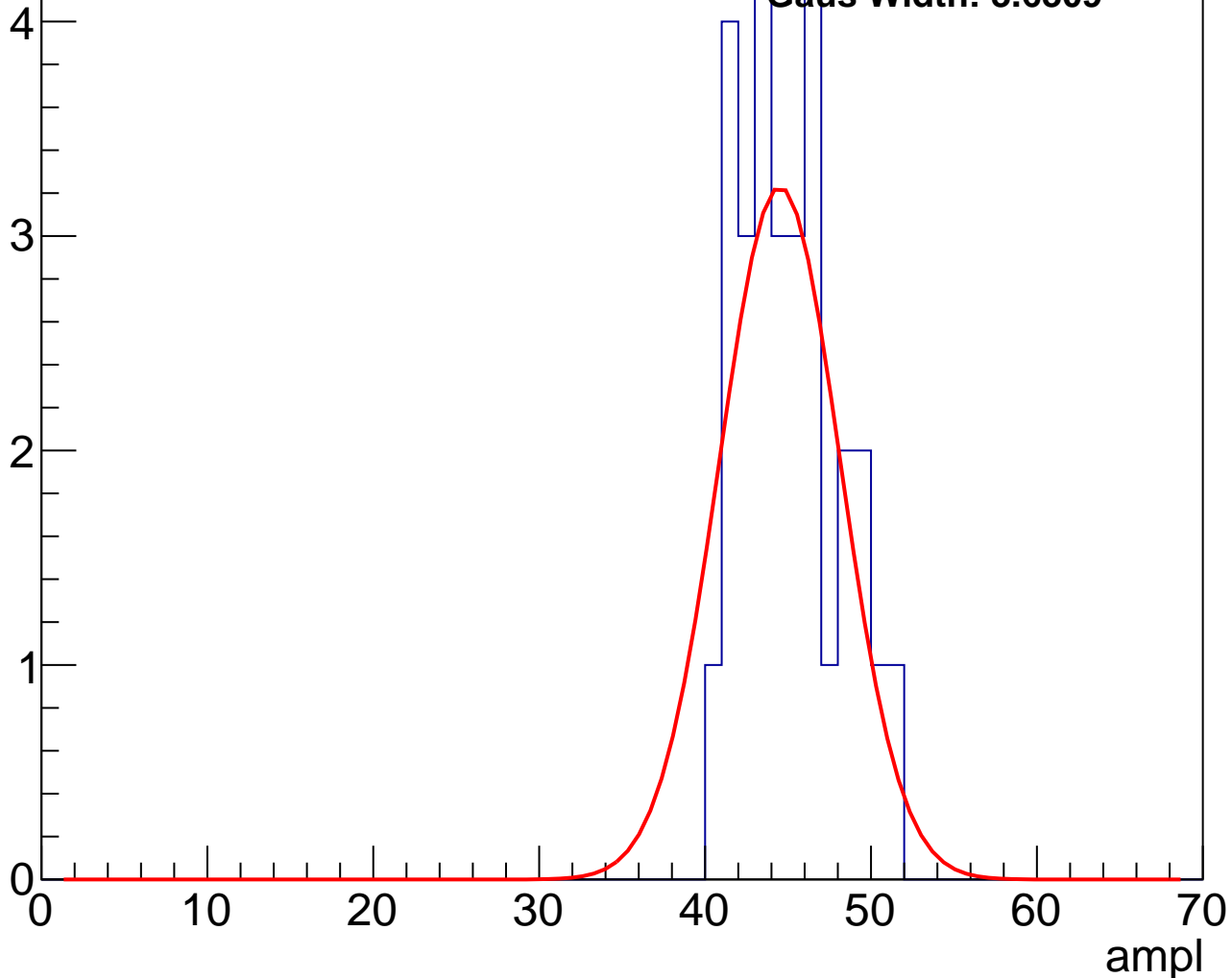
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	31
Mean	44.65
Std Dev	2.869

Gaus mean : 44.5046

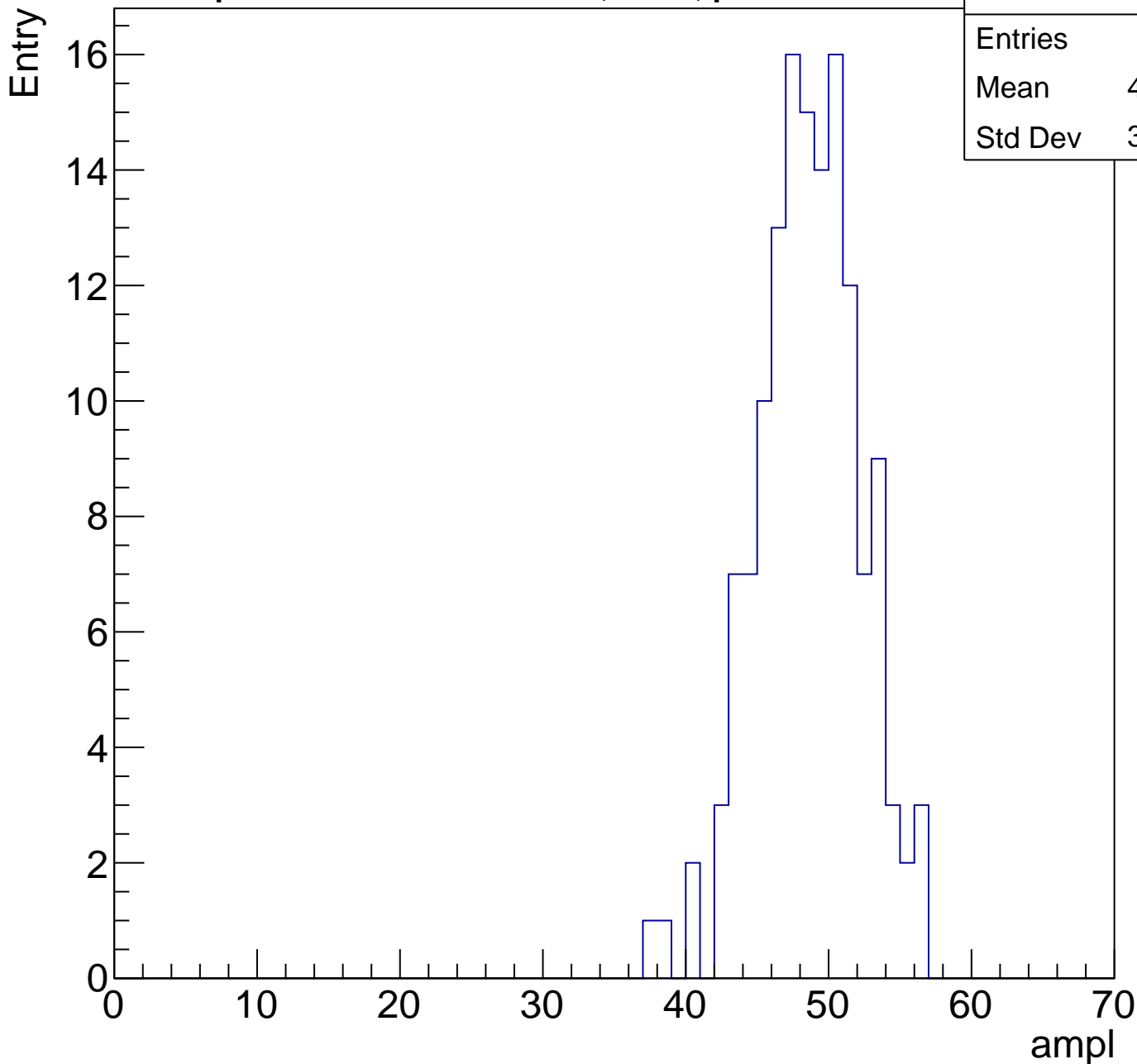
Gaus Width: 3.6309



B0L101S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	48.15
Std Dev	3.593



B0L101S, U14-ch52, adc4

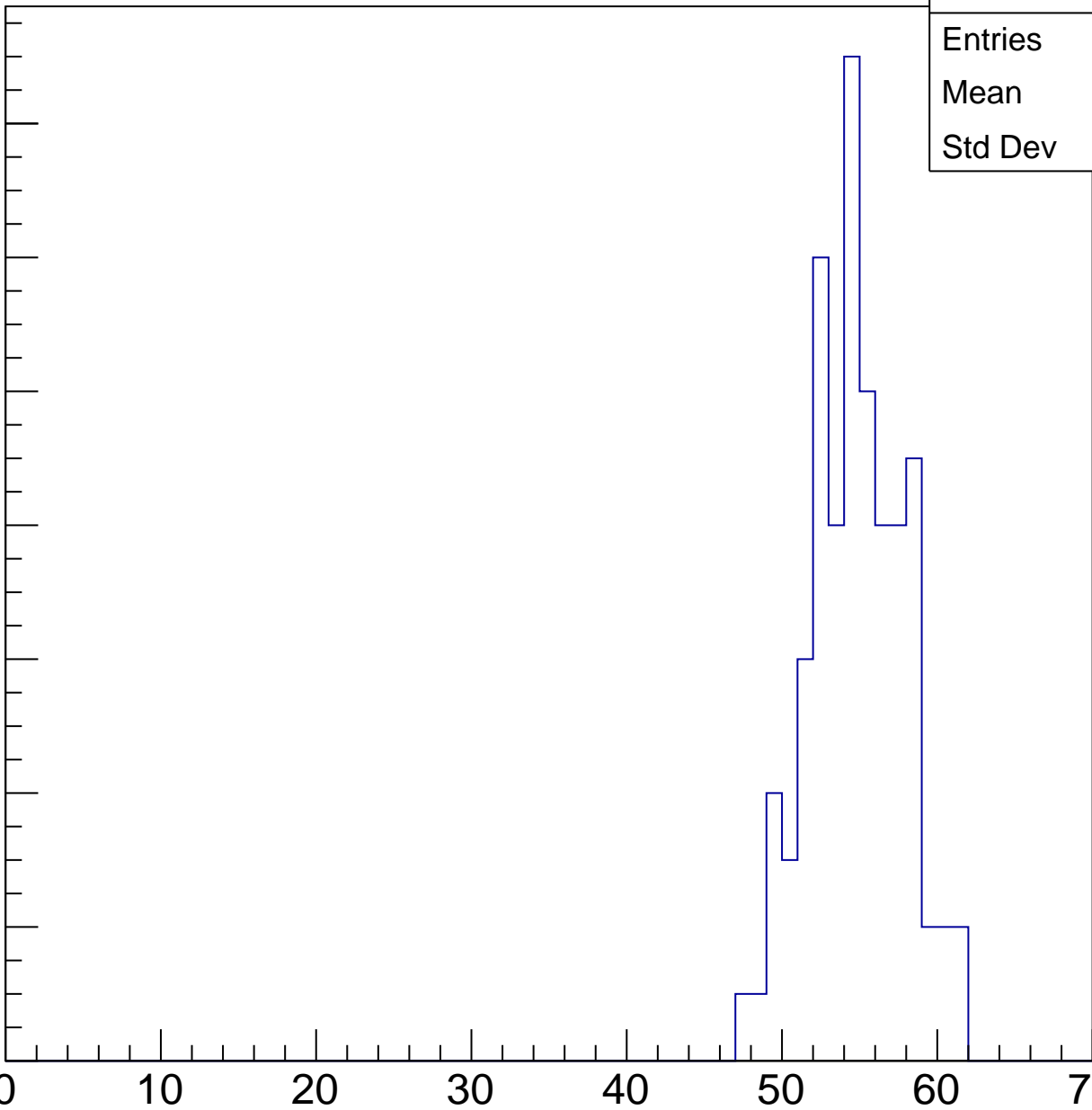
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	54.3
Std Dev	2.989

ampl



B0L101S, U14-ch52, adc5

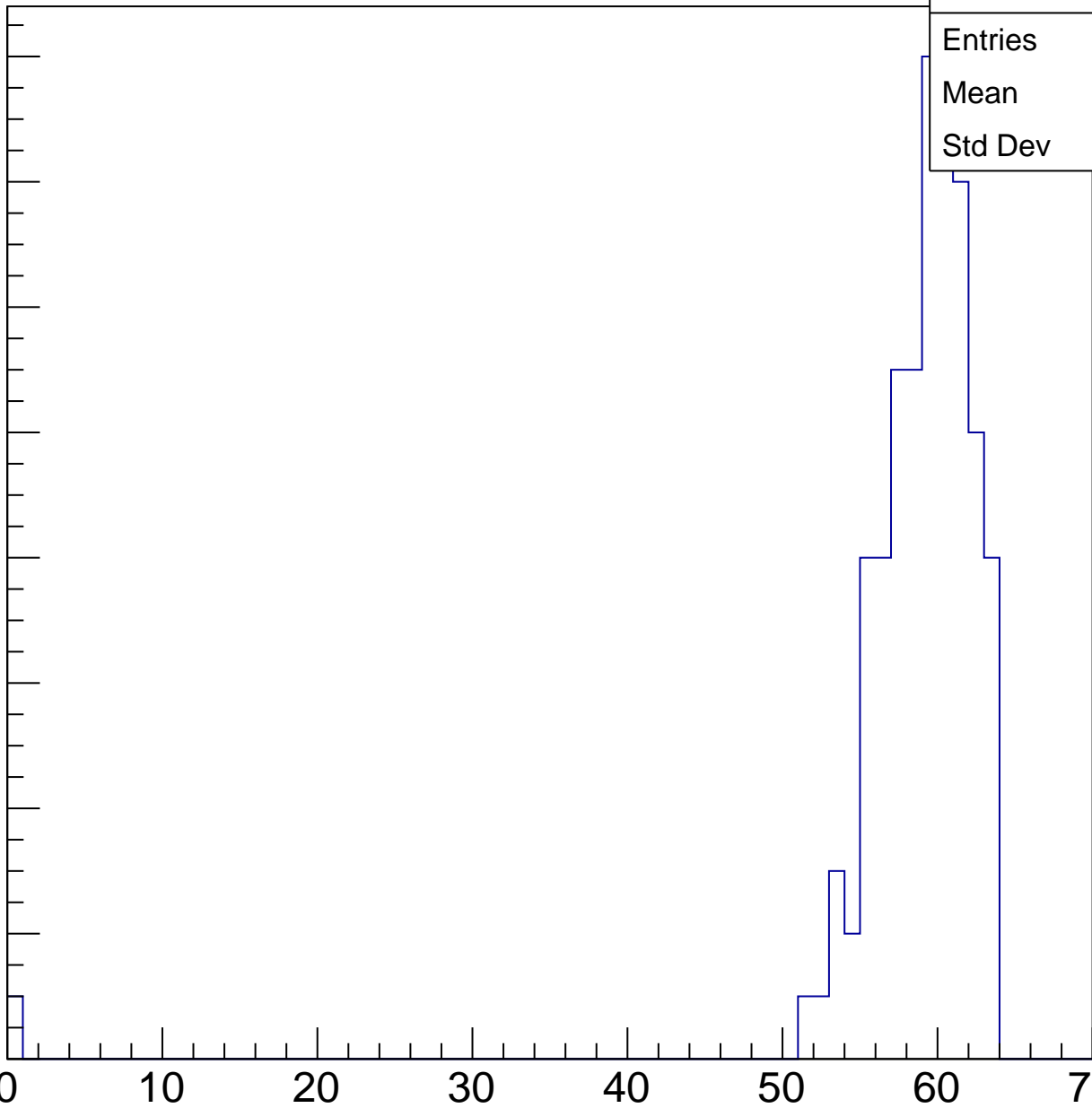
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.21
Std Dev	6.228

ampl



B0L101S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	33
Mean	61.55
Std Dev	1.742

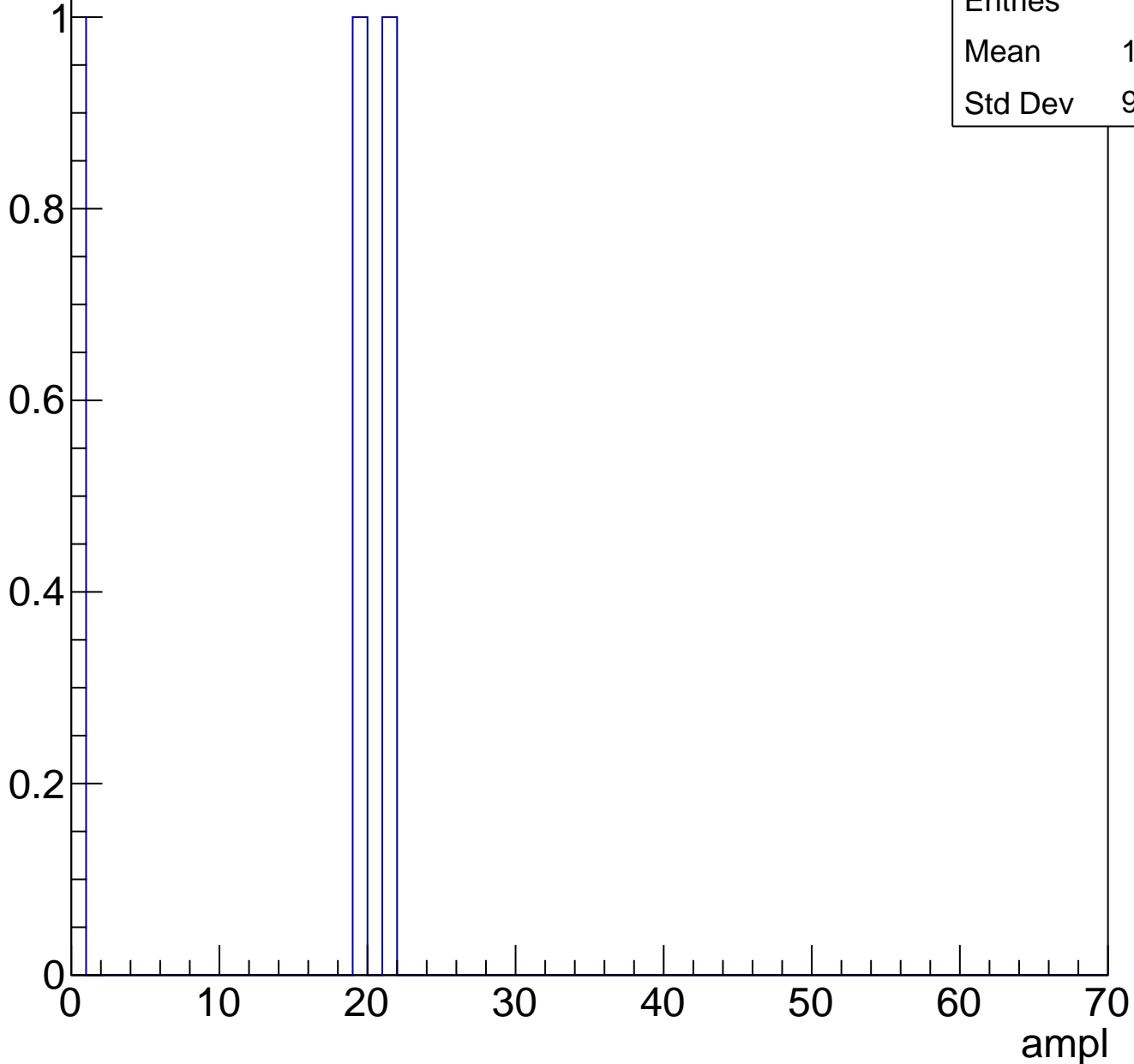
ampl

0 10 20 30 40 50 60 70

B0L101S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	29.39
Std Dev	4.233

Gaus mean : 30.2318

Gaus Width: 3.5532

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

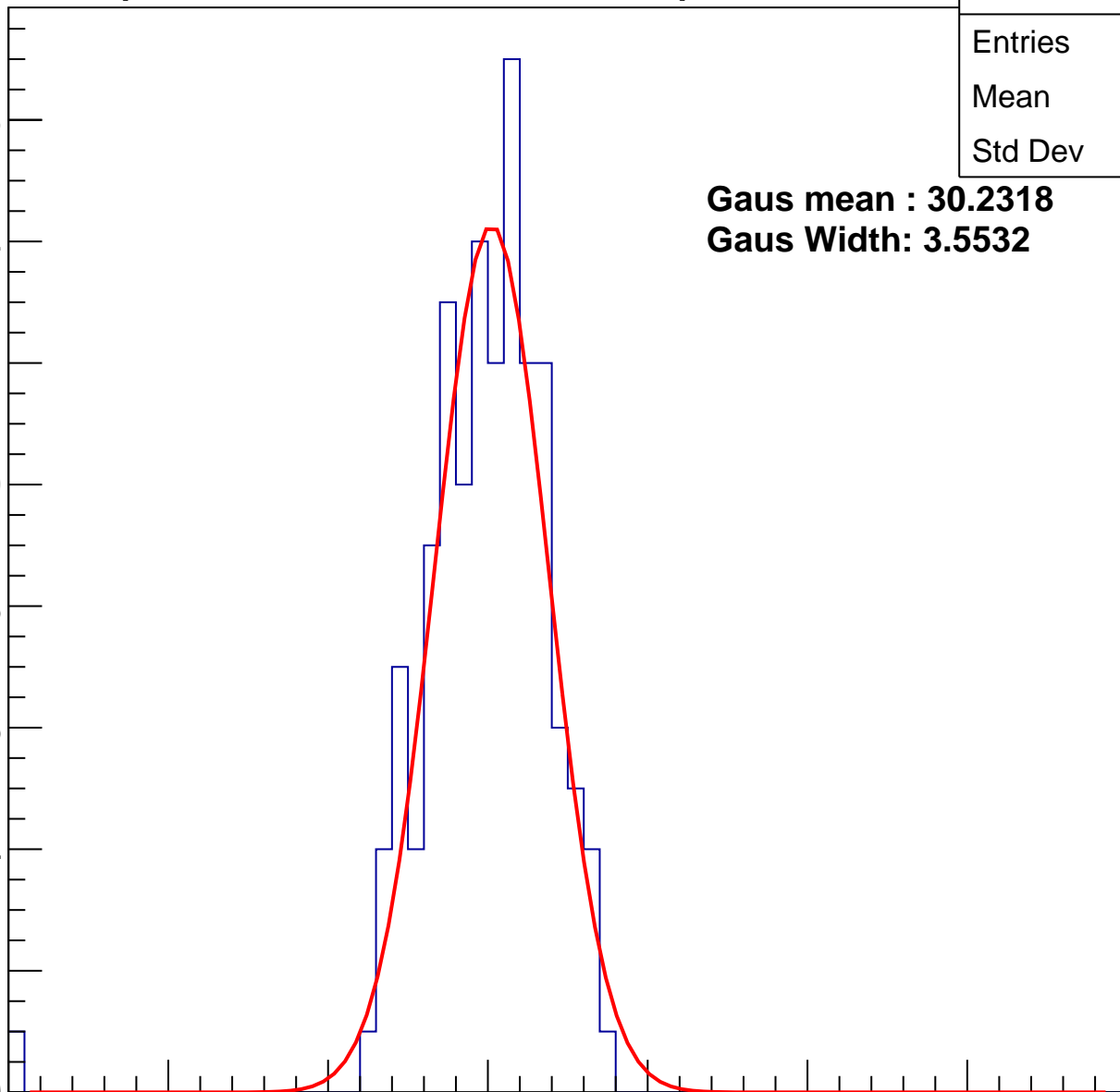
40

50

60

70

ampl



B0L101S, U14-ch53, adc1

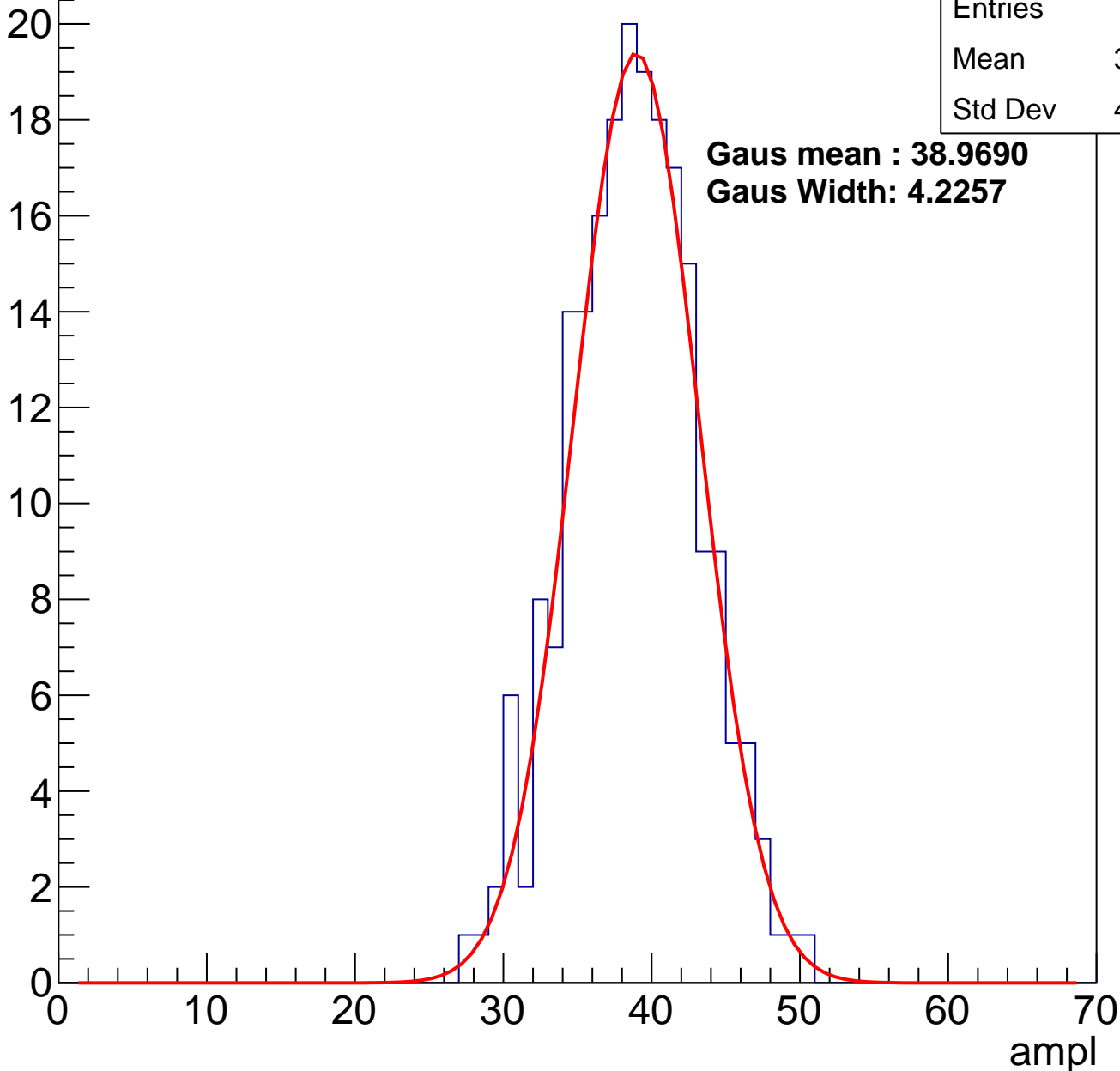
calib_packv5_042523_0143.root, FC#1, port C1

Entries	212
Mean	38.32
Std Dev	4.313

Gaus mean : 38.9690

Gaus Width: 4.2257

Entry



B0L101S, U14-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	78
Mean	45.05
Std Dev	3.046

Gaus mean : 45.5653

Gaus Width: 2.8490

0

10

20

30

40

50

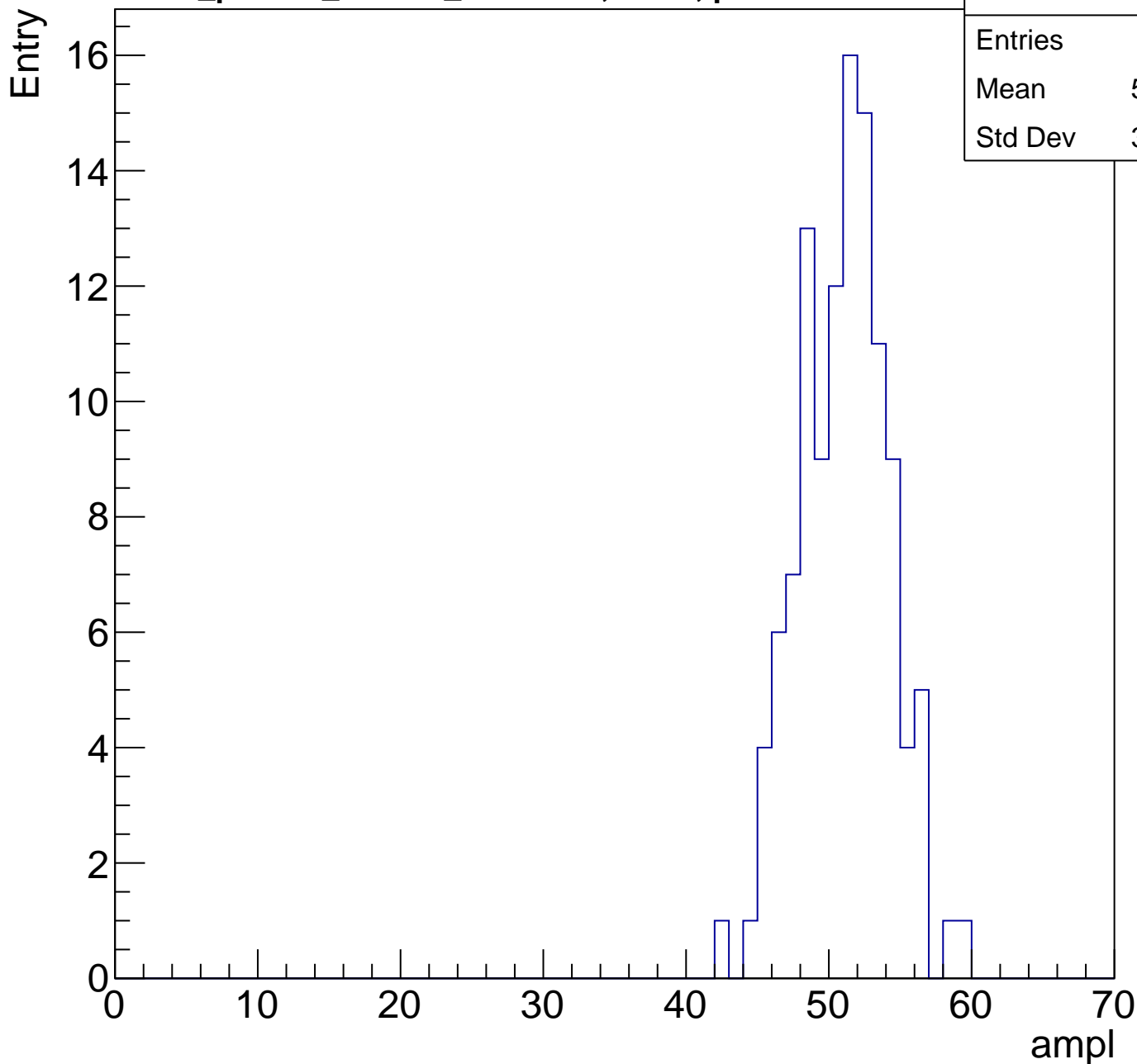
60

ampl

B0L101S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	50.59
Std Dev	3.126



B0L101S, U14-ch53, adc4

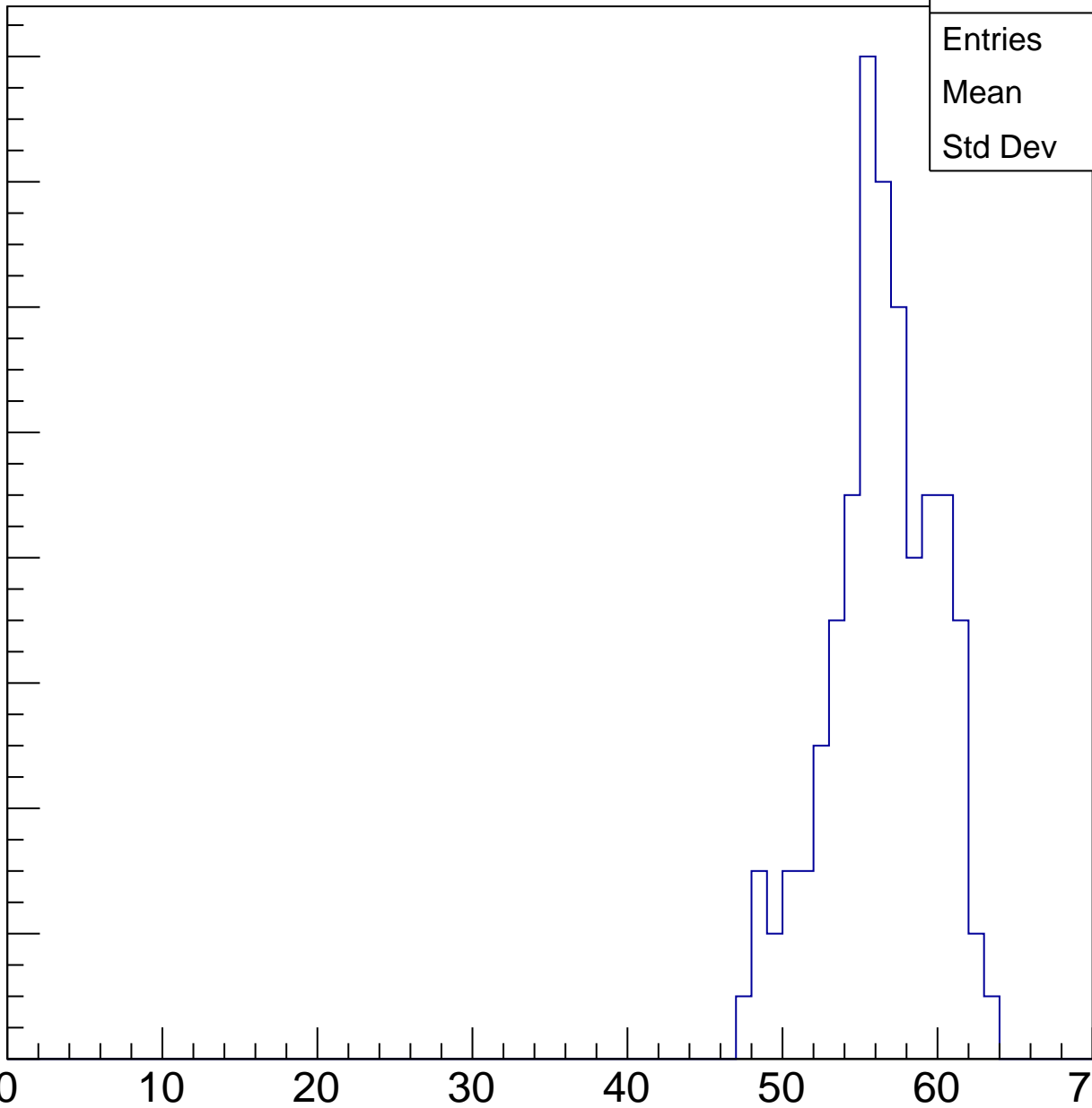
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	55.91
Std Dev	3.439

ampl



B0L101S, U14-ch53, adc5

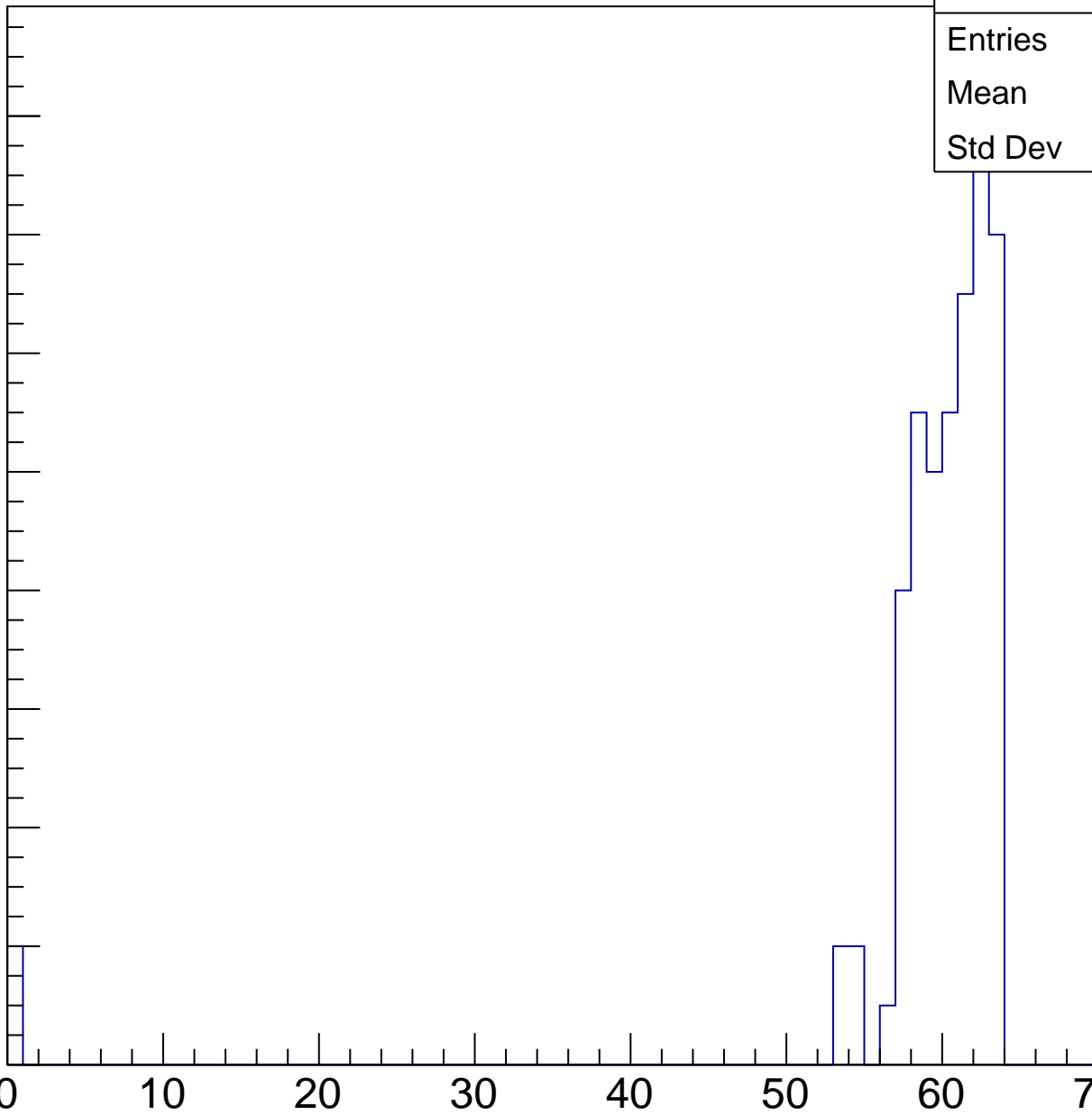
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.71
Std Dev	9.12

ampl

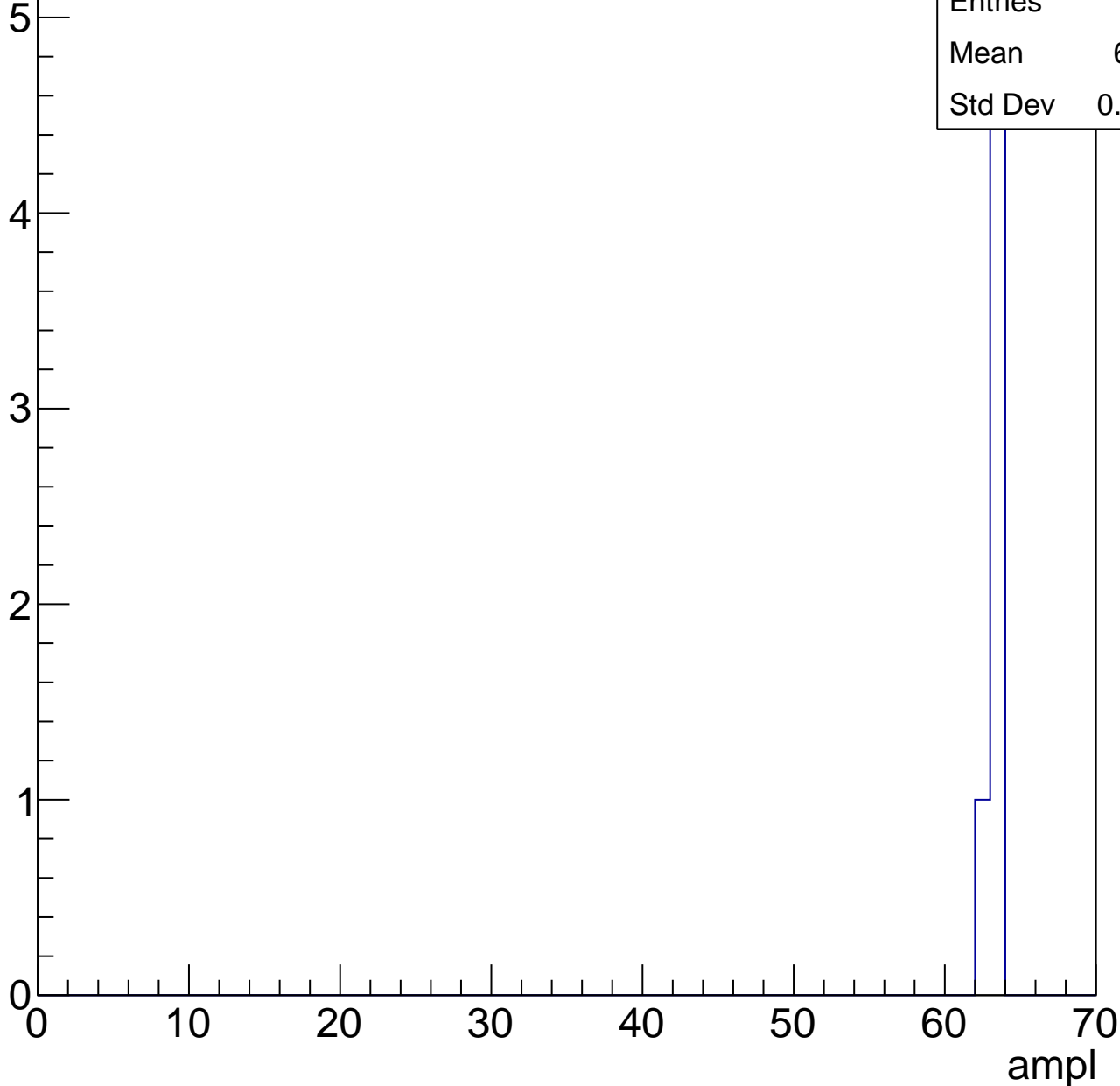


B0L101S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch54, adc0

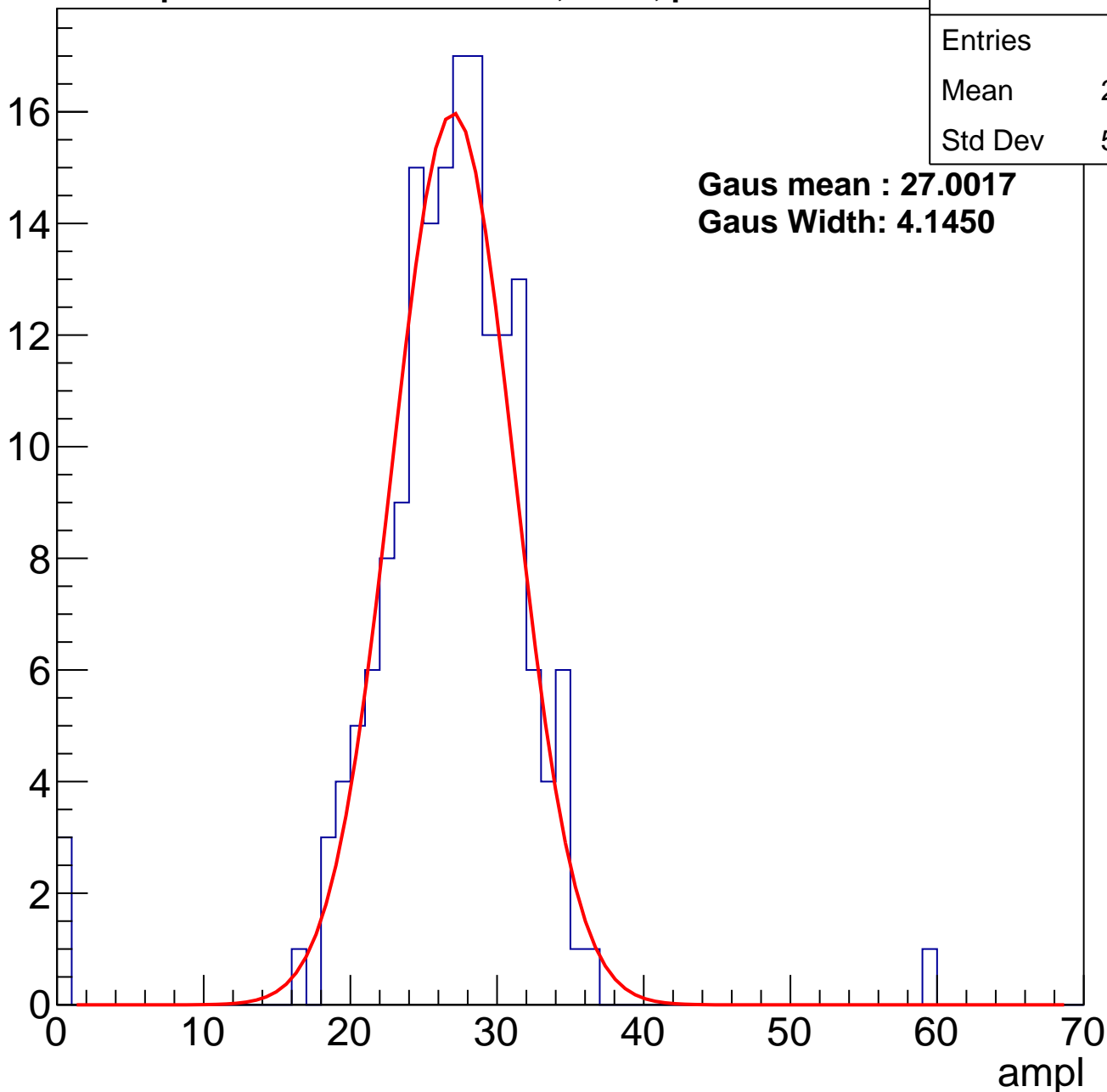
calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	26.35
Std Dev	5.832

Gaus mean : 27.0017

Gaus Width: 4.1450

Entry



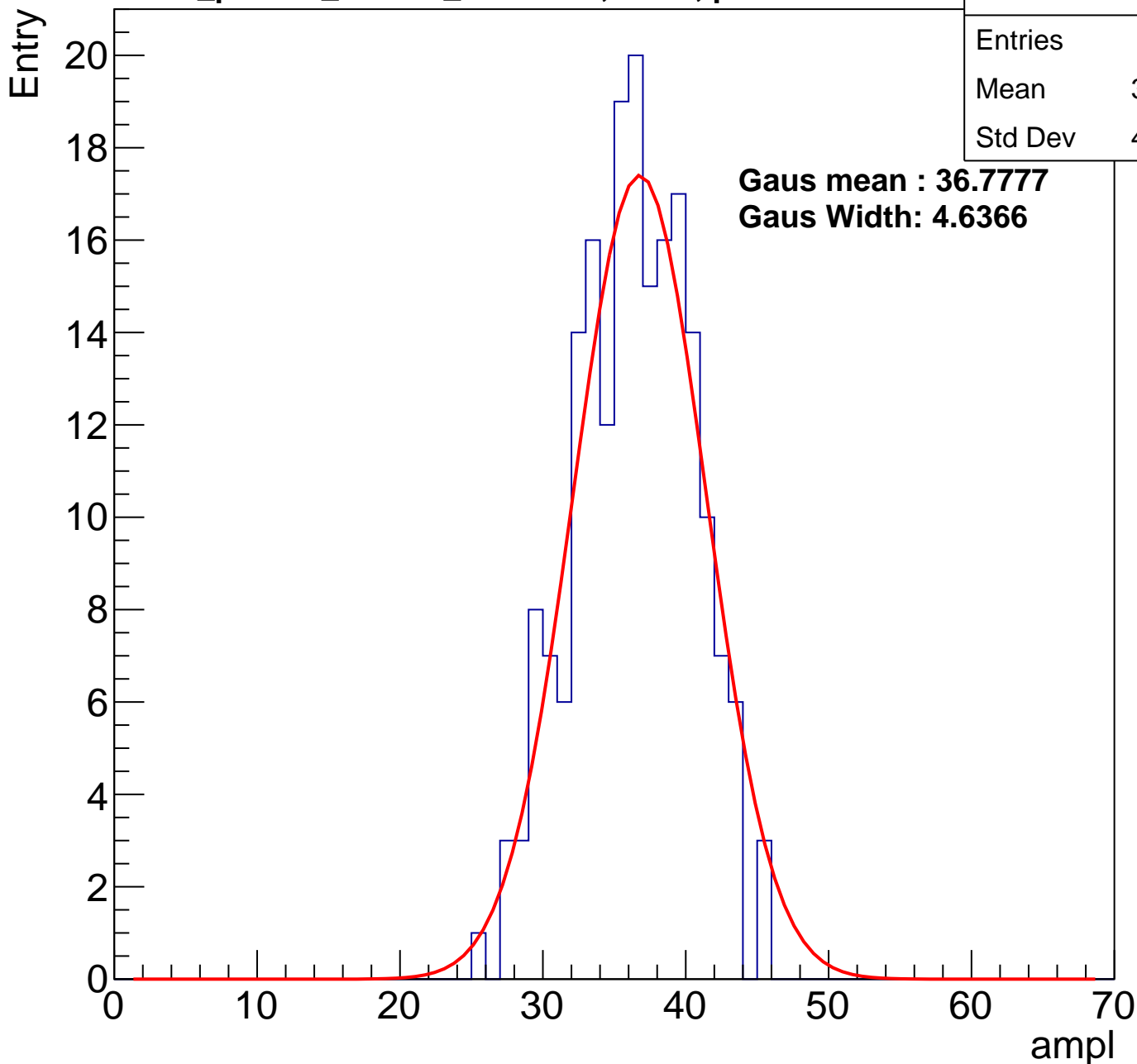
B0L101S, U14-ch54, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	197
Mean	35.89
Std Dev	4.079

Gaus mean : 36.7777

Gaus Width: 4.6366



B0L101S, U14-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	75
Mean	42.44
Std Dev	3.356

Gaus mean : 43.1867

Gaus Width: 2.9653

0

2

4

6

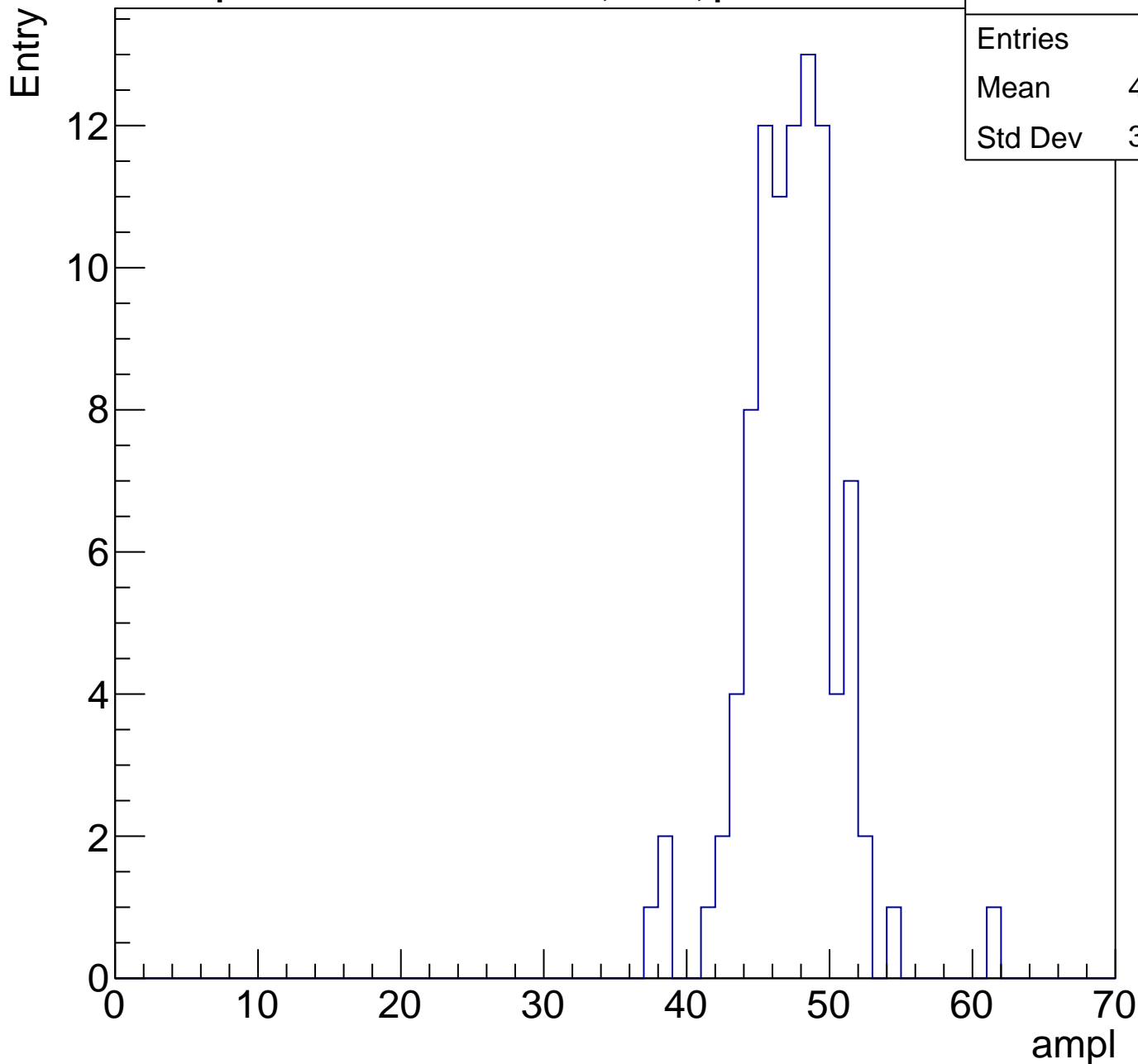
8

10

B0L101S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	46.88
Std Dev	3.353



B0L101S, U14-ch54, adc4

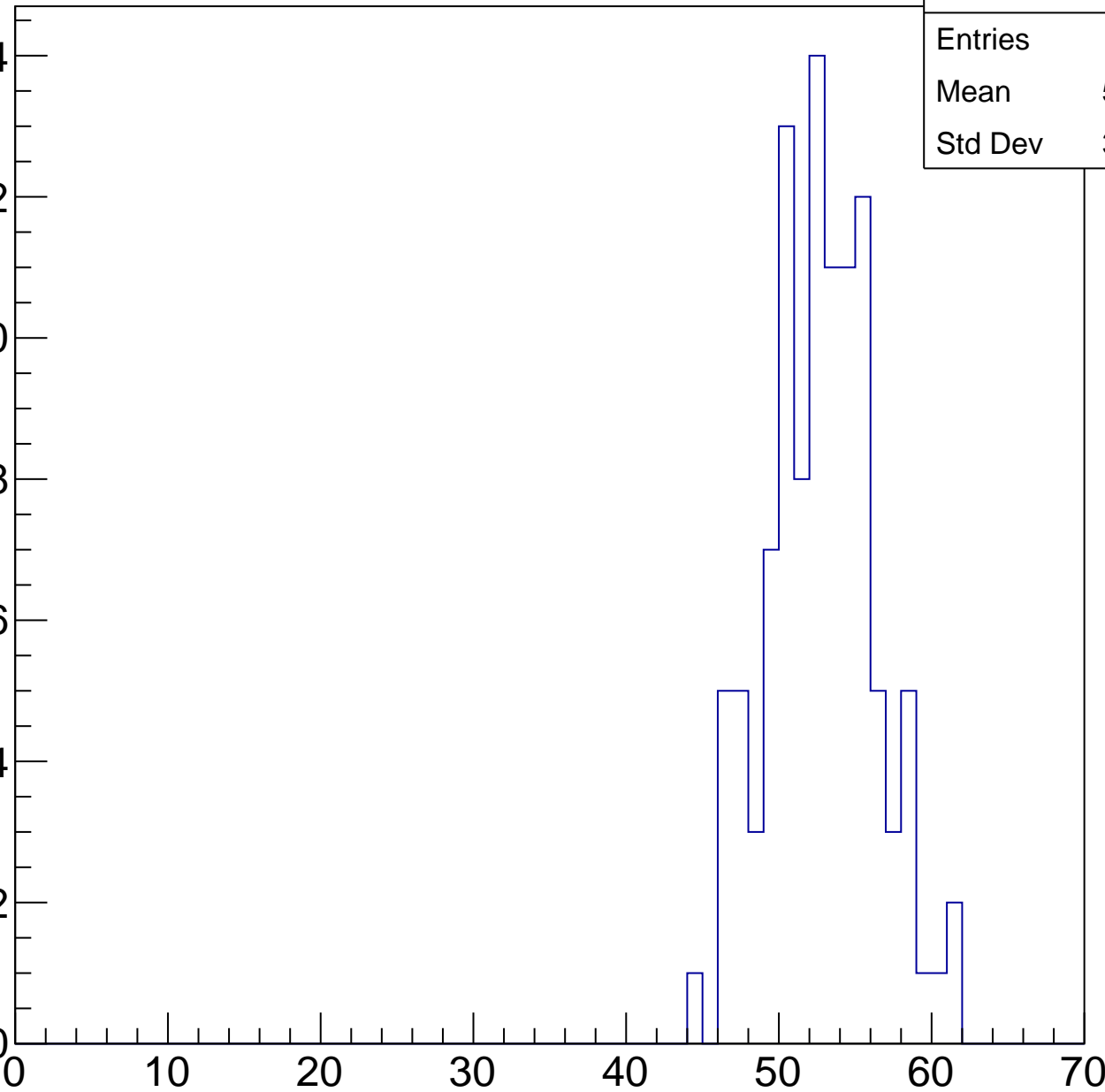
calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	52.35
Std Dev	3.486

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch54, adc5

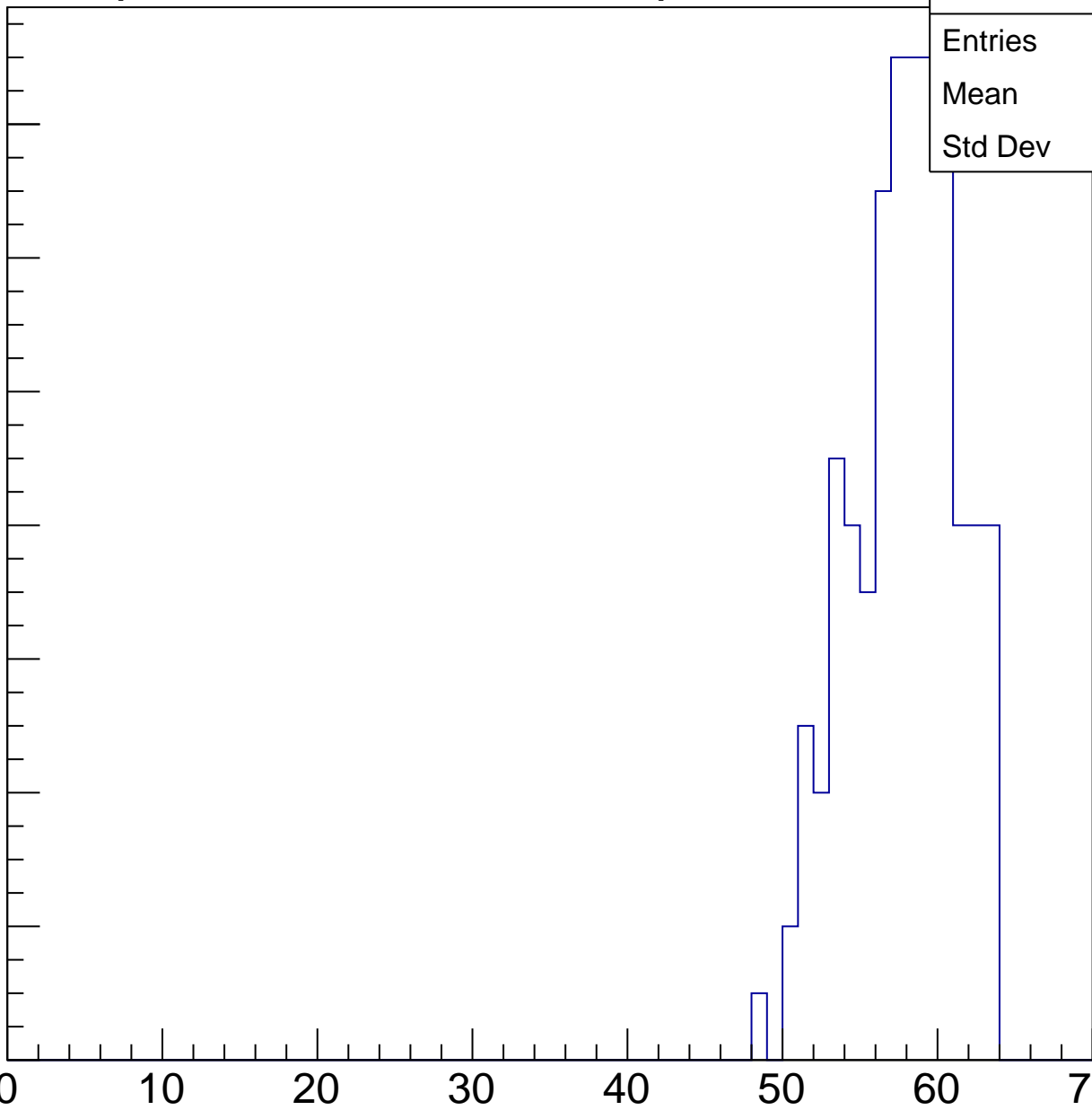
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	133
Mean	57.38
Std Dev	3.384

ampl



B0L101S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

46

Mean

58.33

Std Dev

12.61

ampl

0

10

20

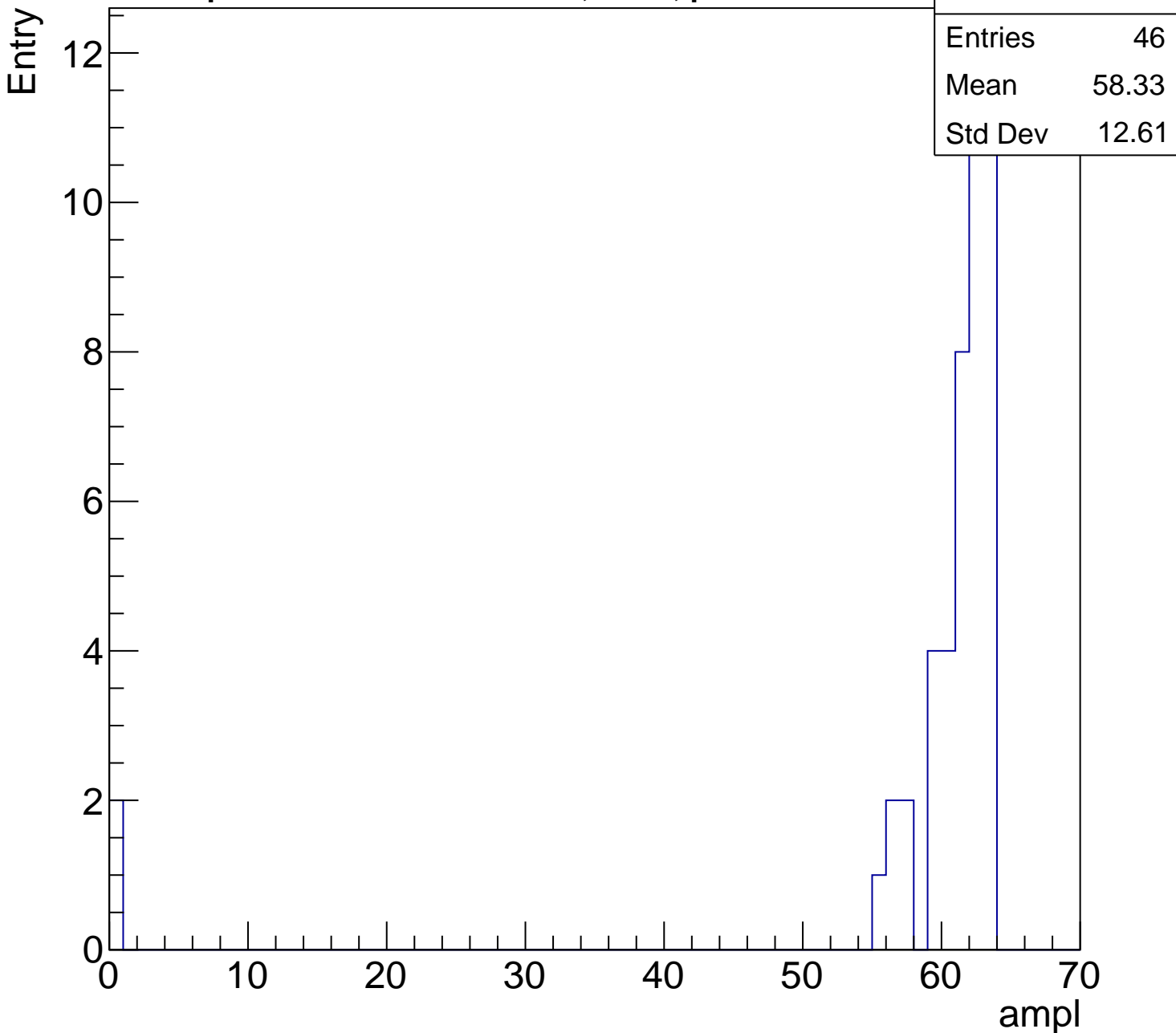
30

40

50

60

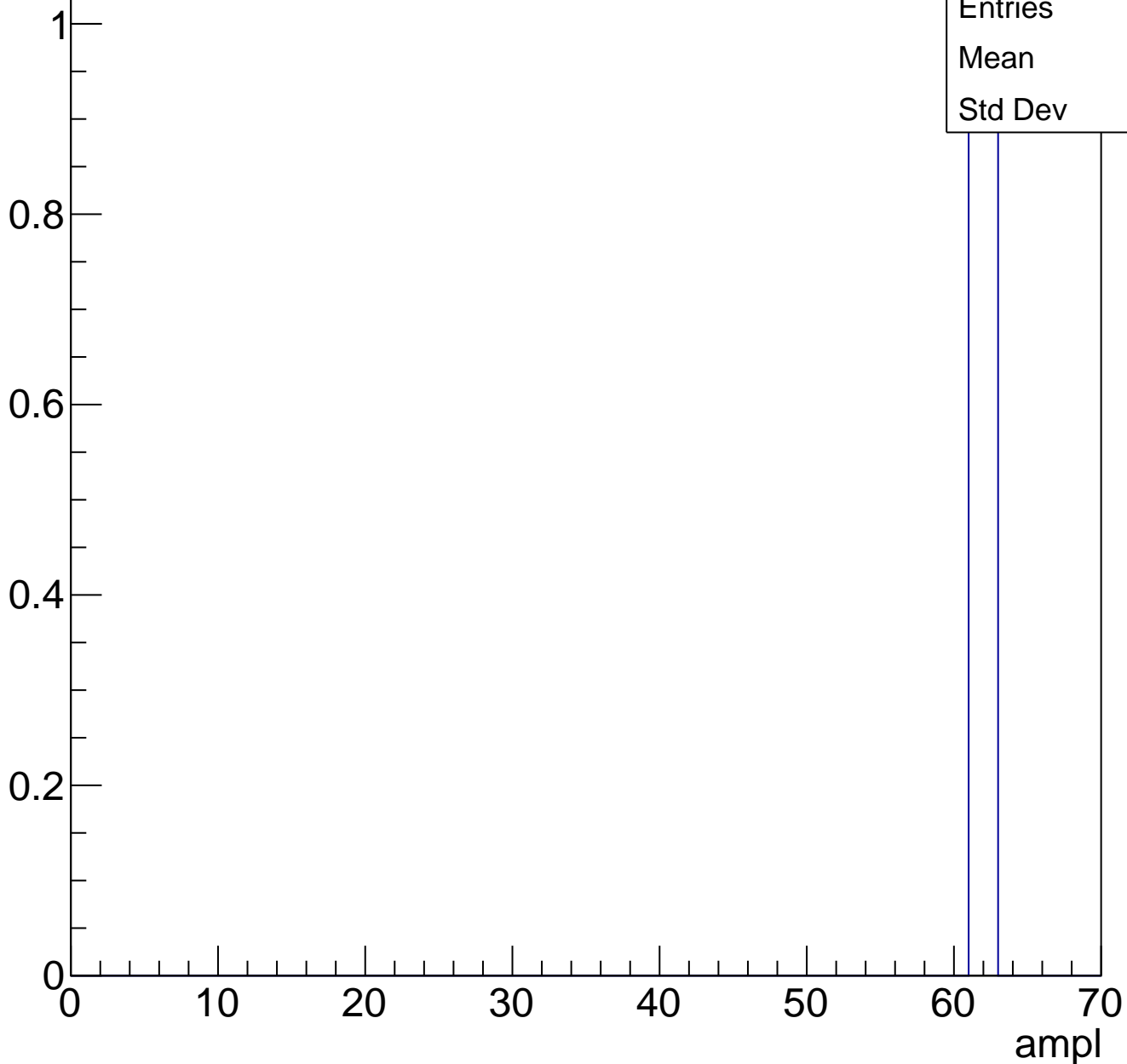
70



B0L101S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch55, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	30.32
Std Dev	4.558

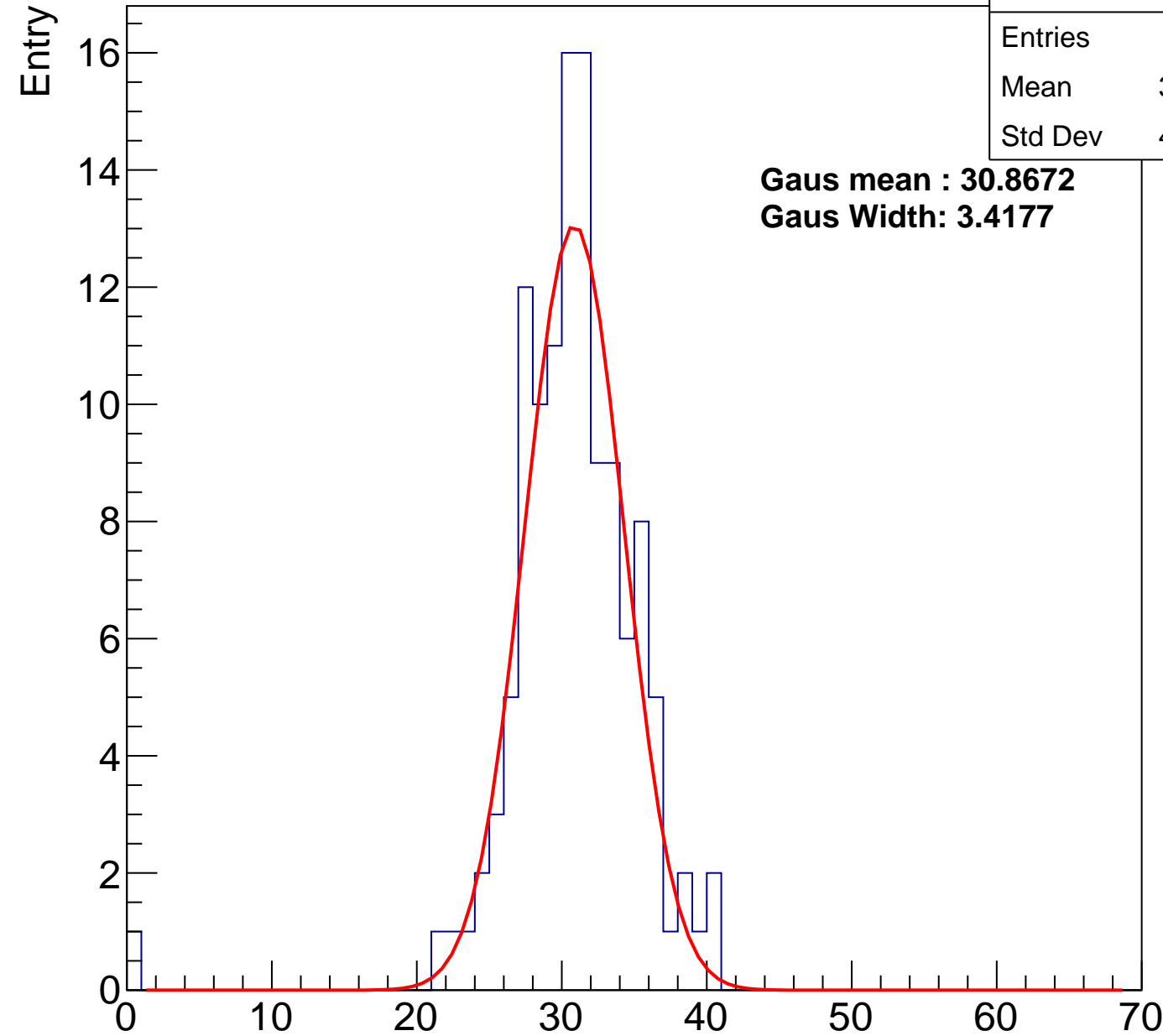
Gaus mean : 30.8672

Gaus Width: 3.4177

Entry

16
14
12
10
8
6
4
2
0

ampl



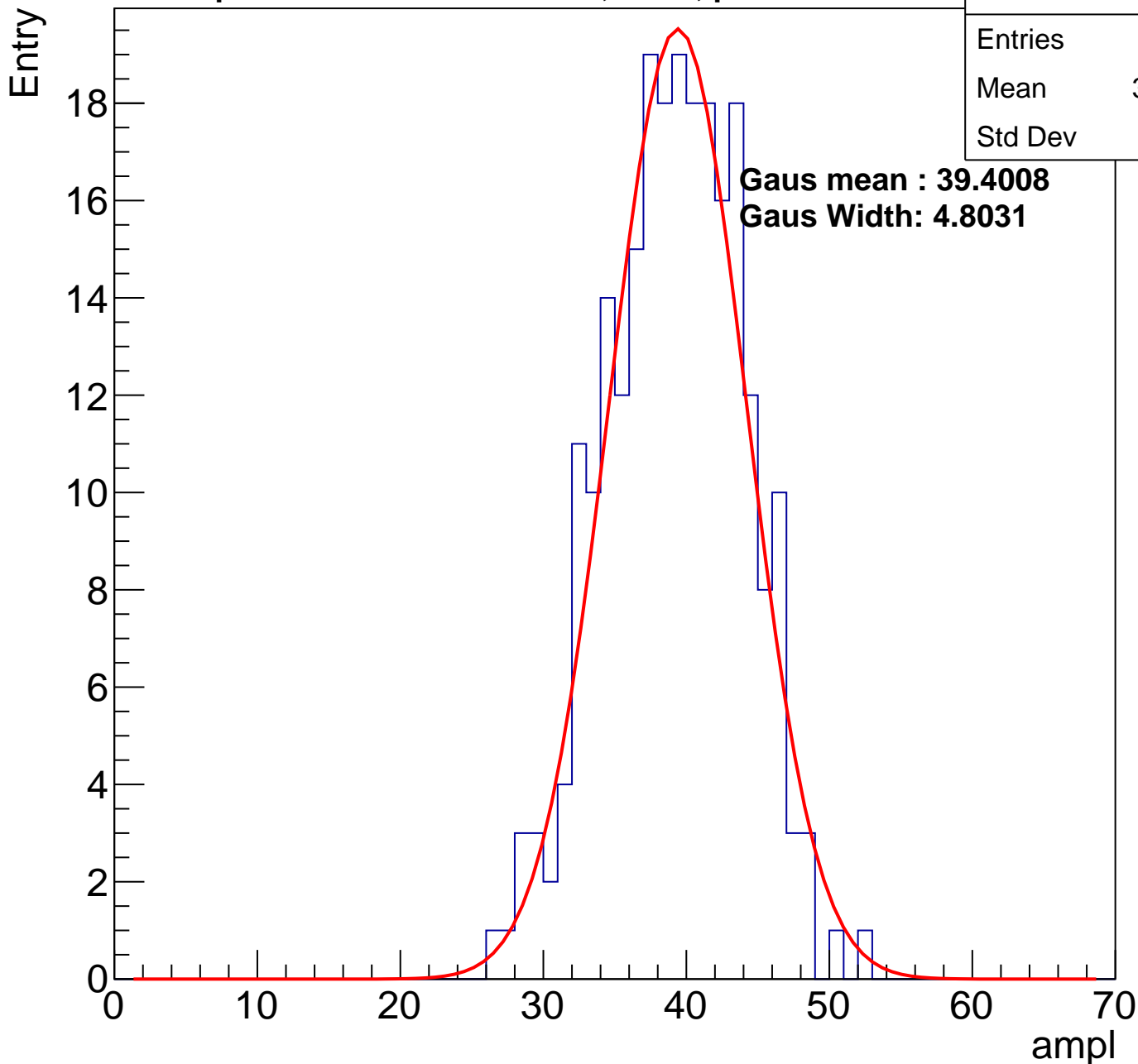
B0L101S, U14-ch55, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	240
Mean	38.72
Std Dev	4.73

Gaus mean : 39.4008

Gaus Width: 4.8031



B0L101S, U14-ch55, adc2

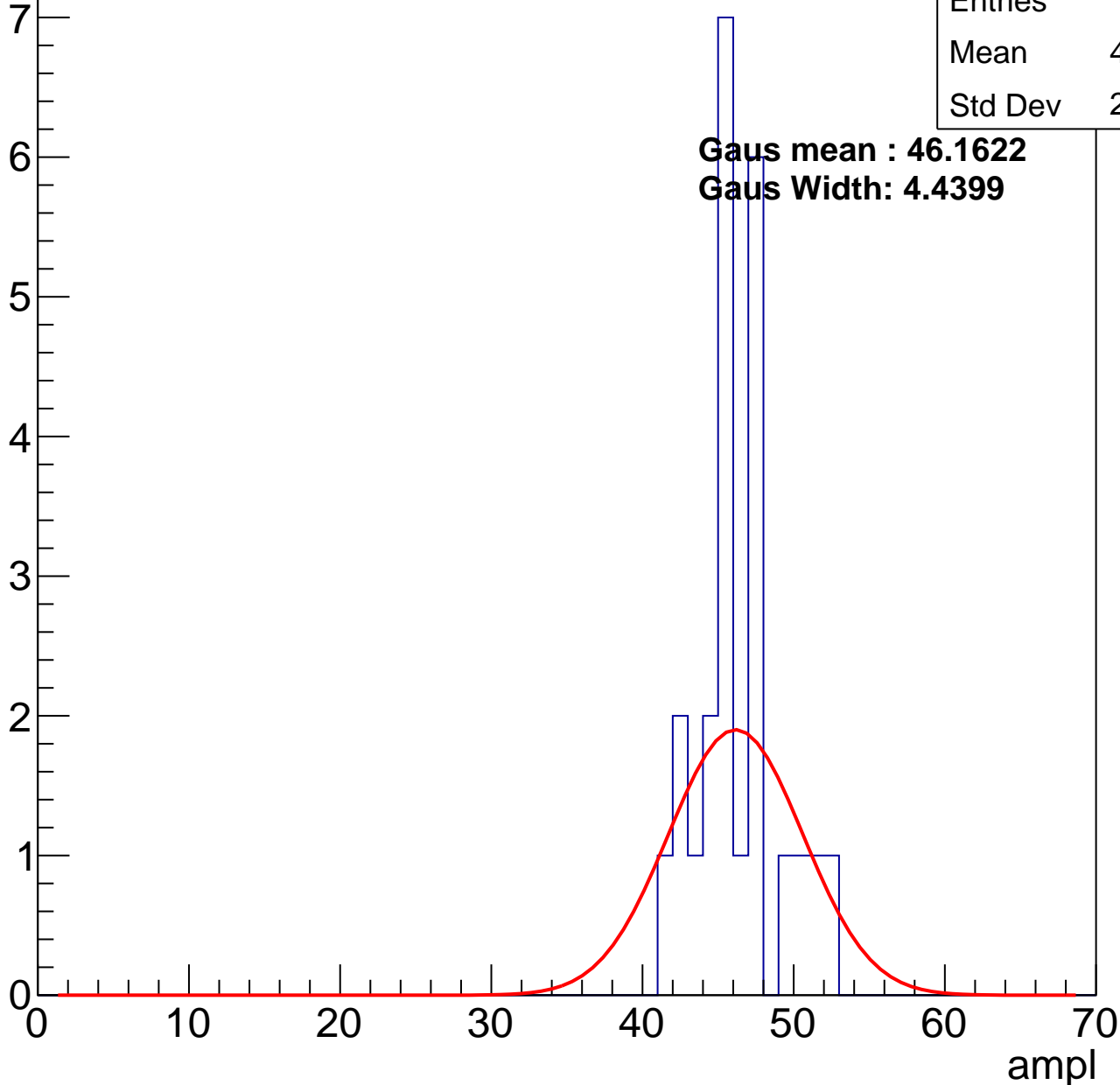
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	45.88
Std Dev	2.682

Gaus mean : 46.1622

Gaus Width: 4.4399

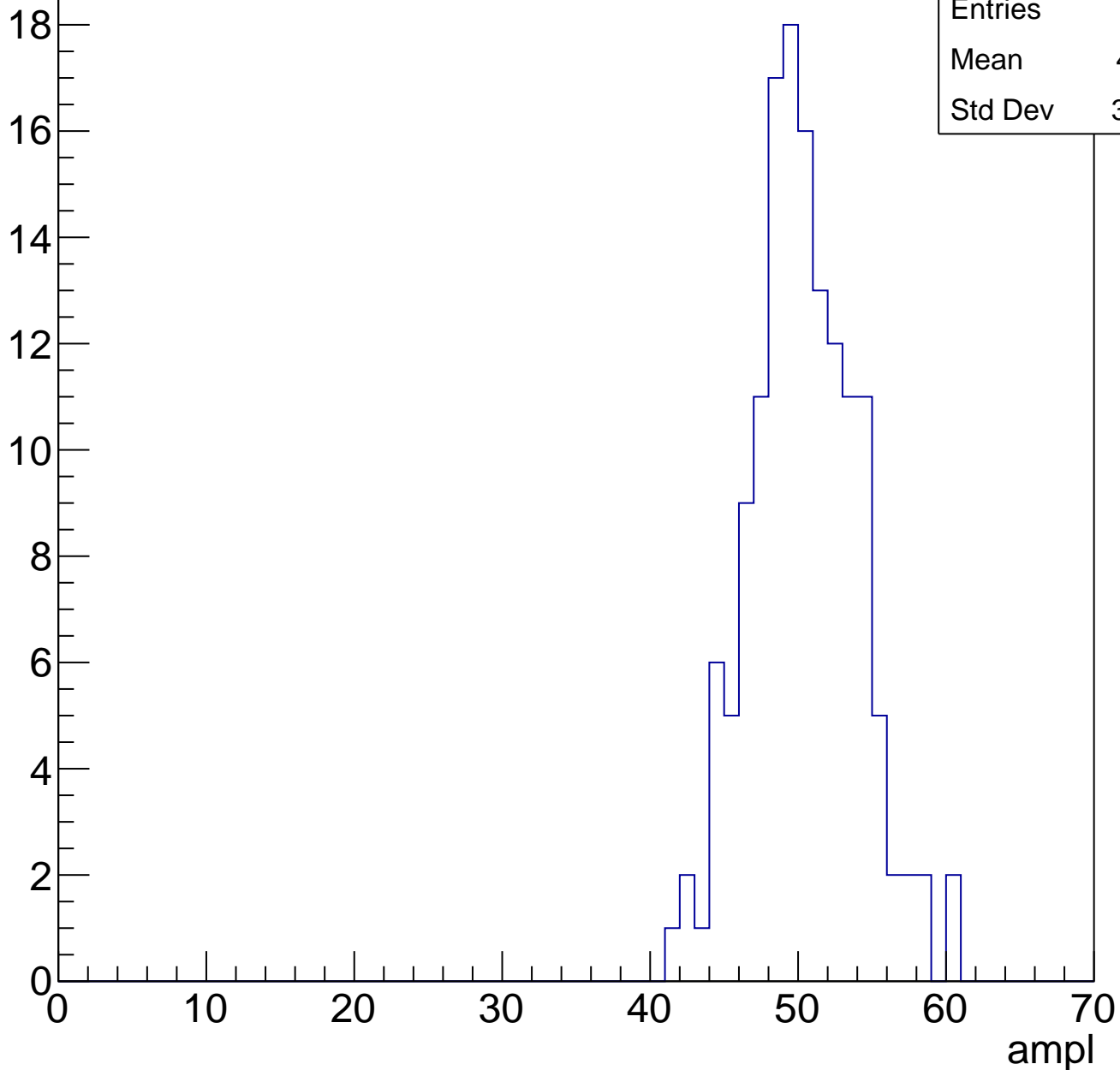


B0L101S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	49.91
Std Dev	3.594

Entry

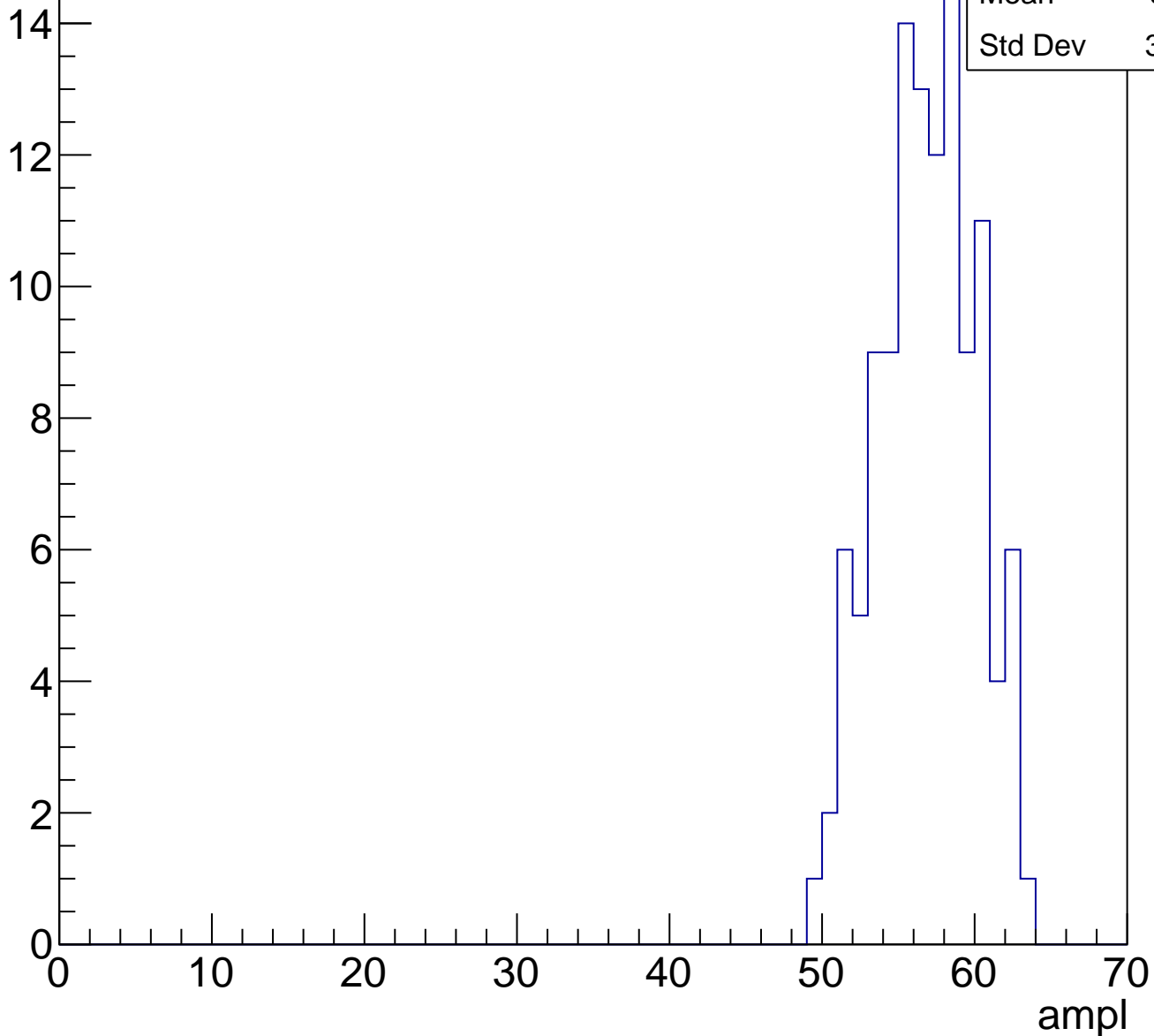


B0L101S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	56.41
Std Dev	3.144

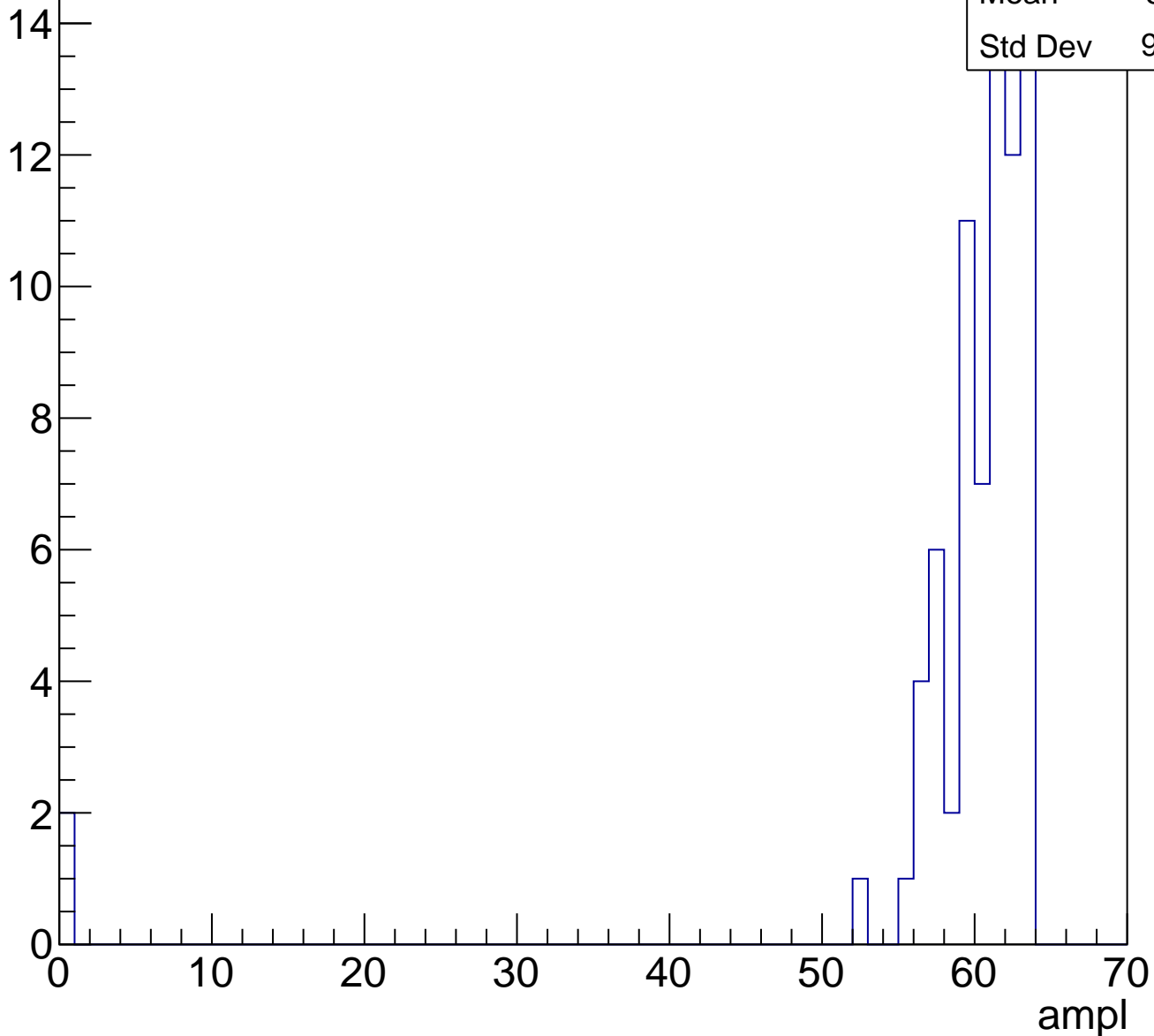
Entry



B0L101S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#1, port C1

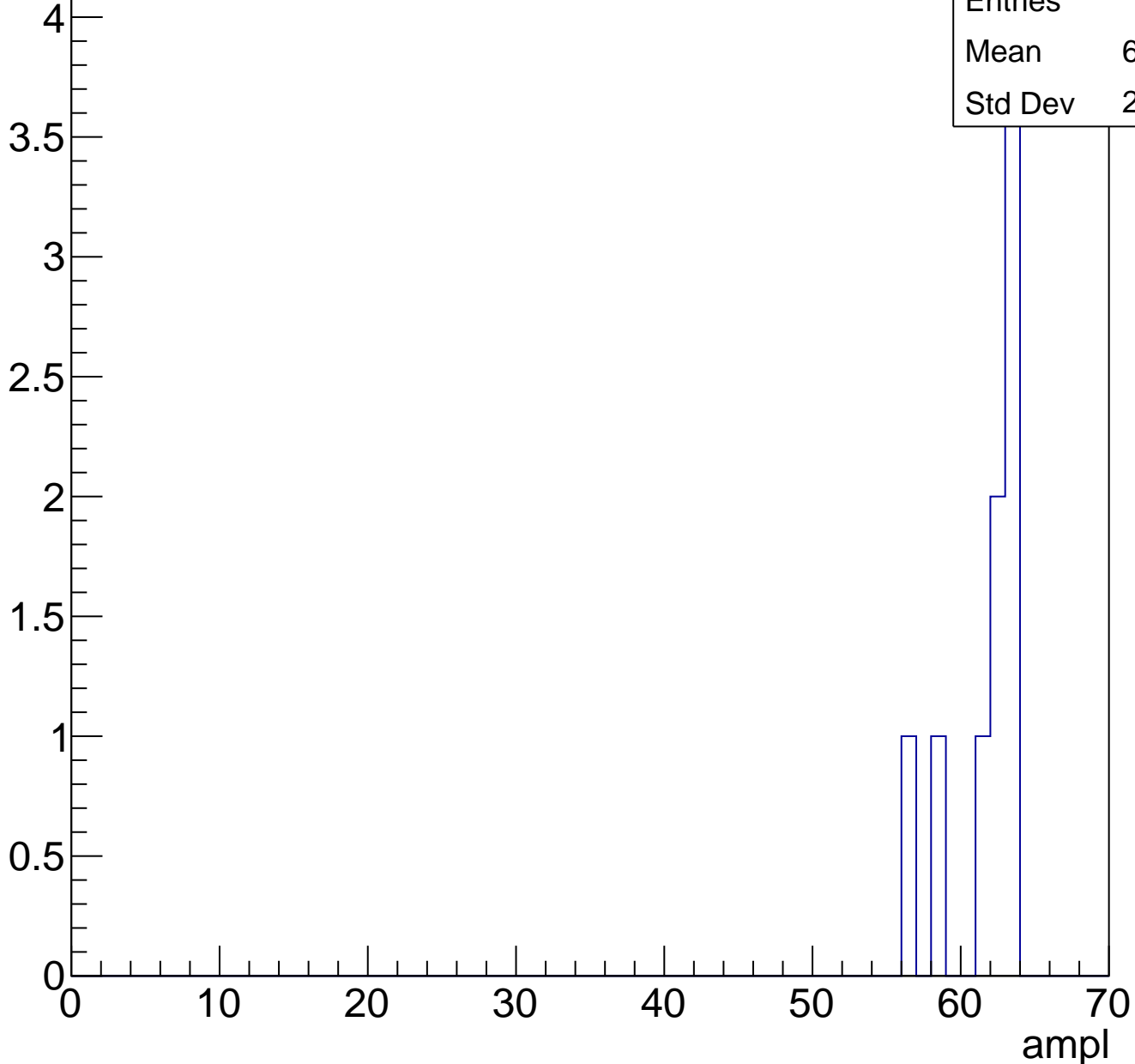
Entry



B0L101S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

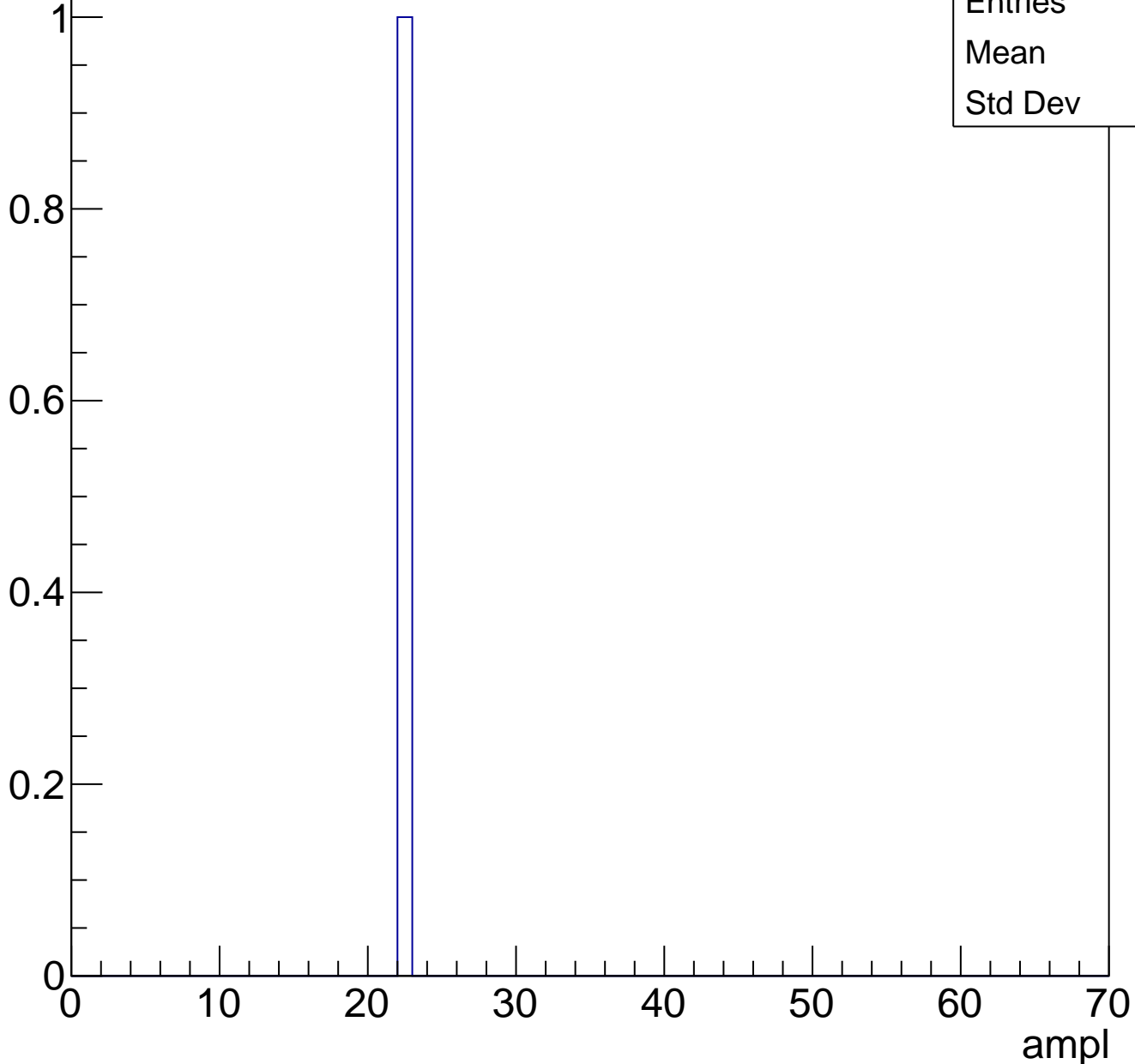


Entries	9
Mean	61.22
Std Dev	2.393

B0L101S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L101S, U14-ch56, adc0

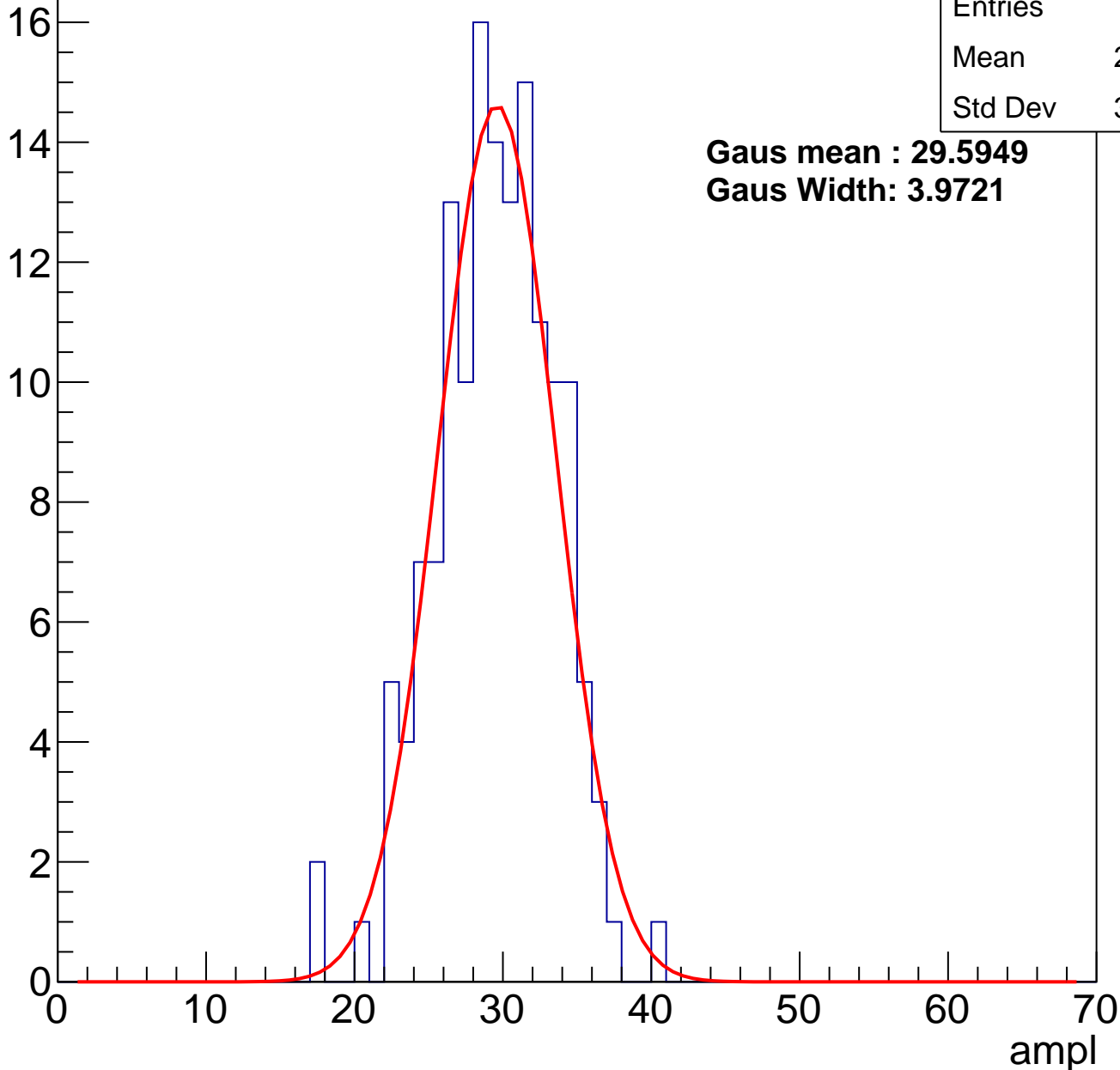
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.04
Std Dev	3.952

Gaus mean : 29.5949

Gaus Width: 3.9721

Entry



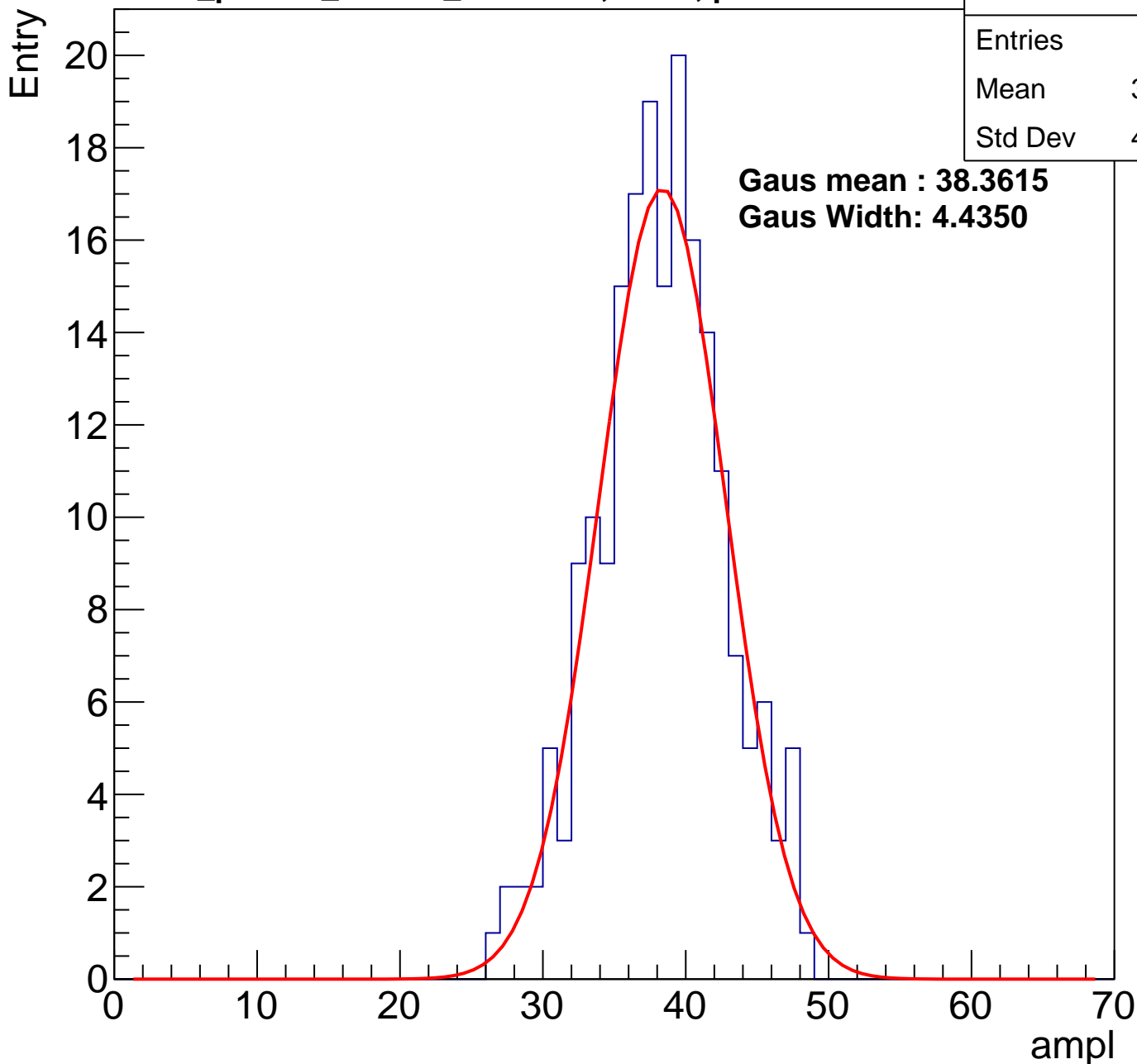
B0L101S, U14-ch56, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	197
Mean	37.76
Std Dev	4.436

Gaus mean : 38.3615

Gaus Width: 4.4350



B0L101S, U14-ch56, adc2

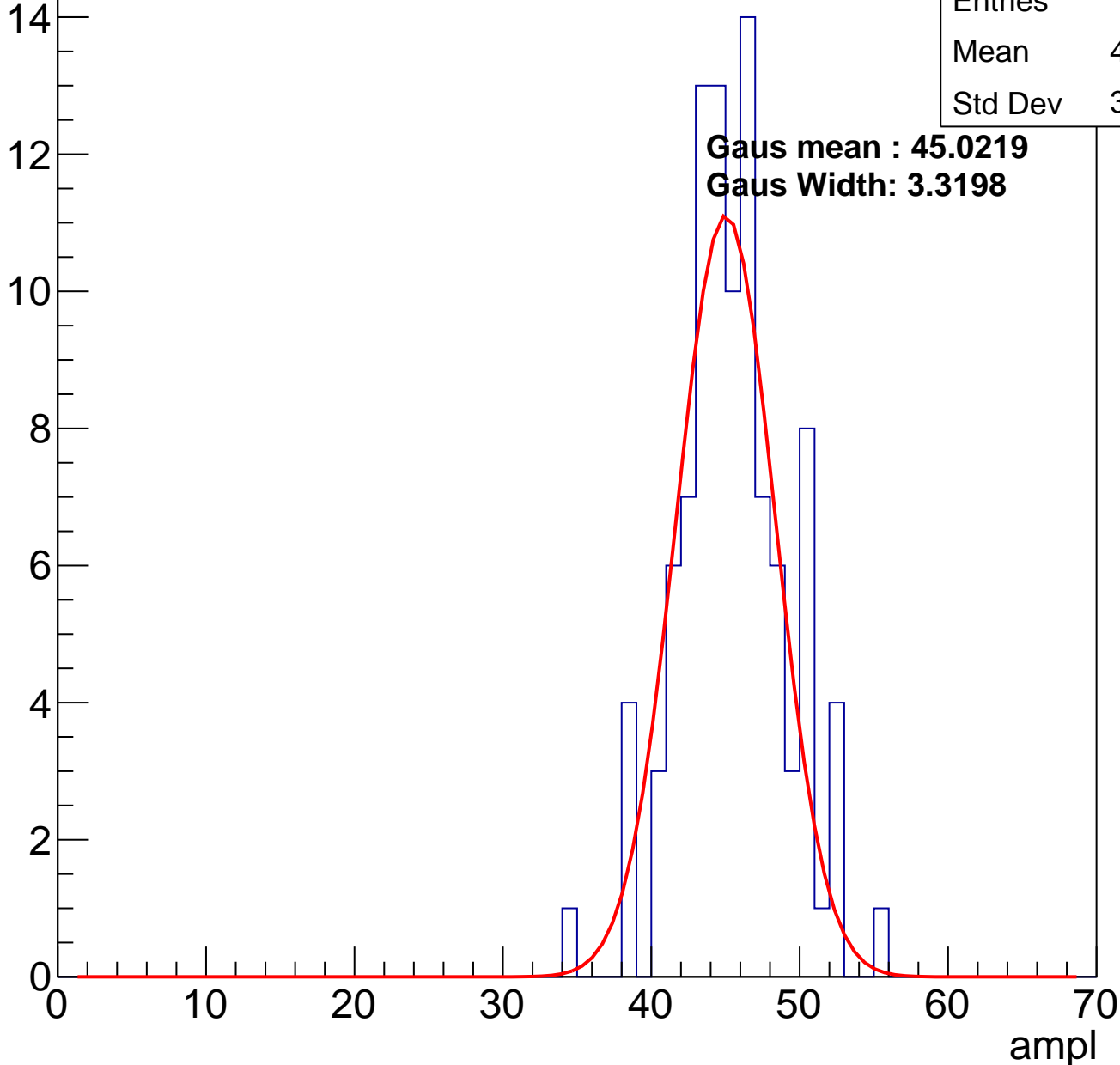
calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	45.04
Std Dev	3.576

Gaus mean : 45.0219

Gaus Width: 3.3198

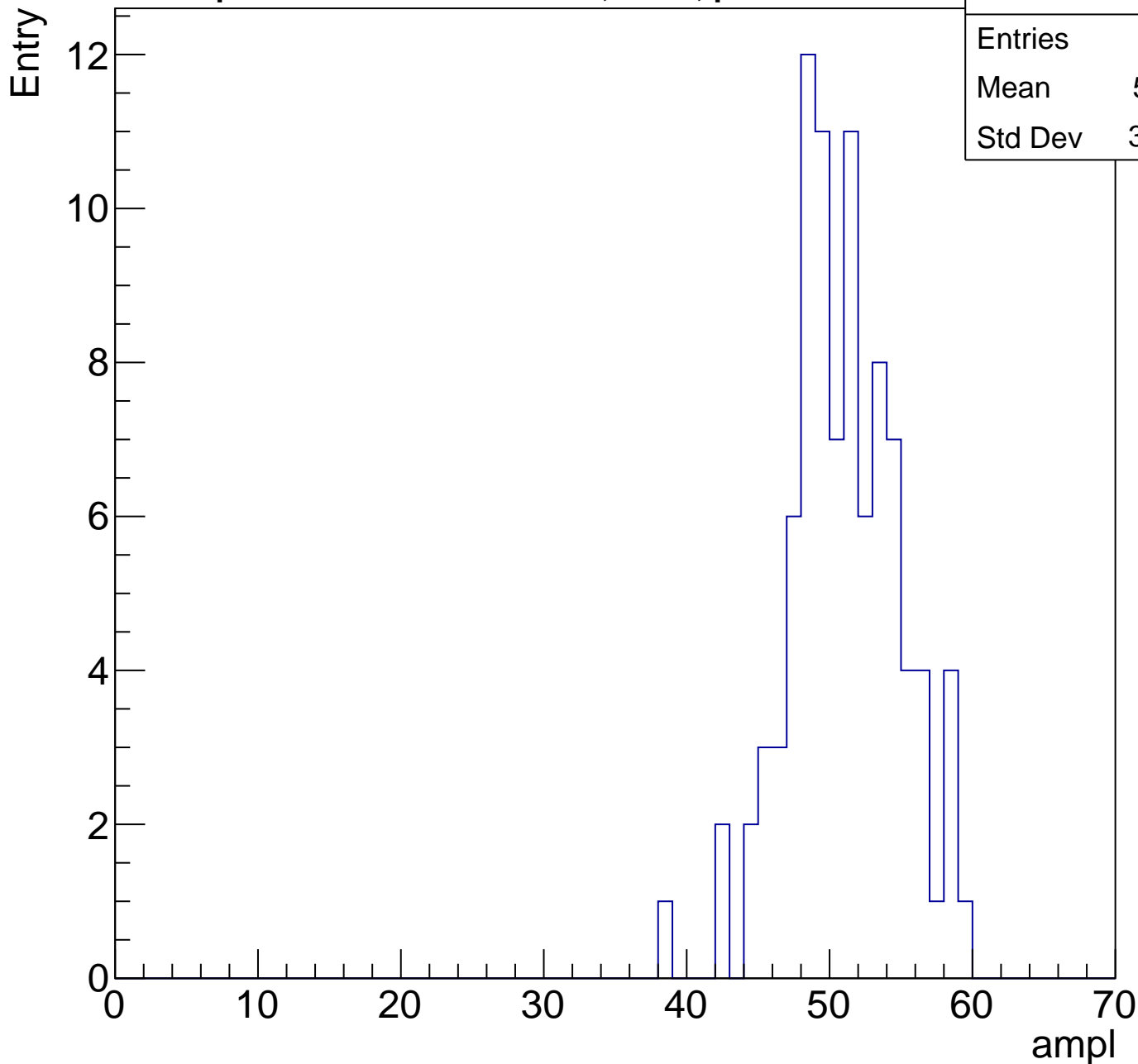
Entry



B0L101S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#1, port C1

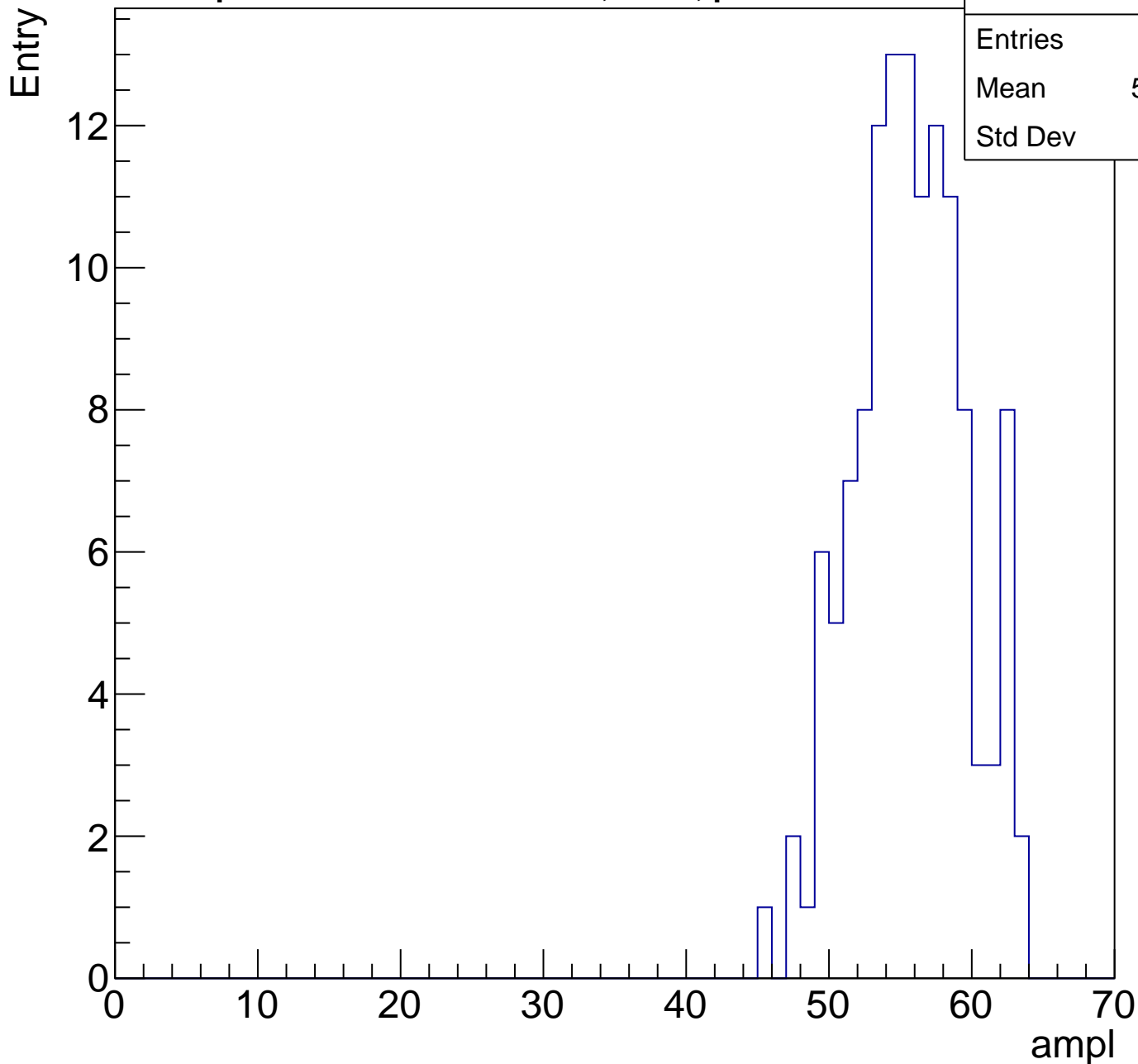
Entries	93
Mean	50.51
Std Dev	3.887



B0L101S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	55.17
Std Dev	3.83



B0L101S, U14-ch56, adc5

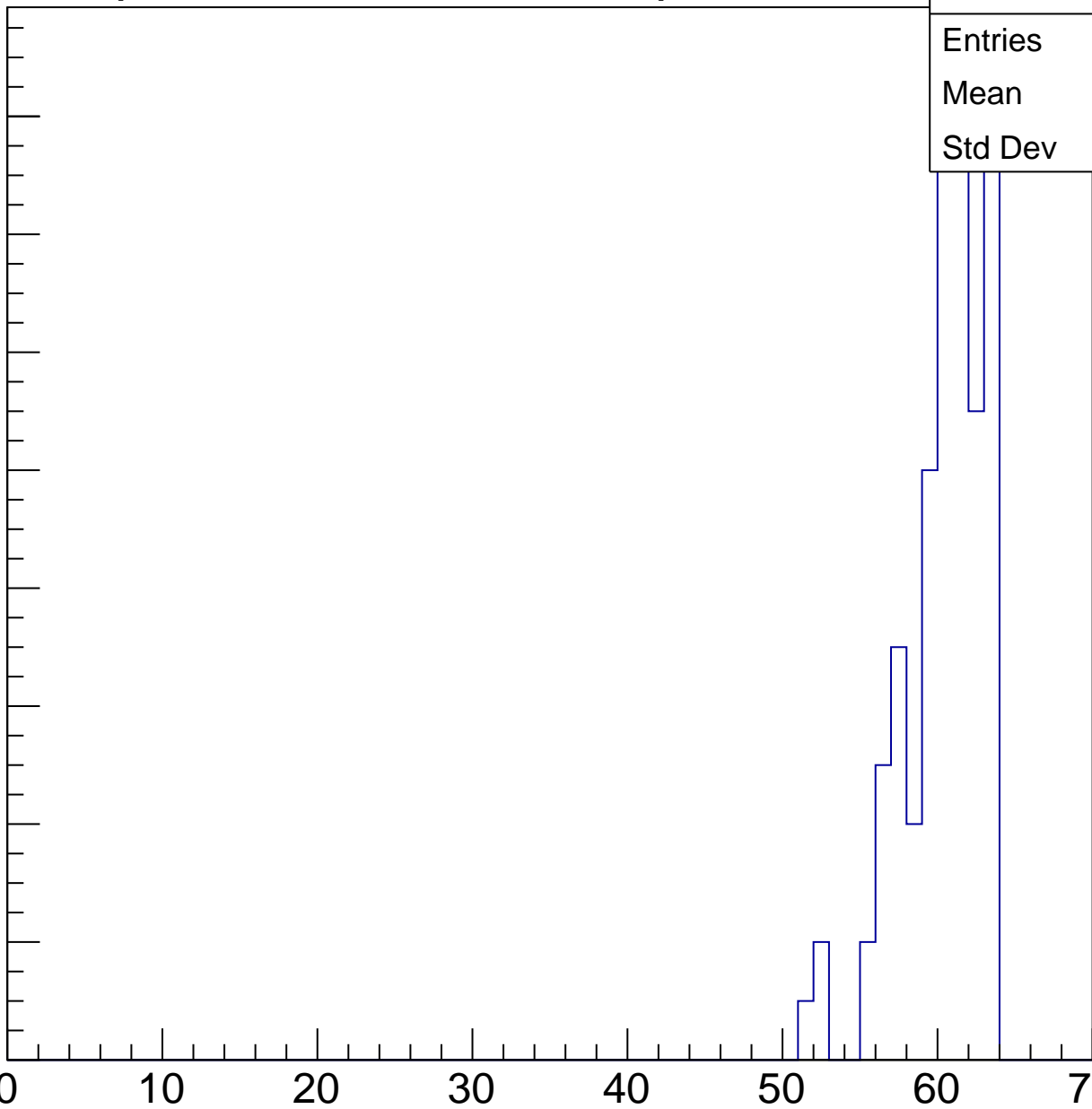
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.91
Std Dev	2.634

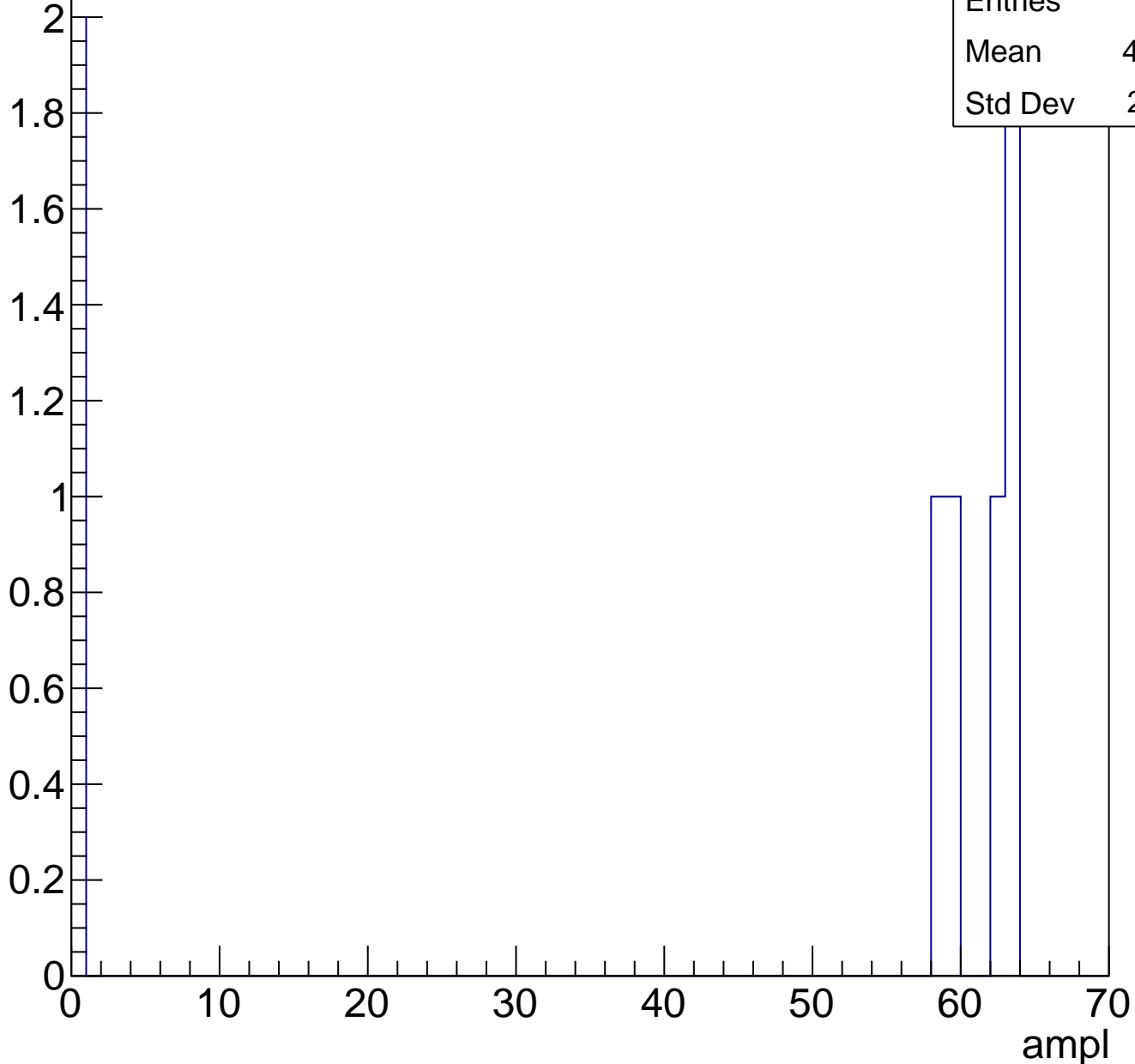
ampl



B0L101S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

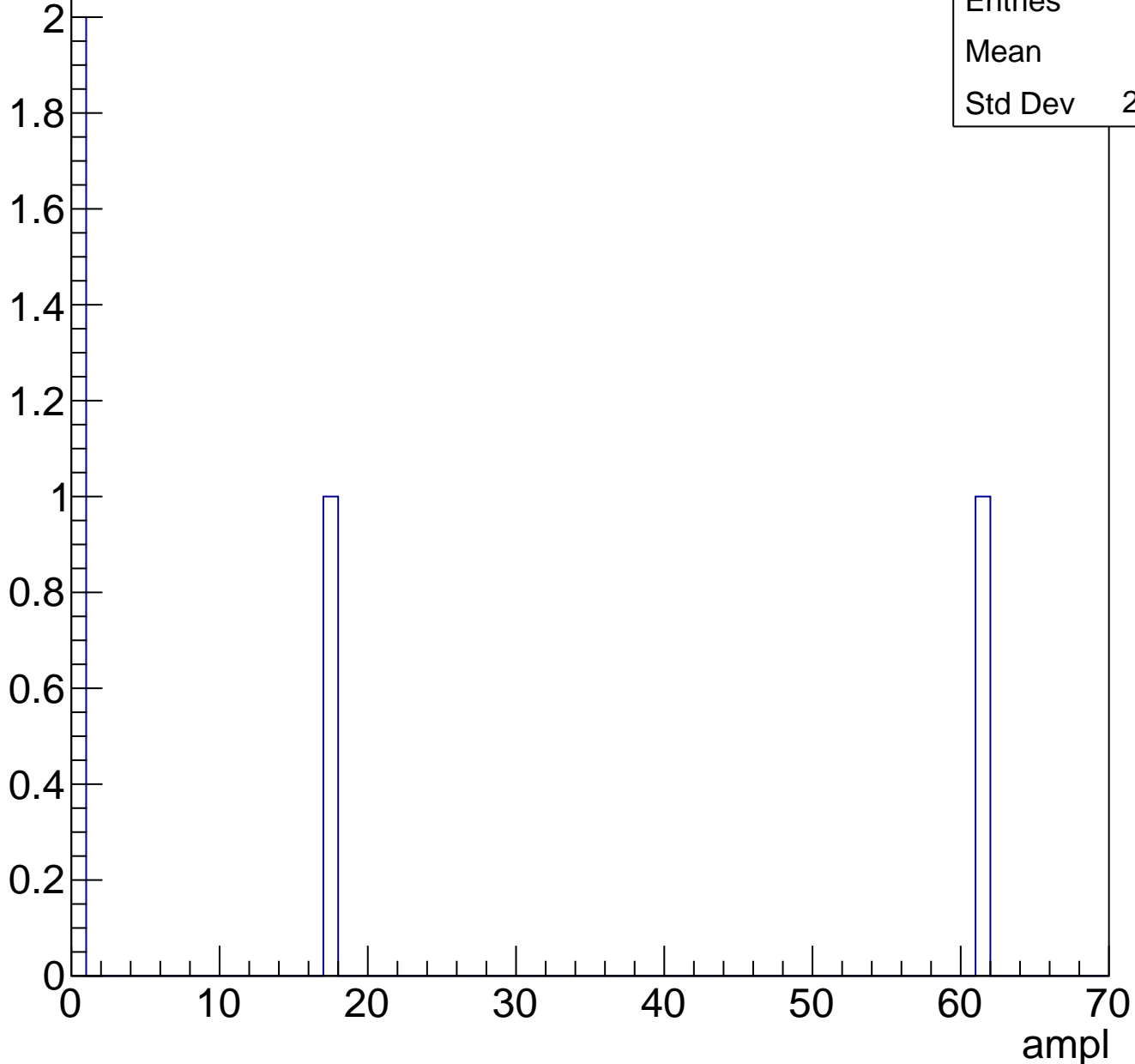


Entries	7
Mean	43.57
Std Dev	27.61

B0L101S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	19.5
Std Dev	24.94

B0L101S, U14-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	28.81
Std Dev	3.79

Gaus mean : 29.3331

Gaus Width: 3.9728

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

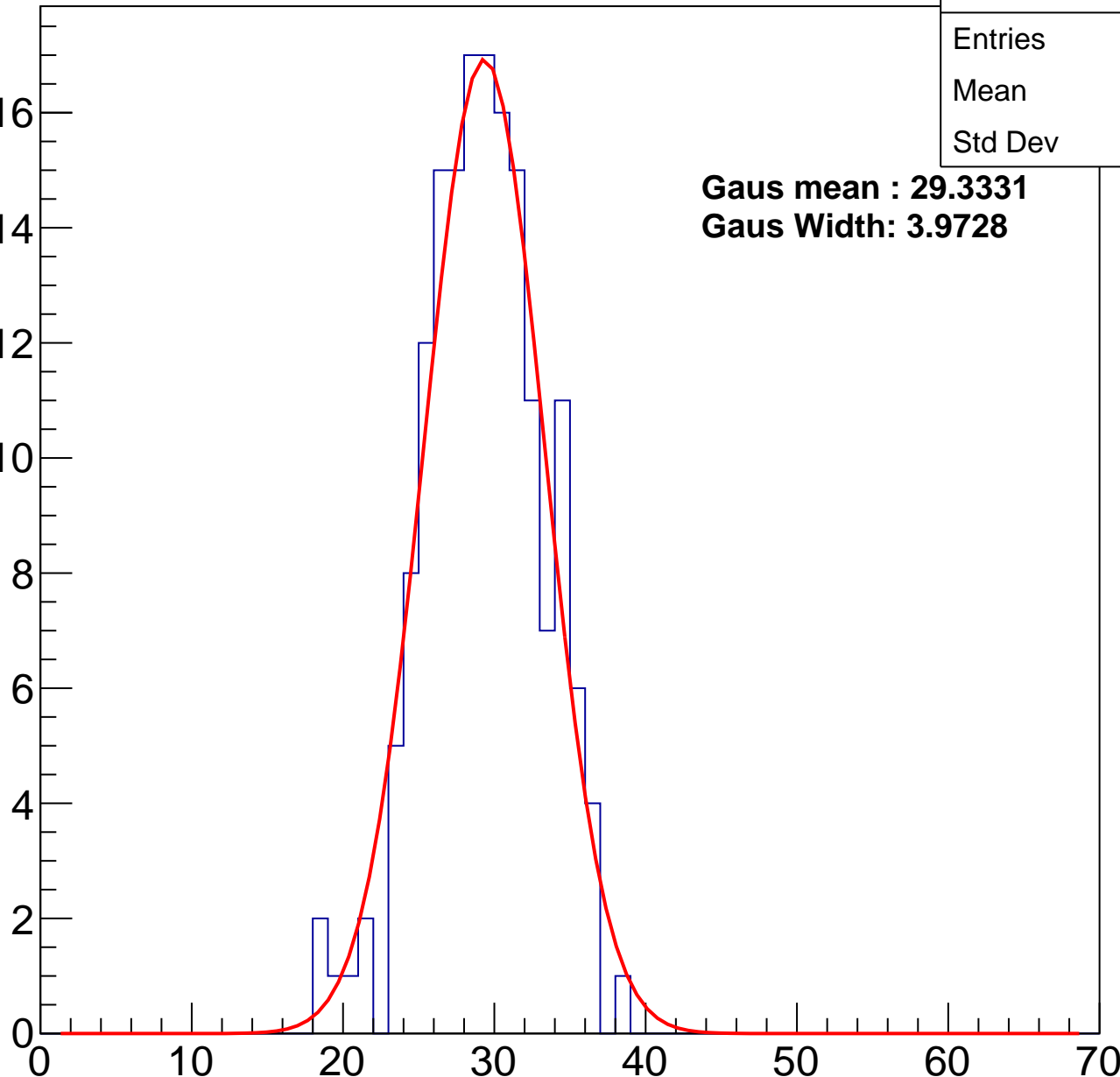
40

50

60

70

ampl



B0L101S, U14-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	240
Mean	38.77
Std Dev	4.665

Gaus mean : 39.0916

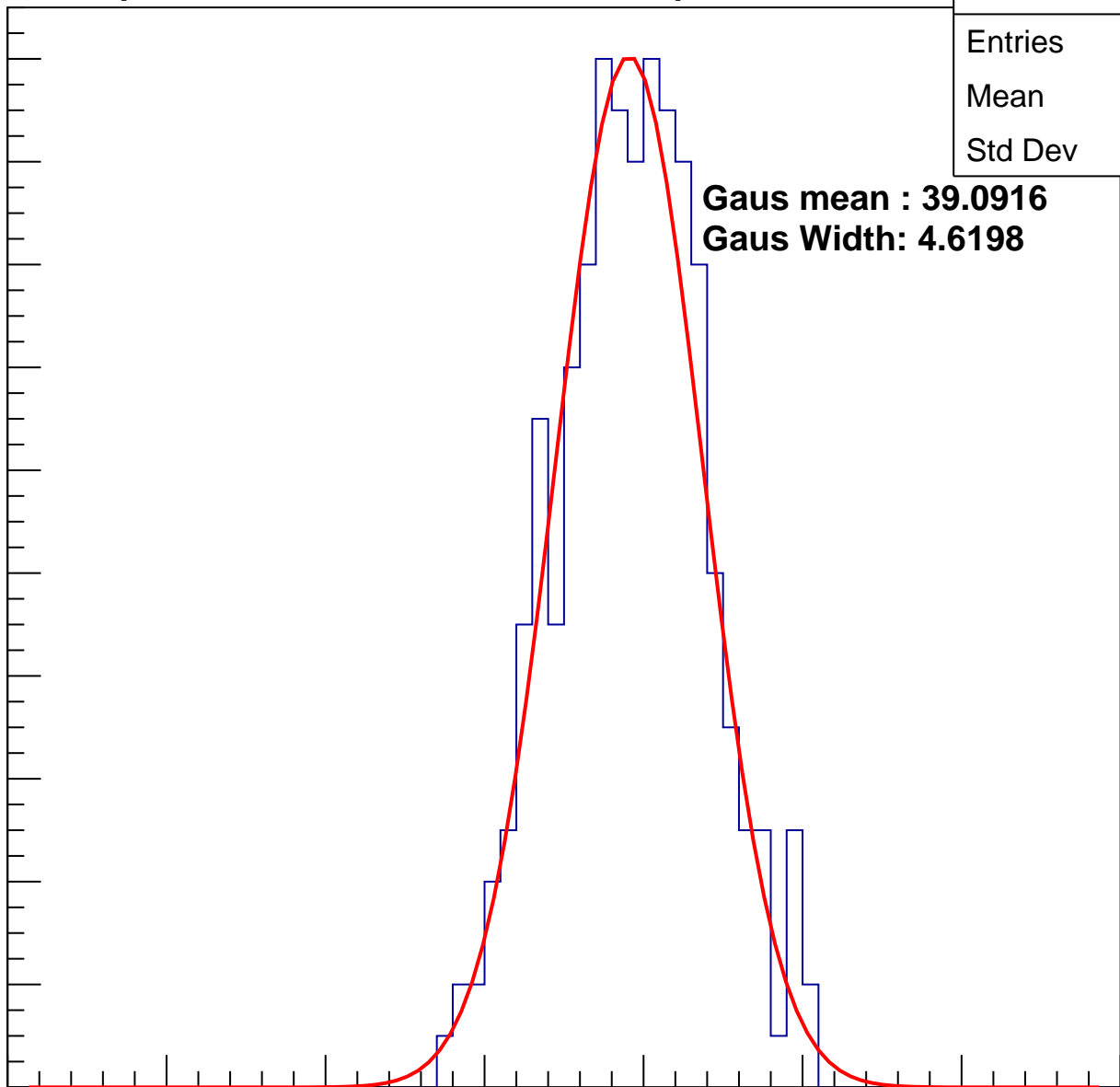
Gaus Width: 4.6198

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch57, adc2

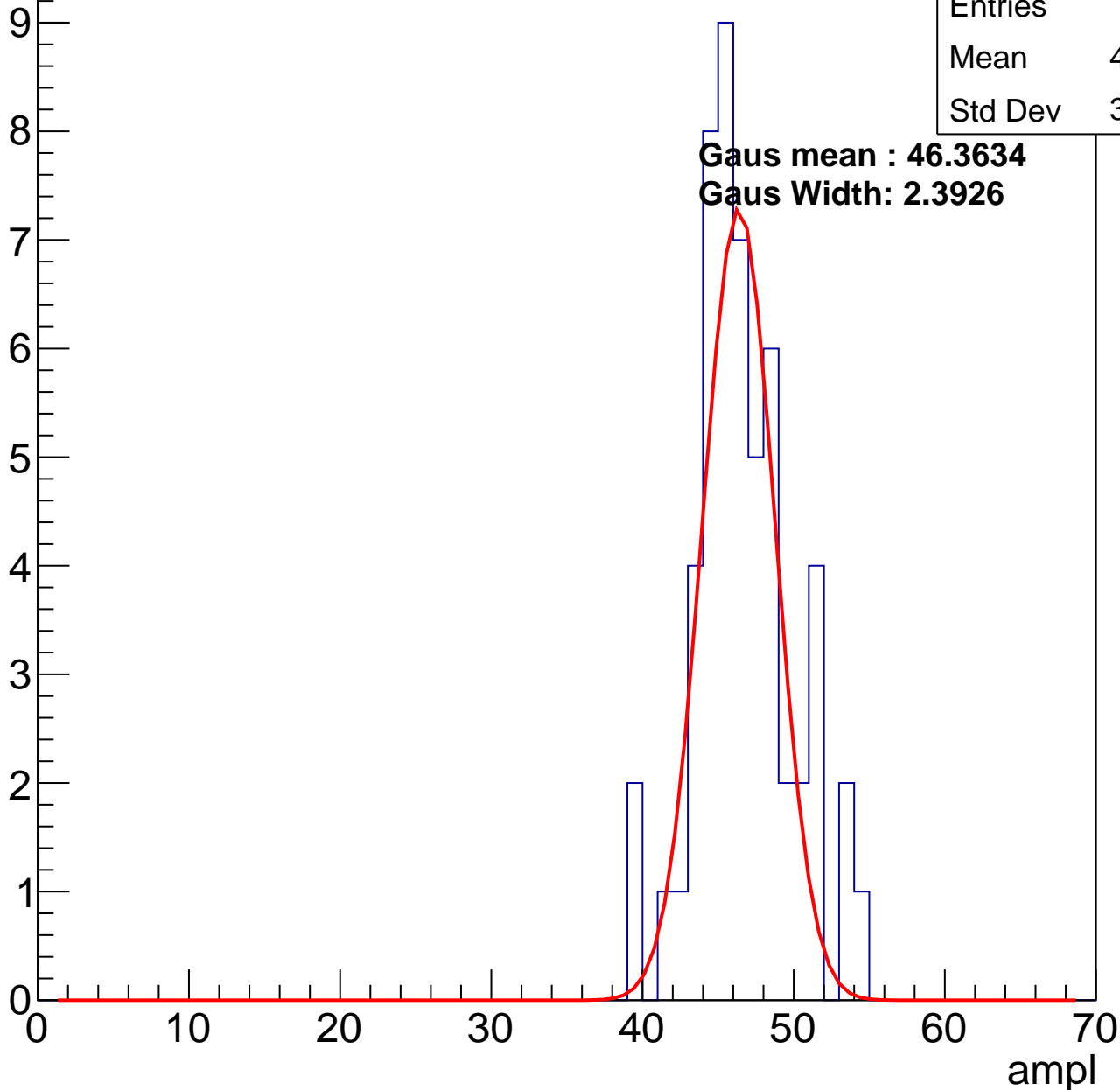
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	46.24
Std Dev	3.203

Gaus mean : 46.3634

Gaus Width: 2.3926



B0L101S, U14-ch57, adc3

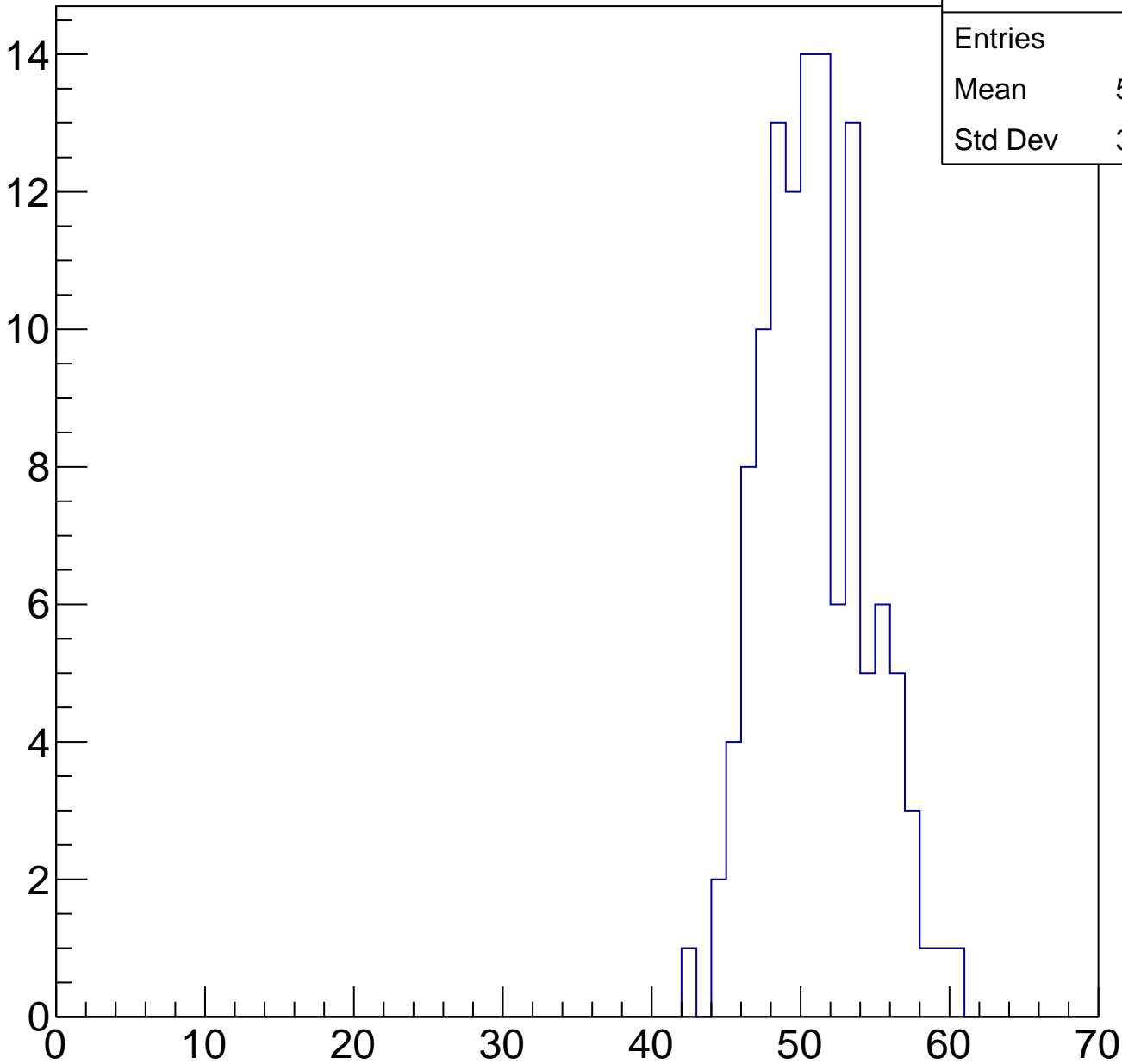
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	50.45
Std Dev	3.492

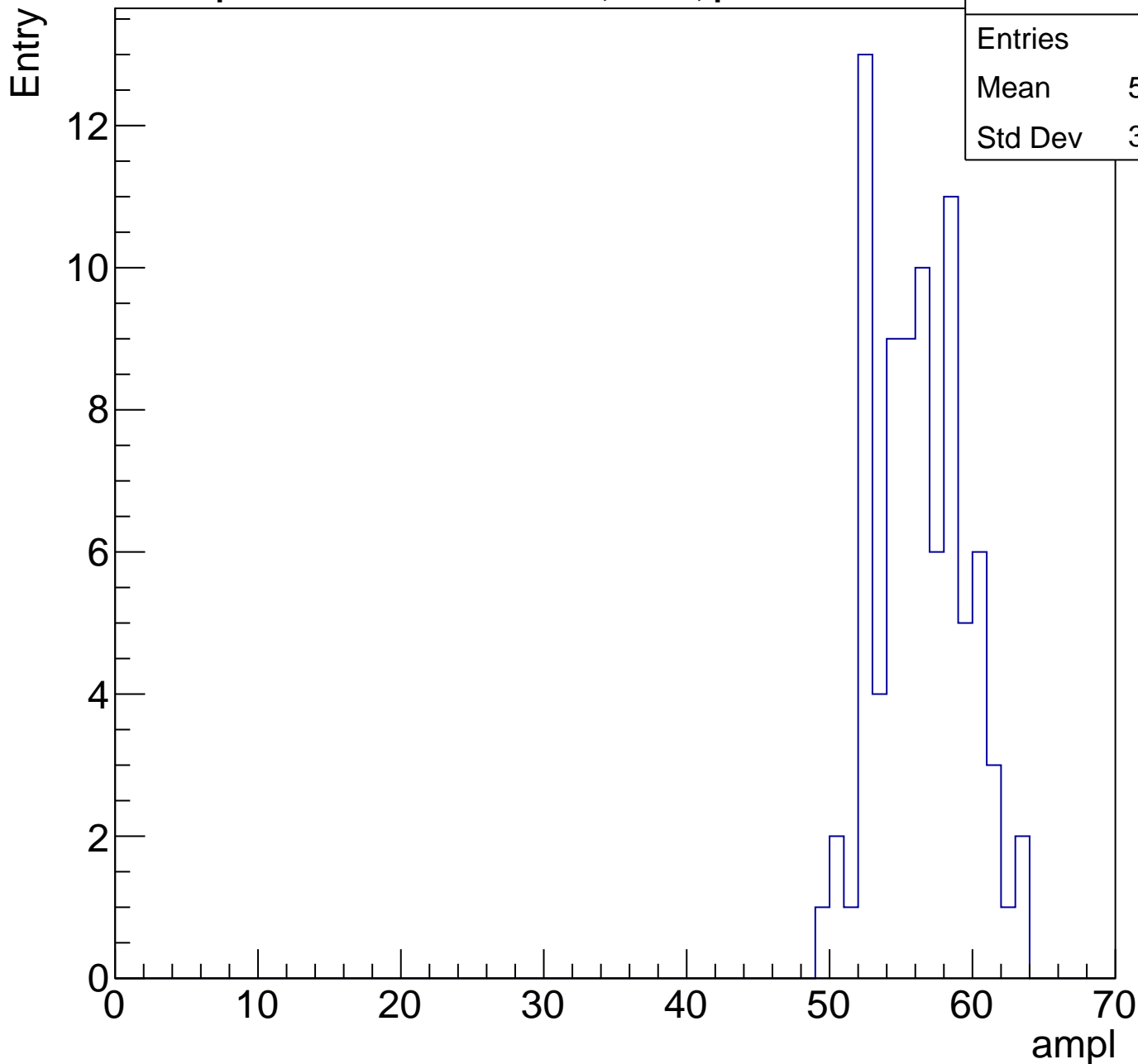
ampl



B0L101S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	83
Mean	55.84
Std Dev	3.168



B0L101S, U14-ch57, adc5

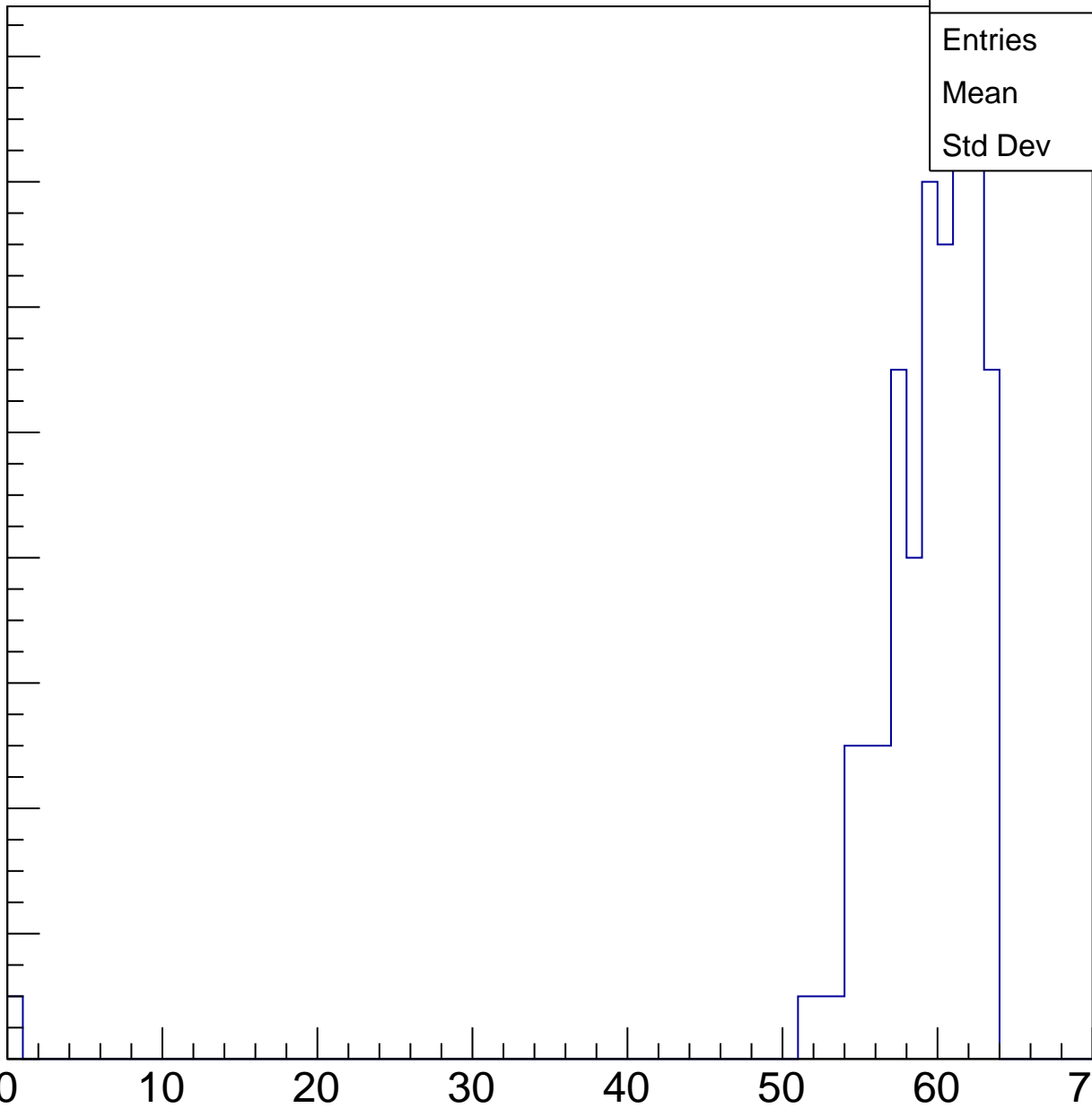
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.67
Std Dev	6.349

ampl

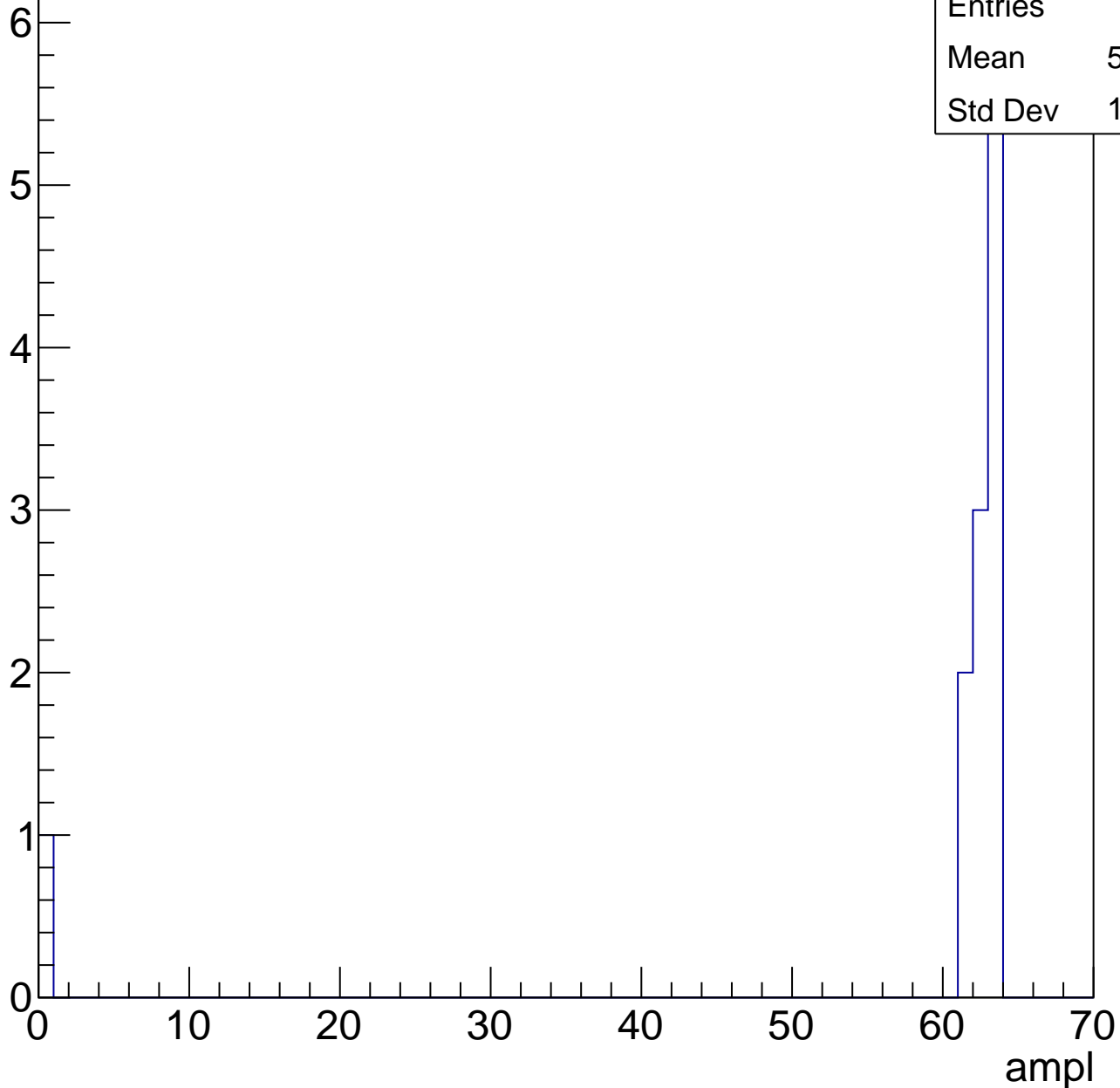


B0L101S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	57.17
Std Dev	17.25



B0L101S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch58, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

29.86

Std Dev

4.237

Gaus mean : 30.4799

Gaus Width: 3.4416

0

10

20

30

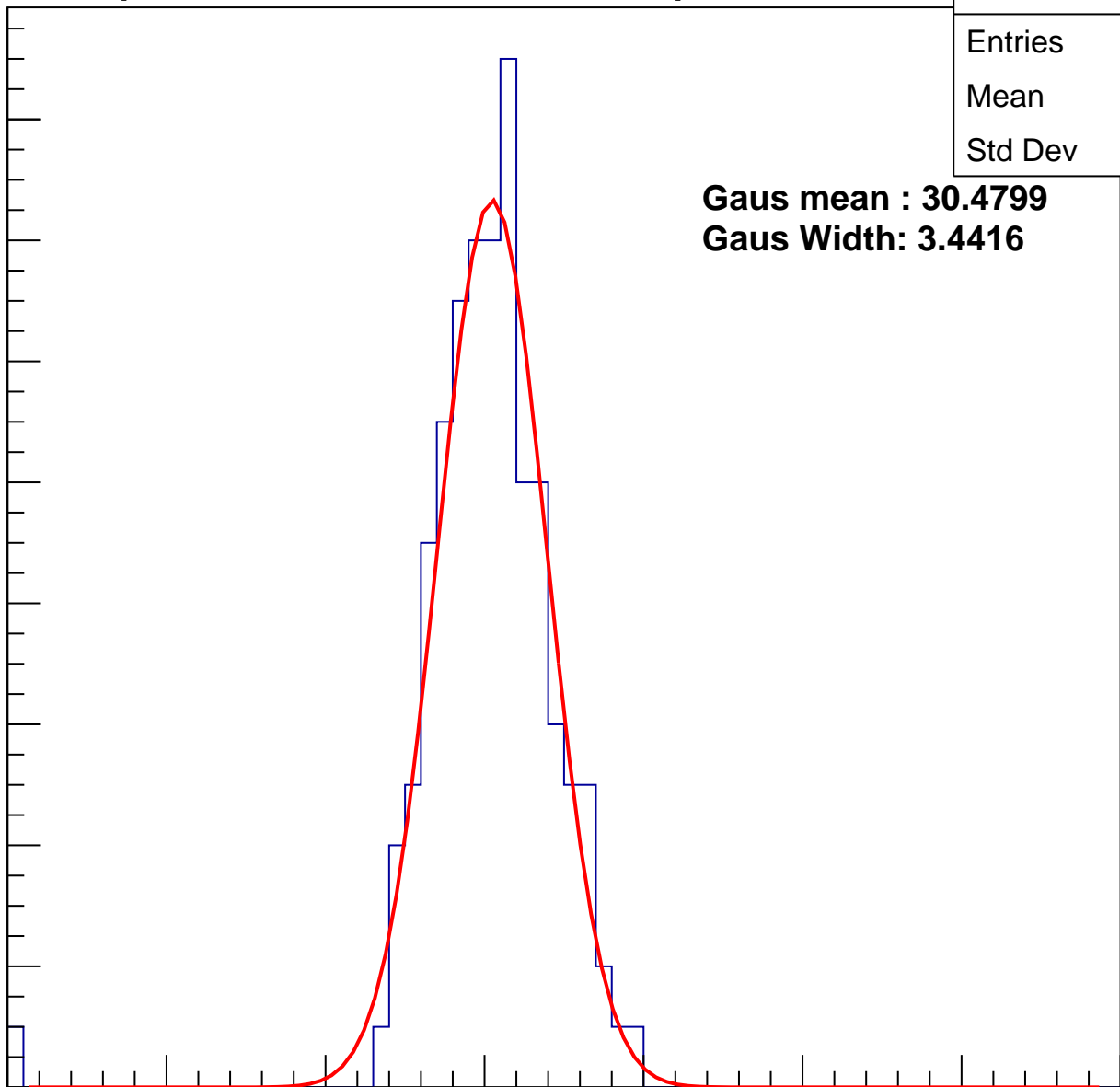
40

50

60

70

ampl



B0L101S, U14-ch58, adc1

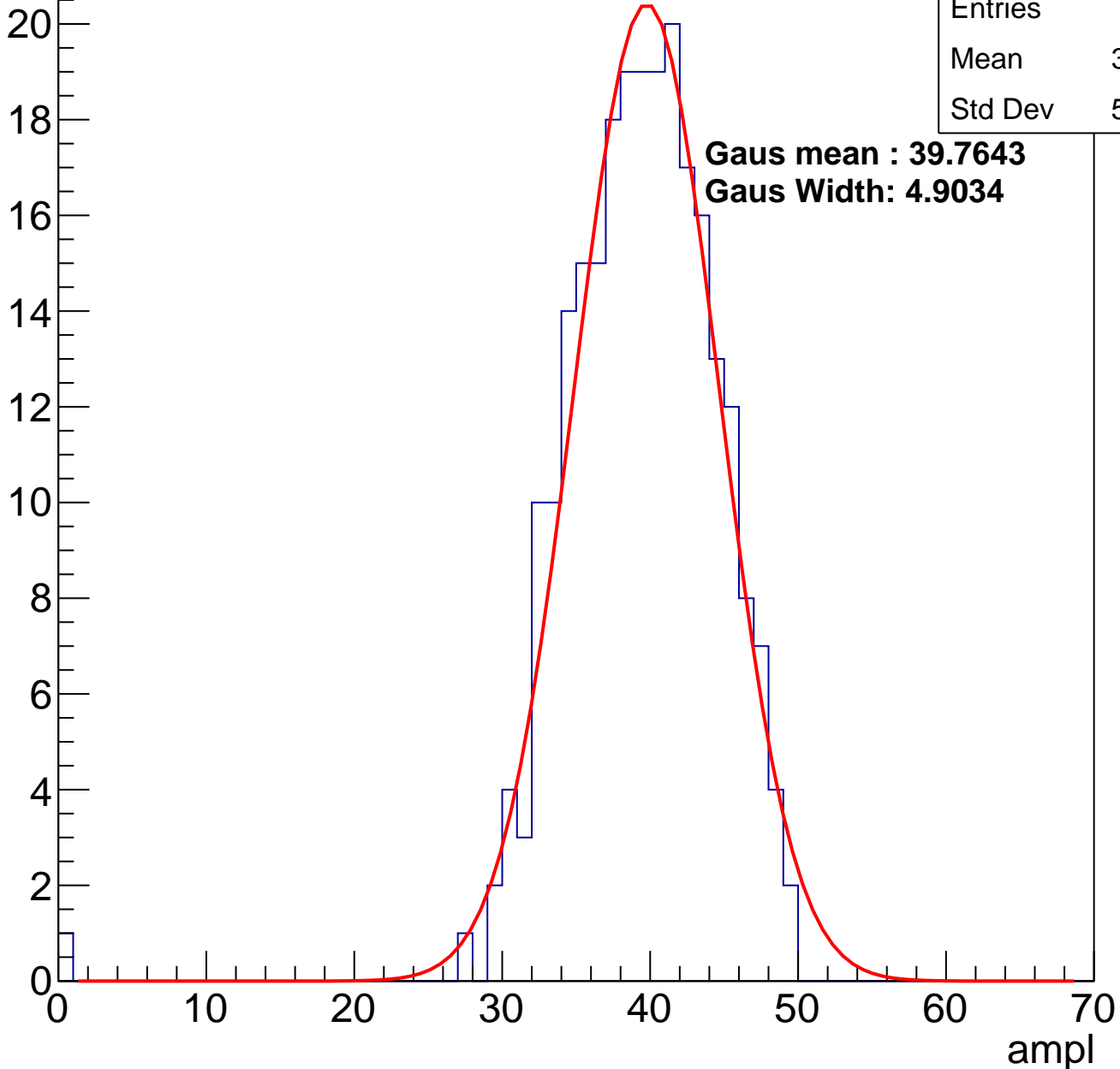
calib_packv5_042523_0143.root, FC#1, port C1

Entries	249
Mean	38.95
Std Dev	5.178

Gaus mean : 39.7643

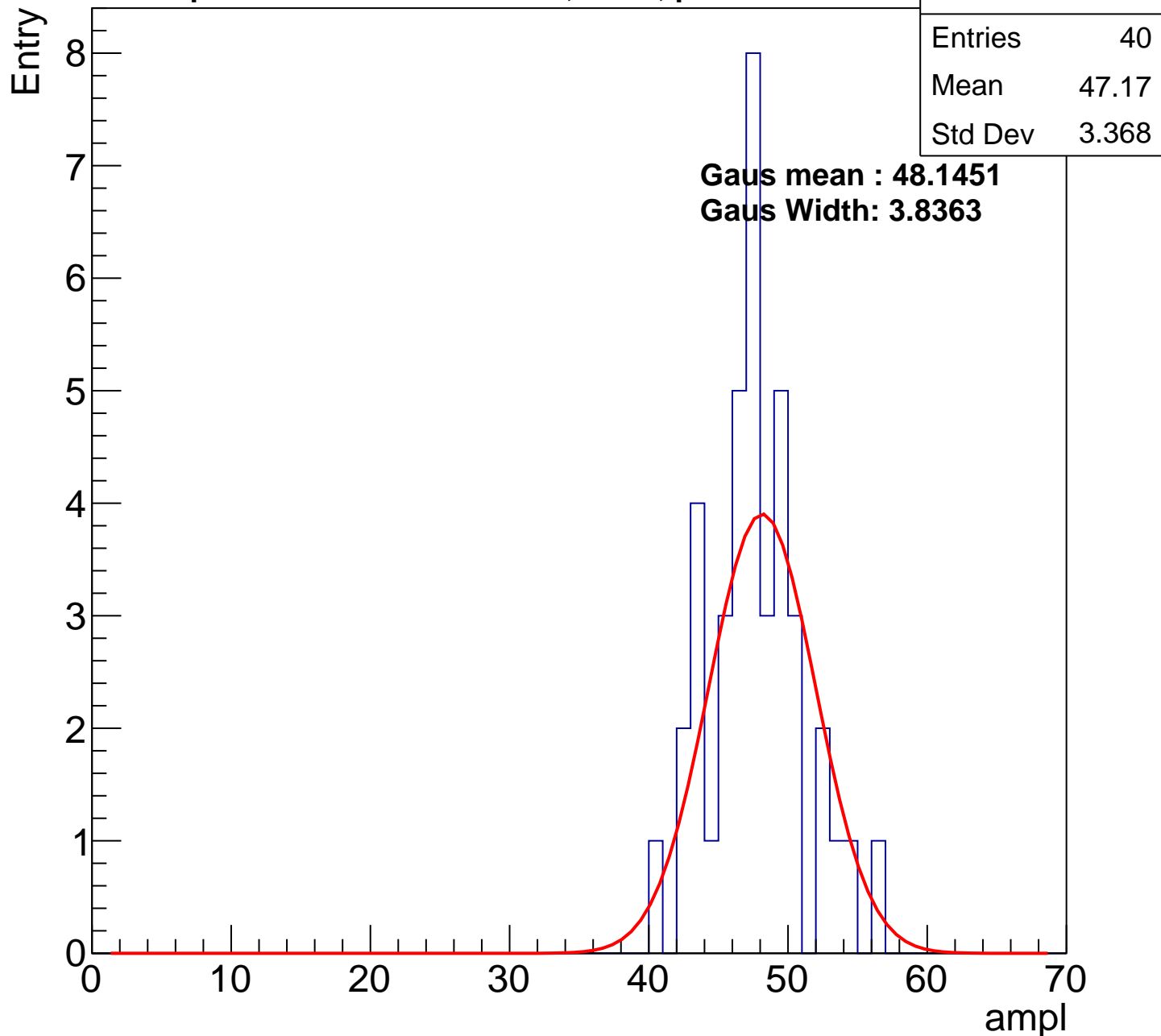
Gaus Width: 4.9034

Entry



B0L101S, U14-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1



B0L101S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	51.04
Std Dev	3.649

Entry

16

14

12

10

8

6

4

2

0

0

10

20

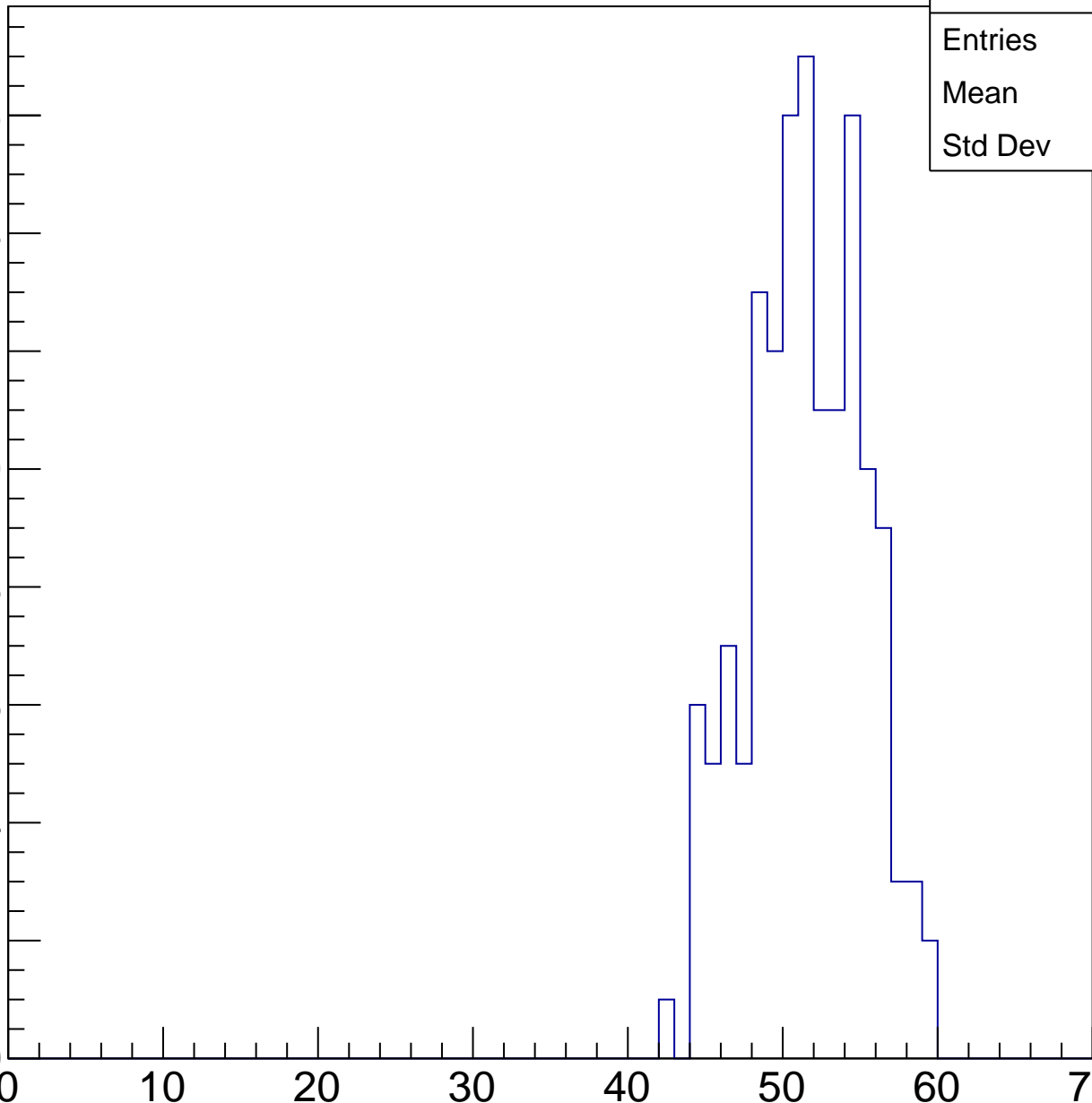
30

40

50

60

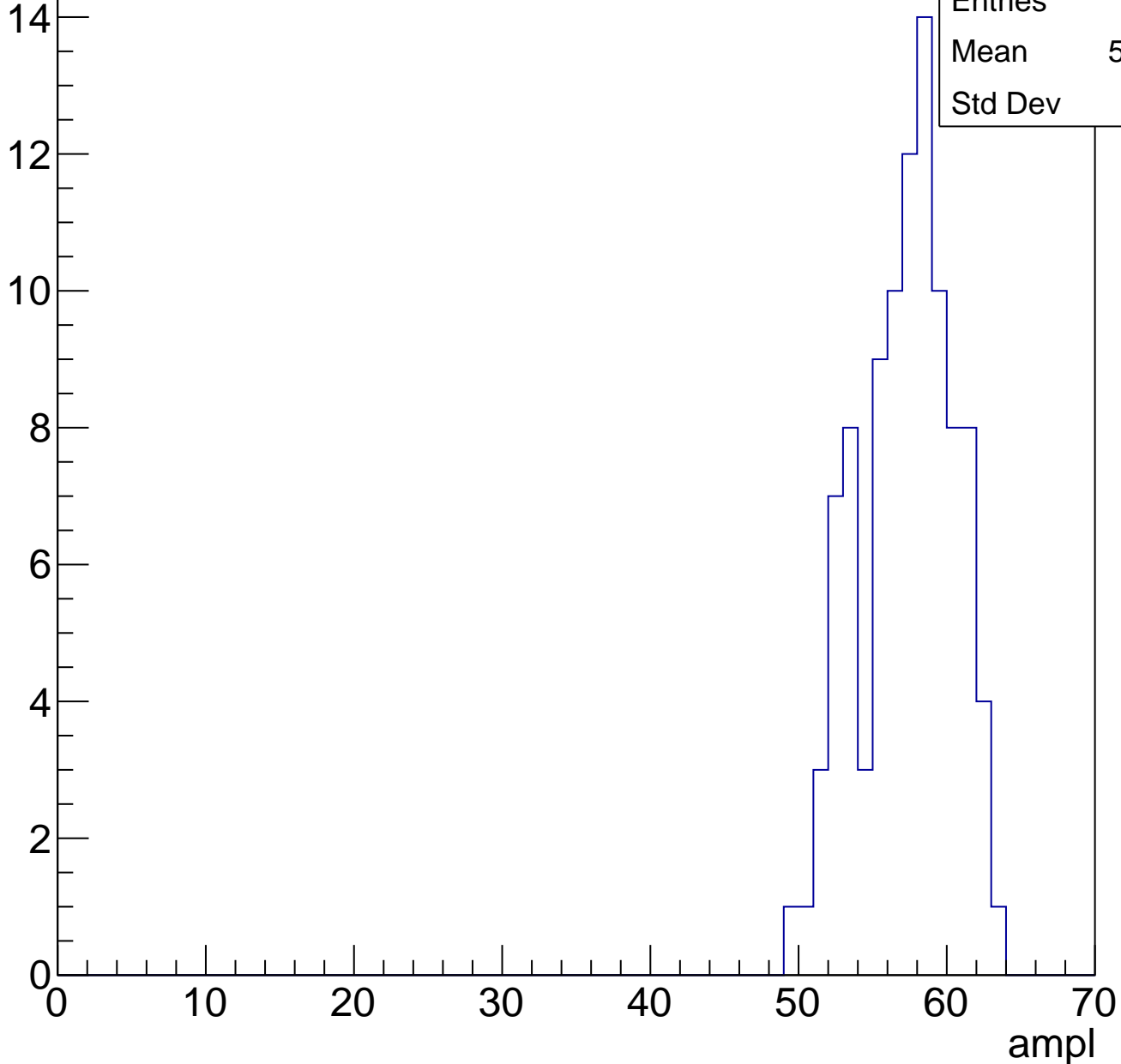
ampl



B0L101S, U14-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch58, adc5

calib_packv5_042523_0143.root, FC#1, port C1

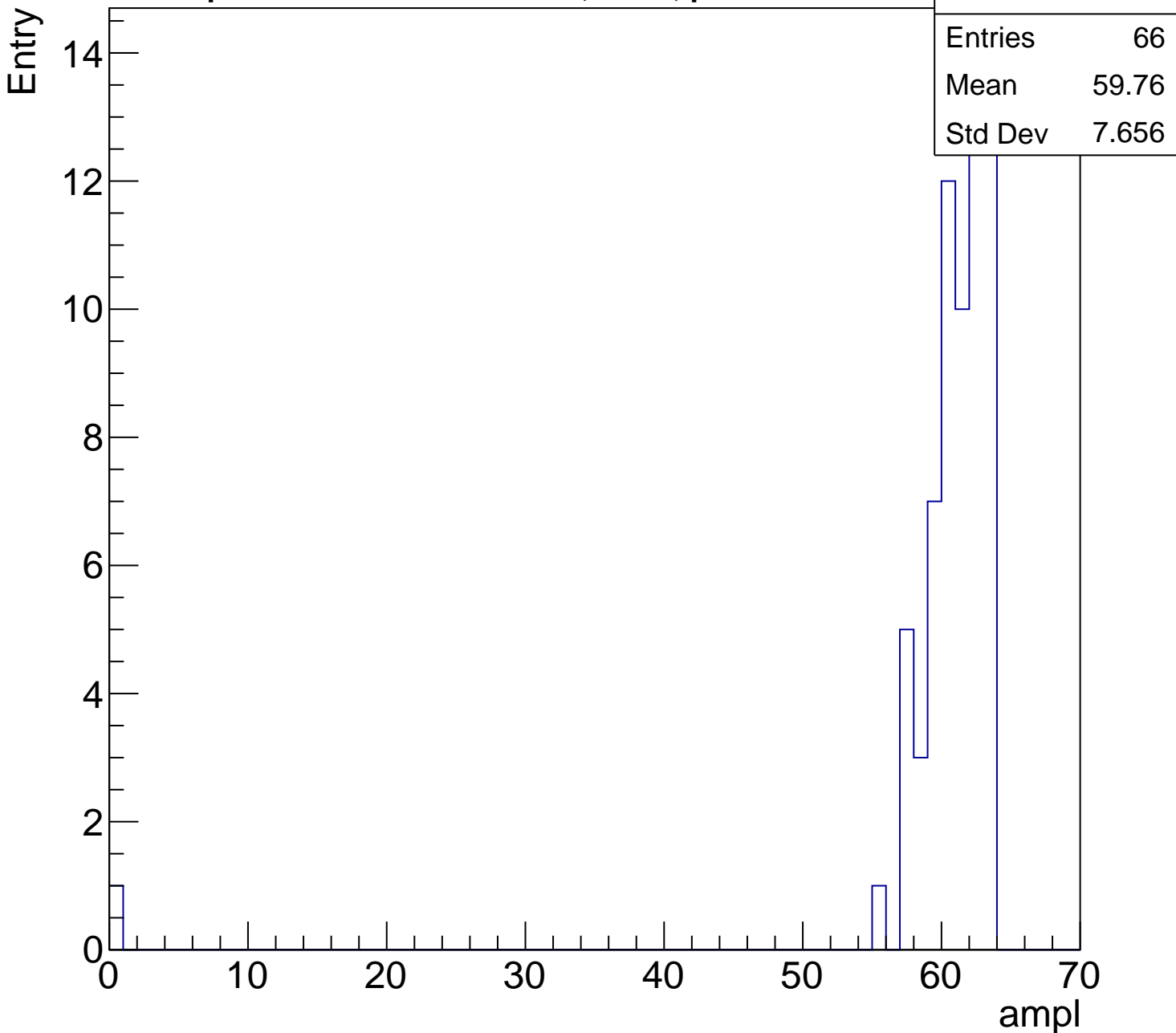
Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.76
Std Dev	7.656

ampl

0 10 20 30 40 50 60 70

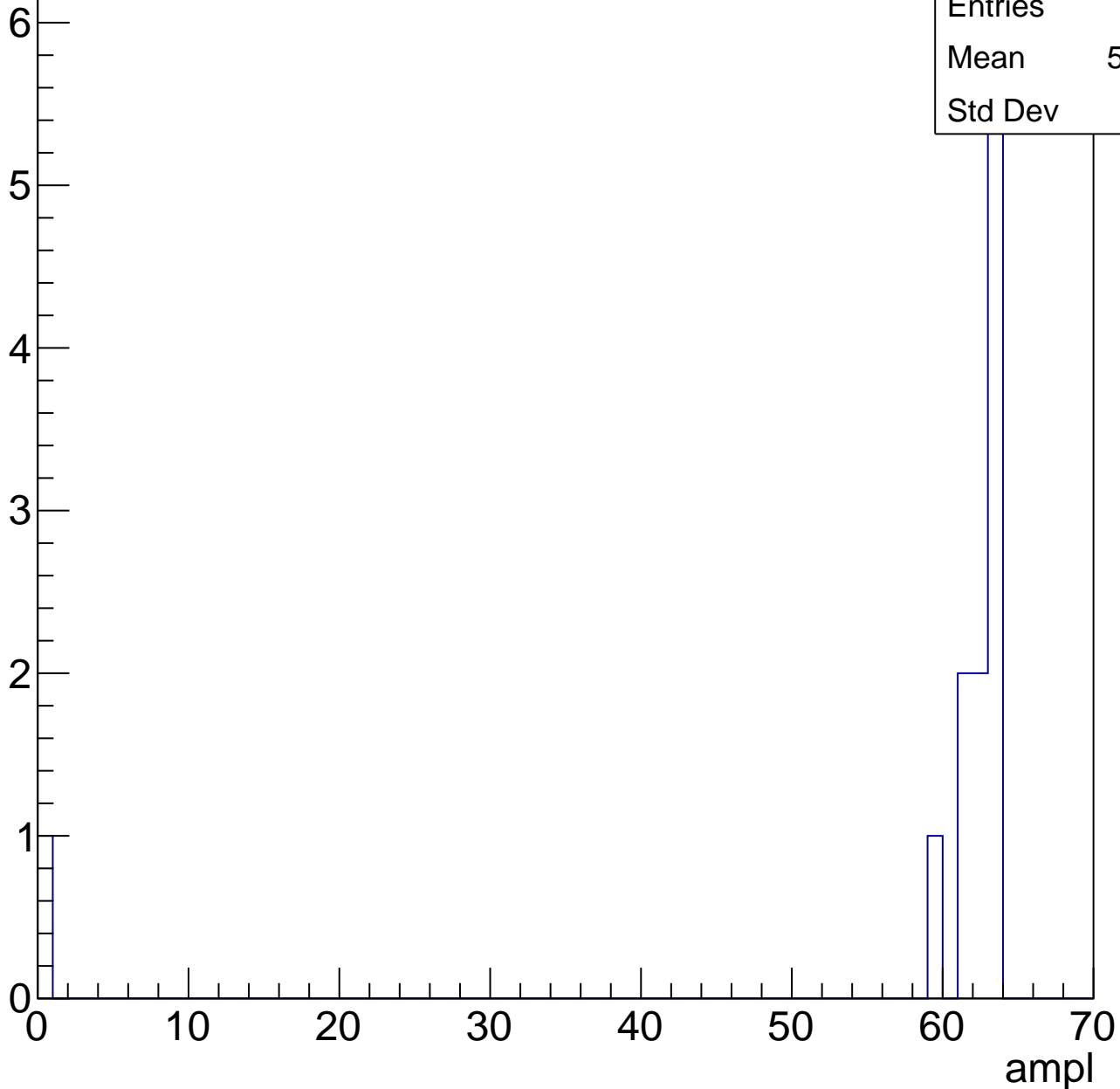


B0L101S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.92
Std Dev	17.2



B0L101S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L101S, U14-ch59, adc0

calib_packv5_042523_0143.root, FC#1, port C1

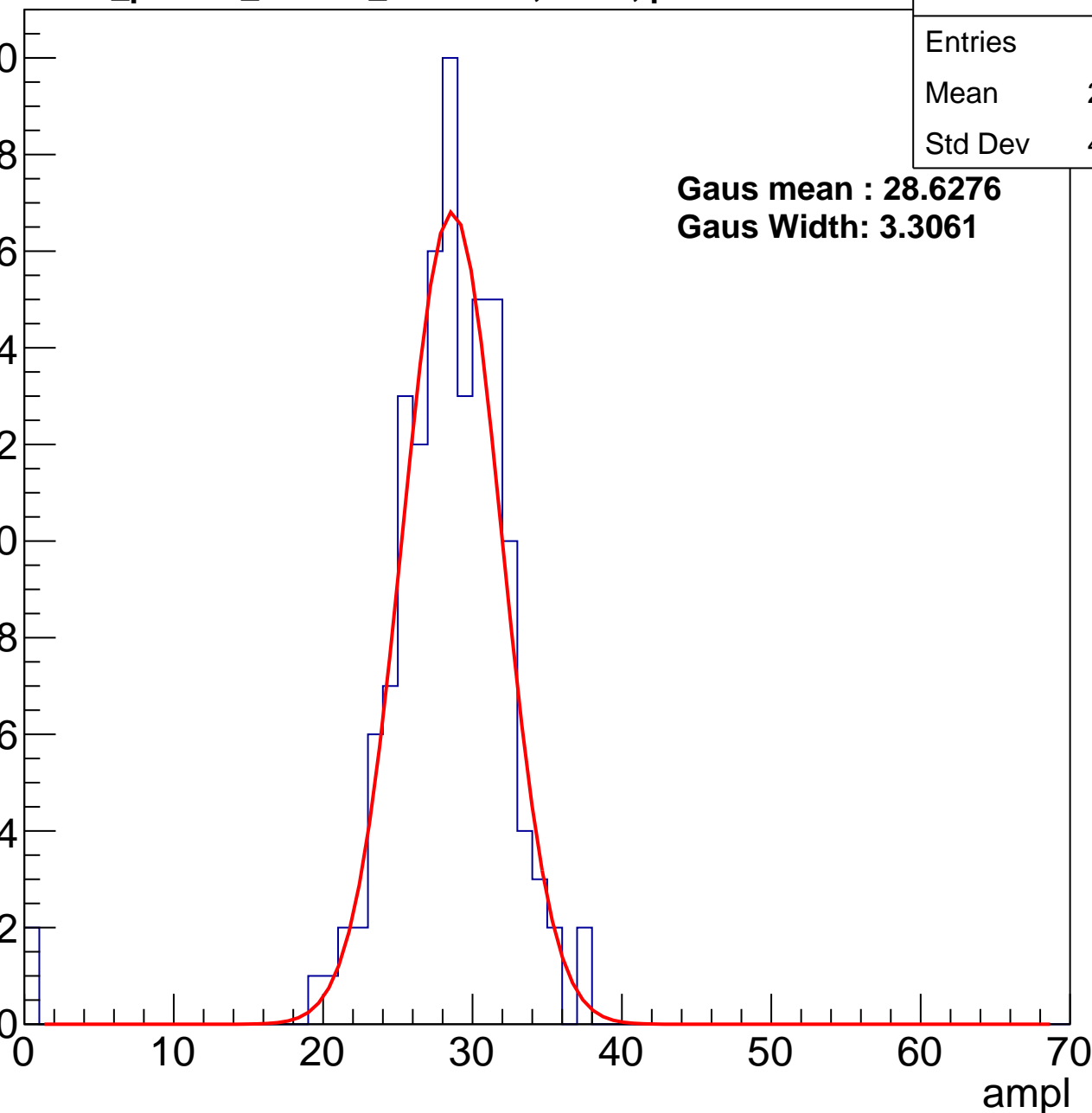
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	146
Mean	27.74
Std Dev	4.646

Gaus mean : 28.6276

Gaus Width: 3.3061



B0L101S, U14-ch59, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

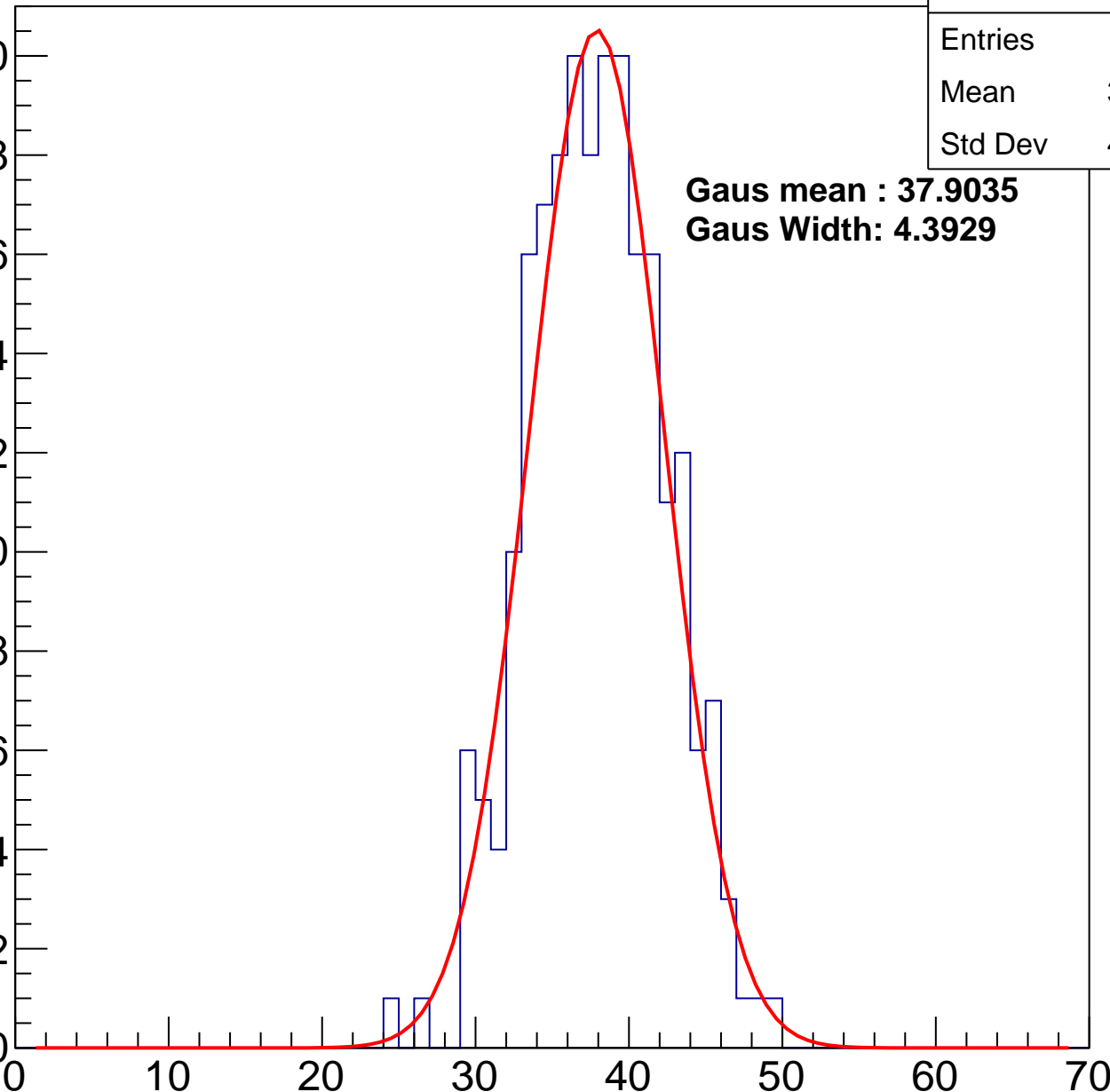
20
18
16
14
12
10
8
6
4
2
0

Entries	230
Mean	37.46
Std Dev	4.335

Gaus mean : 37.9035

Gaus Width: 4.3929

ampl



B0L101S, U14-ch59, adc2

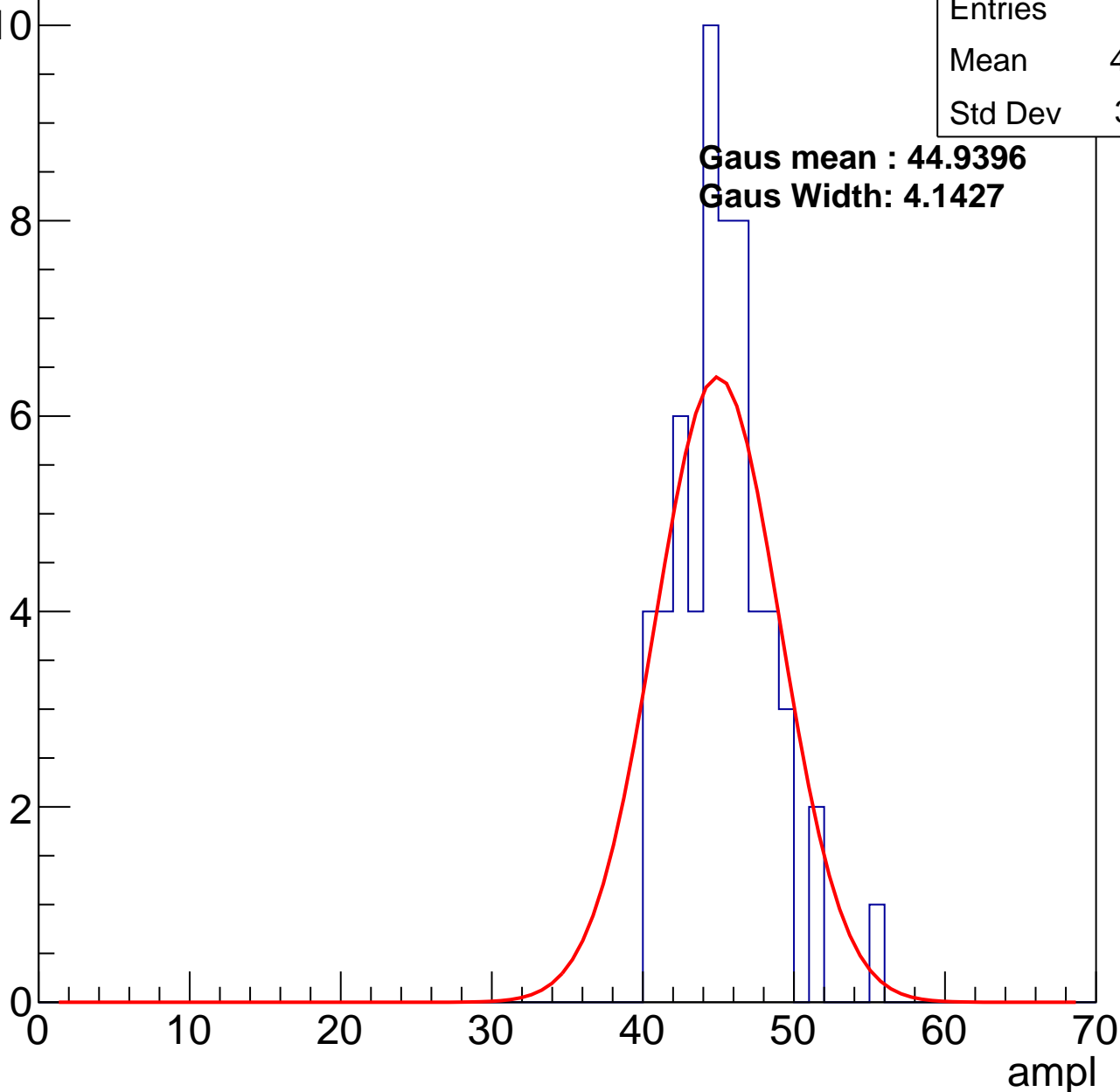
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	44.83
Std Dev	3.001

Gaus mean : 44.9396

Gaus Width: 4.1427



B0L101S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	130
Mean	49.28
Std Dev	3.208

ampl

0

10

20

30

40

50

60

70

B0L101S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#1, port C1

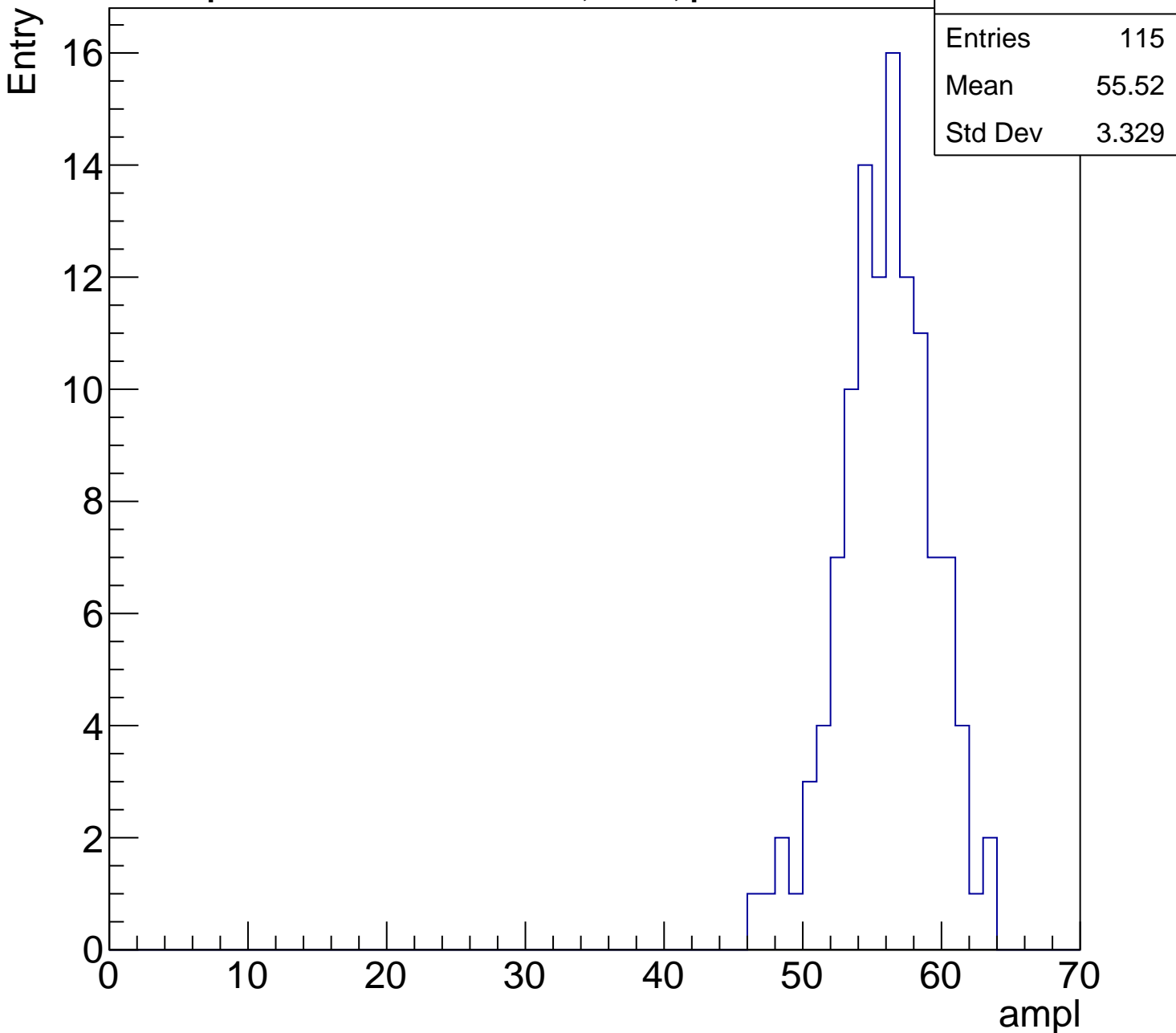
Entries	115
Mean	55.52
Std Dev	3.329

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch59, adc5

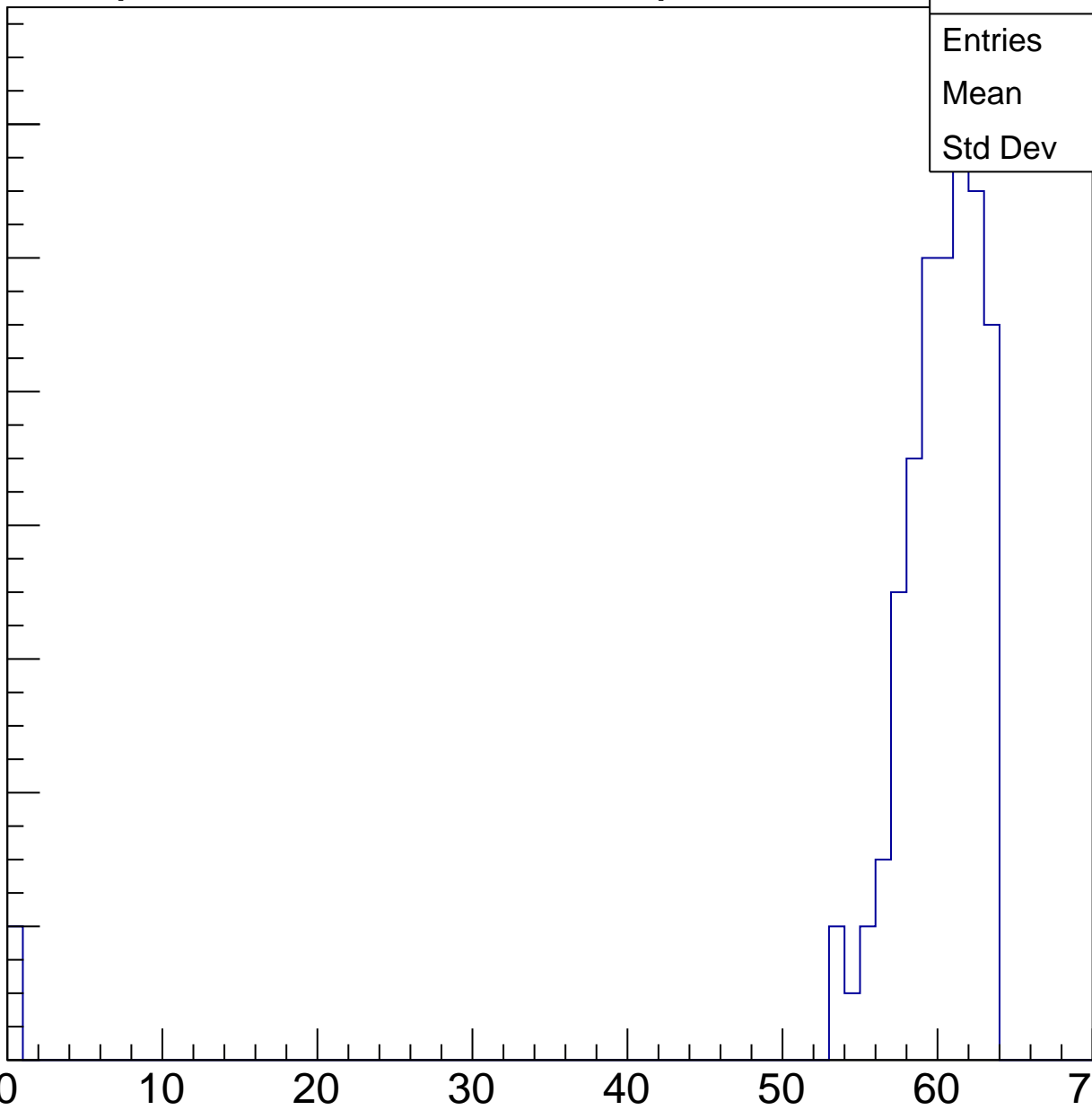
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.44
Std Dev	9.175

ampl

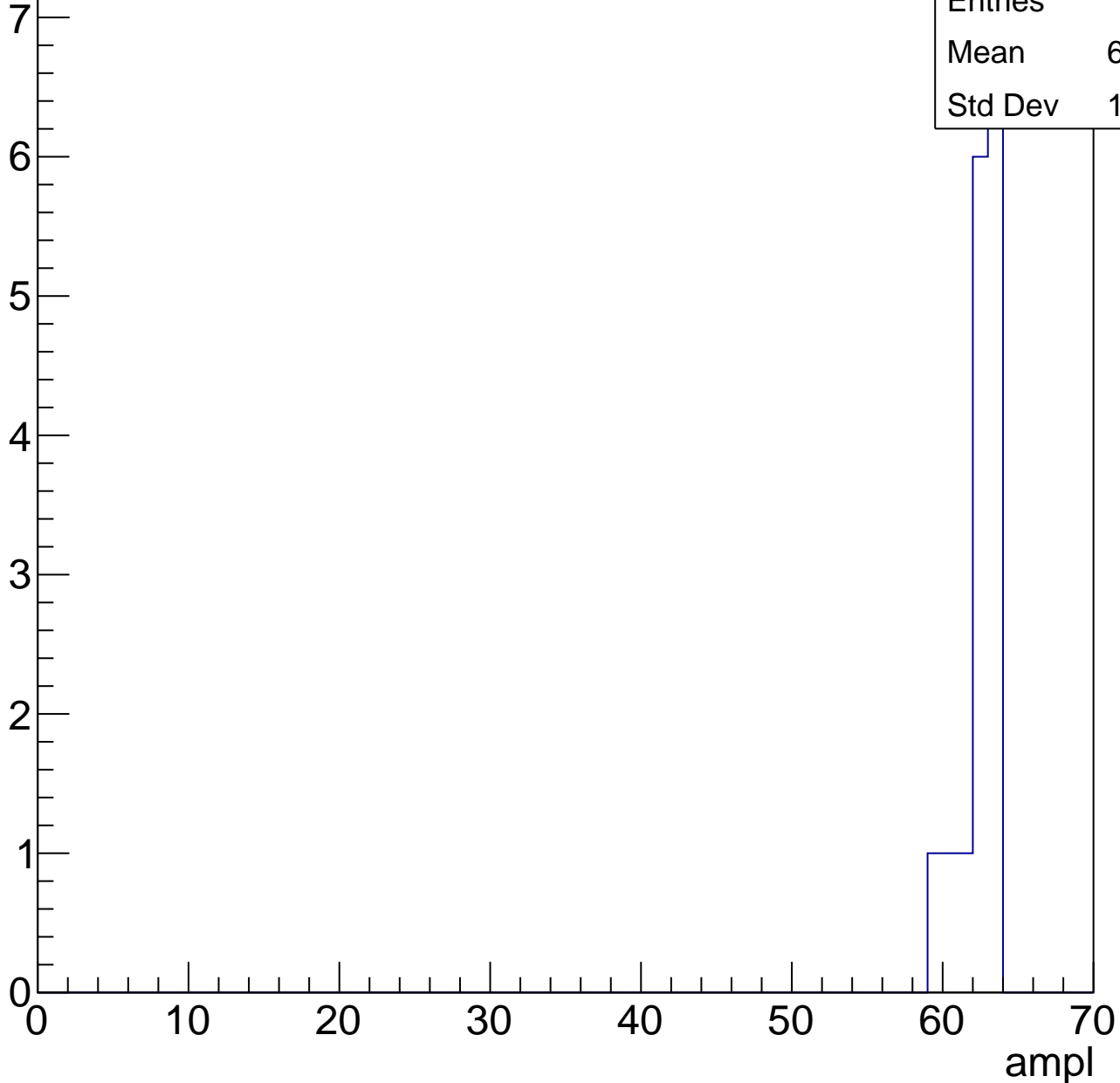


B0L101S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	62.06
Std Dev	1.144



B0L101S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch60, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	88
Mean	28.31
Std Dev	4.286

Gaus mean : 29.0473

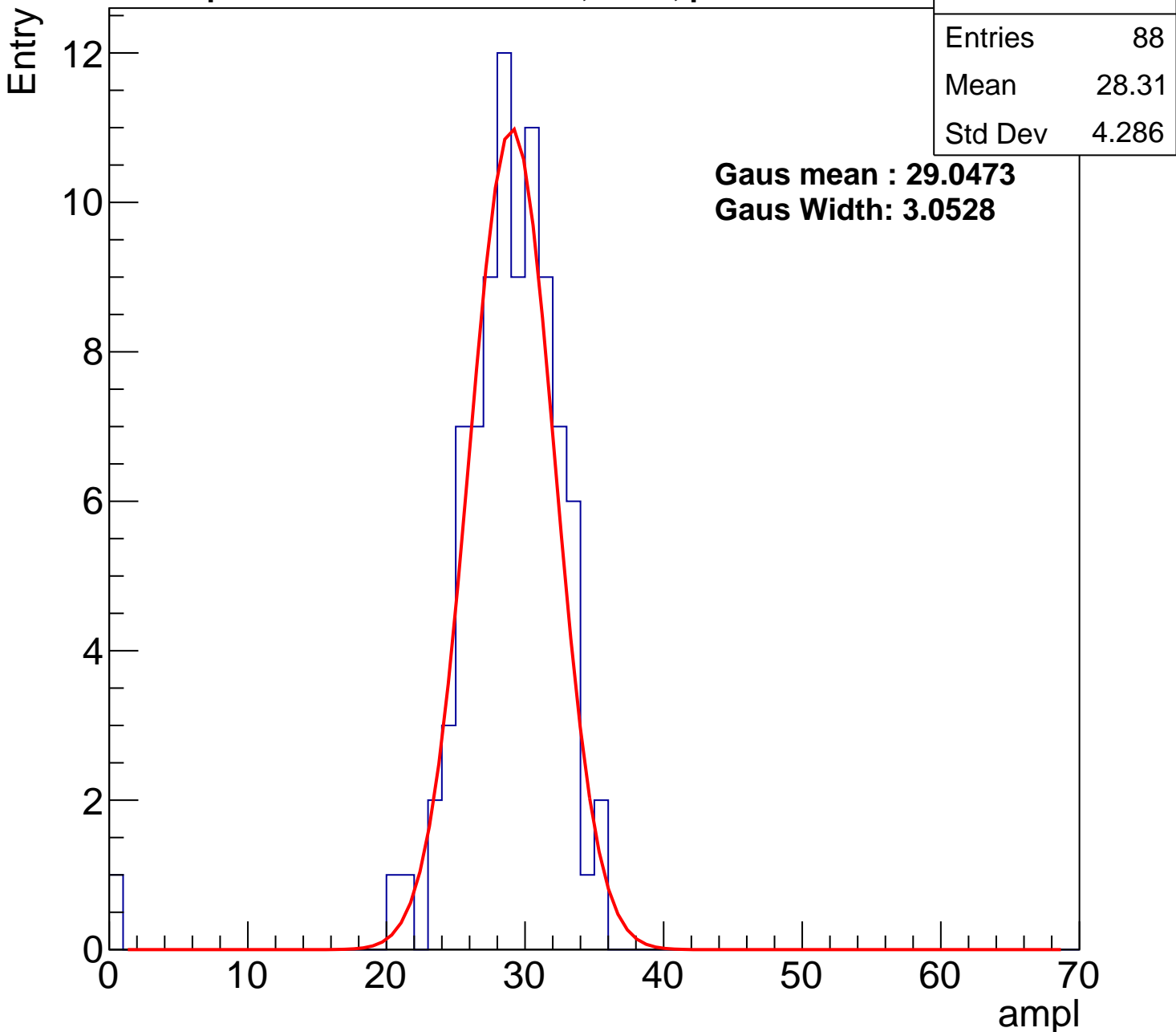
Gaus Width: 3.0528

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



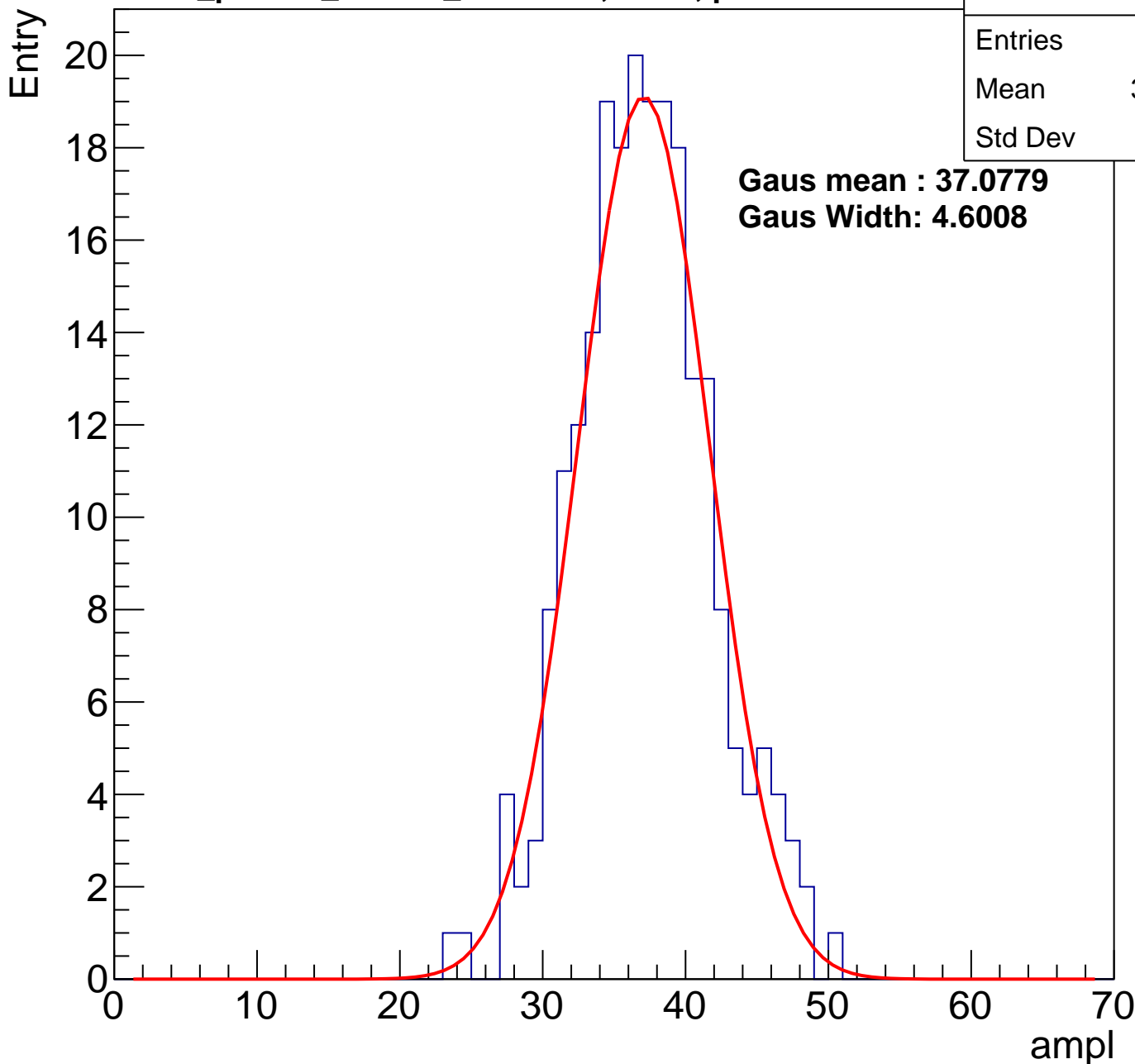
B0L101S, U14-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	227
Mean	36.67
Std Dev	4.701

Gaus mean : 37.0779

Gaus Width: 4.6008



B0L101S, U14-ch60, adc2

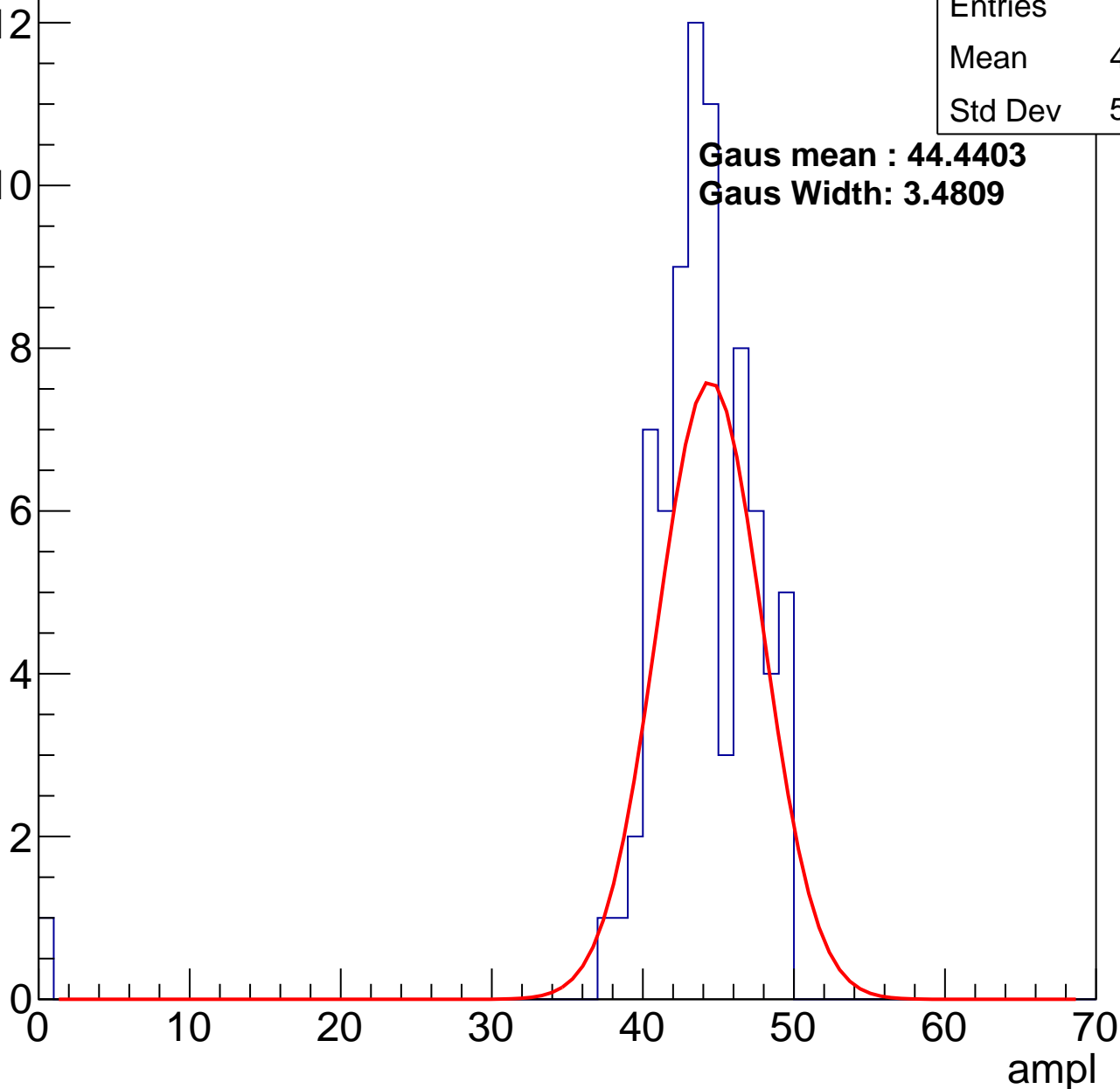
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	76
Mean	43.14
Std Dev	5.744

Gaus mean : 44.4403

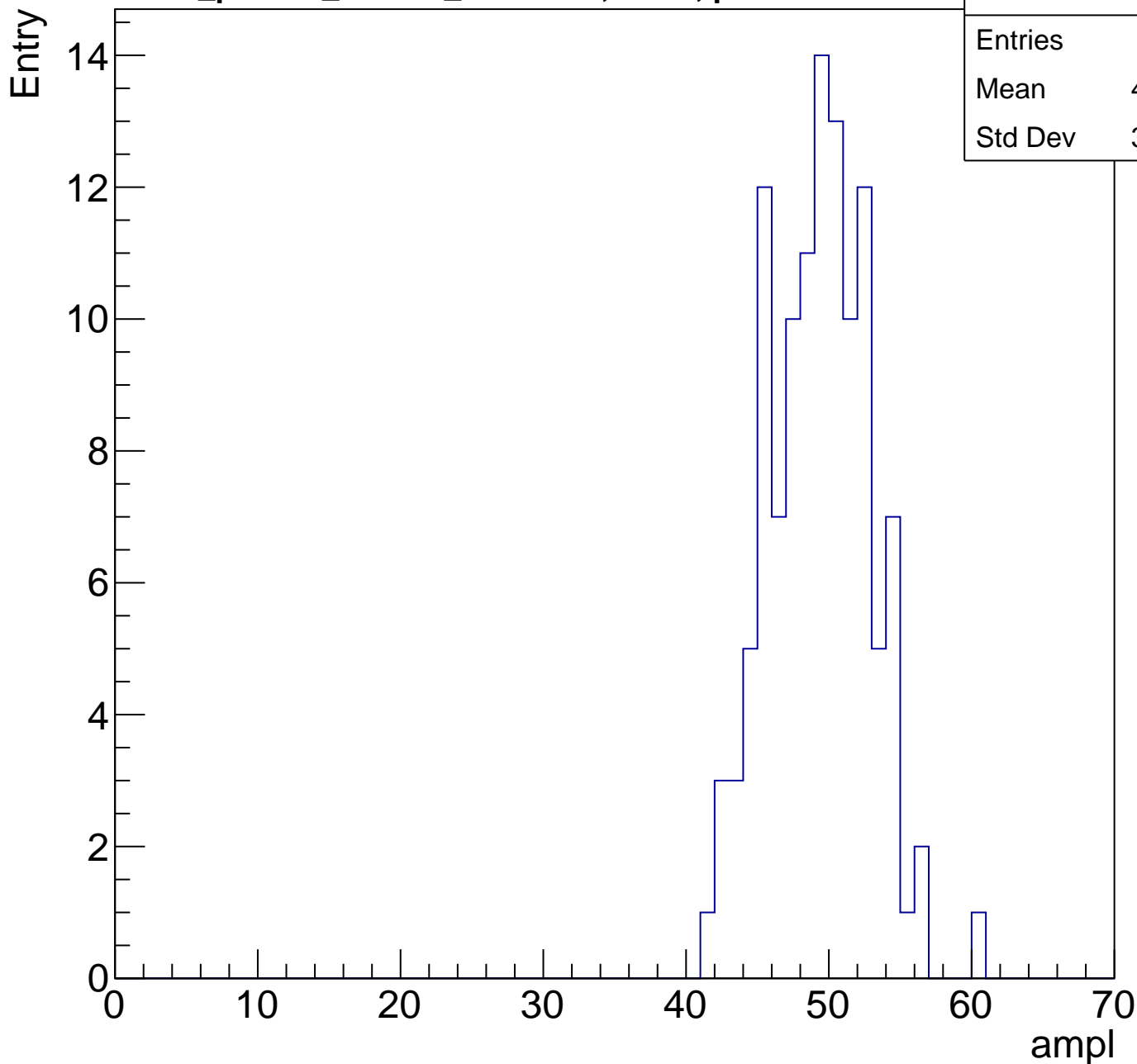
Gaus Width: 3.4809



B0L101S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	48.85
Std Dev	3.482



B0L101S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

104

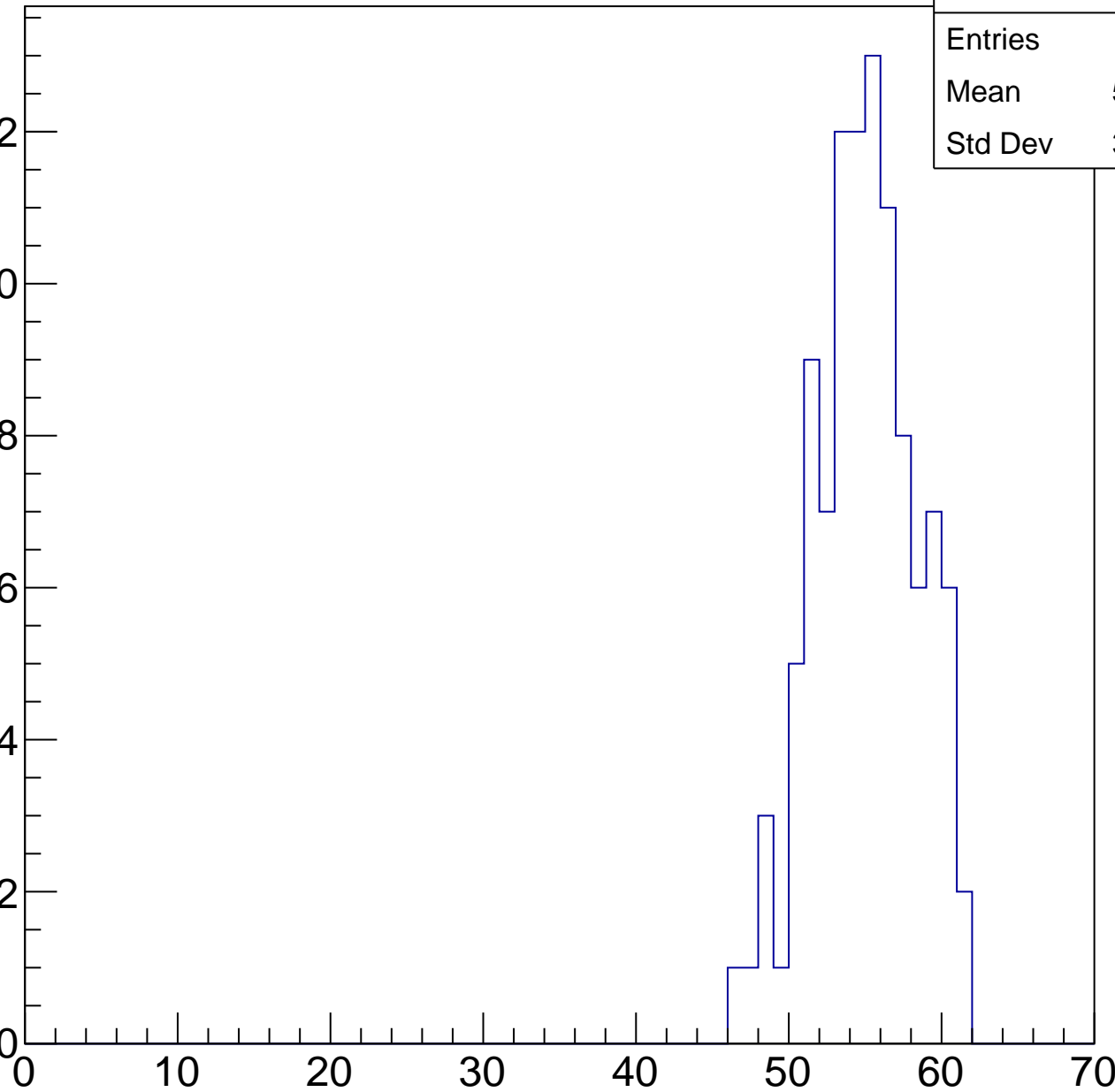
Mean

54.55

Std Dev

3.296

ampl



B0L101S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

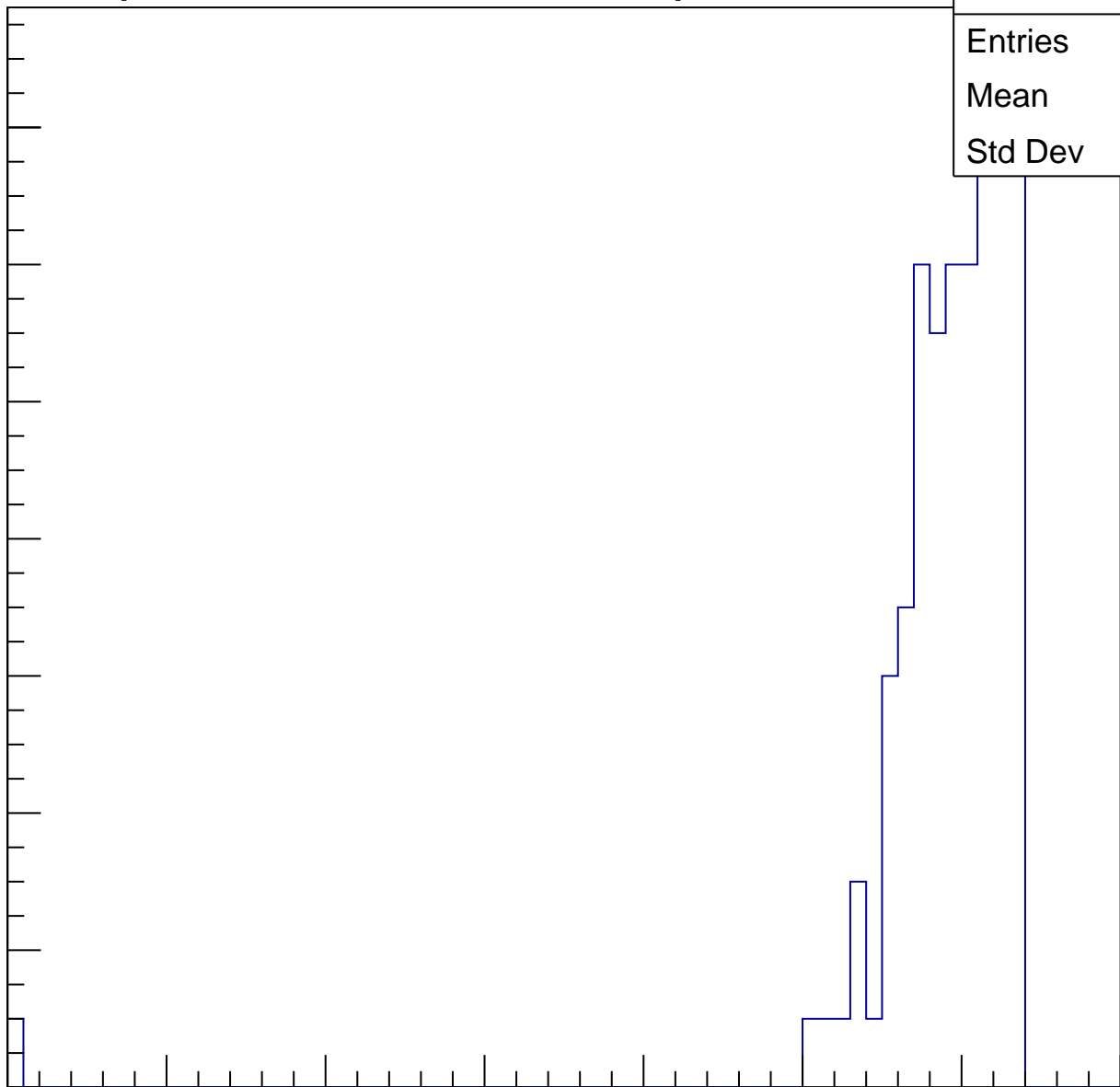
2

0

Entries	111
Mean	58.59
Std Dev	6.321

ampl

0 10 20 30 40 50 60 70

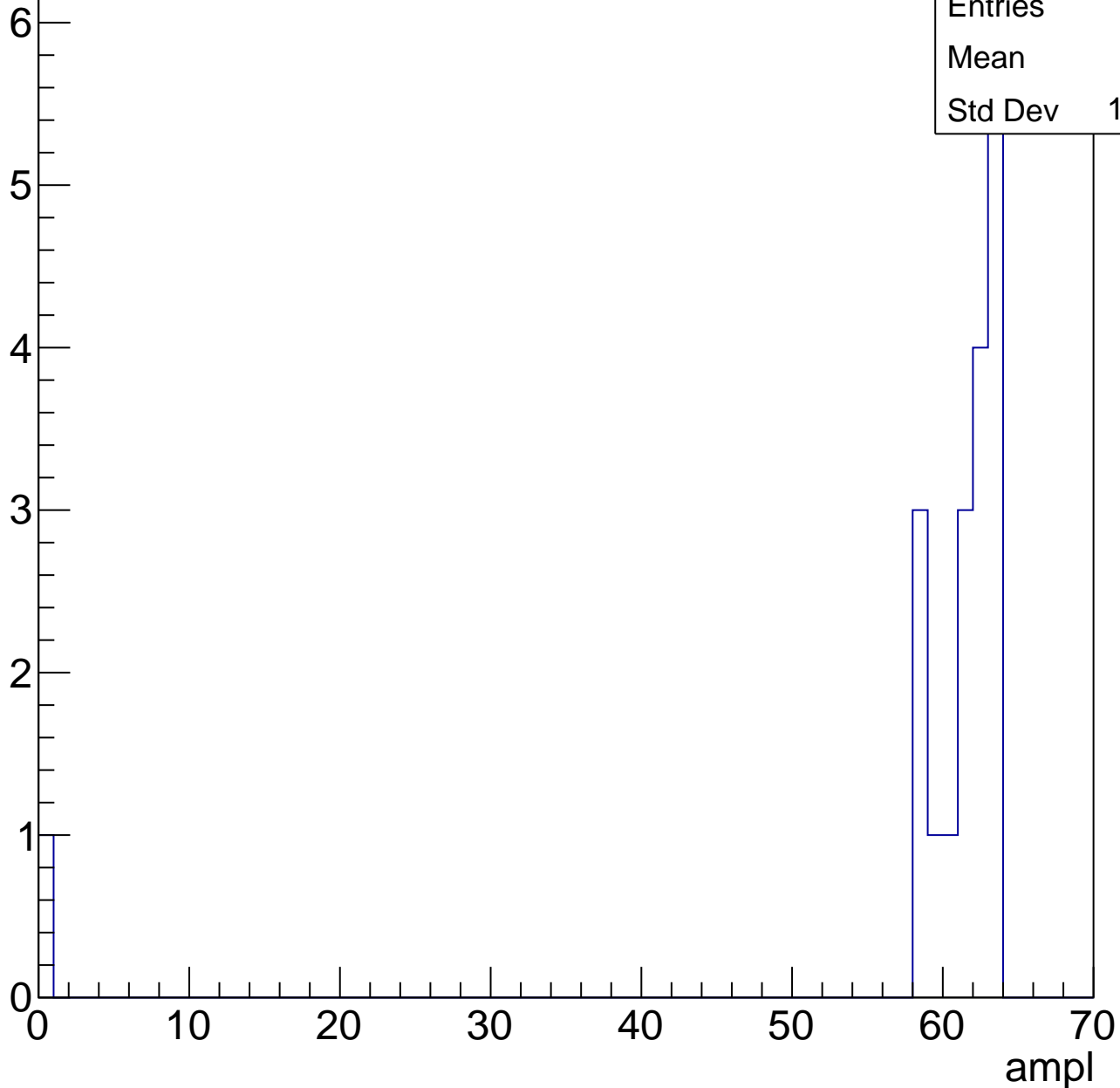


B0L101S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

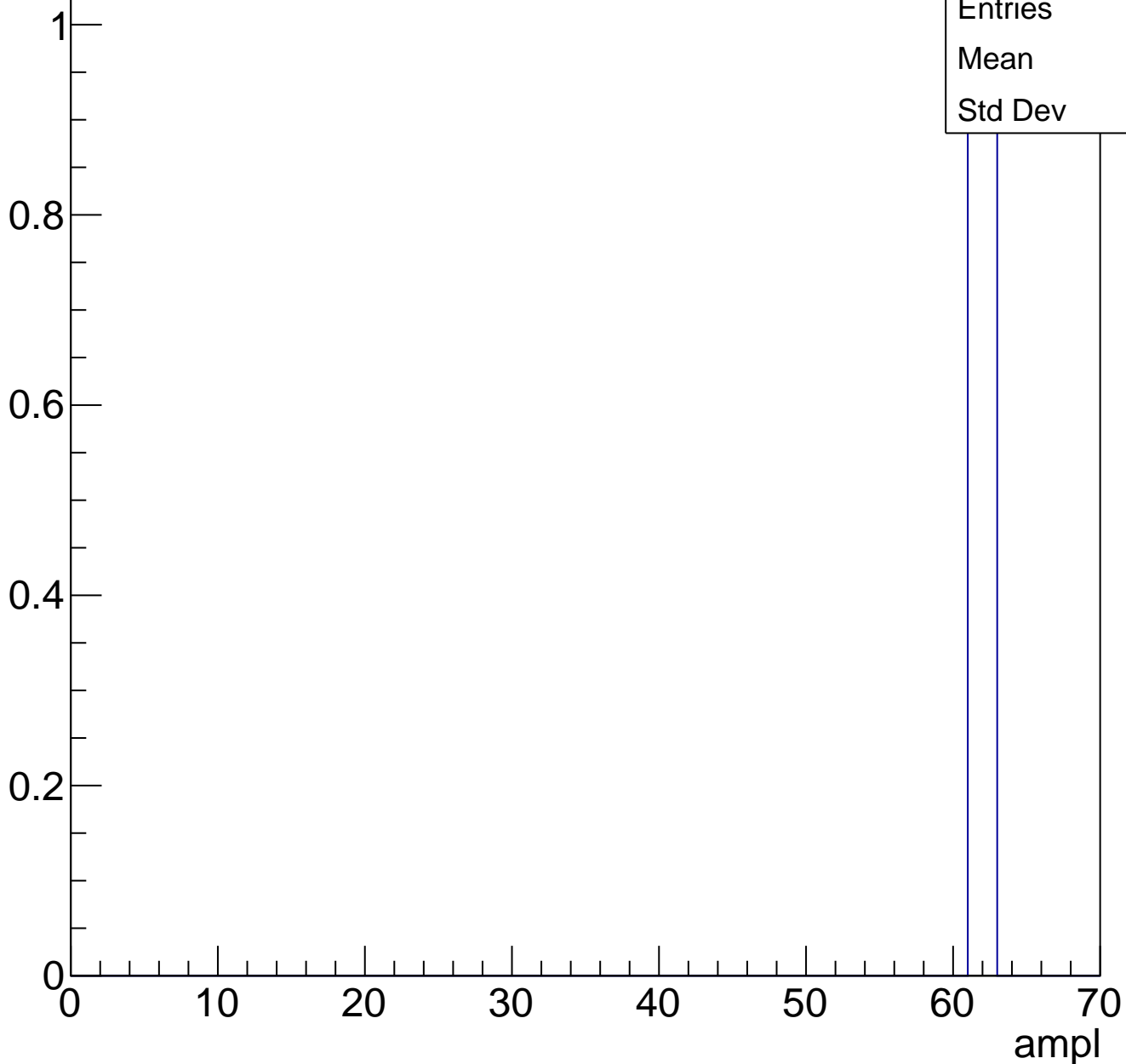
Entries	19
Mean	58
Std Dev	13.78



B0L101S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



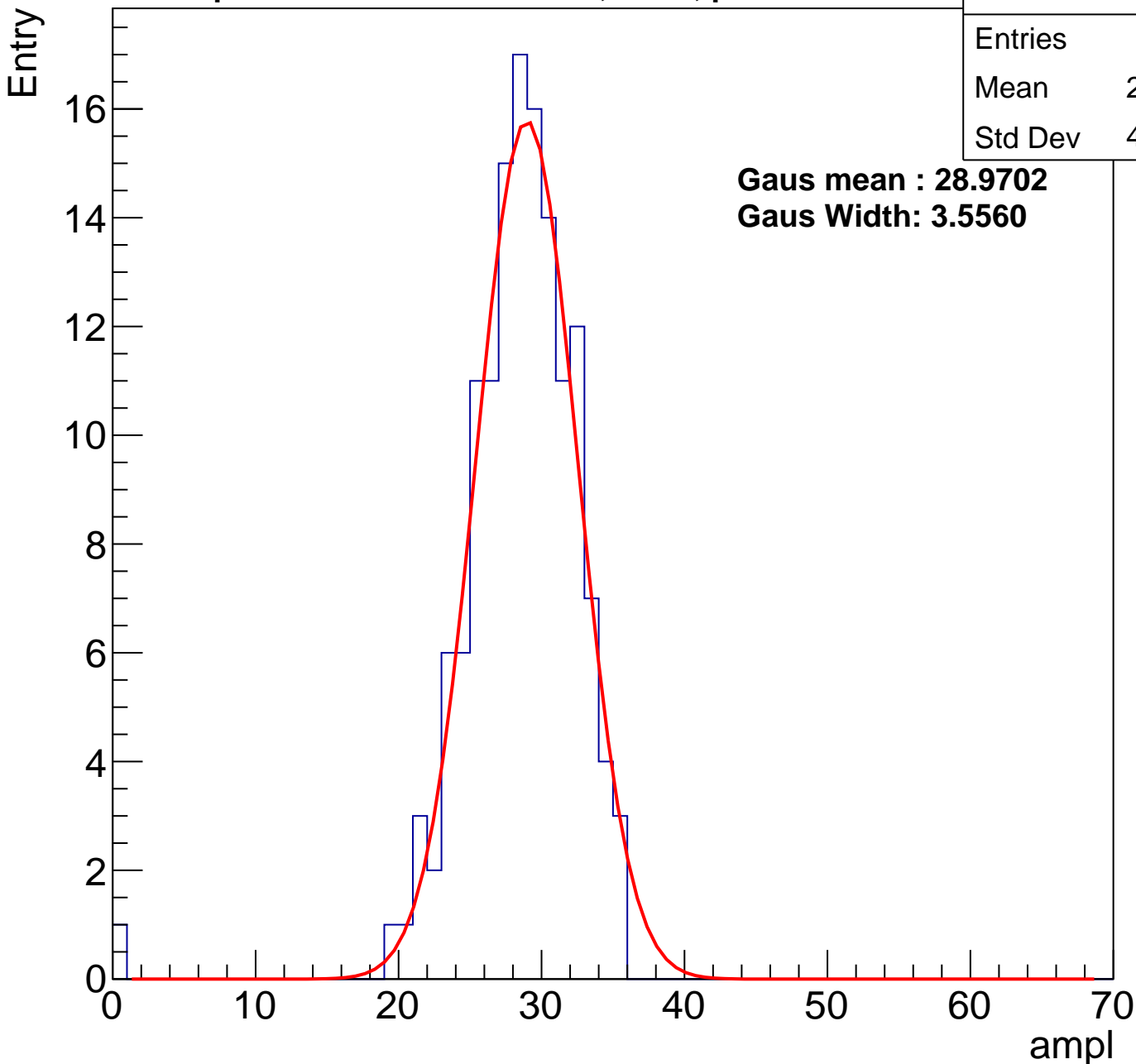
B0L101S, U14-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	28.02
Std Dev	4.109

Gaus mean : 28.9702

Gaus Width: 3.5560



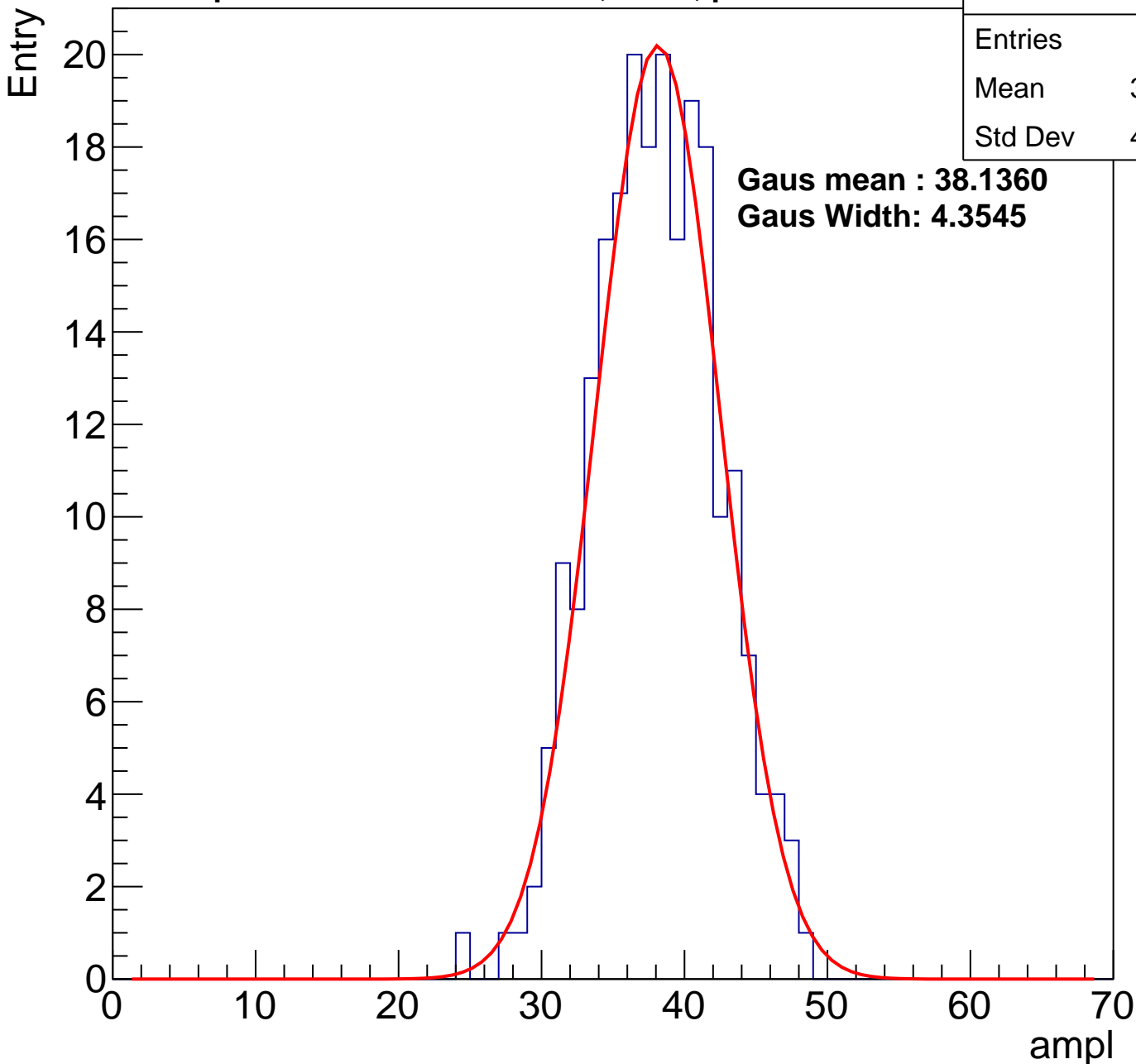
B0L101S, U14-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	224
Mean	37.55
Std Dev	4.305

Gaus mean : 38.1360

Gaus Width: 4.3545



B0L101S, U14-ch61, adc2

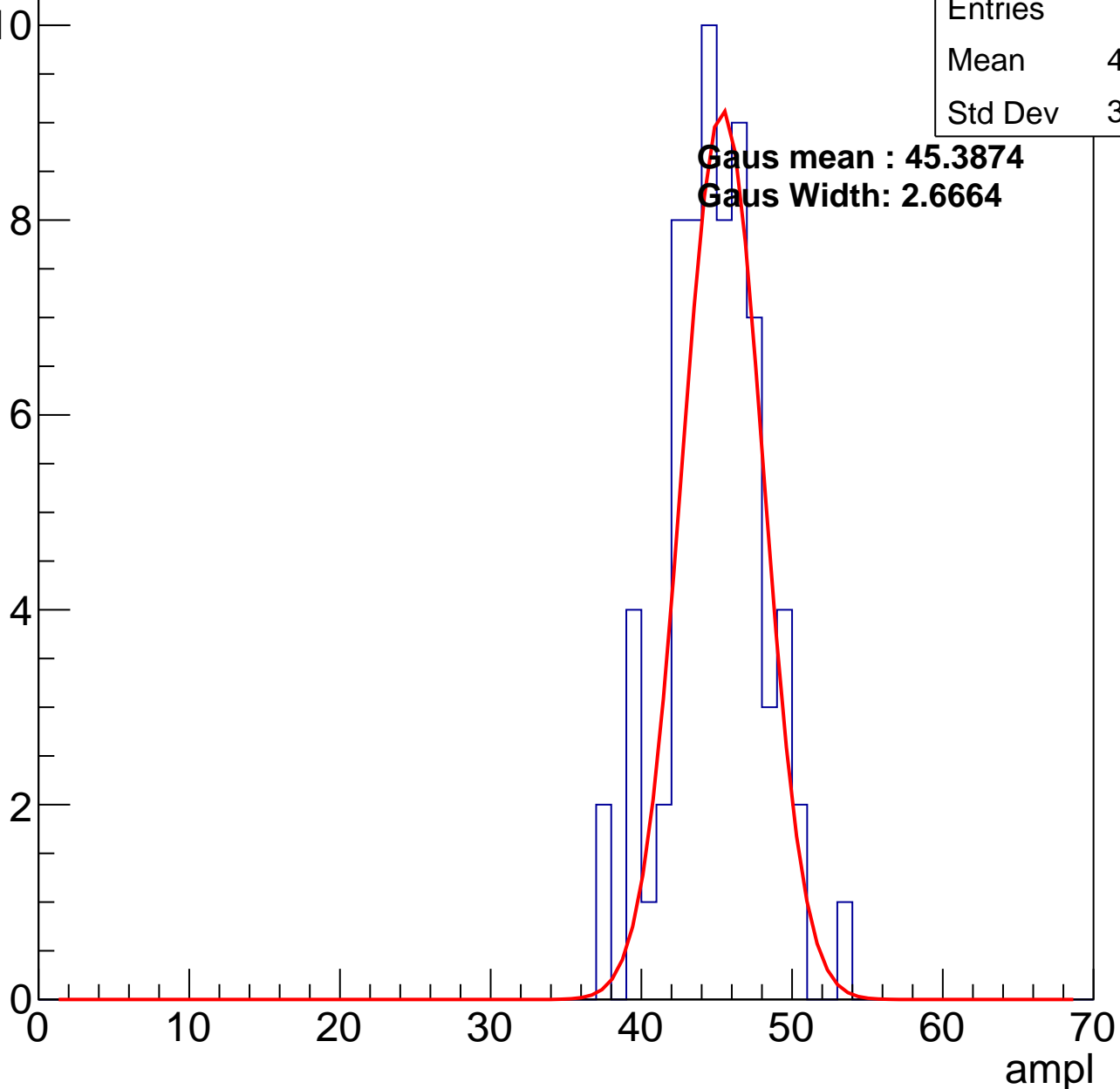
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	69
Mean	44.46
Std Dev	3.105

Gaus mean : 45.3874

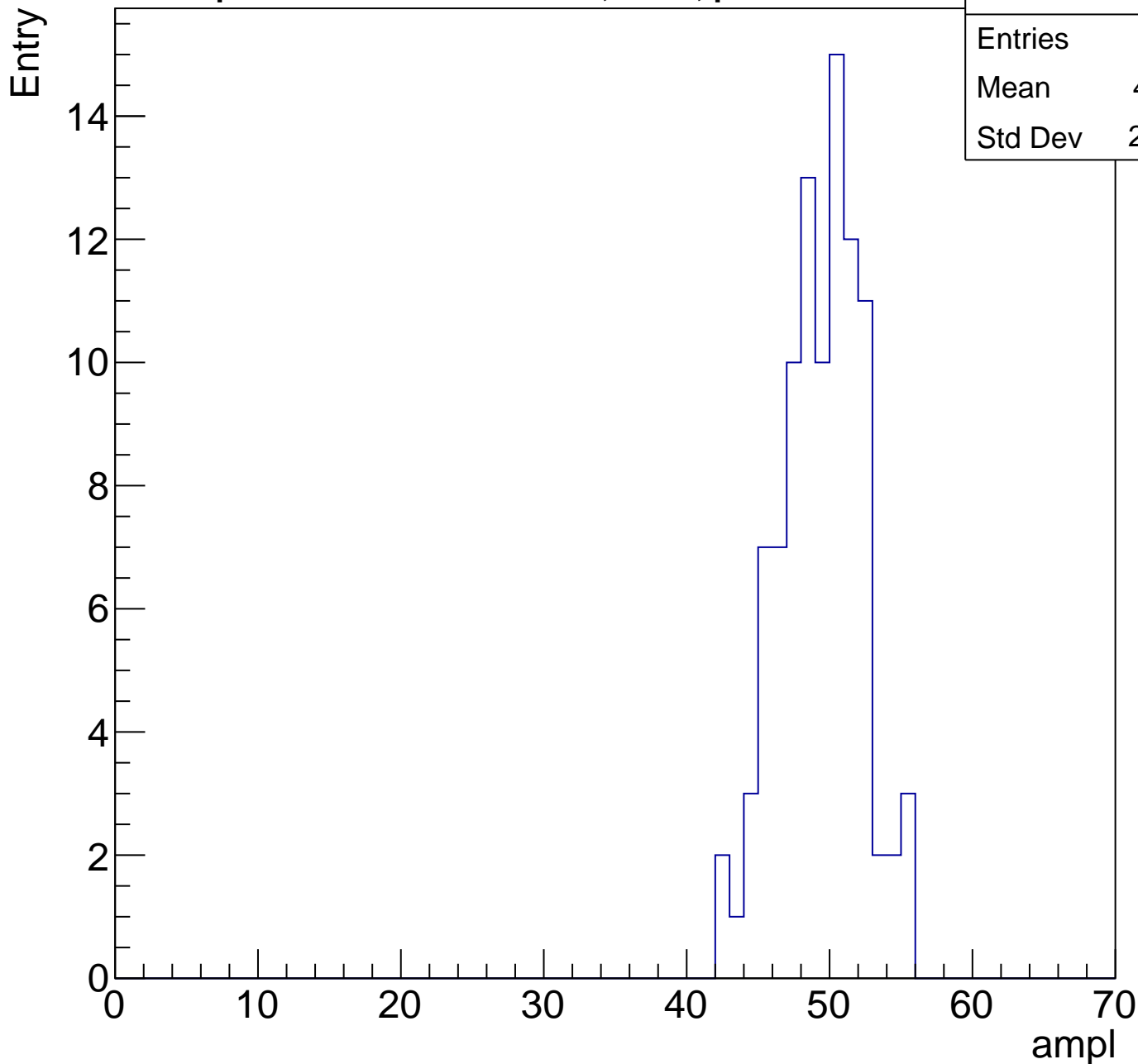
Gaus Width: 2.6664



B0L101S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	48.91
Std Dev	2.836



B0L101S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	55.07
Std Dev	3.472

Entry

16

14

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B0L101S, U14-ch61, adc5

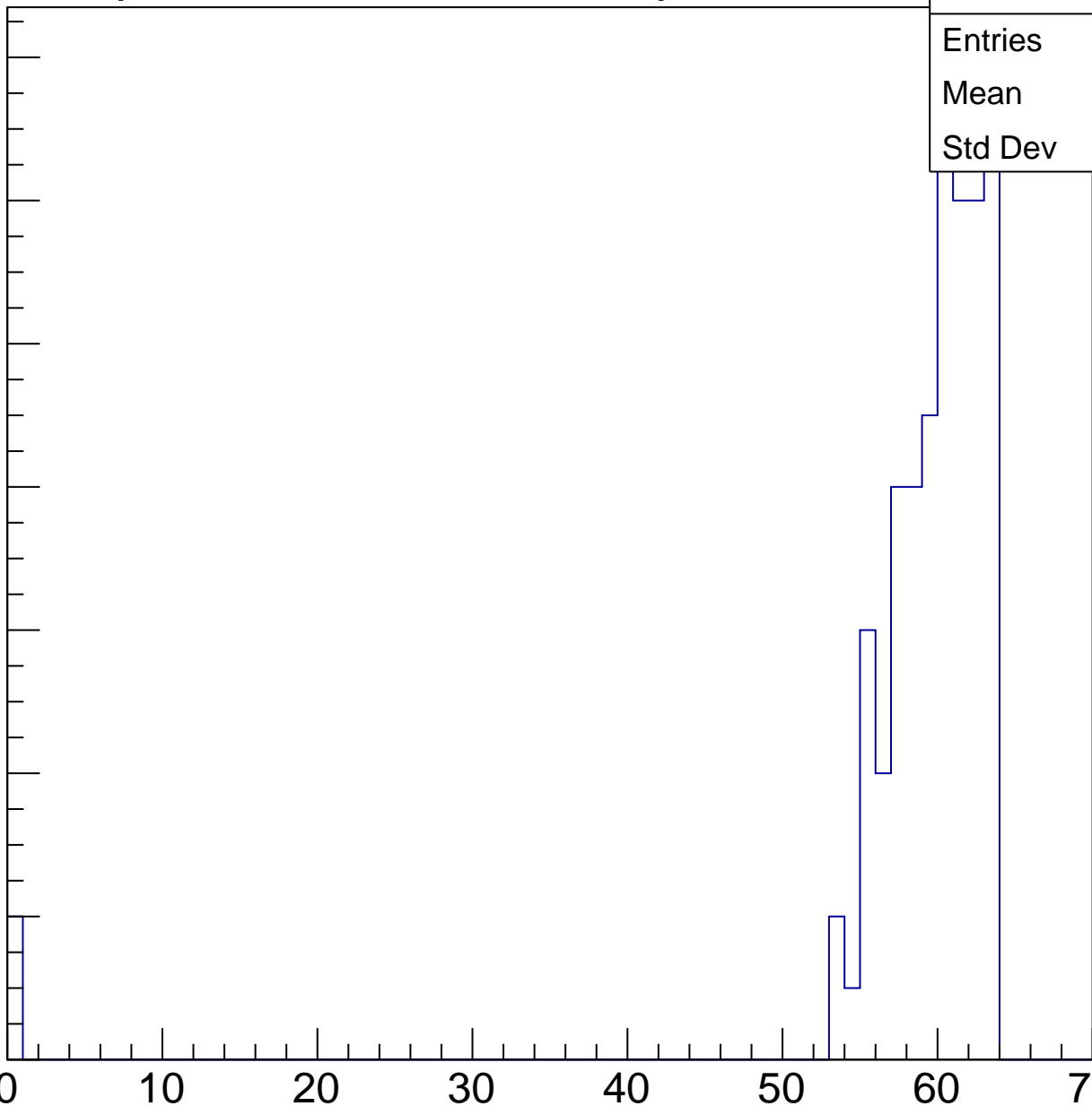
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.29
Std Dev	9.076

ampl

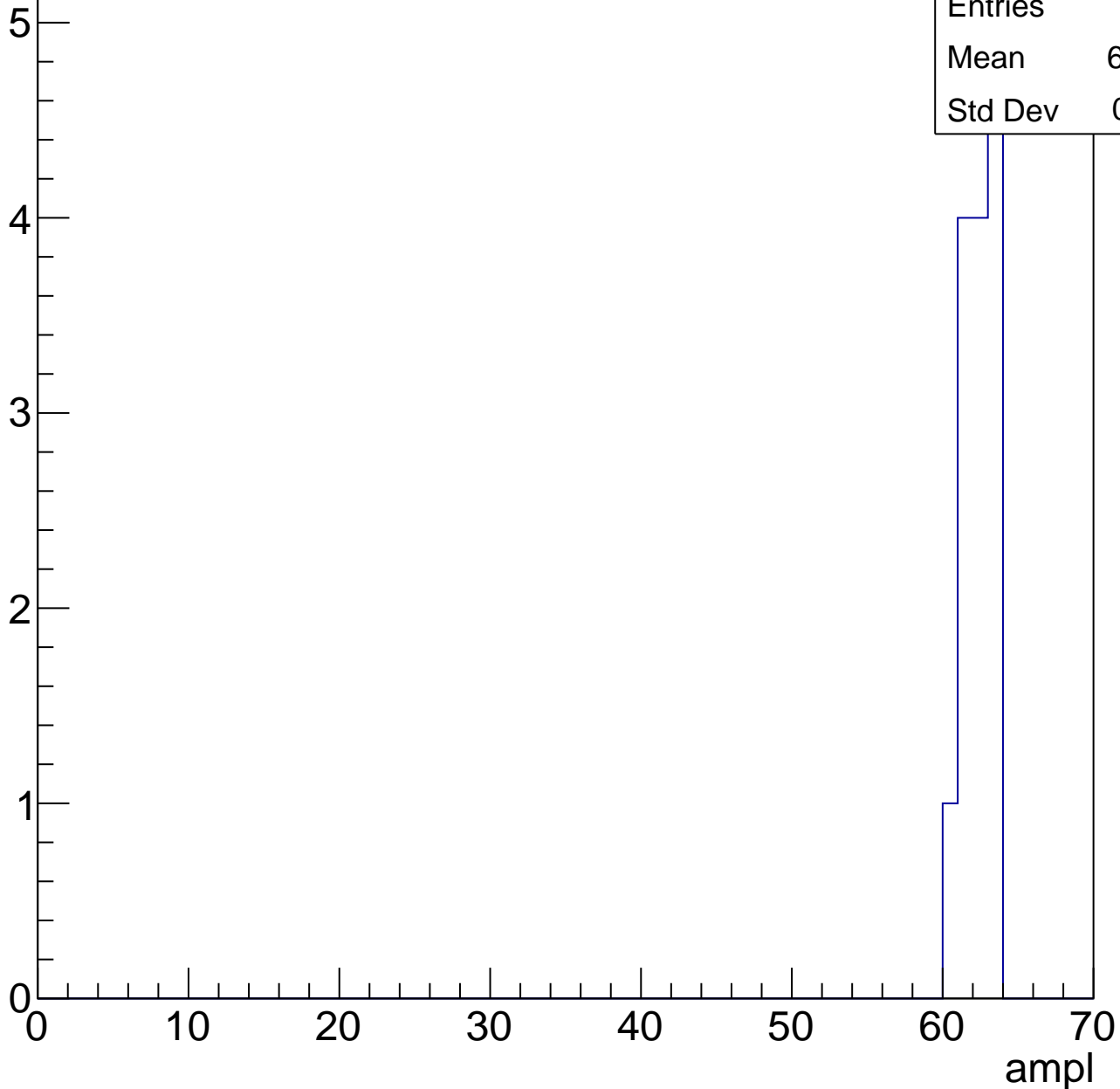


B0L101S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.93
Std Dev	0.961



B0L101S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch62, adc0

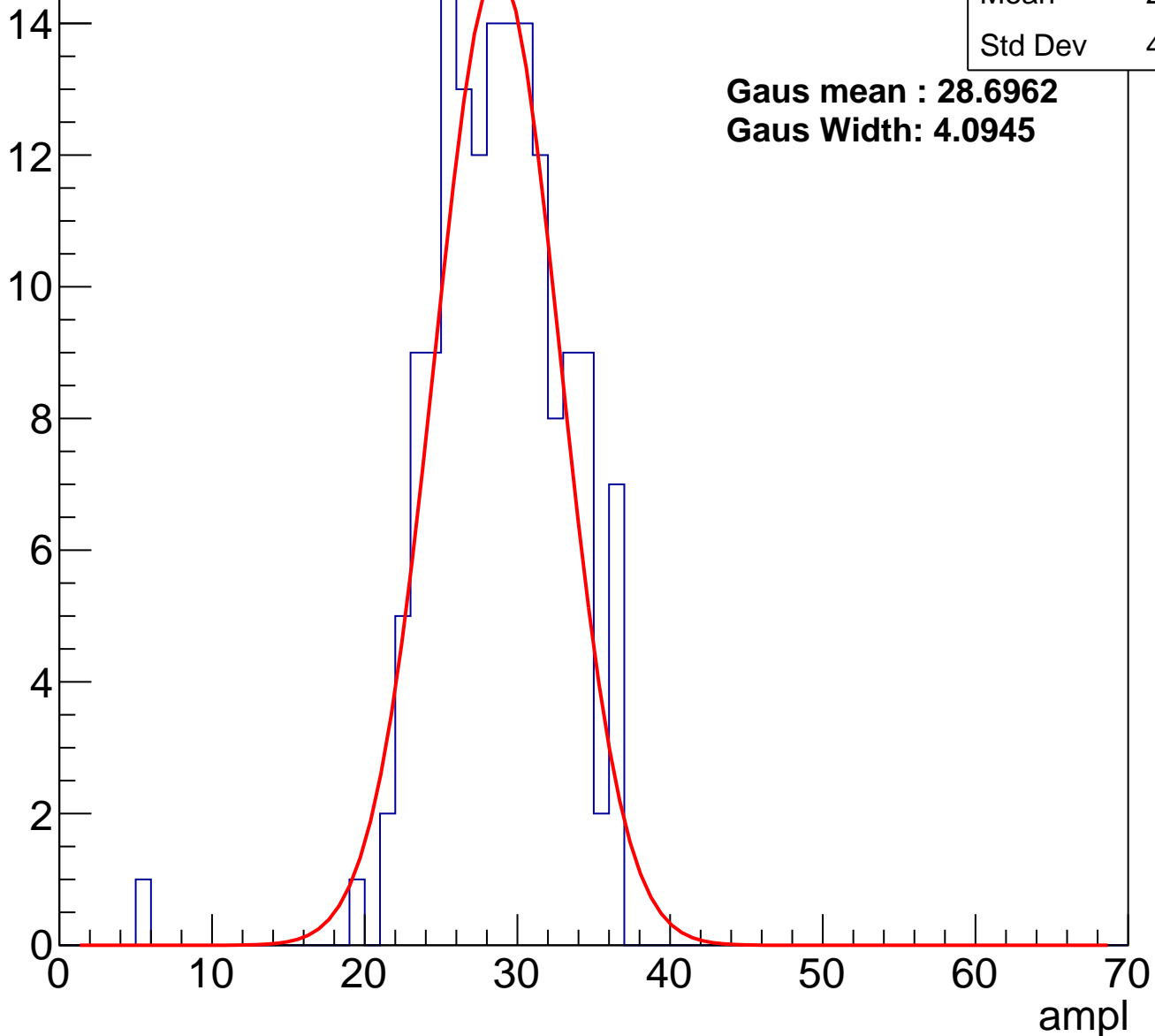
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.25
Std Dev	4.272

Gaus mean : 28.6962

Gaus Width: 4.0945

Entry



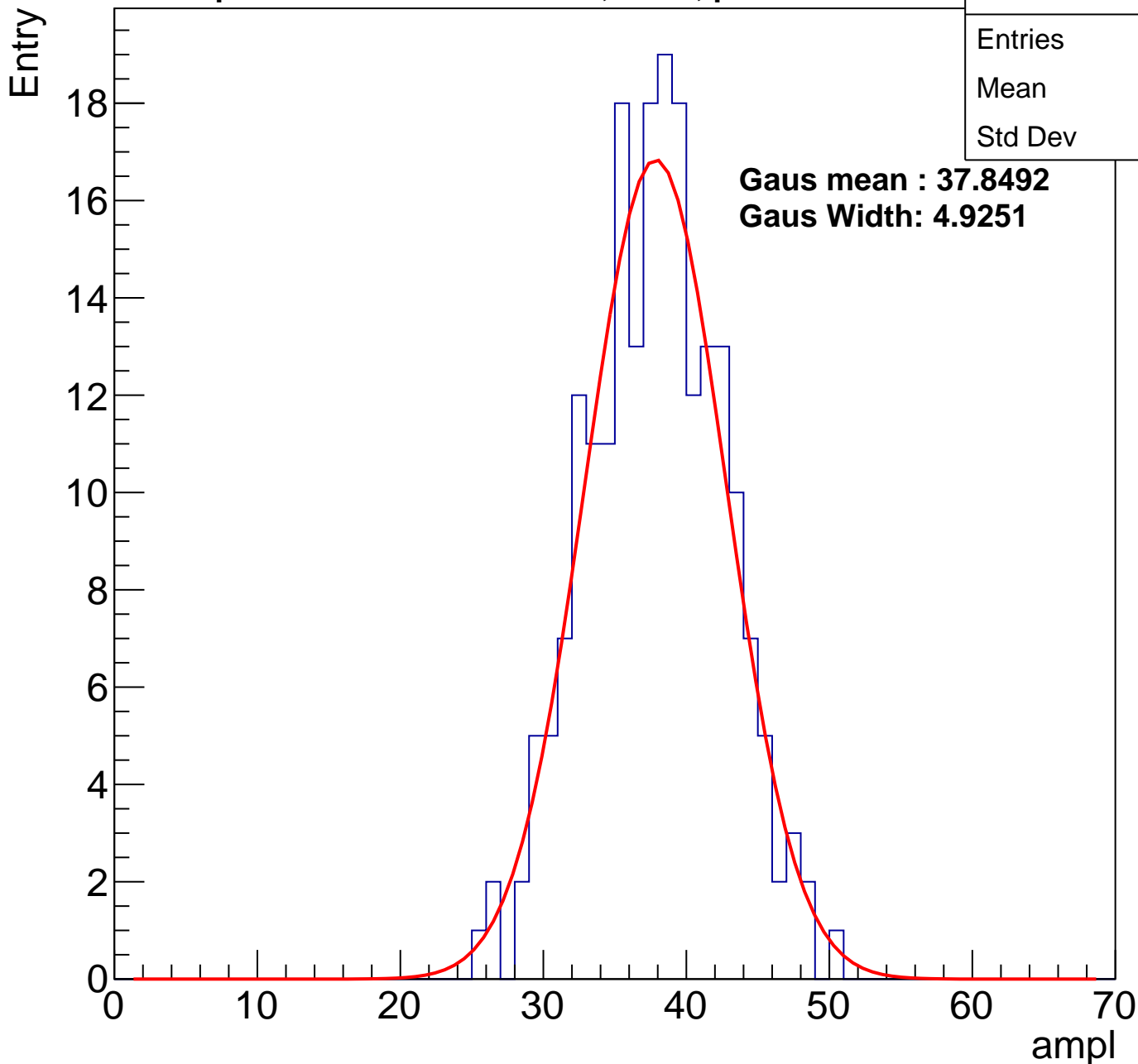
B0L101S, U14-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	210
Mean	37.4
Std Dev	4.69

Gaus mean : 37.8492

Gaus Width: 4.9251



B0L101S, U14-ch62, adc2

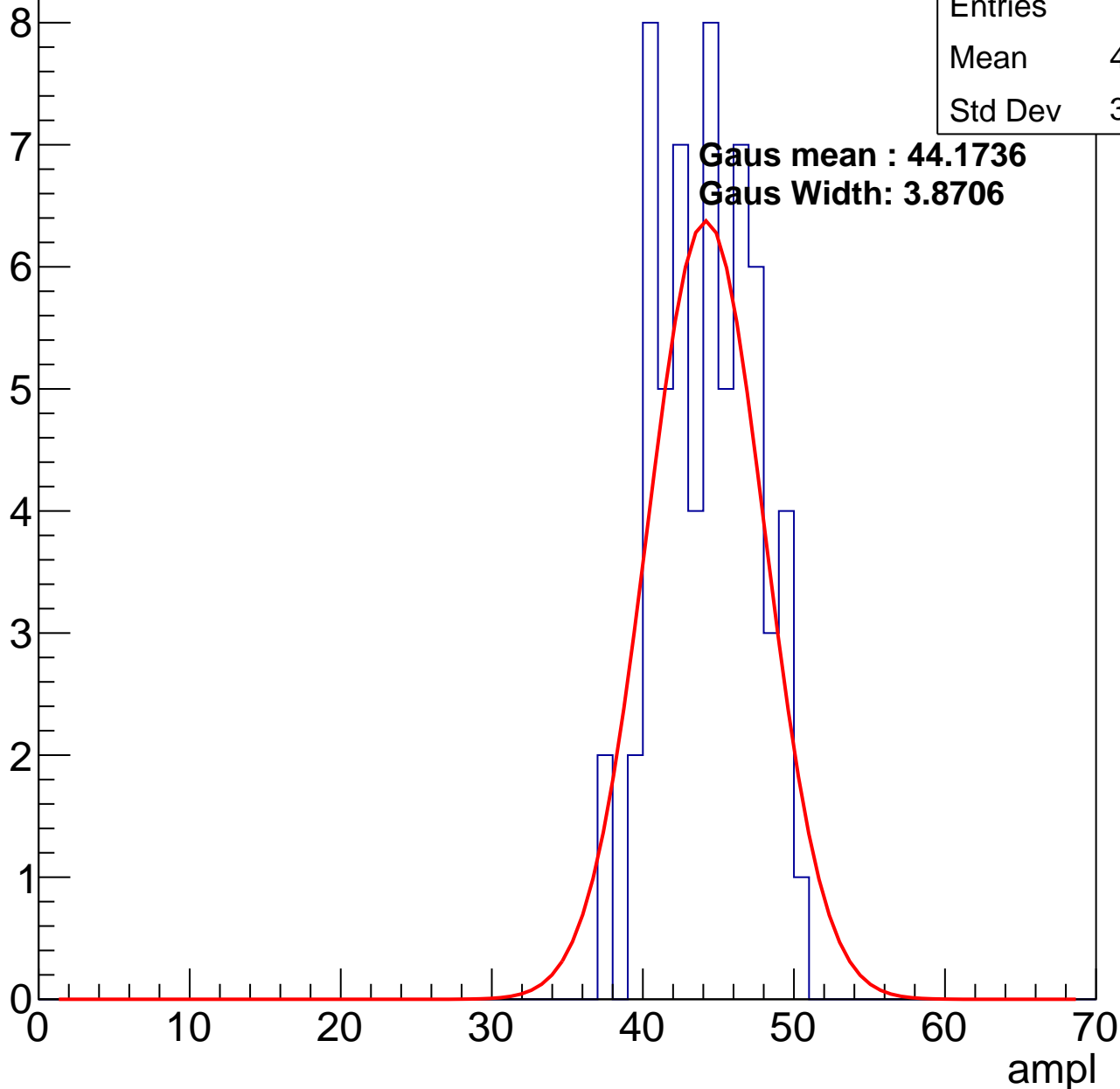
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	62
Mean	43.77
Std Dev	3.164

Gaus mean : 44.1736

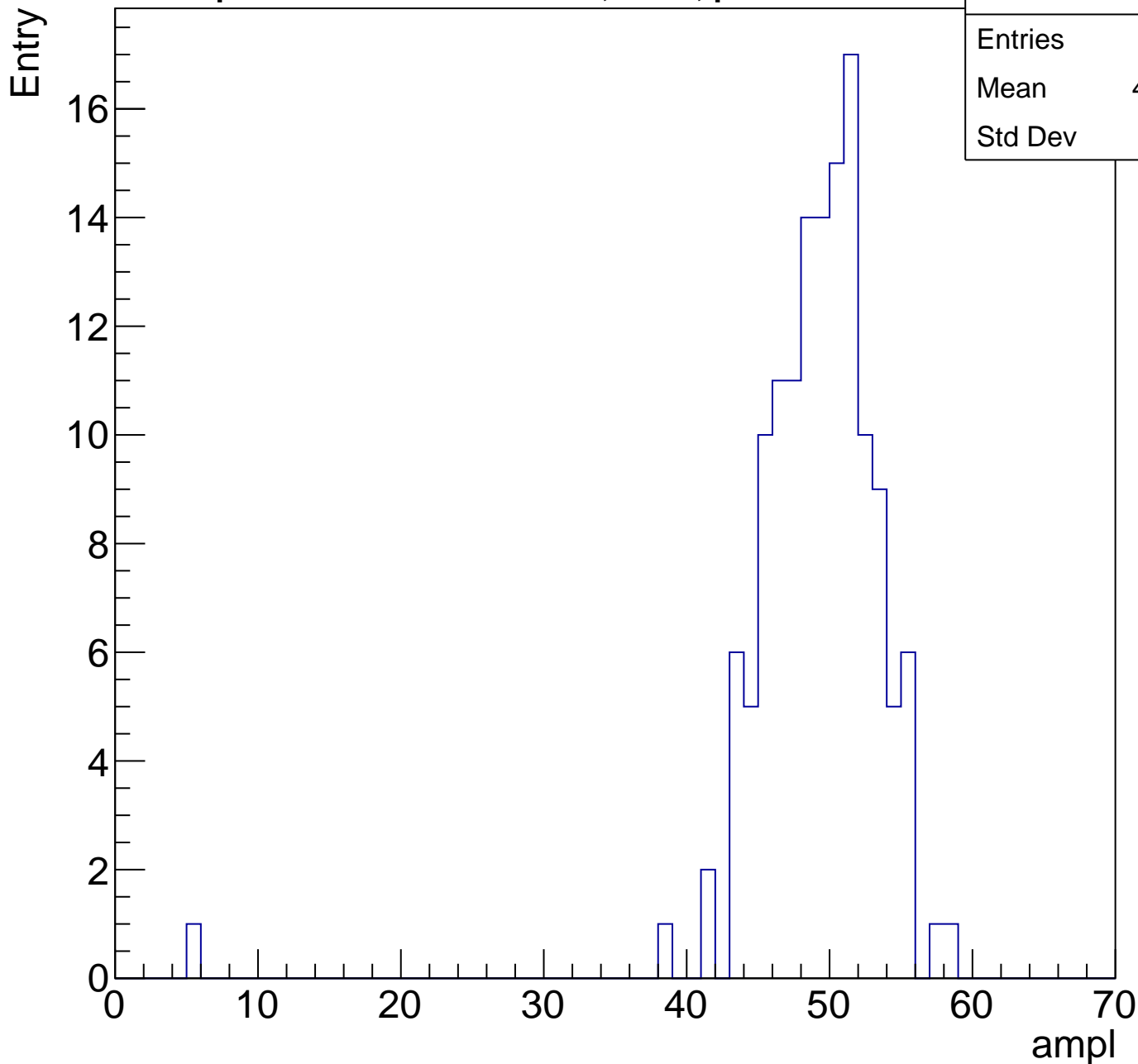
Gaus Width: 3.8706



B0L101S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	48.65
Std Dev	5.11



B0L101S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	55.75
Std Dev	3.208

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

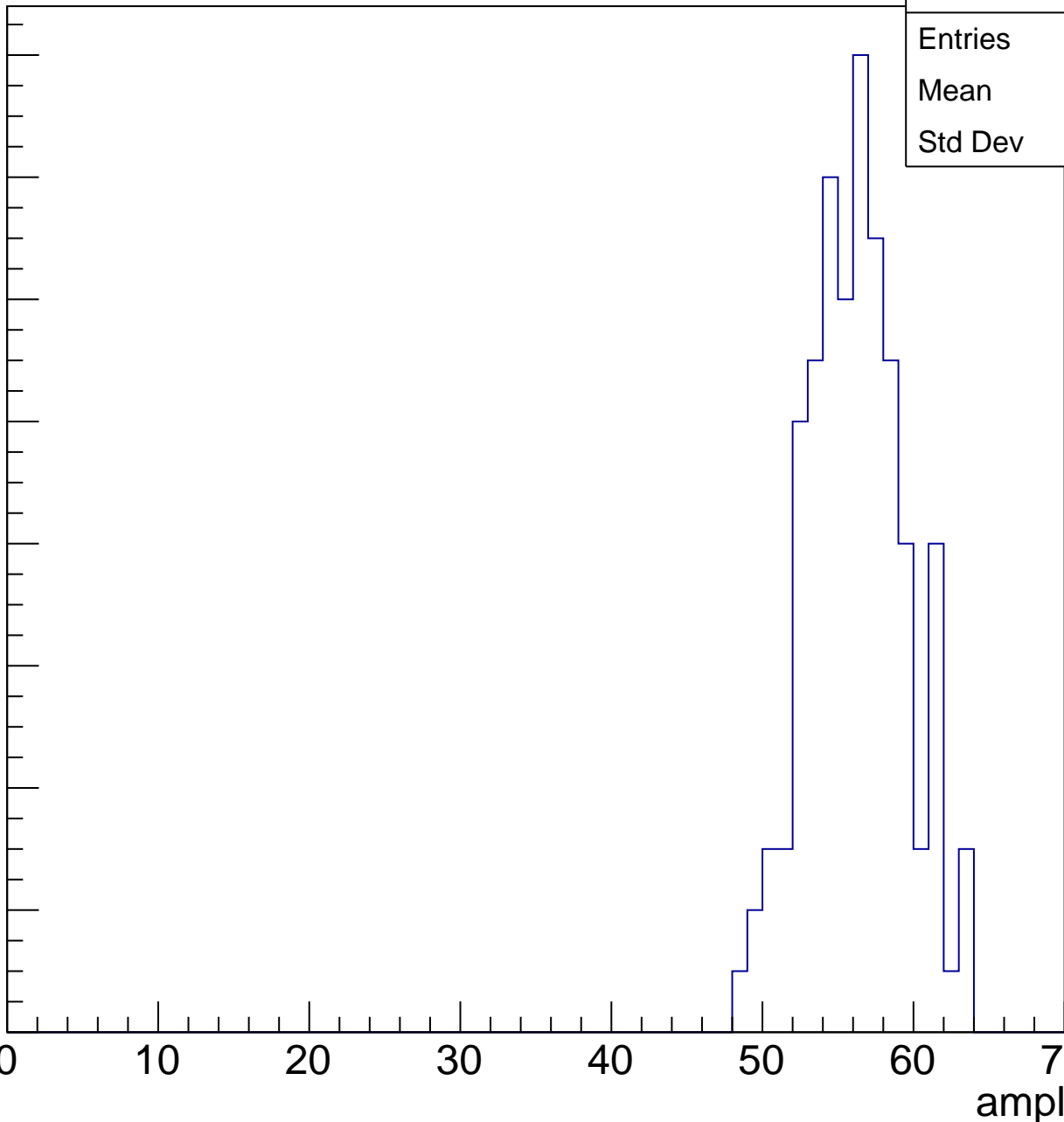
40

50

60

70

ampl



B0L101S, U14-ch62, adc5

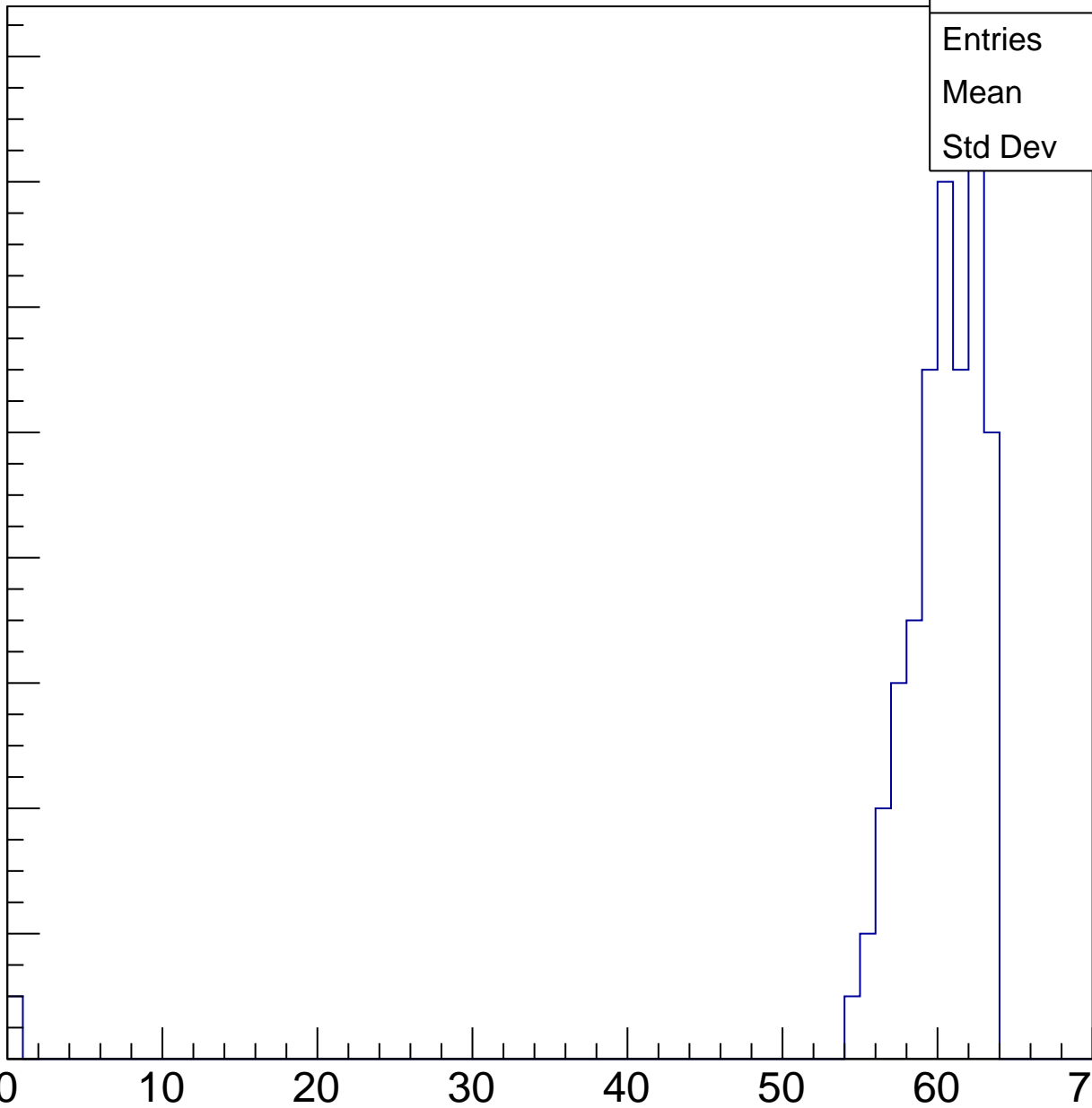
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.25
Std Dev	6.909

ampl

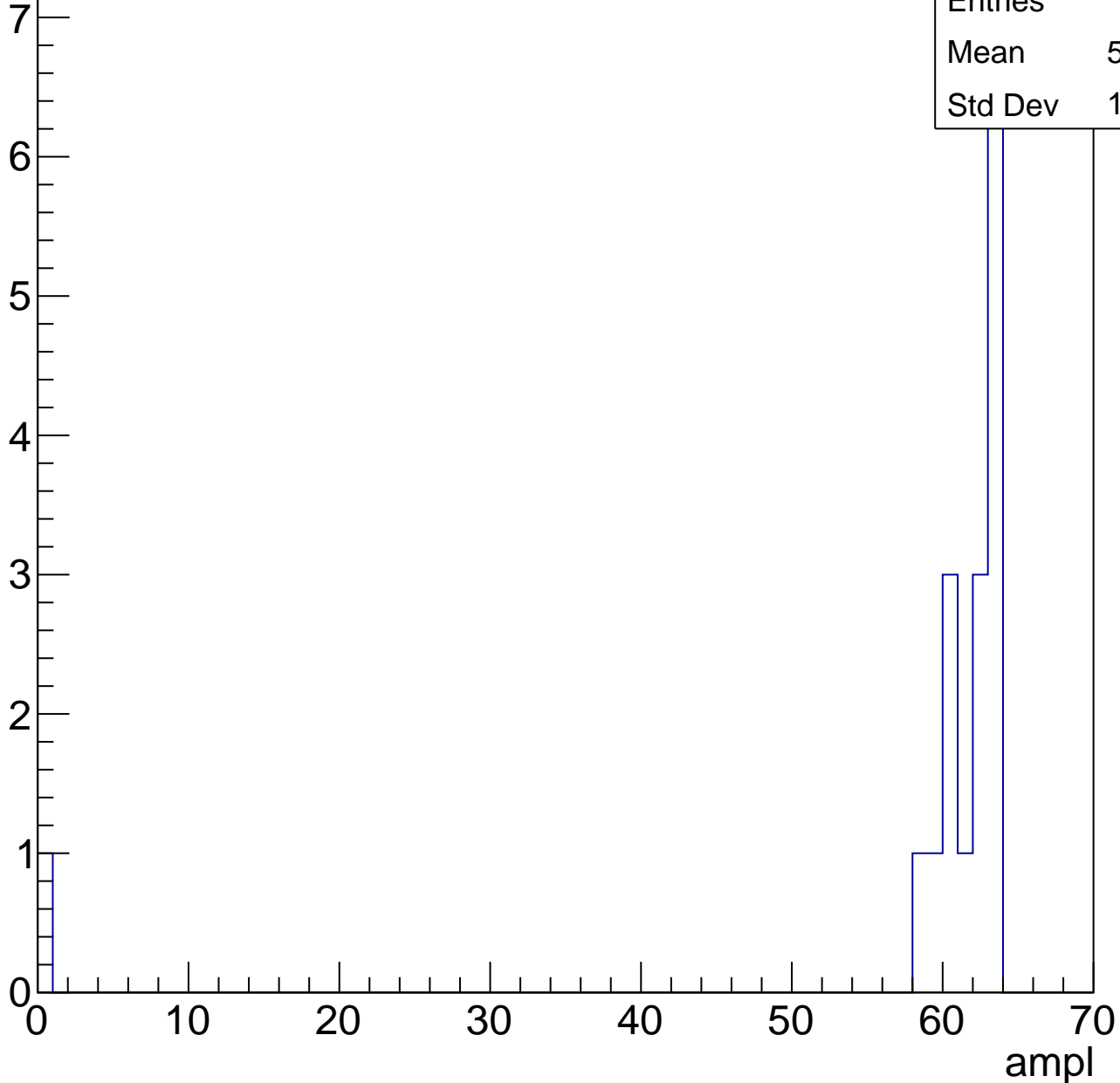


B0L101S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

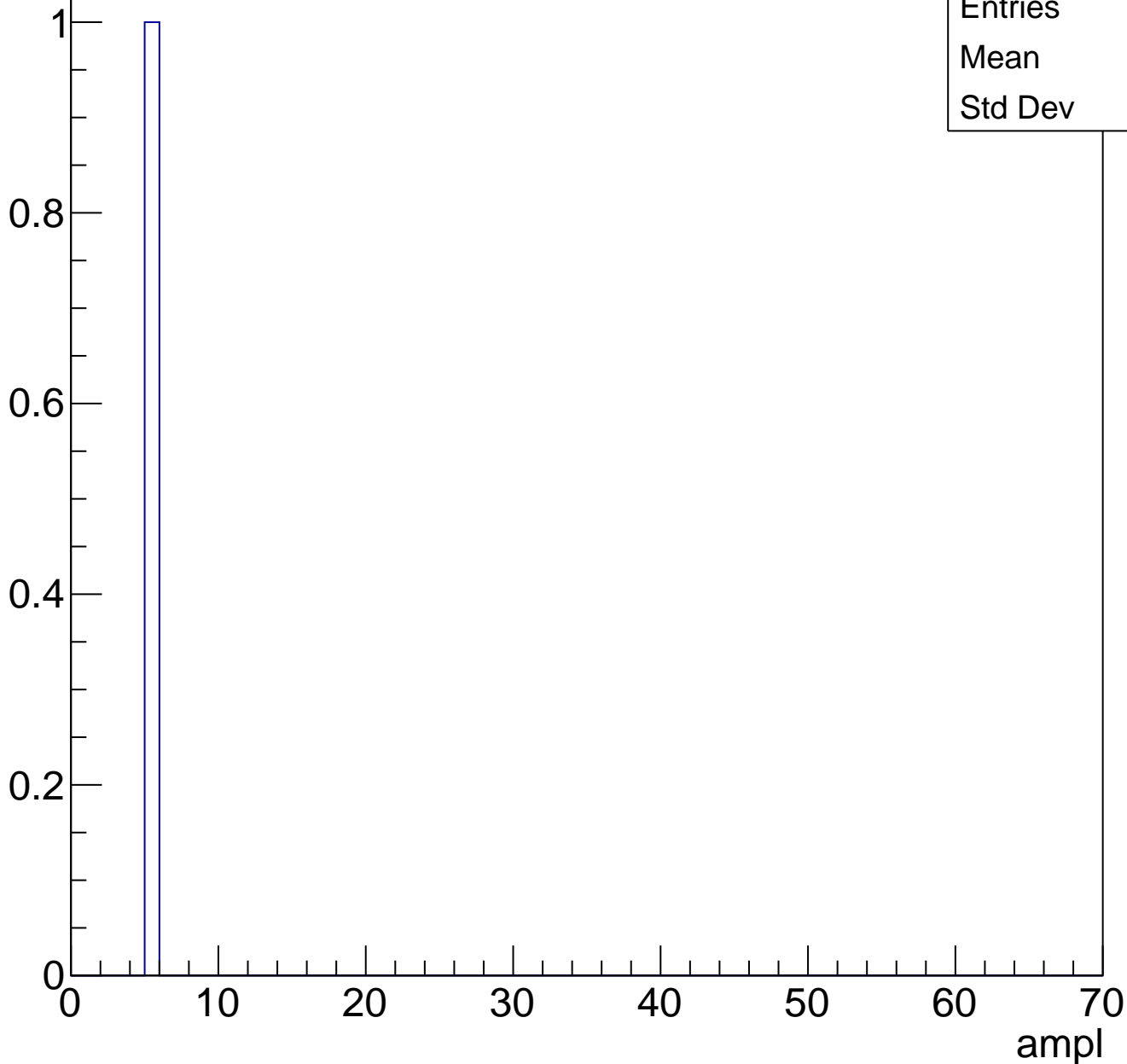
Entries	17
Mean	57.94
Std Dev	14.57



B0L101S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch63, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	27.79
Std Dev	5.941

Gaus mean : 29.0443

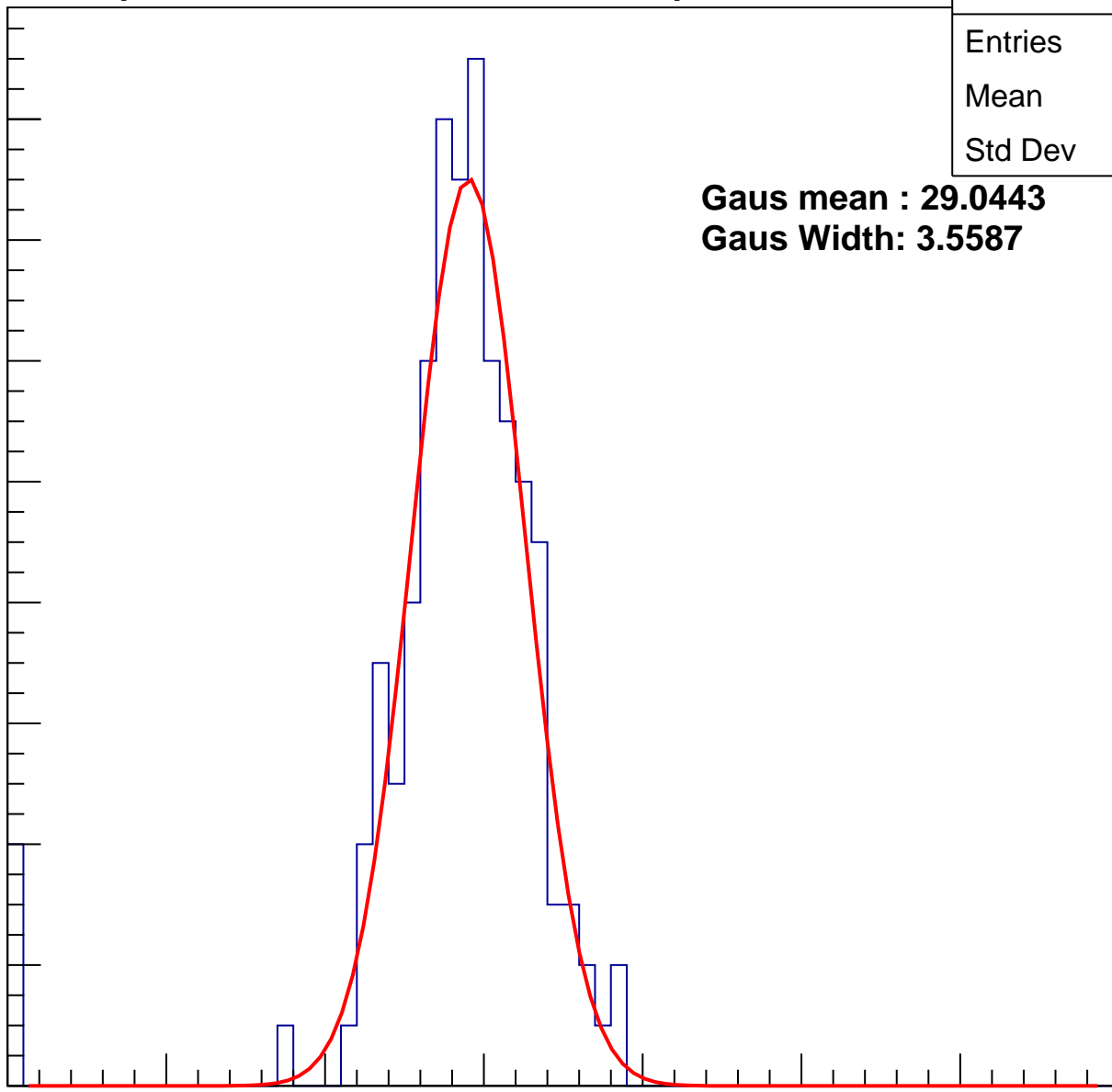
Gaus Width: 3.5587

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



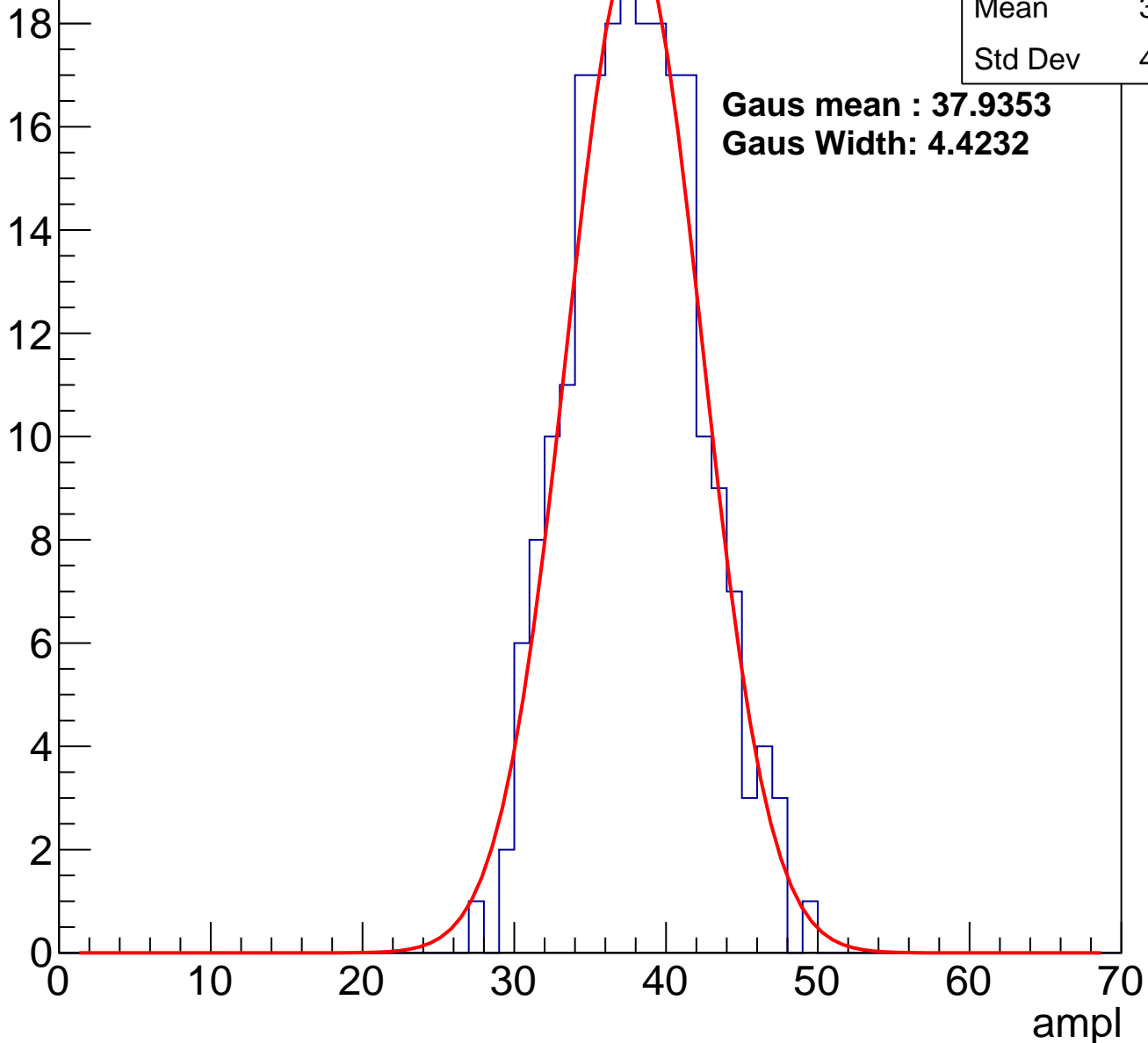
B0L101S, U14-ch63, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	216
Mean	37.53
Std Dev	4.198

Gaus mean : 37.9353
Gaus Width: 4.4232

Entry



B0L101S, U14-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	74
Mean	45.16
Std Dev	2.991

Gaus mean : 45.6638

Gaus Width: 3.0058

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

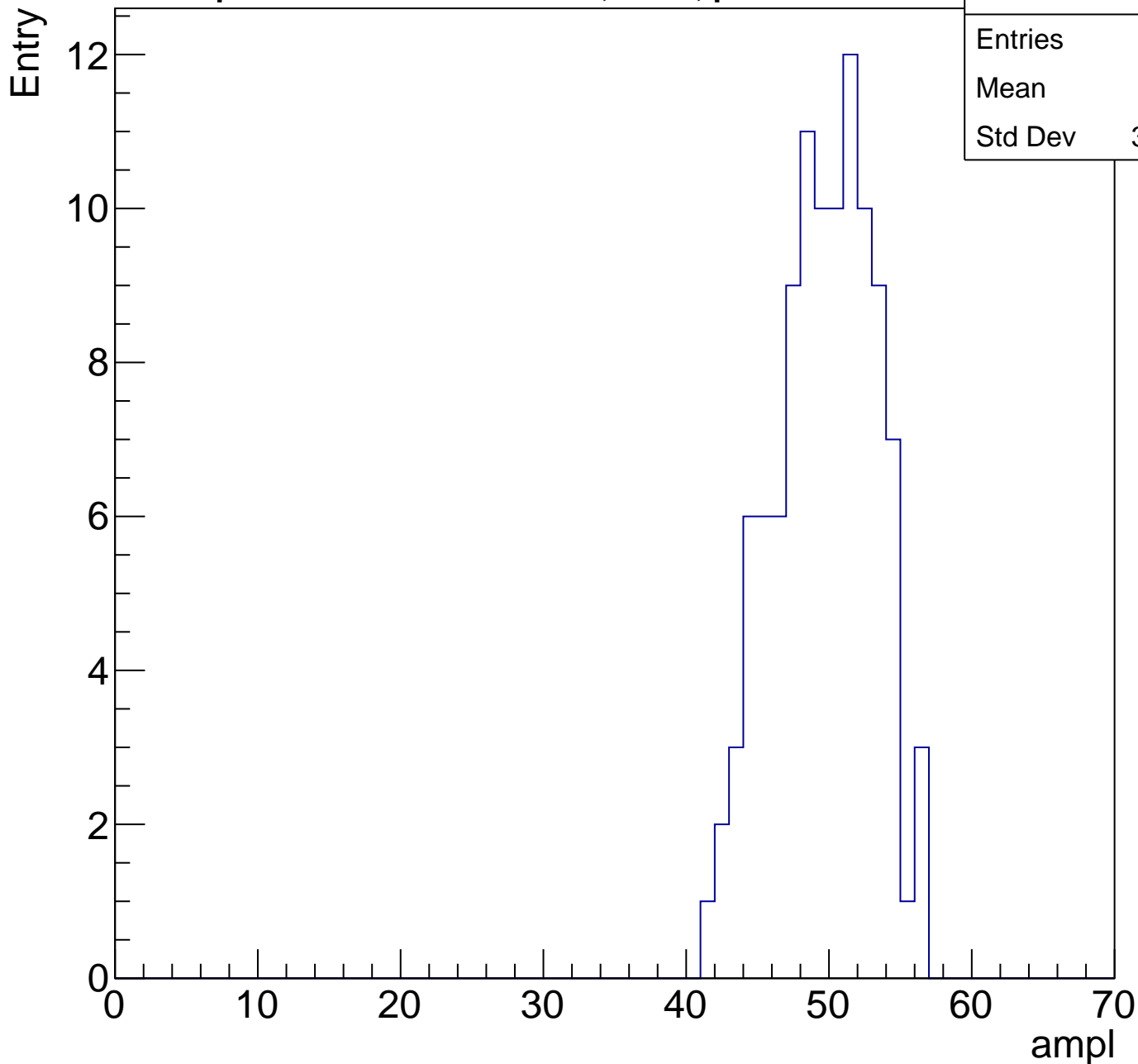
60

70

B0L101S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	49.2
Std Dev	3.452



B0L101S, U14-ch63, adc4

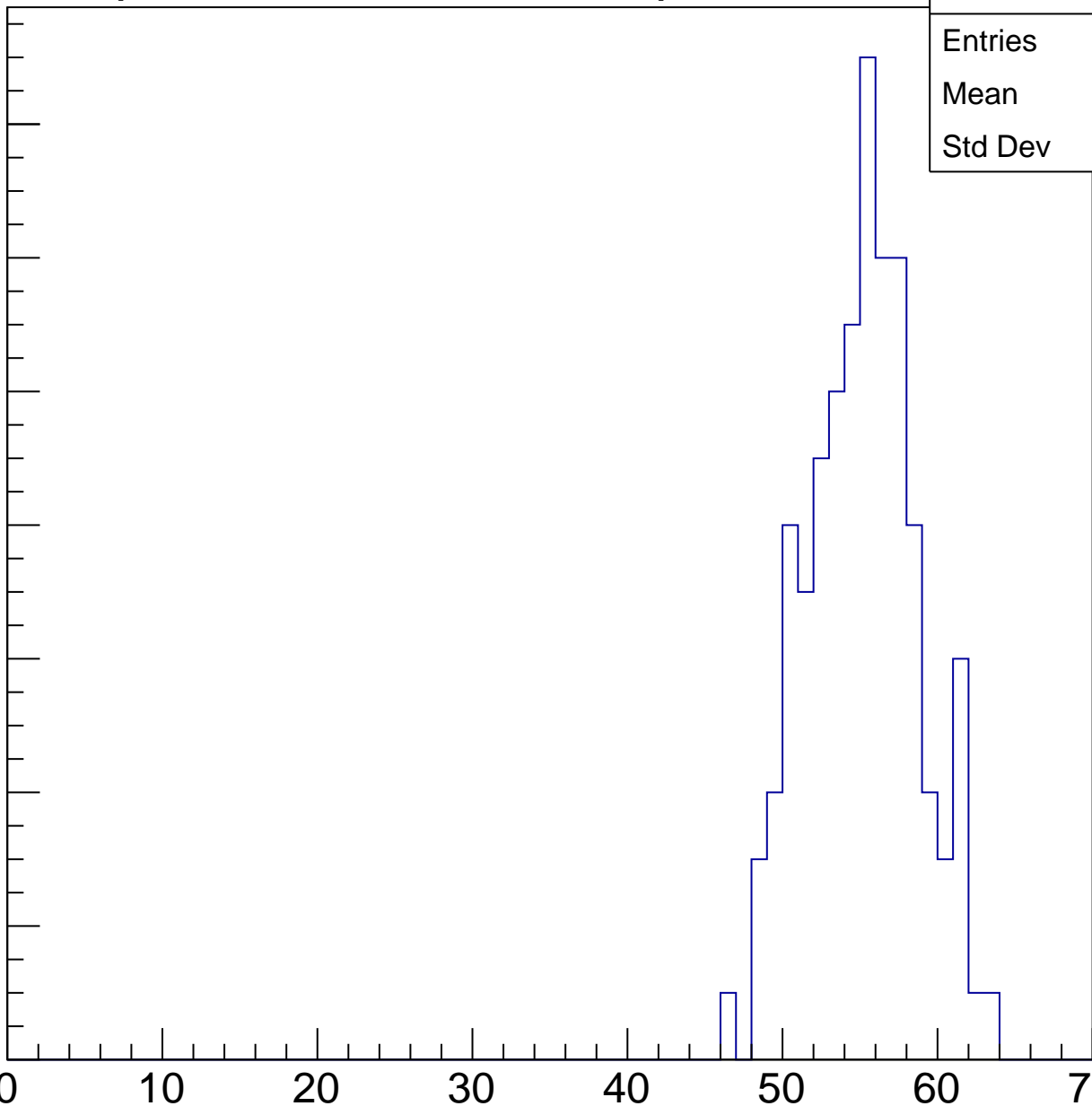
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	54.67
Std Dev	3.493

ampl



B0L101S, U14-ch63, adc5

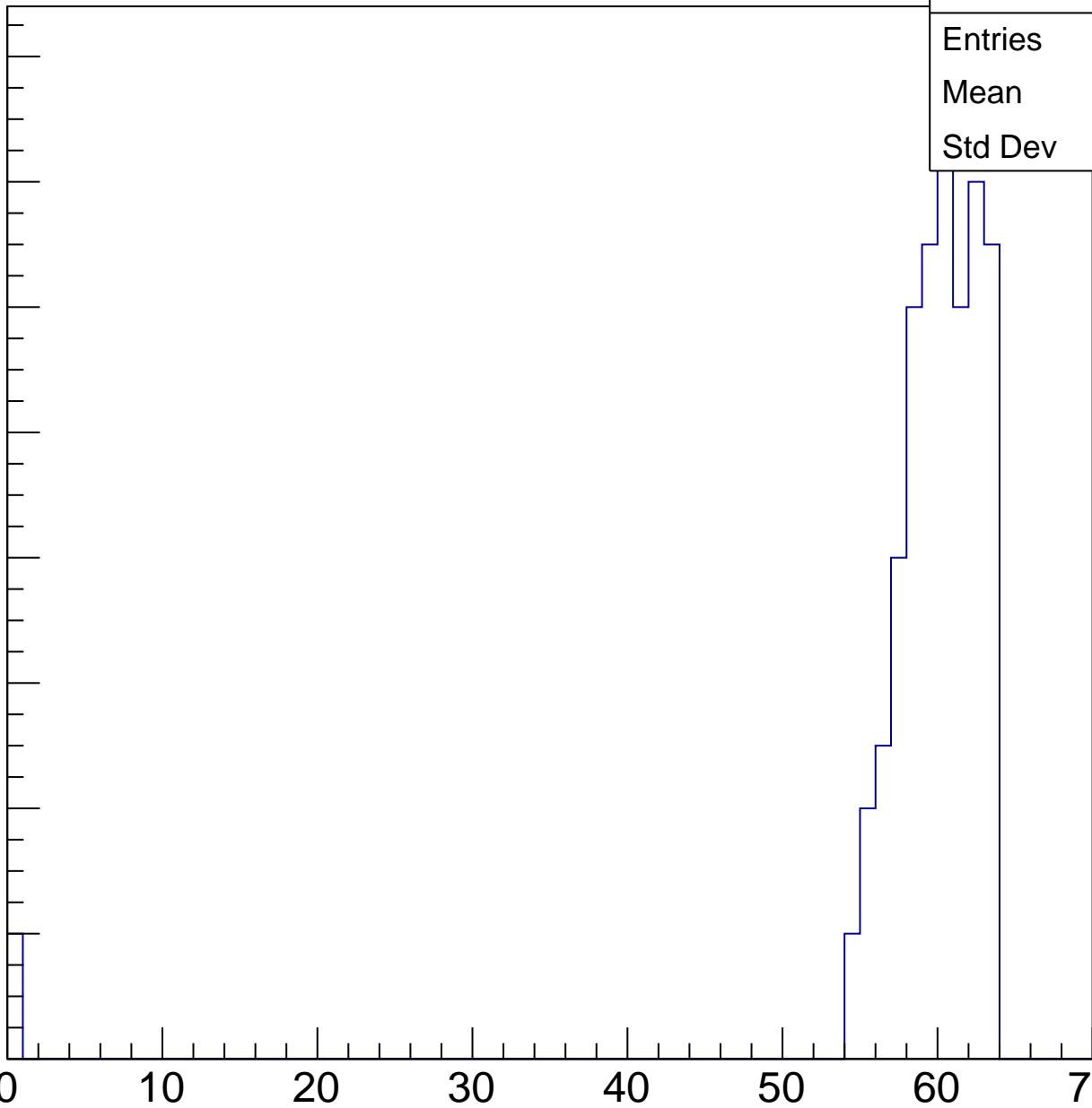
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.48
Std Dev	8.637

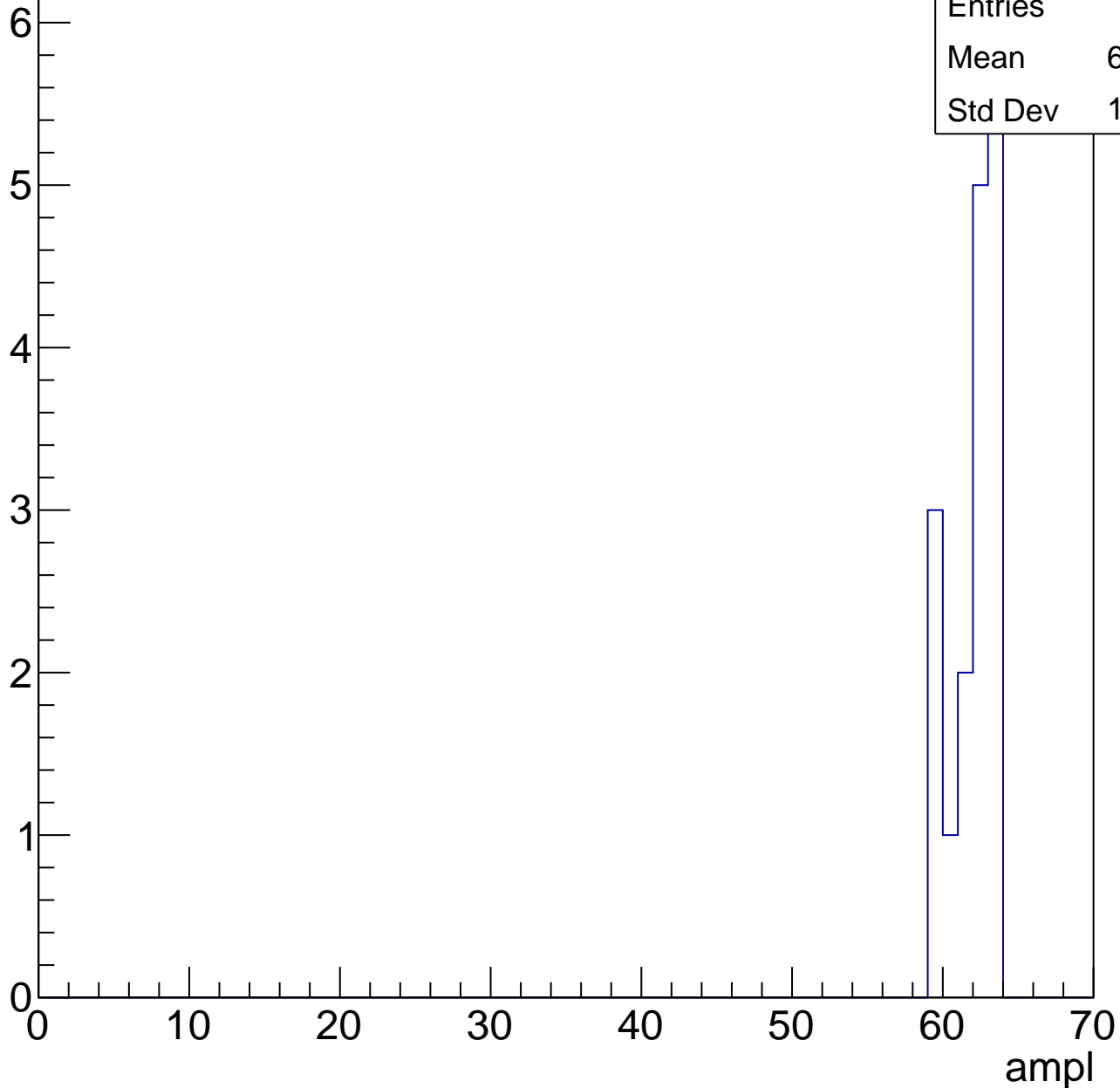
ampl



B0L101S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch64, adc0

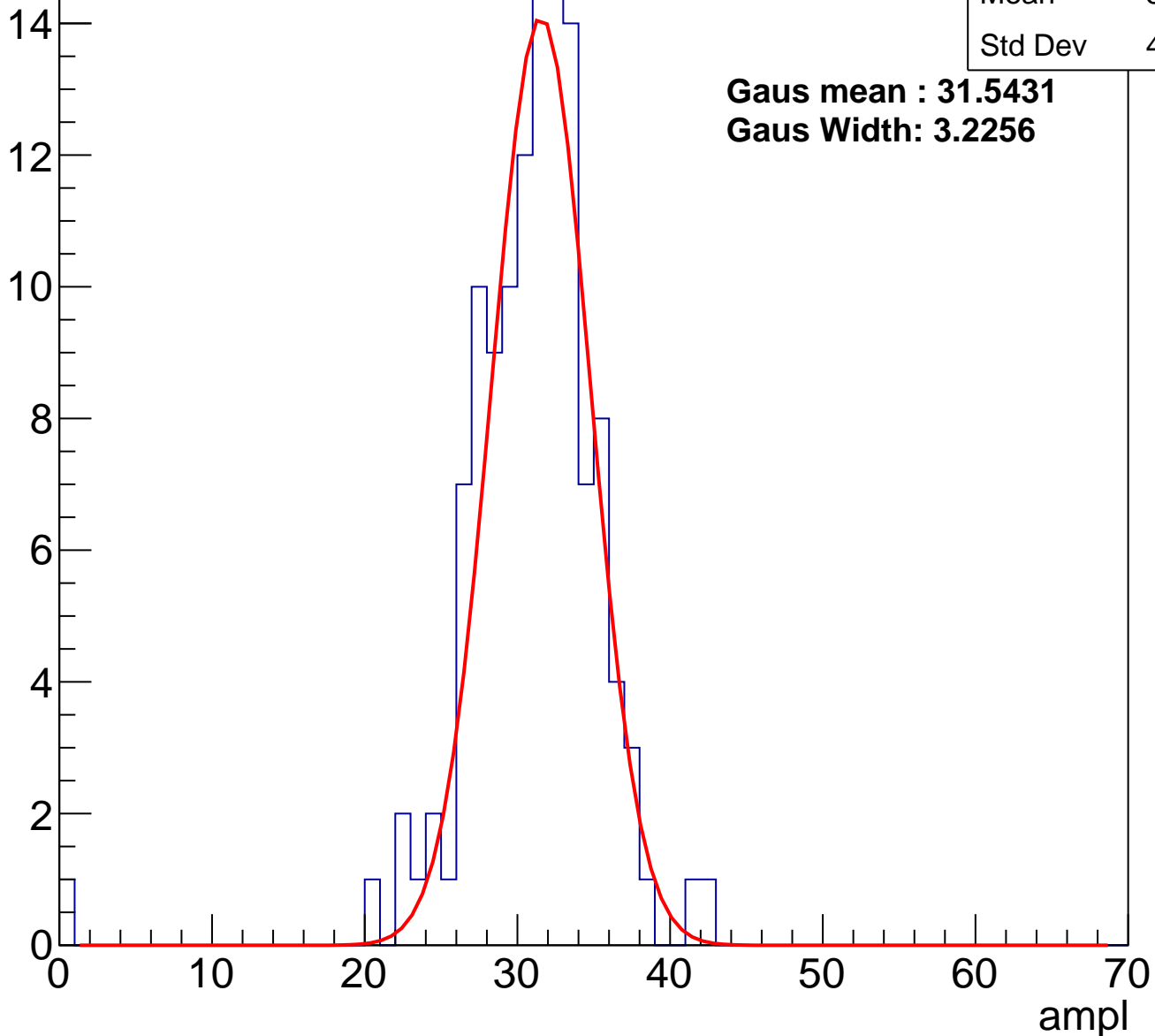
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	30.52
Std Dev	4.579

Gaus mean : 31.5431

Gaus Width: 3.2256

Entry



B0L101S, U14-ch64, adc1

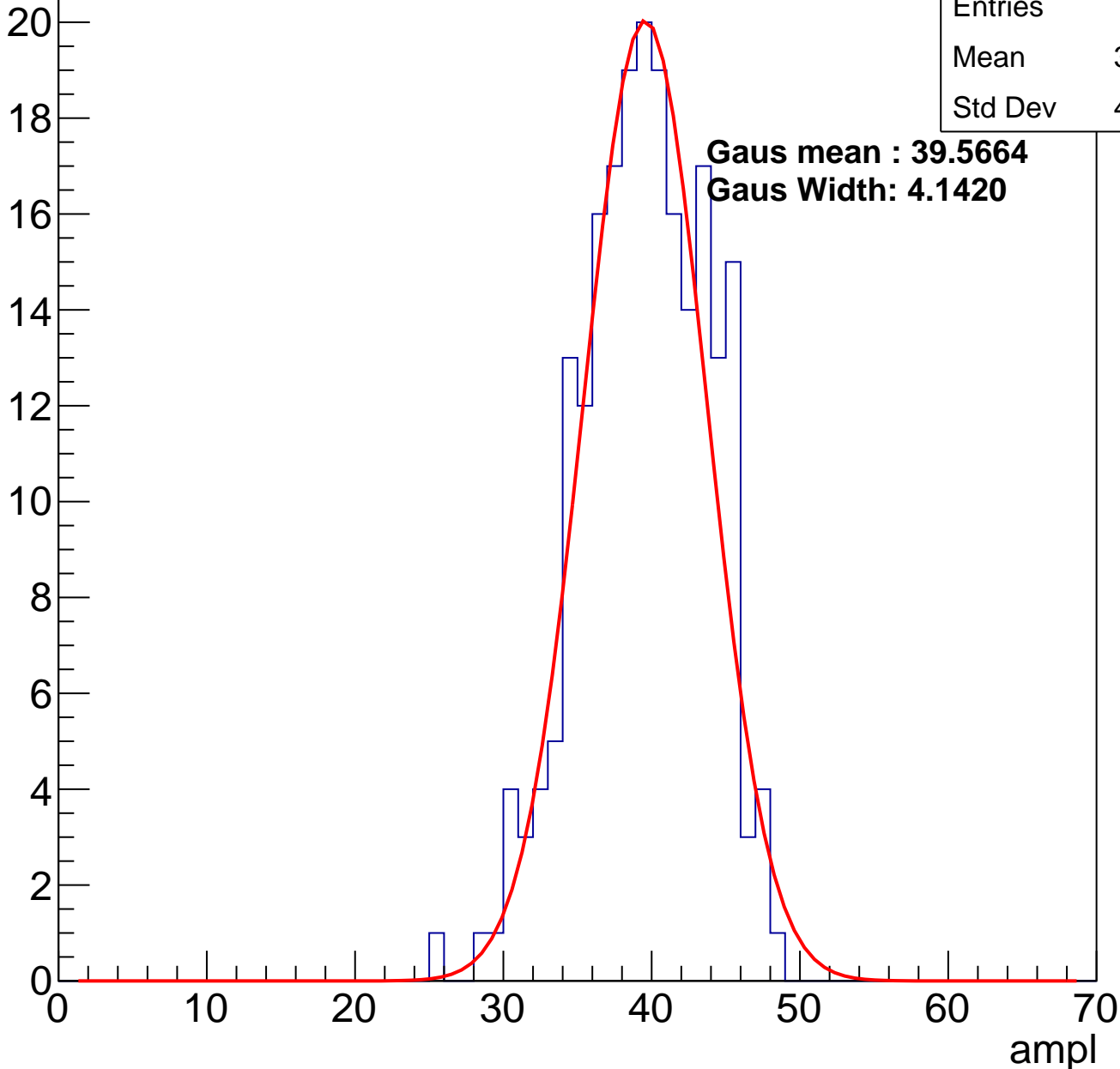
calib_packv5_042523_0143.root, FC#1, port C1

Entries	218
Mean	39.05
Std Dev	4.222

Gaus mean : 39.5664

Gaus Width: 4.1420

Entry



B0L101S, U14-ch64, adc2

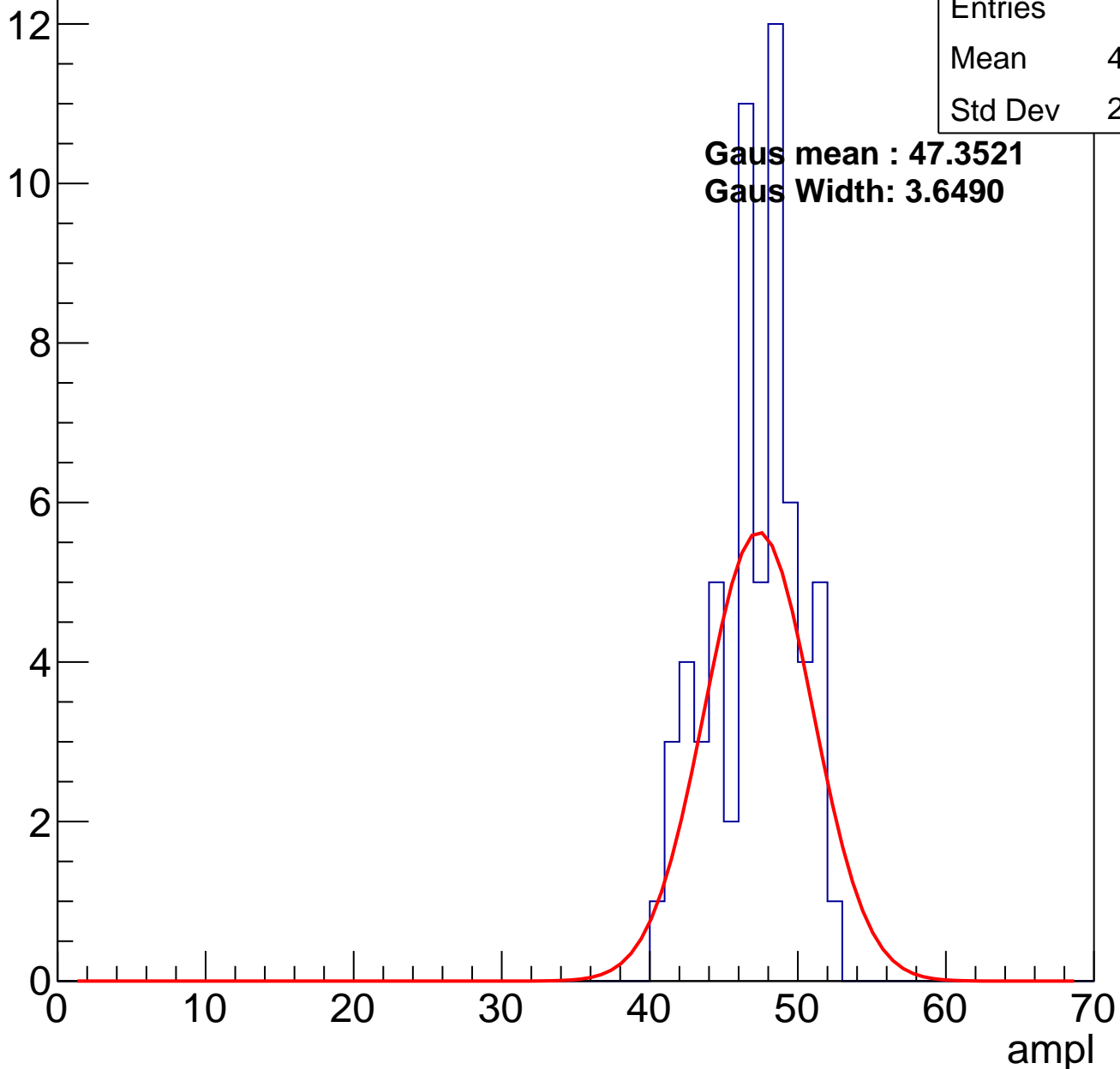
calib_packv5_042523_0143.root, FC#1, port C1

Entries	62
Mean	46.58
Std Dev	2.938

Gaus mean : 47.3521

Gaus Width: 3.6490

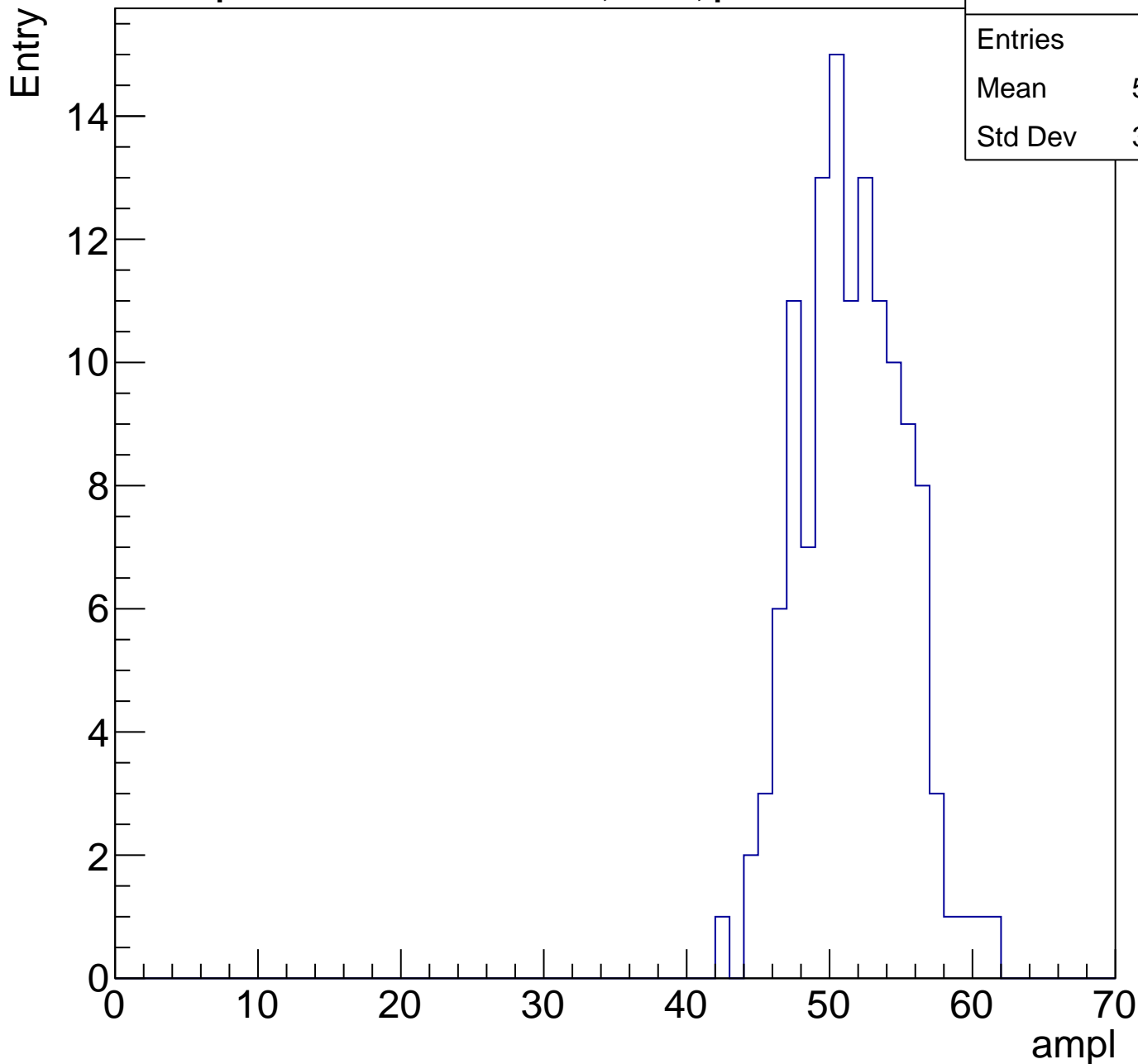
Entry



B0L101S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#1, port C1

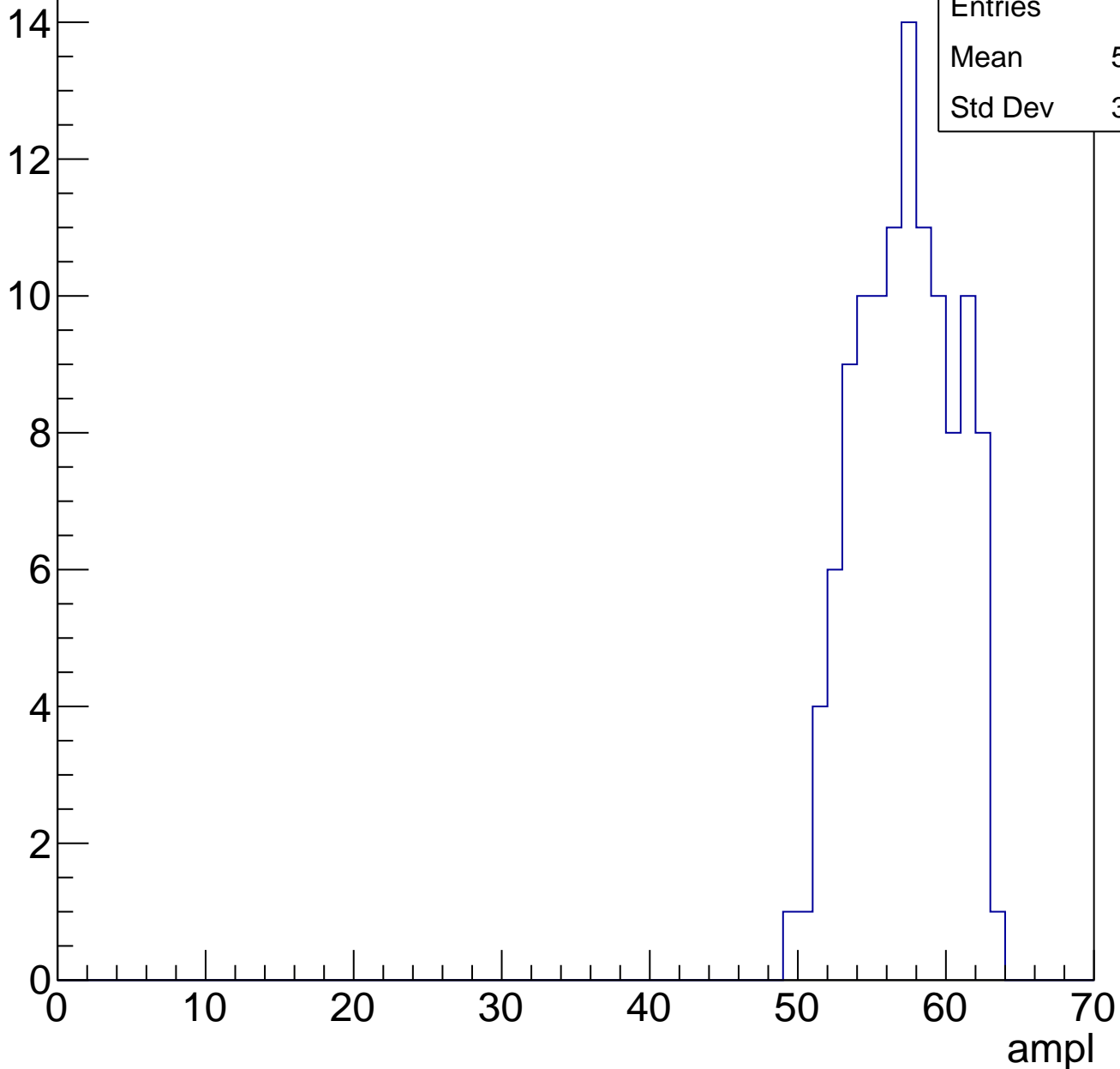
Entries	127
Mean	51.13
Std Dev	3.587



B0L101S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch64, adc5

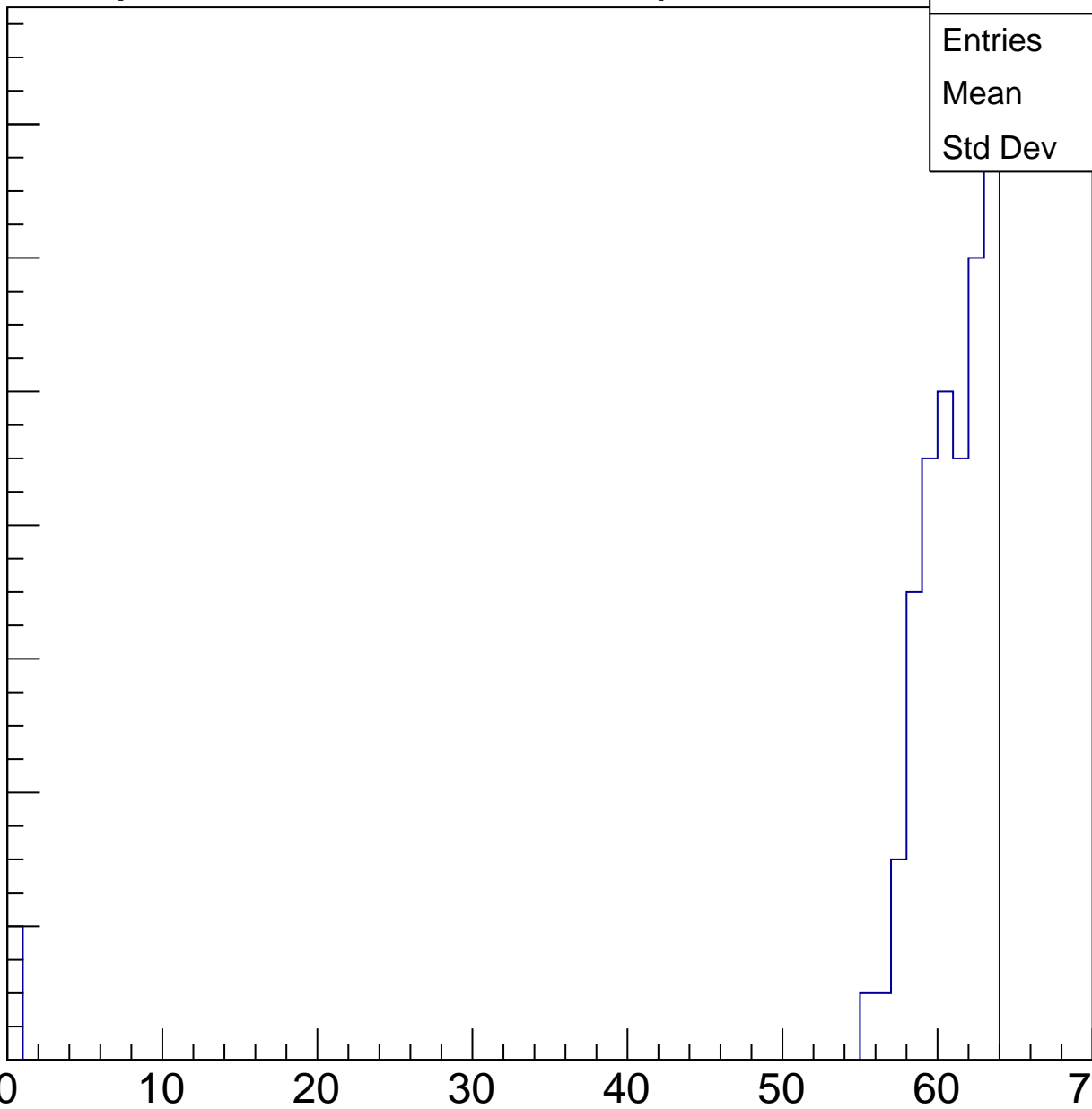
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	58.8
Std Dev	10.35

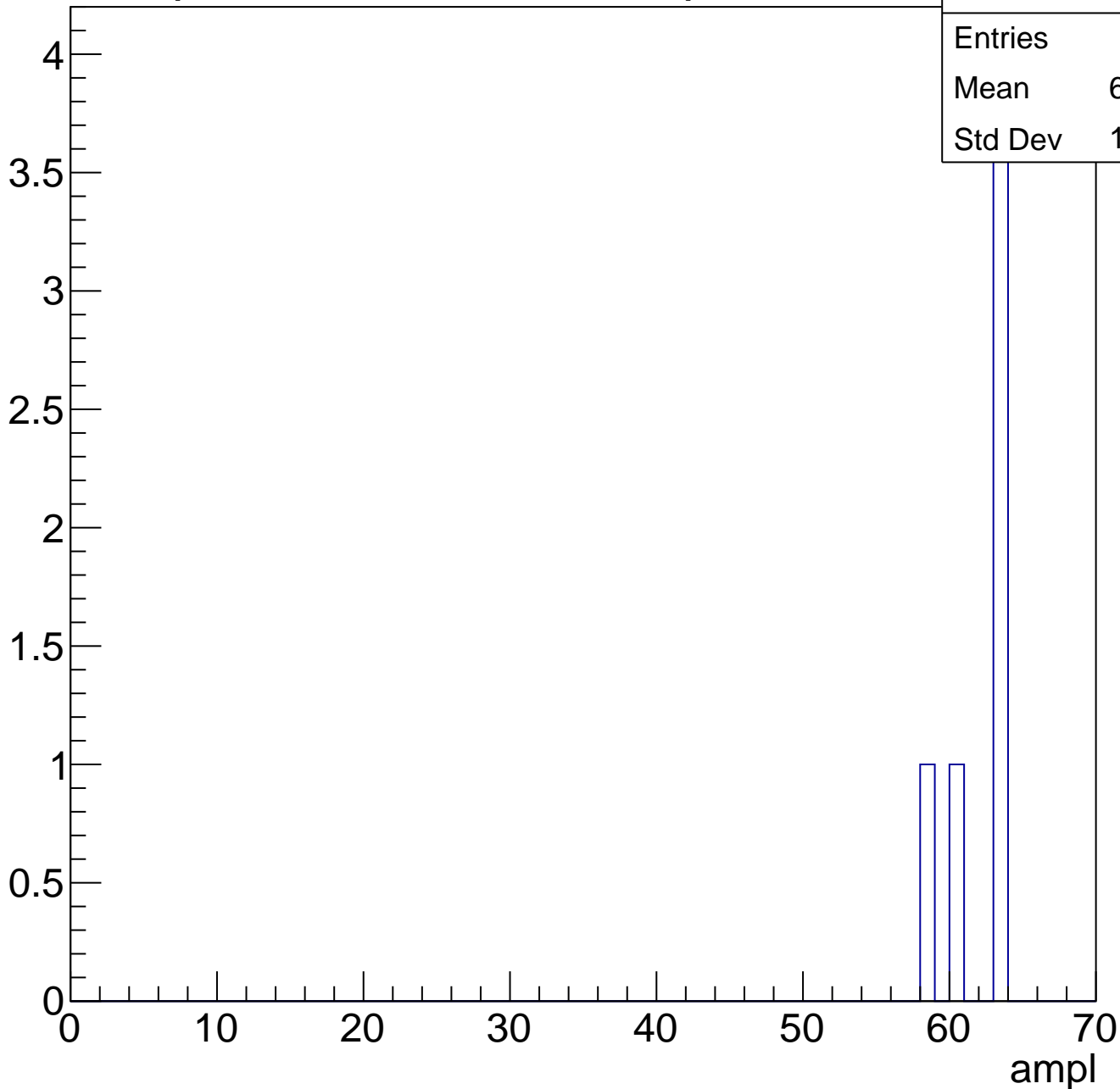
ampl



B0L101S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U14-ch65, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	31.71
Std Dev	3.296

Gaus mean : 32.1284

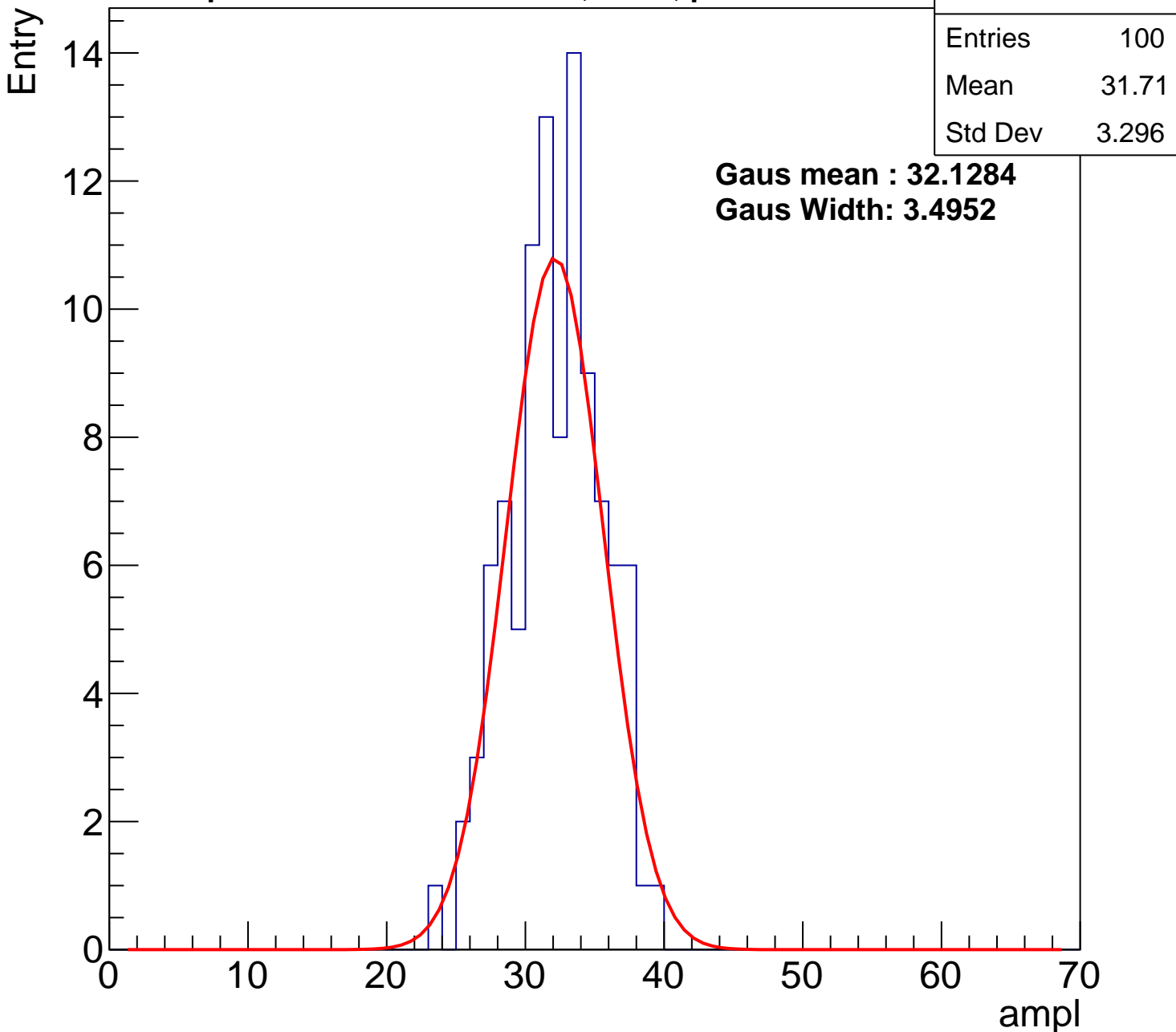
Gaus Width: 3.4952

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

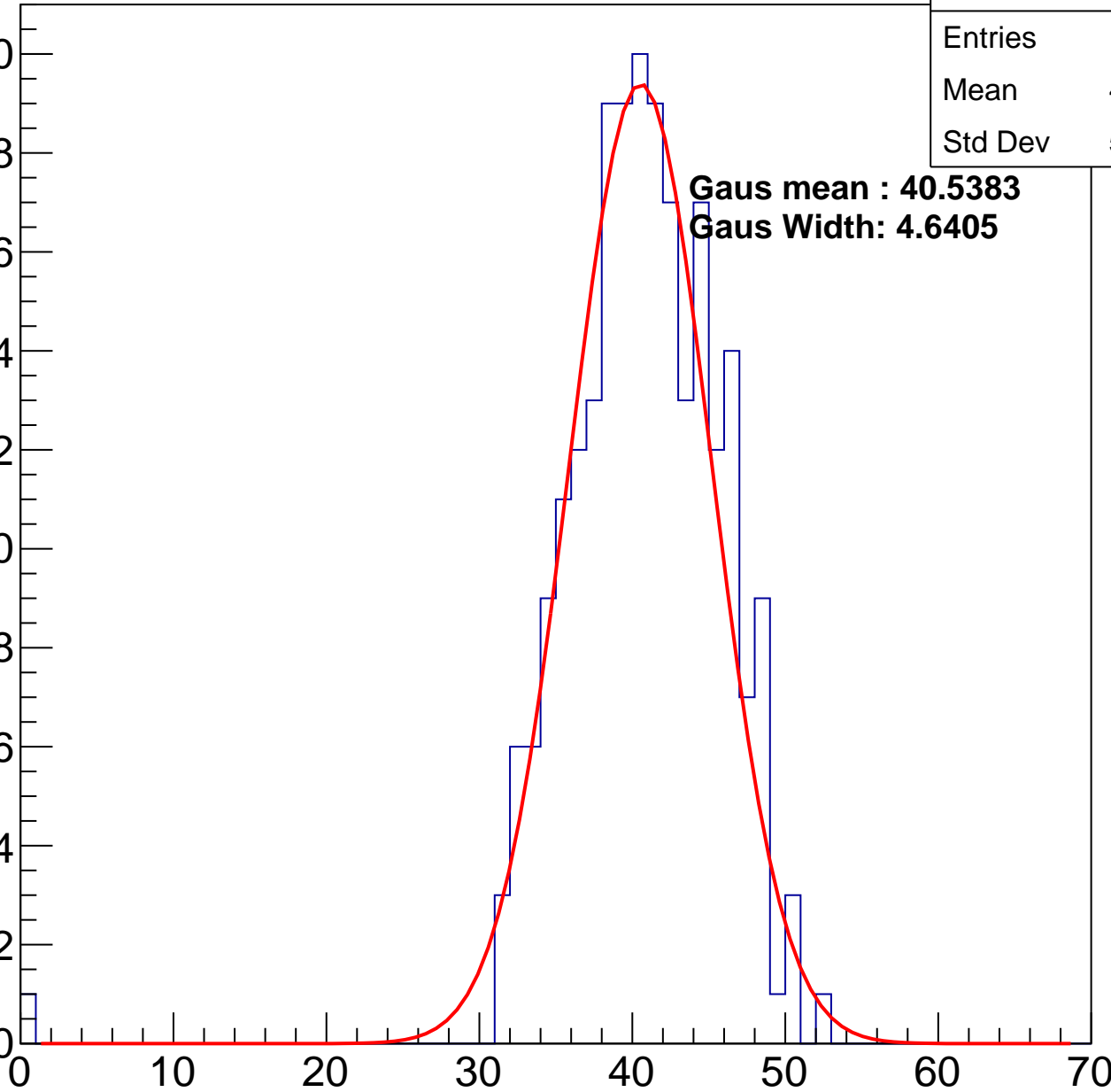
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	232
Mean	40.28
Std Dev	5.182

Gaus mean : 40.5383
Gaus Width: 4.6405

ampl



B0L101S, U14-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	74
Mean	47.65
Std Dev	3.648

Gaus mean : 48.5209
Gaus Width: 3.9776

Entry

10

8

6

4

2

0

0

10

20

30

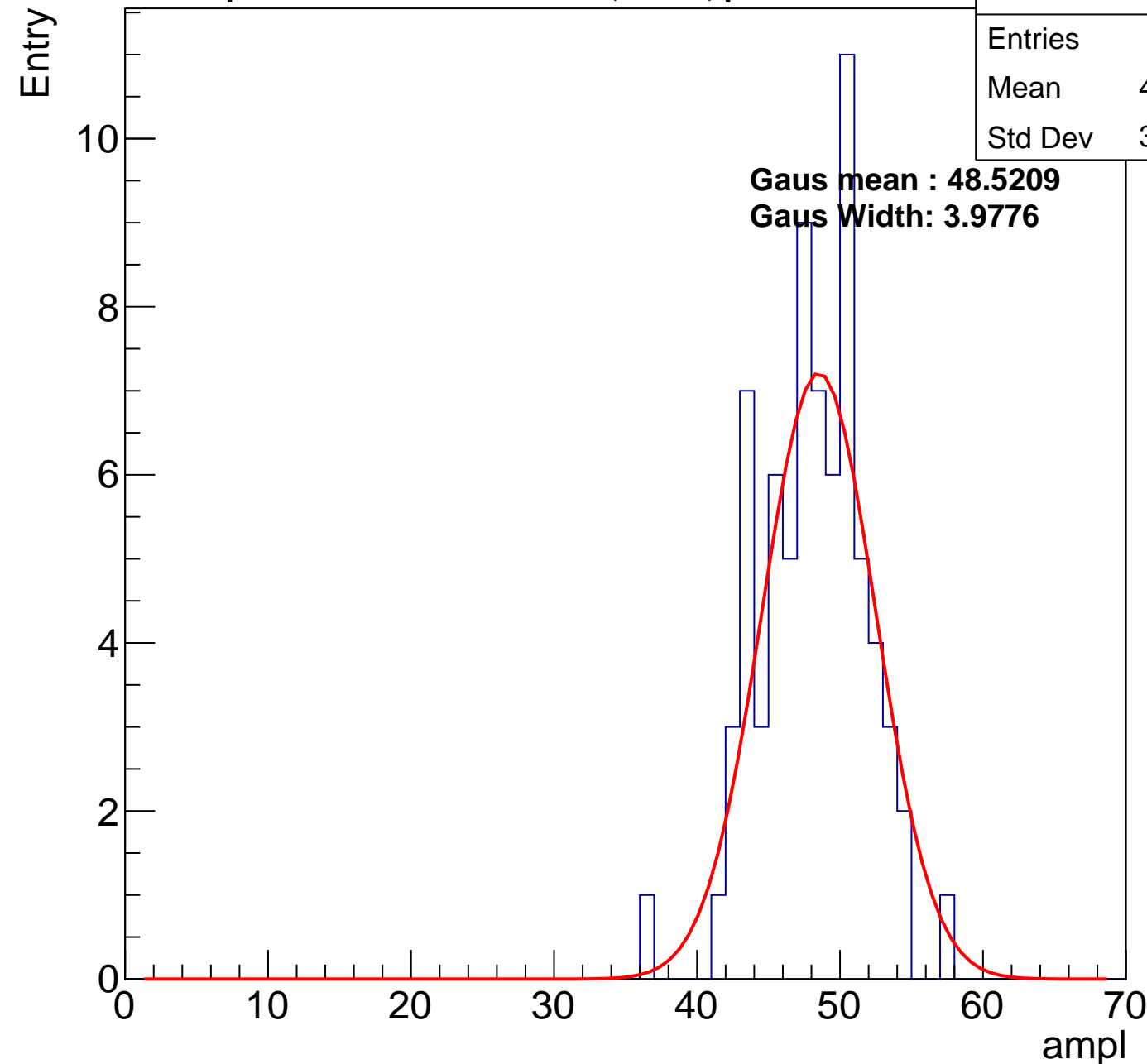
40

50

60

ampl

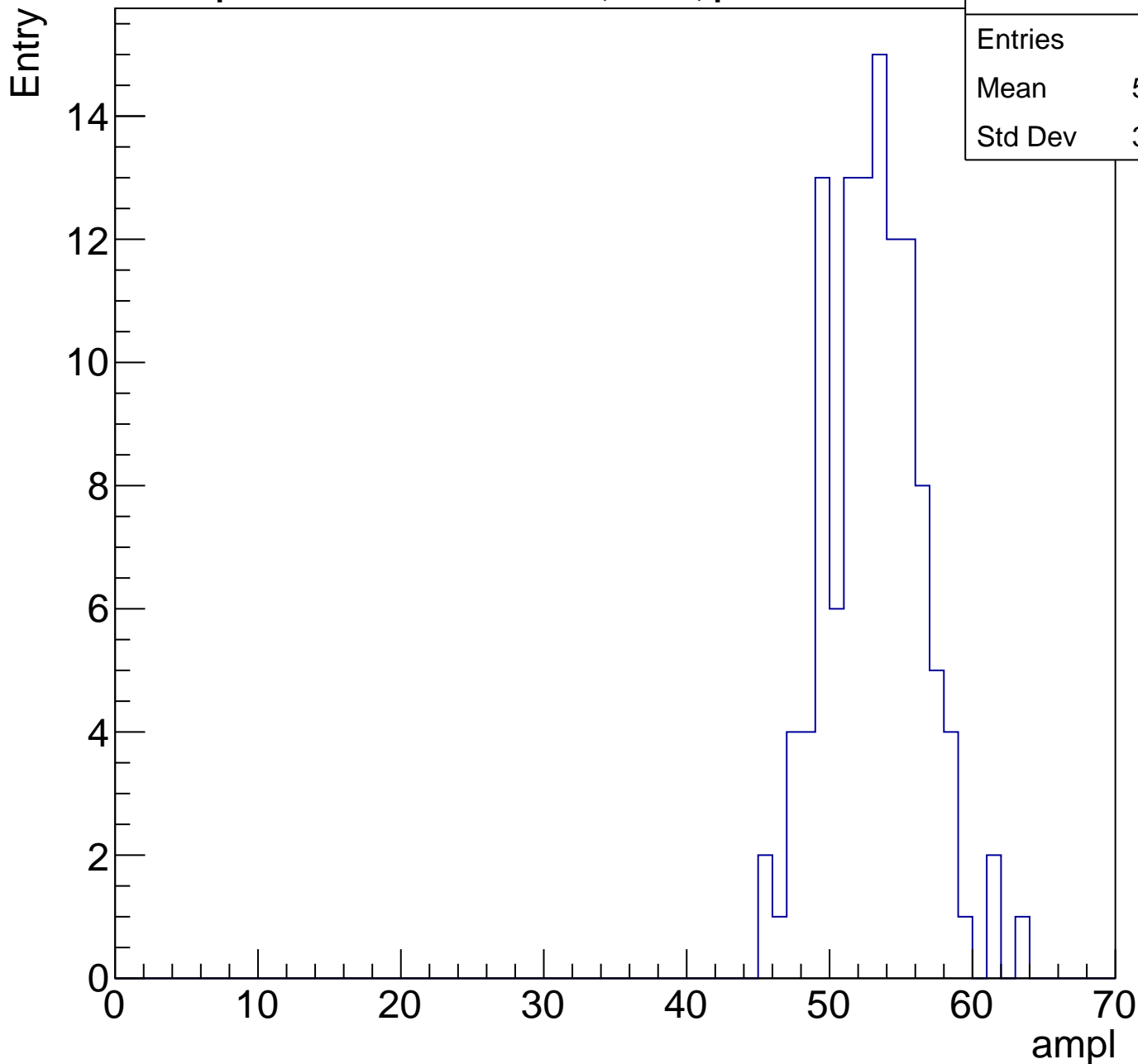
70



B0L101S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	52.62
Std Dev	3.357



B0L101S, U14-ch65, adc4

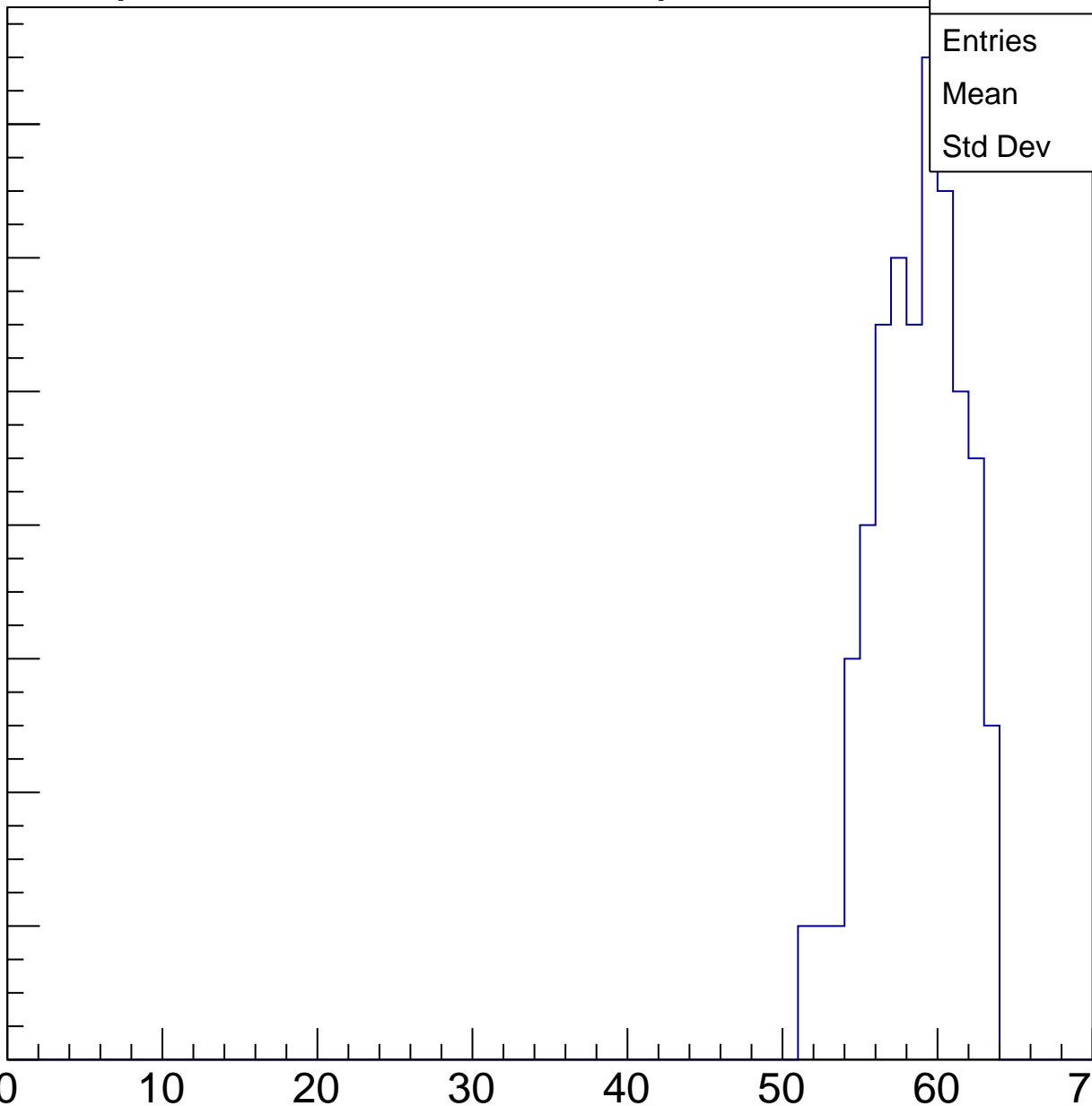
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	58.13
Std Dev	2.862

ampl



B0L101S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	49
Mean	59.55
Std Dev	8.804

0

2

4

6

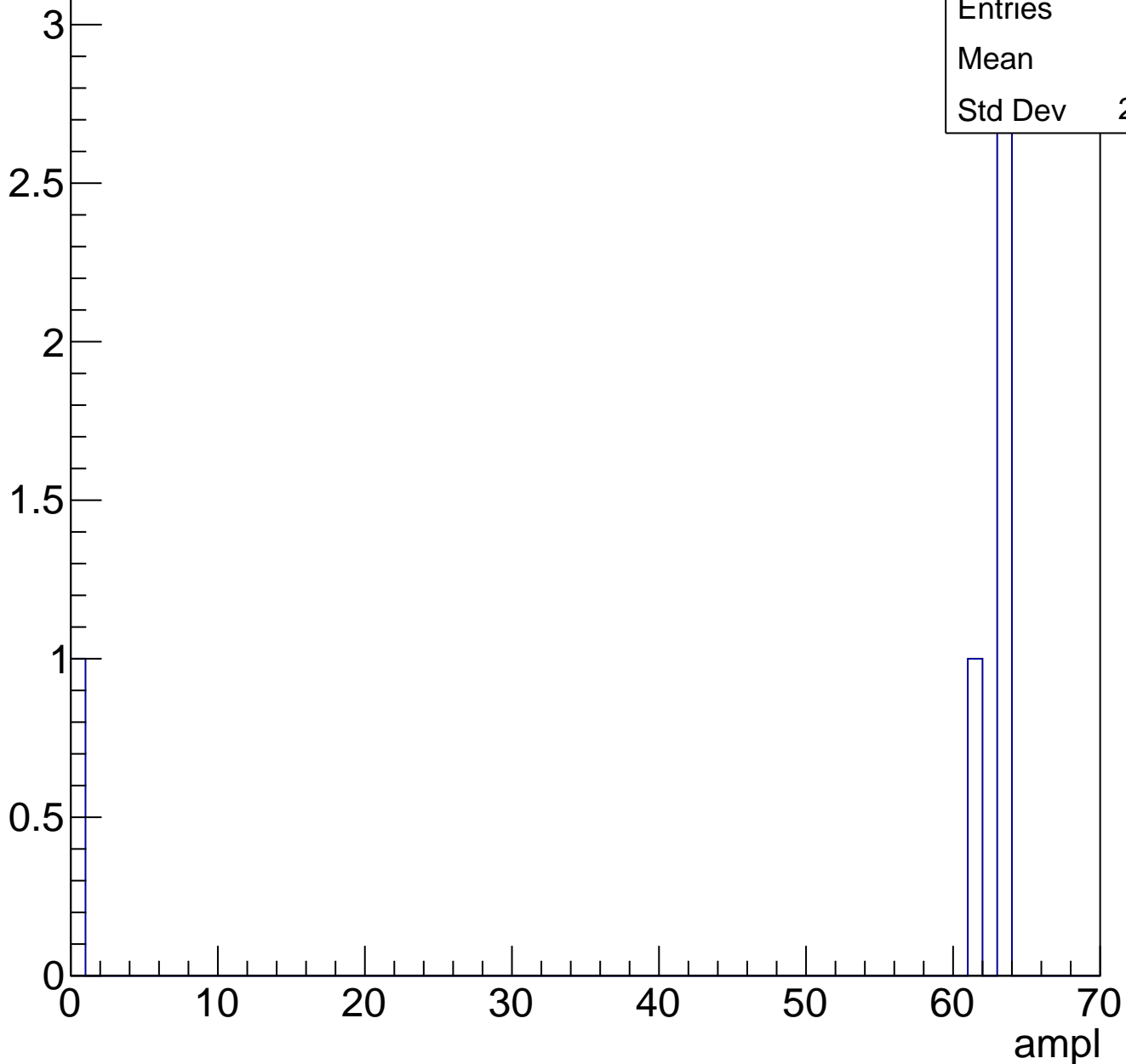
8

10

B0L101S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch66, adc0

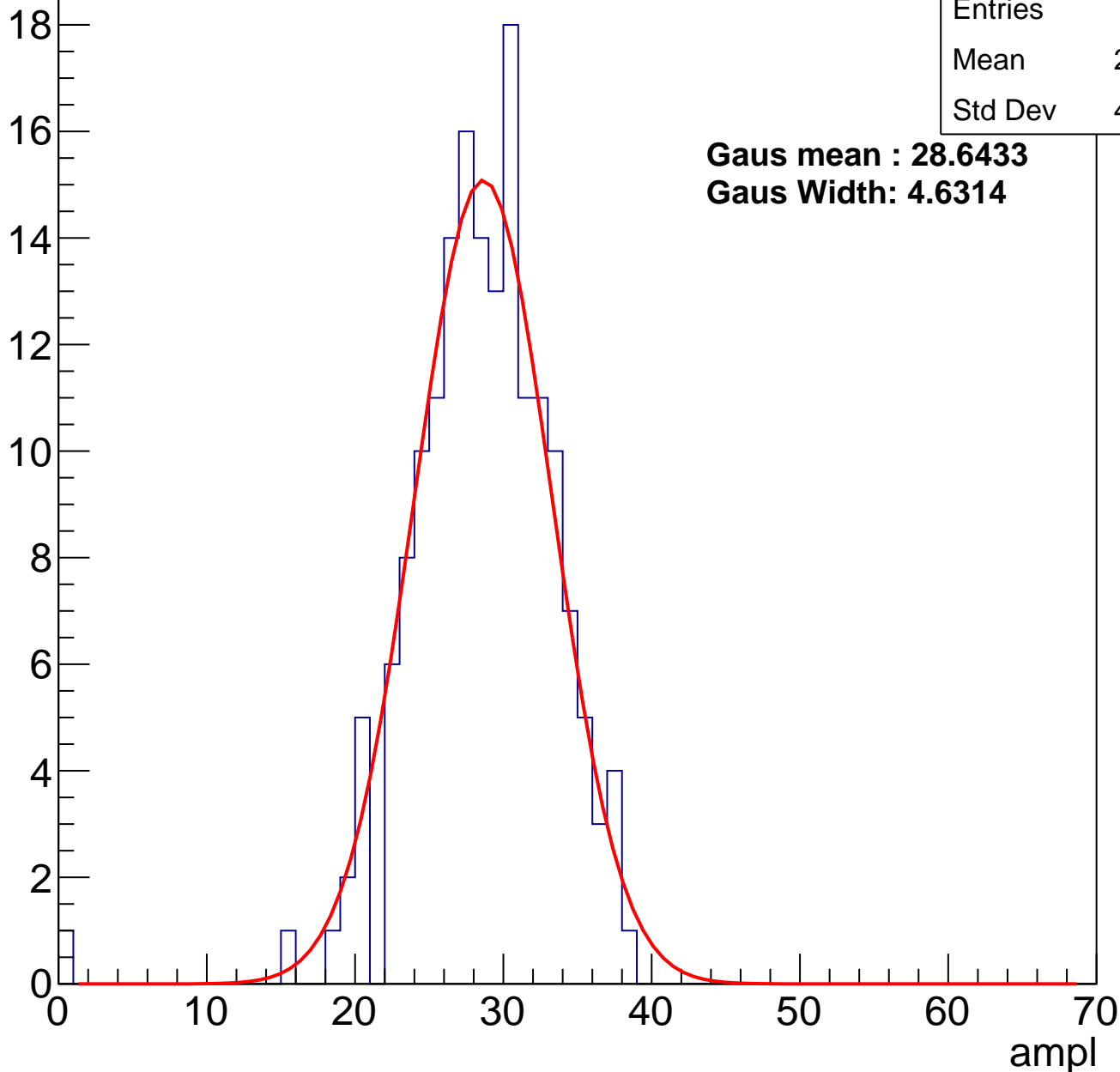
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	28.12
Std Dev	4.824

Gaus mean : 28.6433

Gaus Width: 4.6314

Entry



B0L101S, U14-ch66, adc1

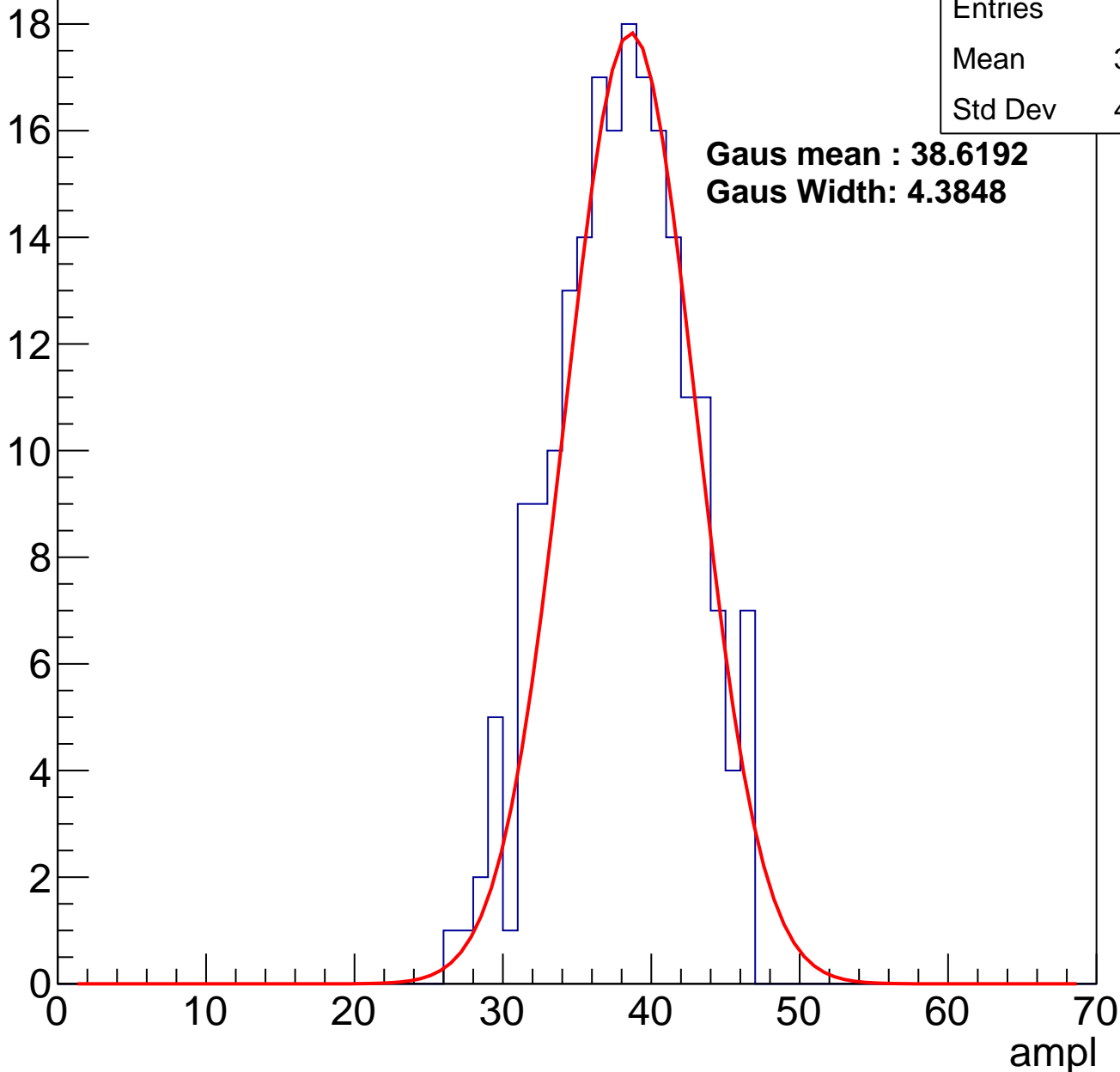
calib_packv5_042523_0143.root, FC#1, port C1

Entries	203
Mean	37.55
Std Dev	4.372

Gaus mean : 38.6192

Gaus Width: 4.3848

Entry



B0L101S, U14-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

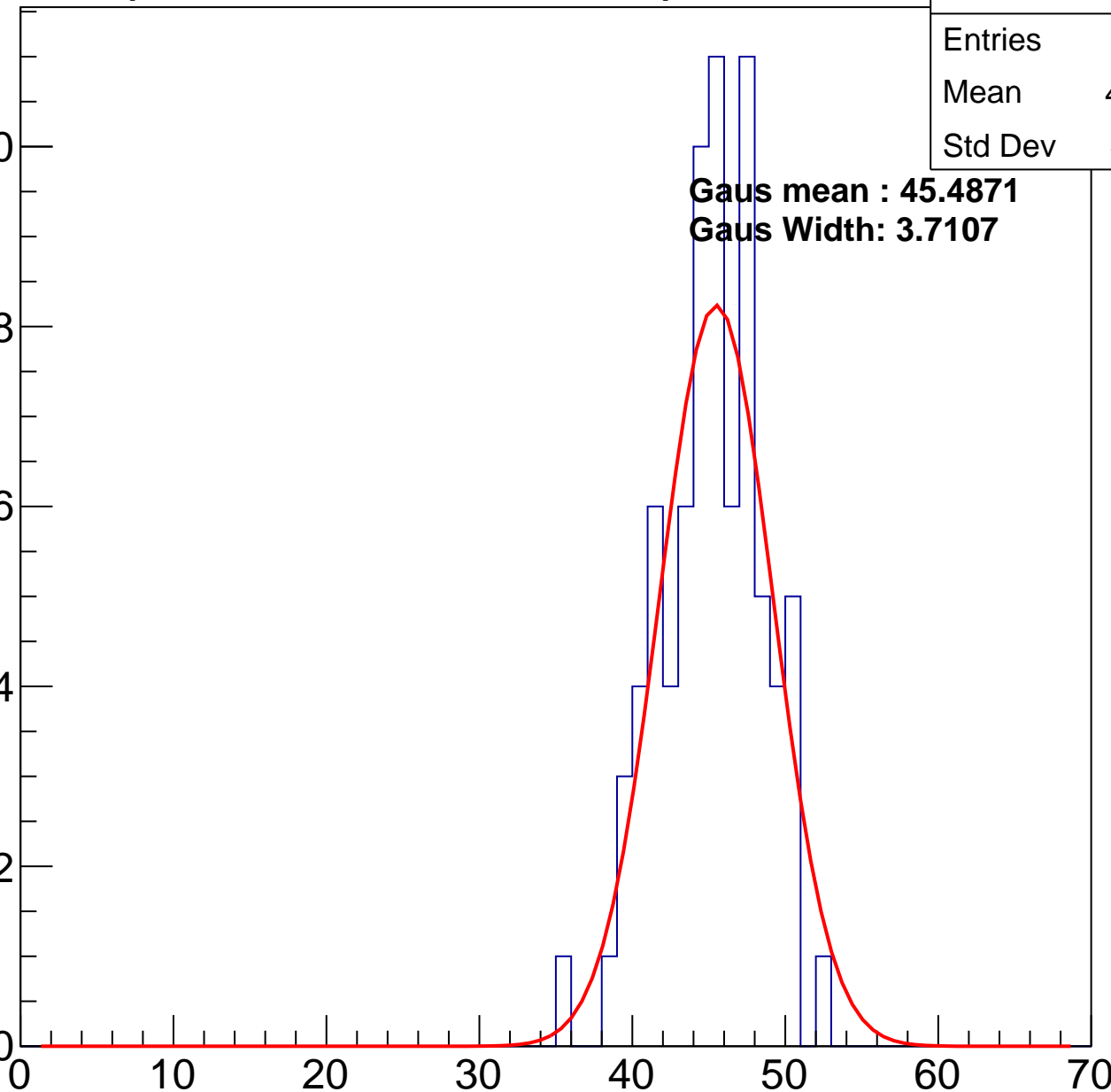
Entries	78
Mean	44.72
Std Dev	3.301

Gaus mean : 45.4871

Gaus Width: 3.7107

10
8
6
4
2
0

ampl

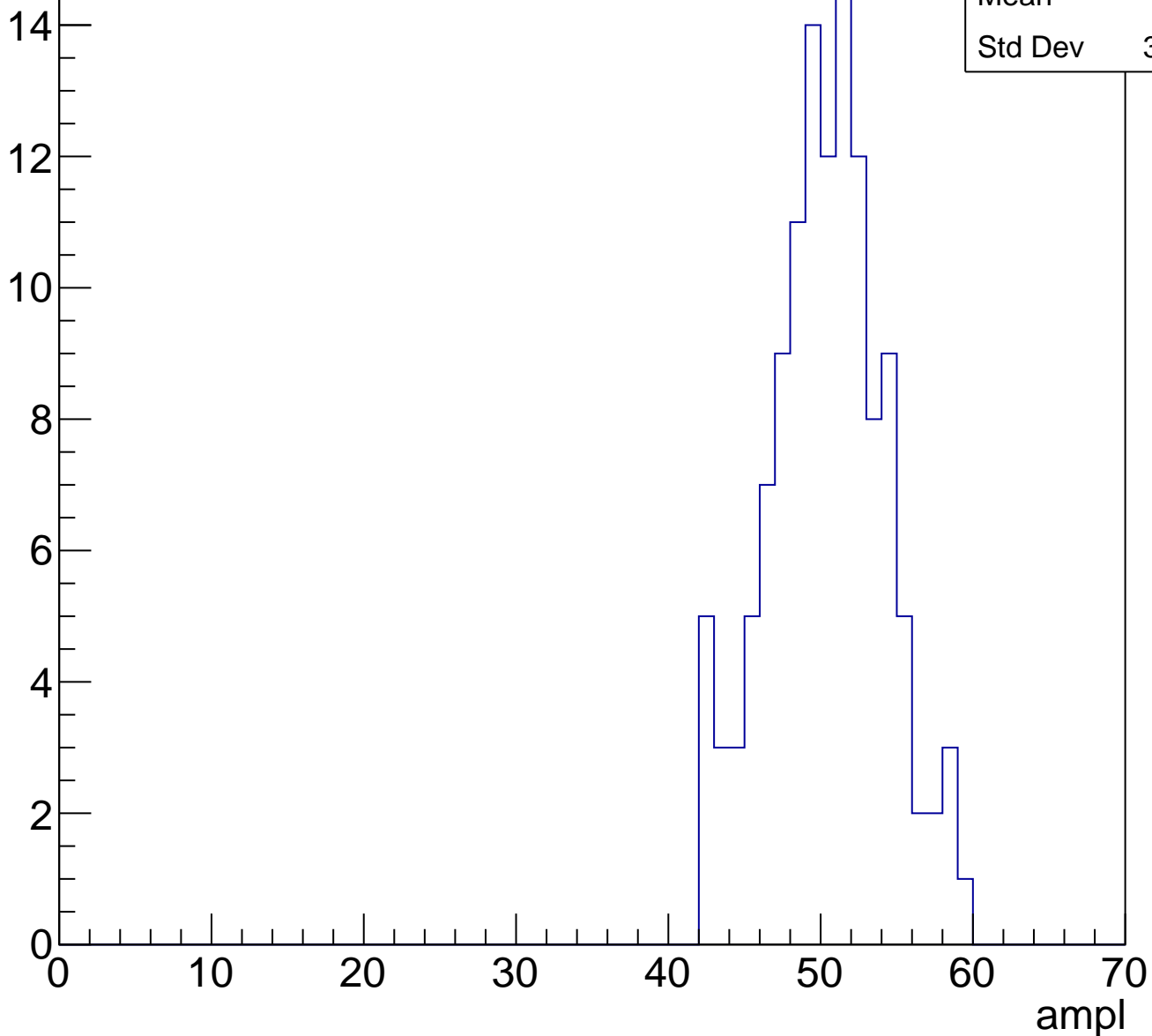


B0L101S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	49.9
Std Dev	3.776

Entry



B0L101S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

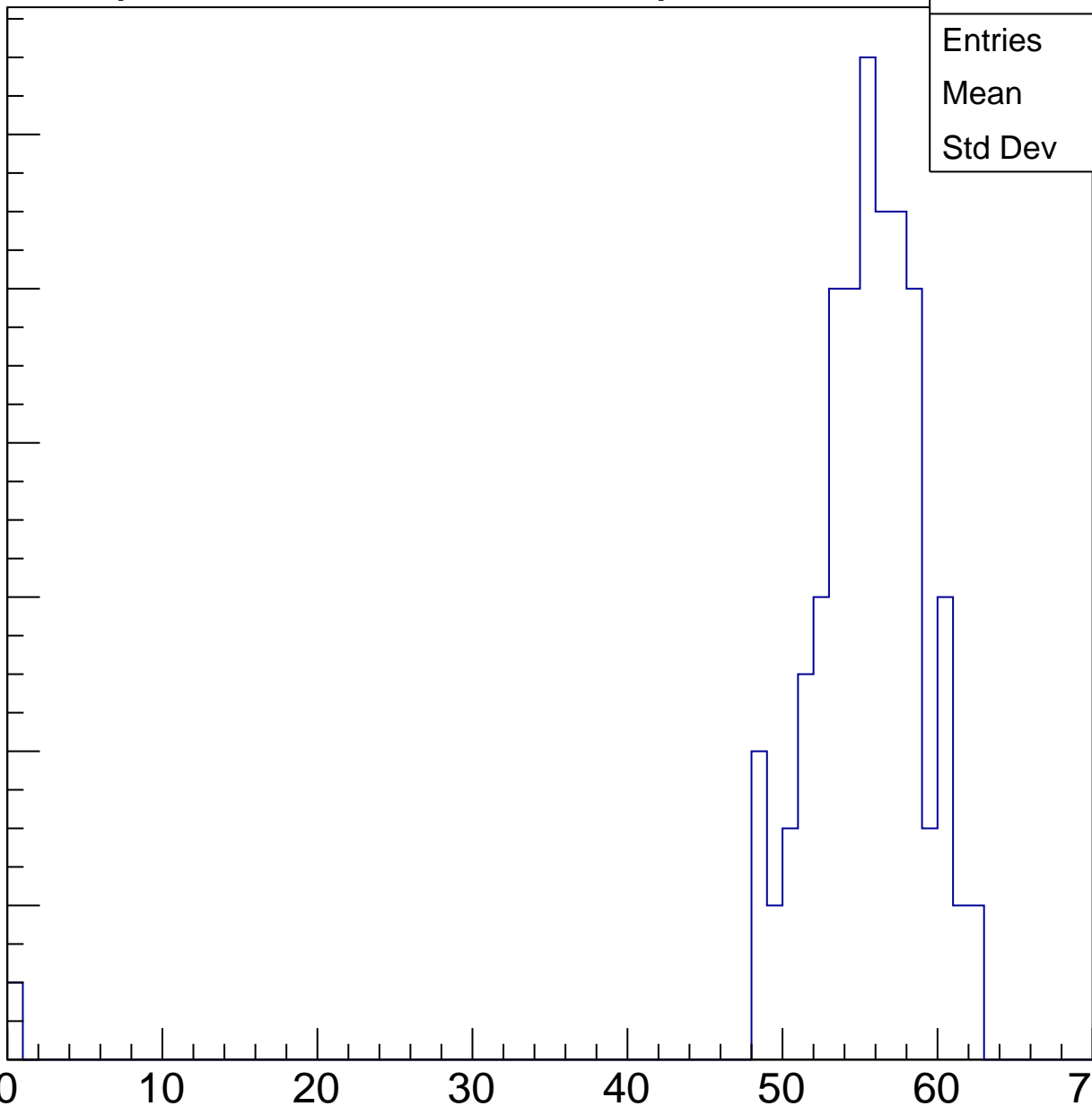
0

Entries 99

Mean 54.53

Std Dev 6.39

ampl



B0L101S, U14-ch66, adc5

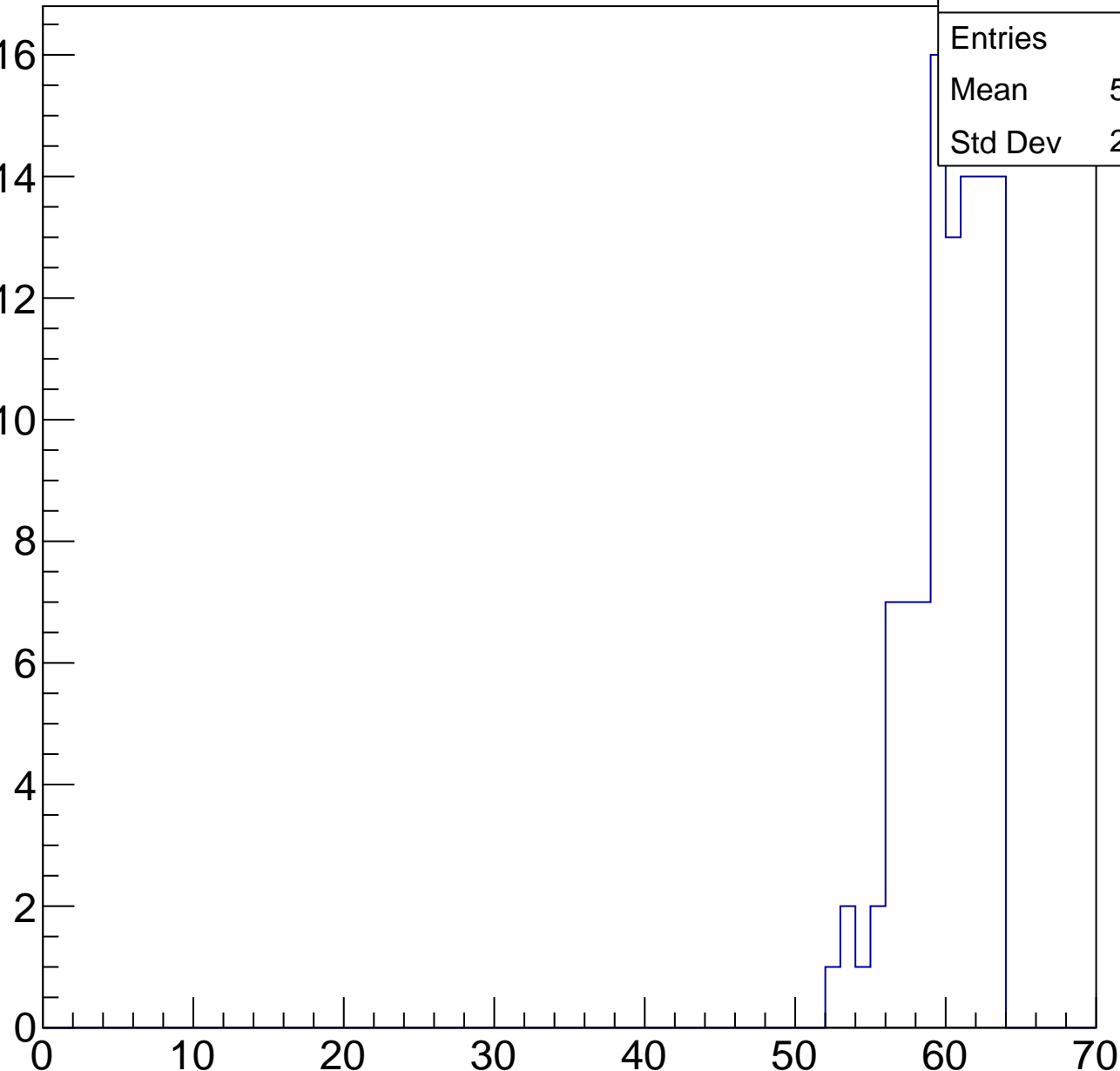
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.66
Std Dev	2.579

ampl

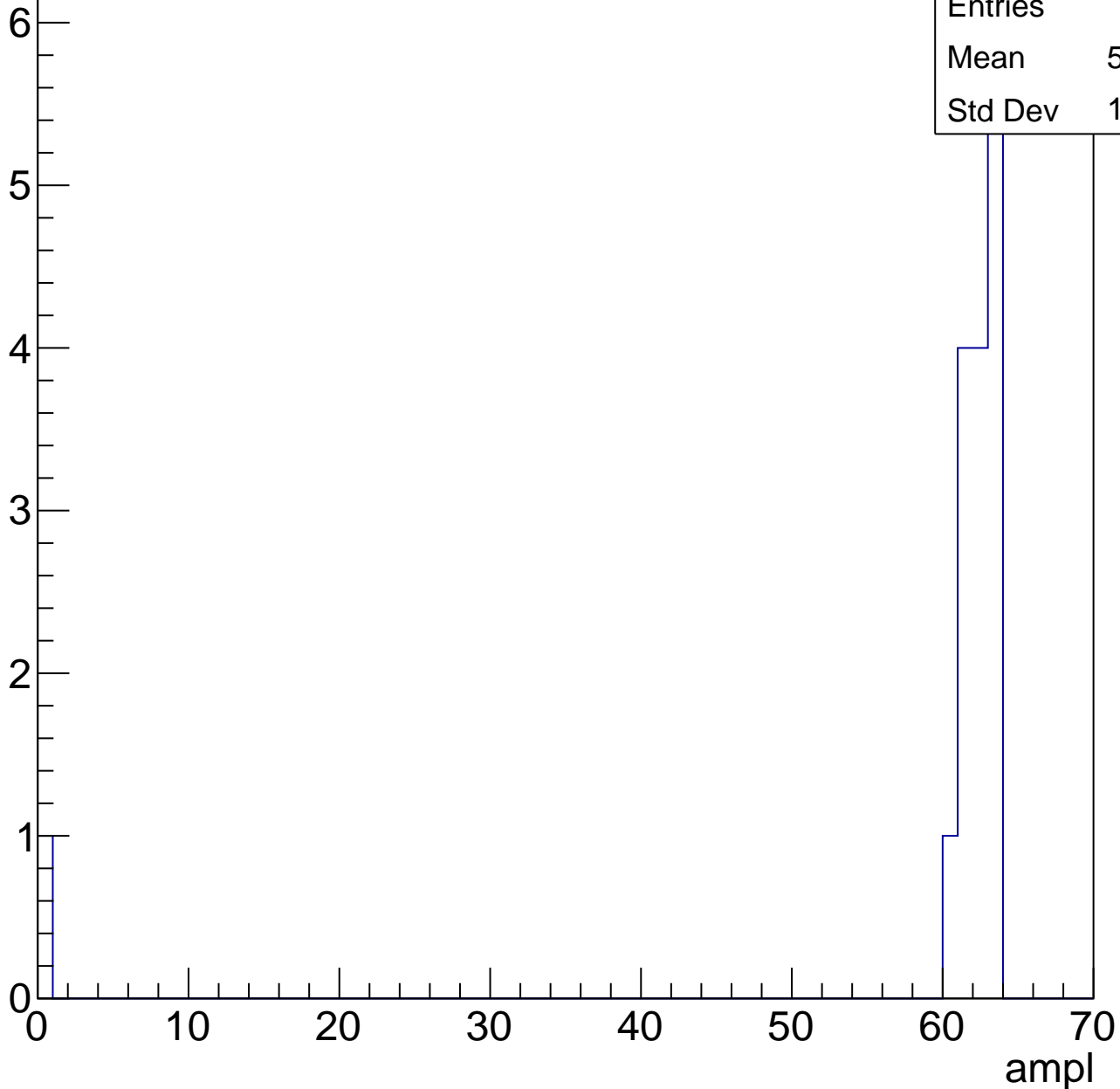


B0L101S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	58.12
Std Dev	15.04



B0L101S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	29.1
Std Dev	3.487

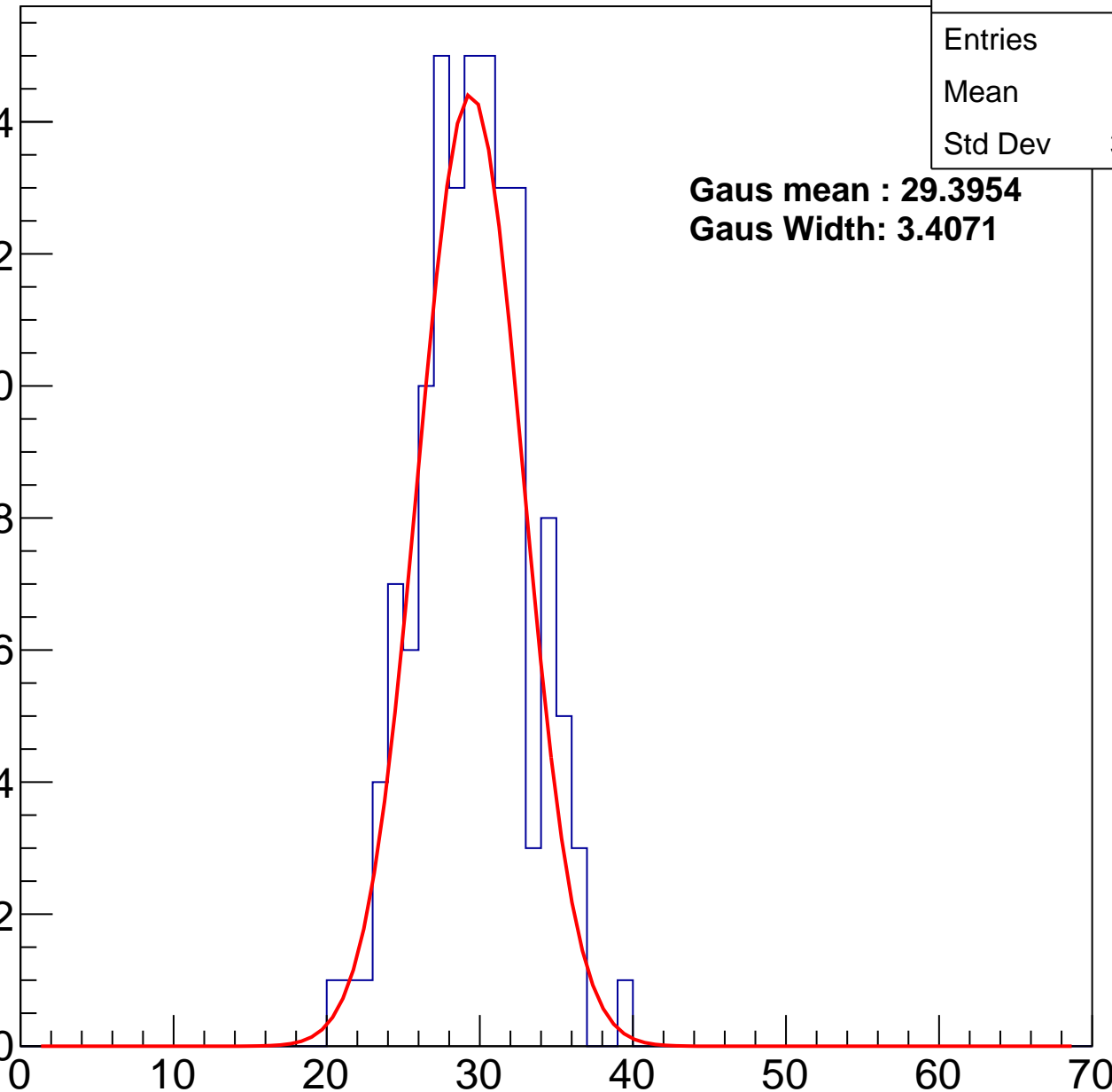
Gaus mean : 29.3954

Gaus Width: 3.4071

Entry

14
12
10
8
6
4
2
0

ampl



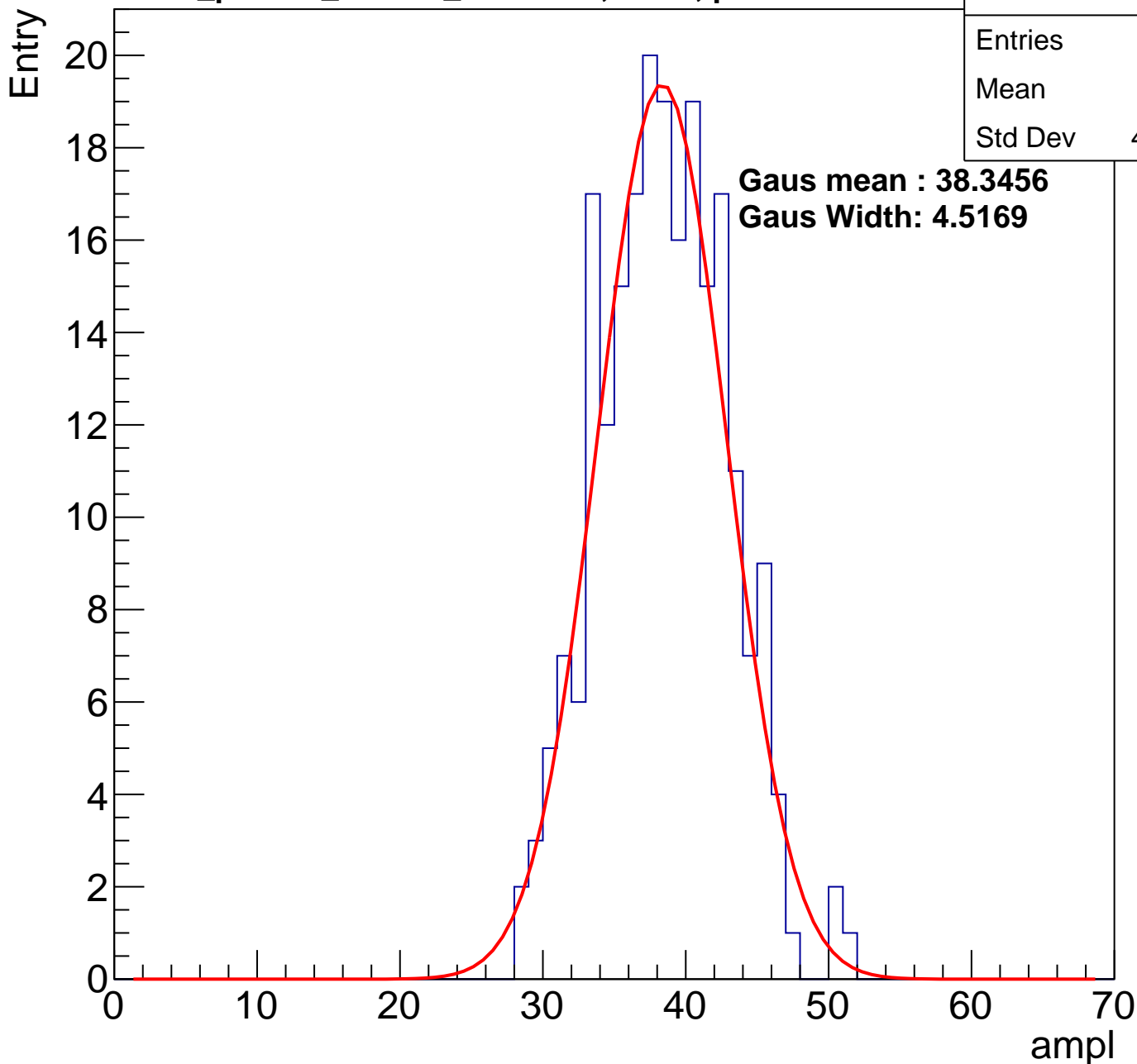
B0L101S, U14-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	225
Mean	38
Std Dev	4.434

Gaus mean : 38.3456

Gaus Width: 4.5169



B0L101S, U14-ch67, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	75
Mean	45.49
Std Dev	3.155

Gaus mean : 46.2267

Gaus Width: 3.1666



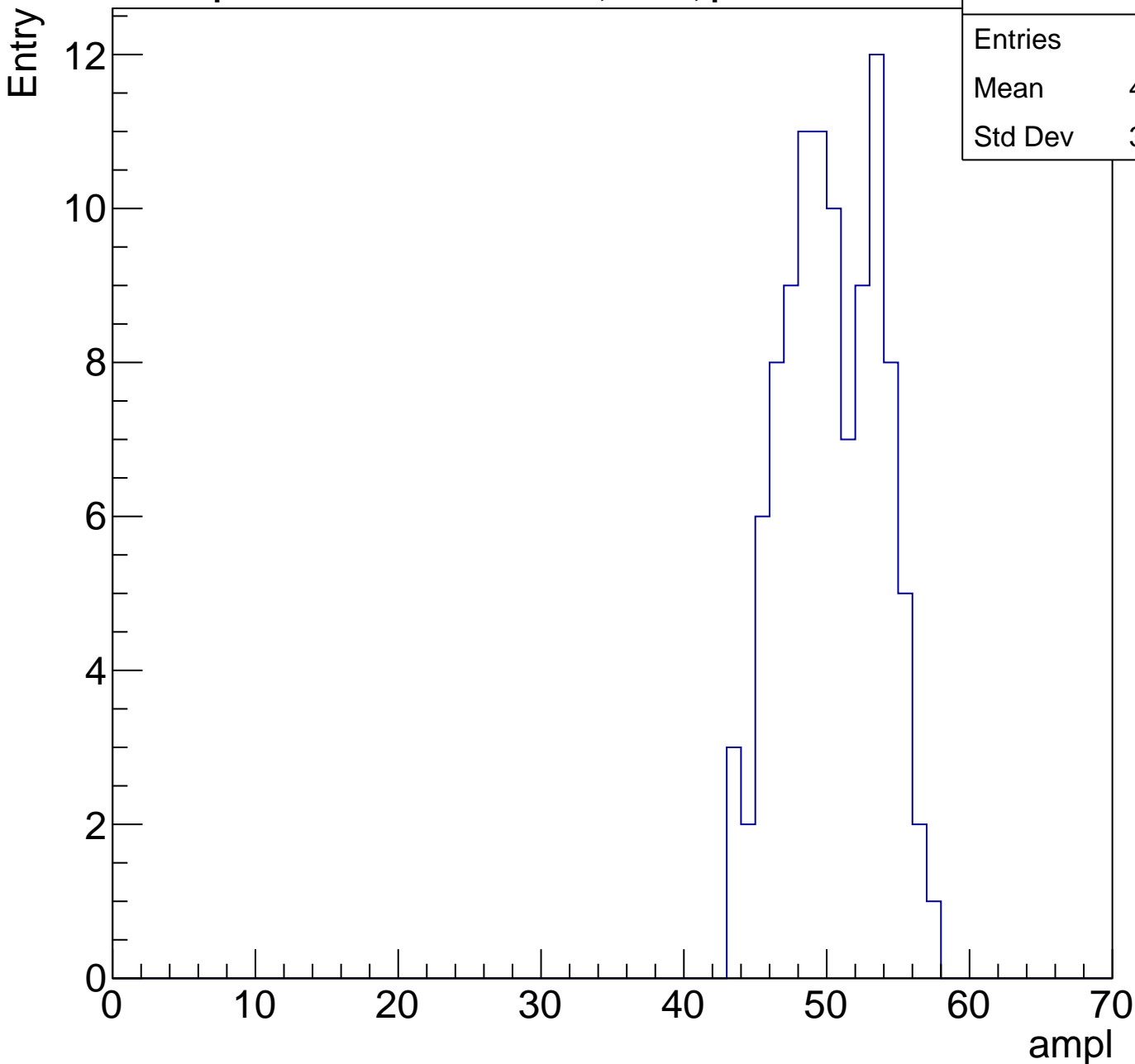
A histogram plot showing the distribution of 'ampl' values. The x-axis ranges from 0 to 70 with major ticks every 10 units and minor ticks every 2 units. The y-axis, labeled 'Entry', ranges from 0 to 10 with major ticks every 2 units and minor ticks every 1 unit. The histogram is represented by blue vertical bars. A smooth red curve is overlaid on the histogram, representing a Gaussian fit. The peak of the distribution is centered around 46. The text 'Gaus mean : 46.2267' and 'Gaus Width: 3.1666' is printed above the peak. In the top right corner, a table provides summary statistics: Entries: 75, Mean: 45.49, Std Dev: 3.155.

ampl	Entry
38	1
40	3
42	5
44	11
46	10
48	8
50	4
52	1

B0L101S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	49.83
Std Dev	3.332



B0L101S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#1, port C1

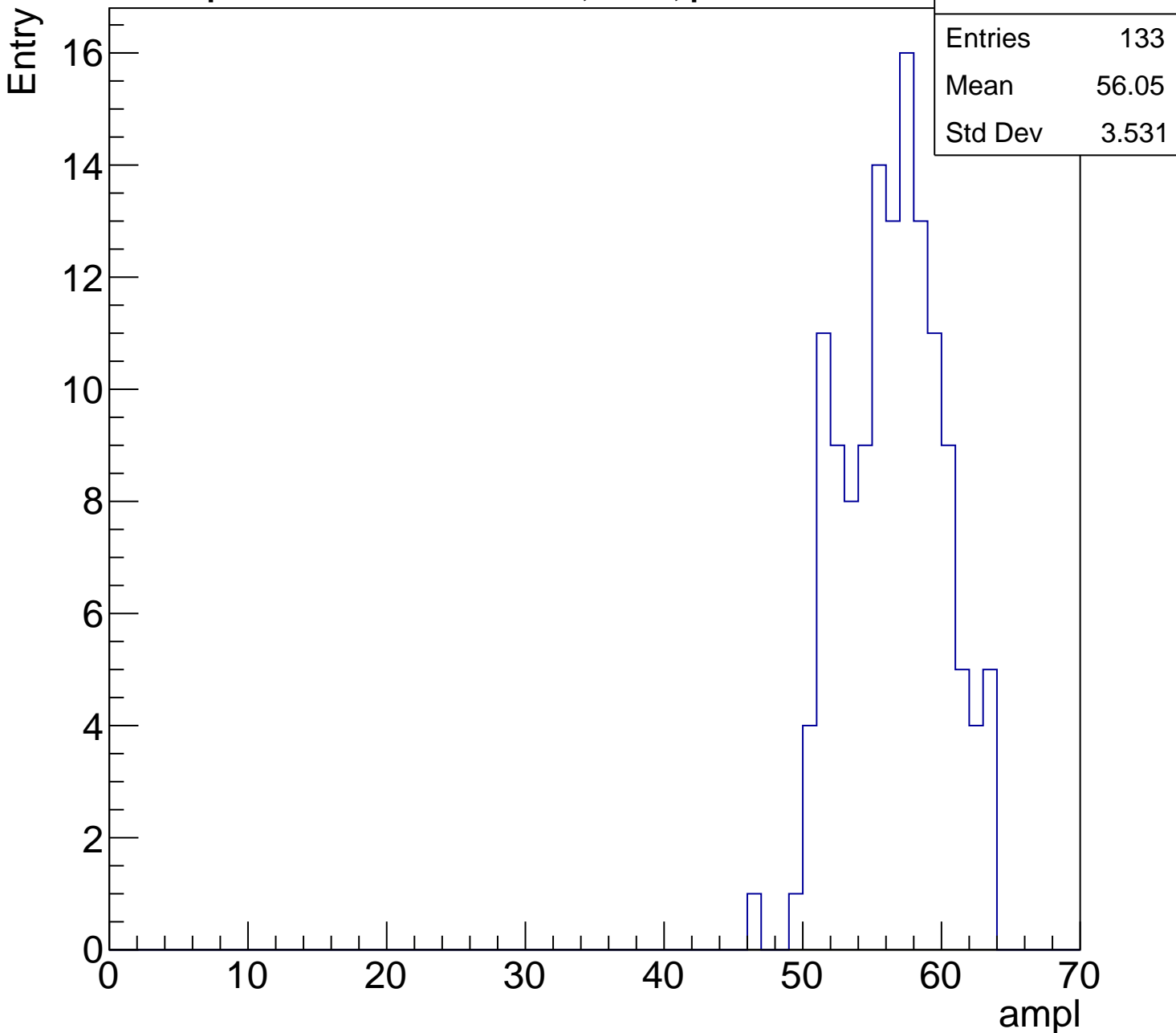
Entries	133
Mean	56.05
Std Dev	3.531

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch67, adc5

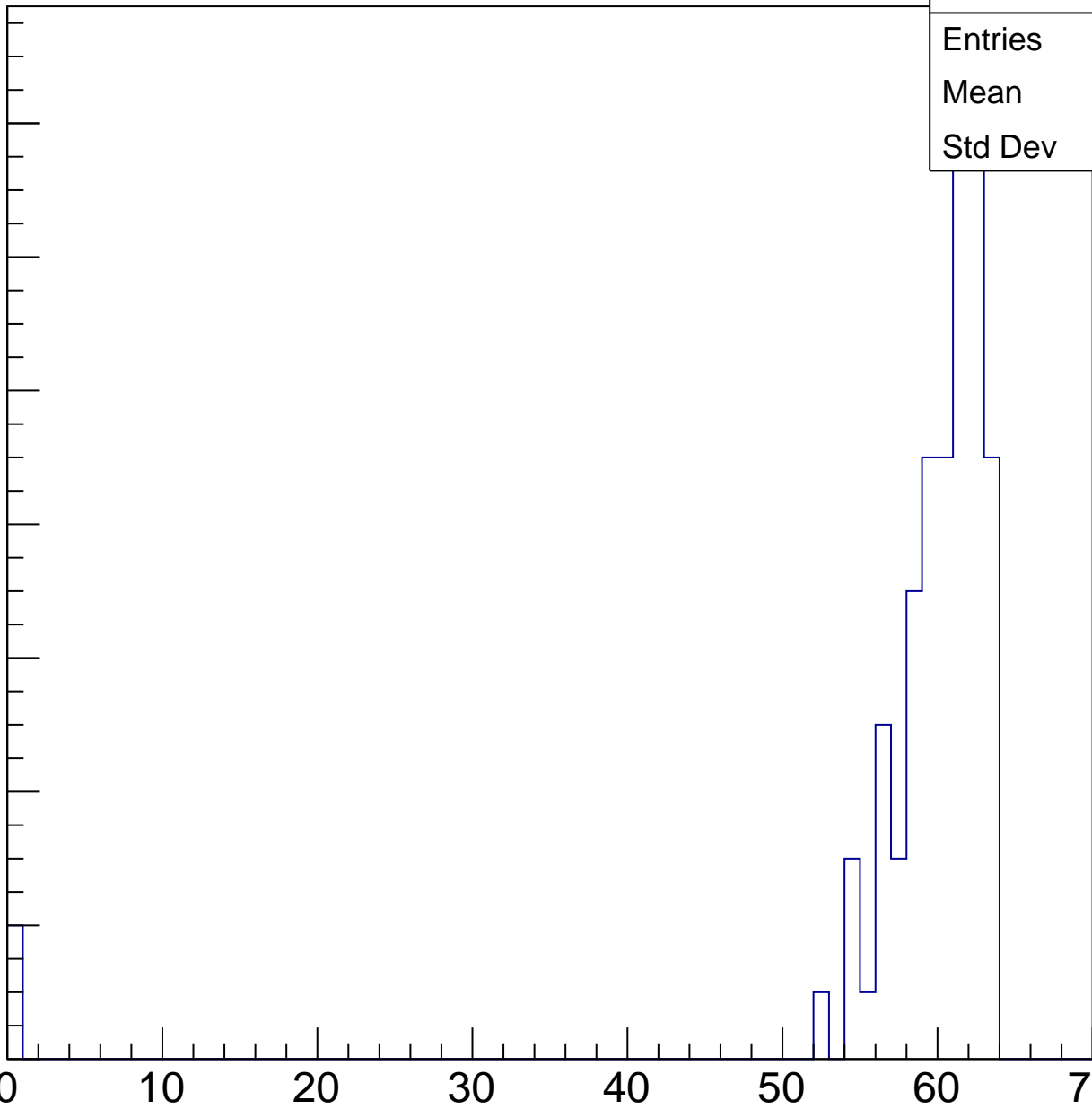
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.31
Std Dev	9.788

ampl

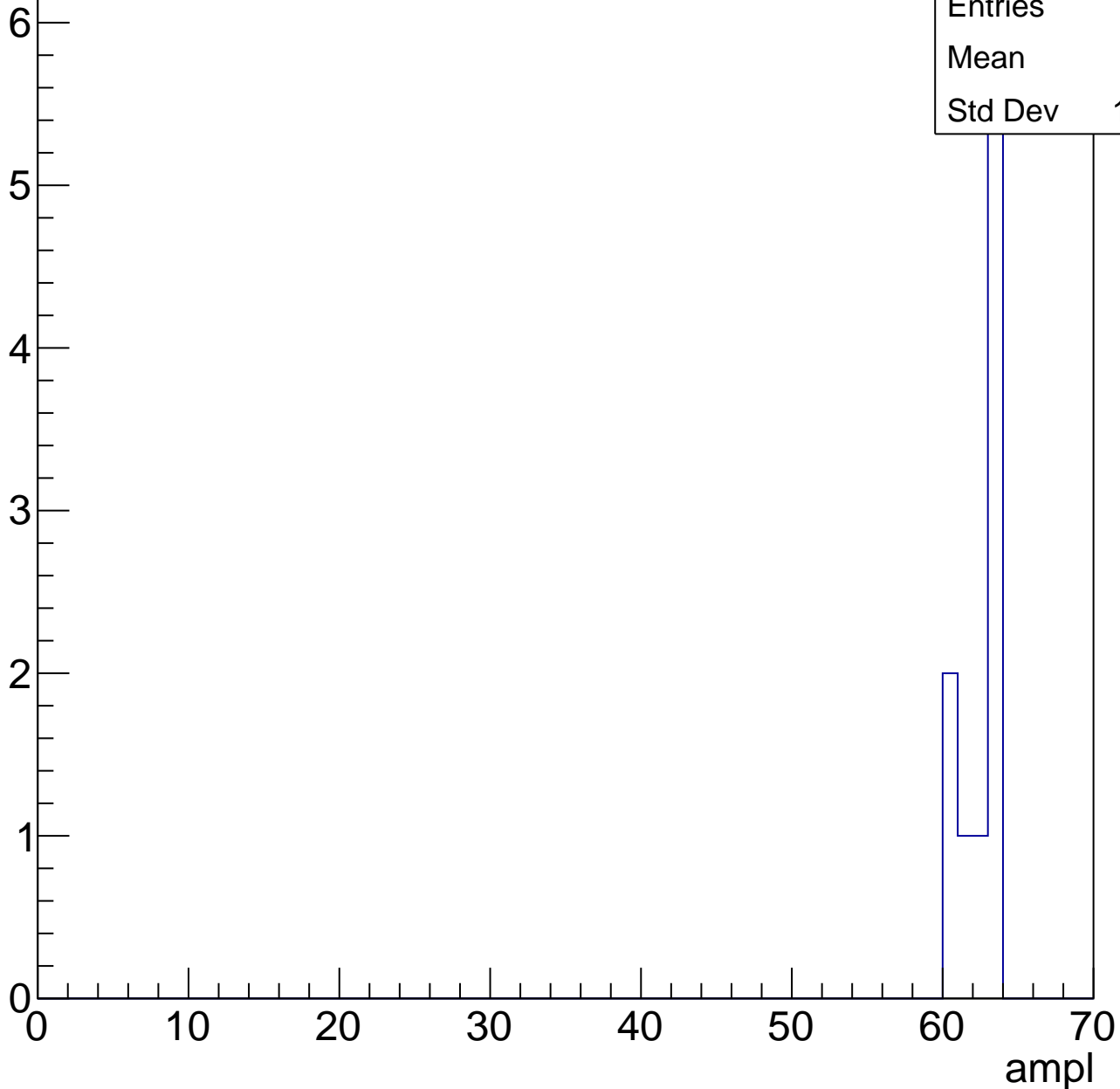


B0L101S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.1
Std Dev	1.221



B0L101S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

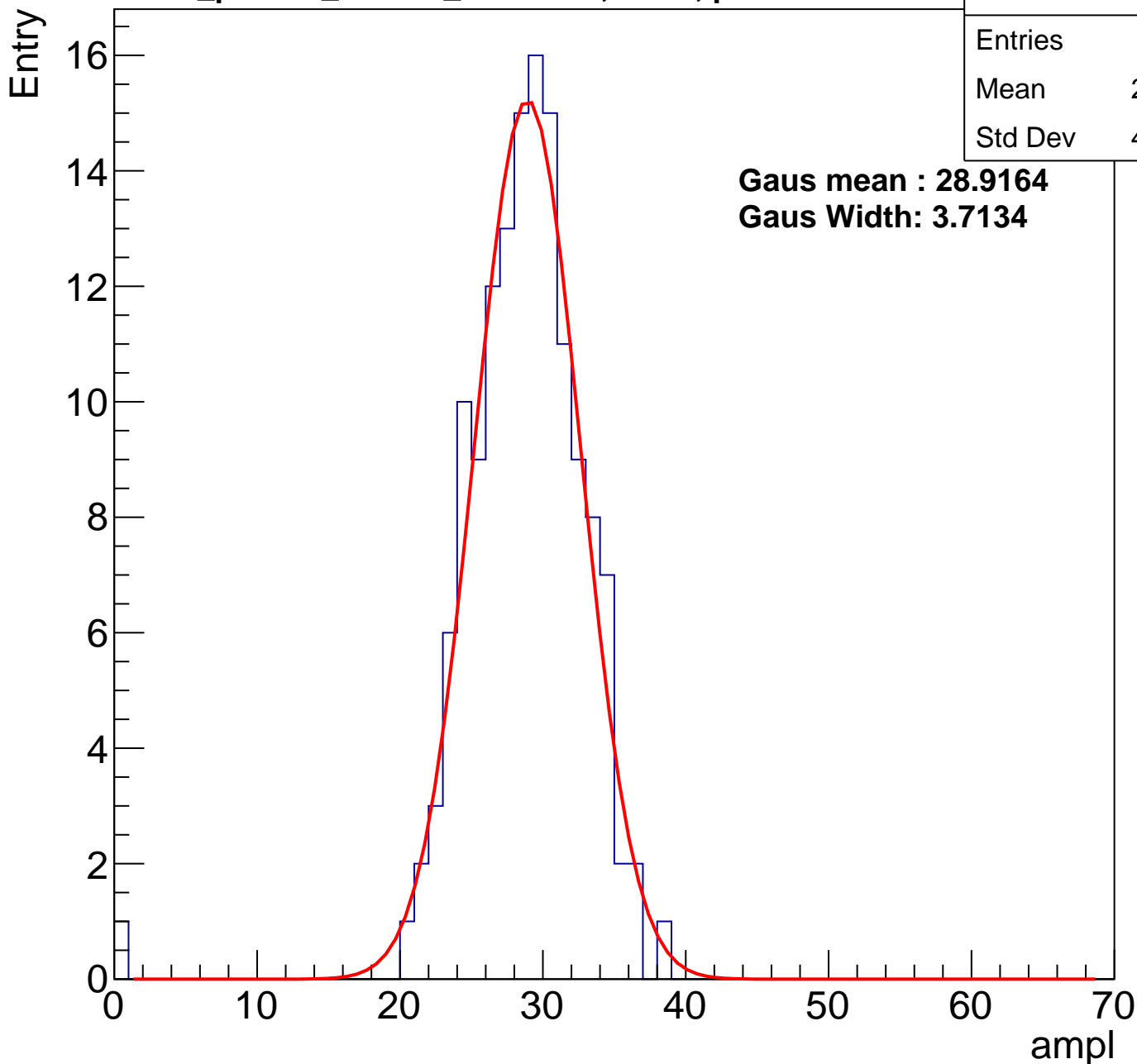
B0L101S, U14-ch68, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.24
Std Dev	4.259

Gaus mean : 28.9164

Gaus Width: 3.7134



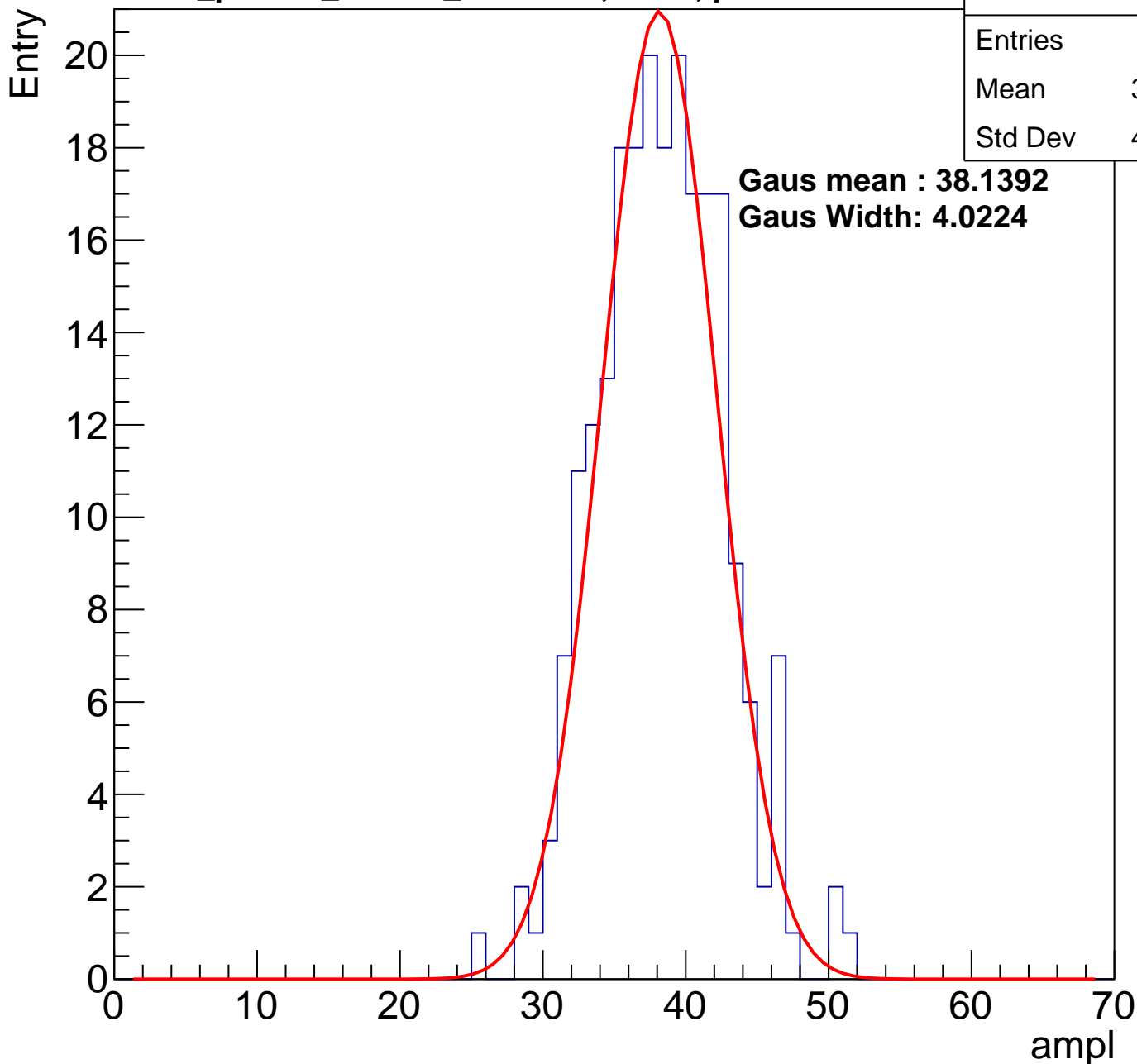
B0L101S, U14-ch68, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	223
Mean	37.85
Std Dev	4.327

Gaus mean : 38.1392

Gaus Width: 4.0224



B0L101S, U14-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	75
Mean	45.67
Std Dev	3.091

Gaus mean : 45.8112
Gaus Width: 2.7101

Entry

10

8

6

4

2

0

0

10

20

30

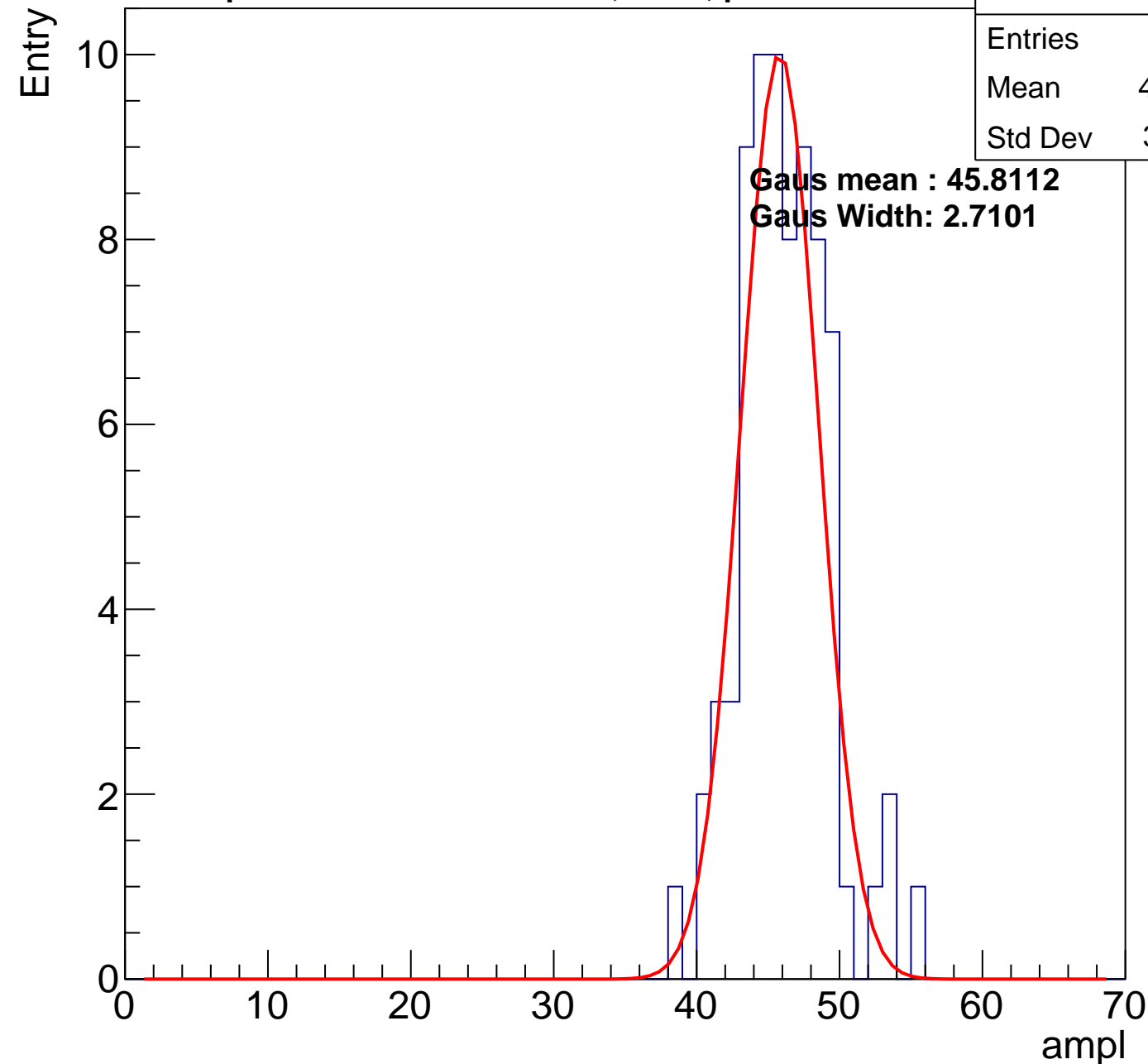
40

50

60

70

ampl



B0L101S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.09
Std Dev	3.54

Entry

12

10

8

6

4

2

0

0

10

20

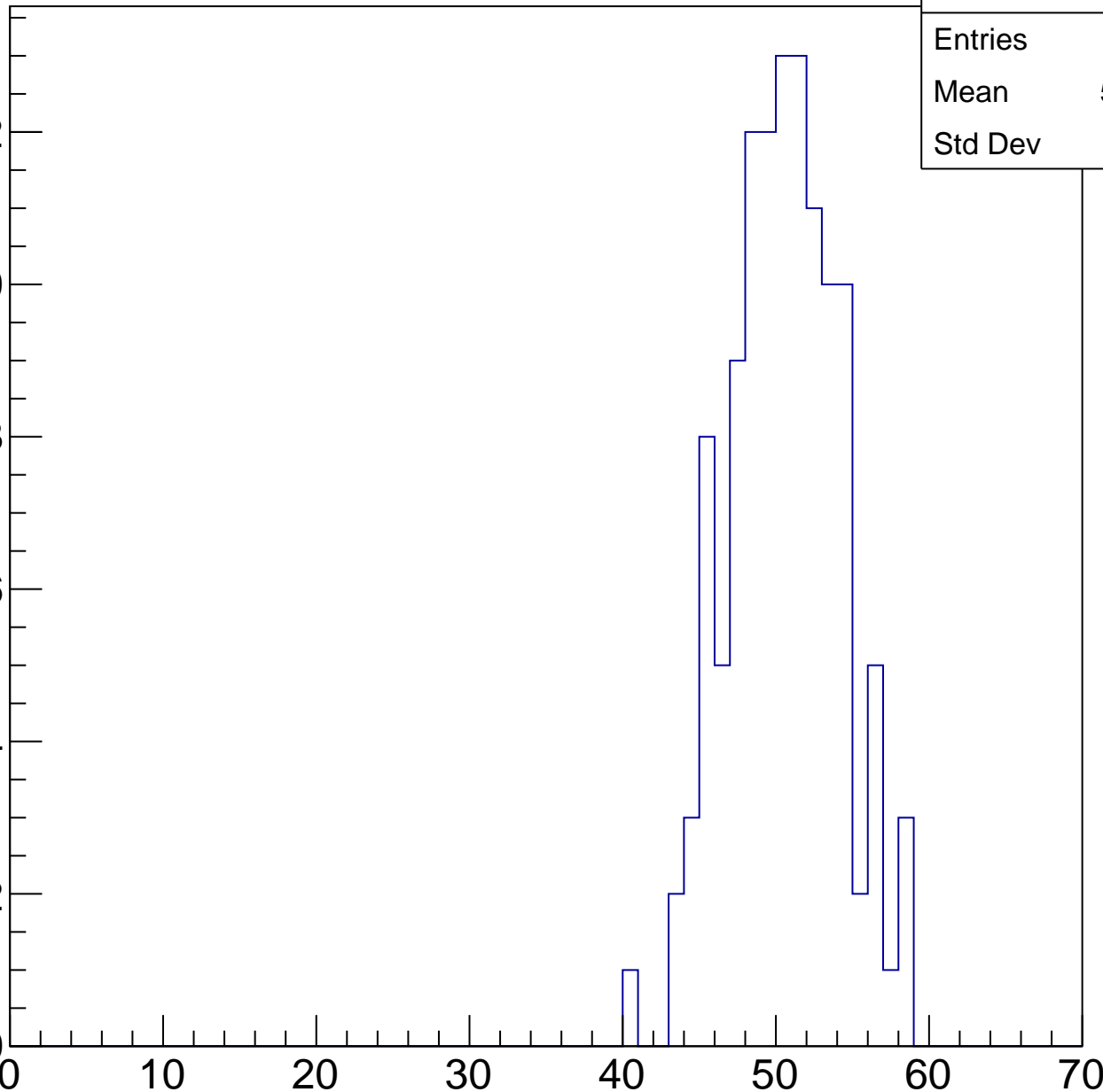
30

40

50

60

ampl



B0L101S, U14-ch68, adc4

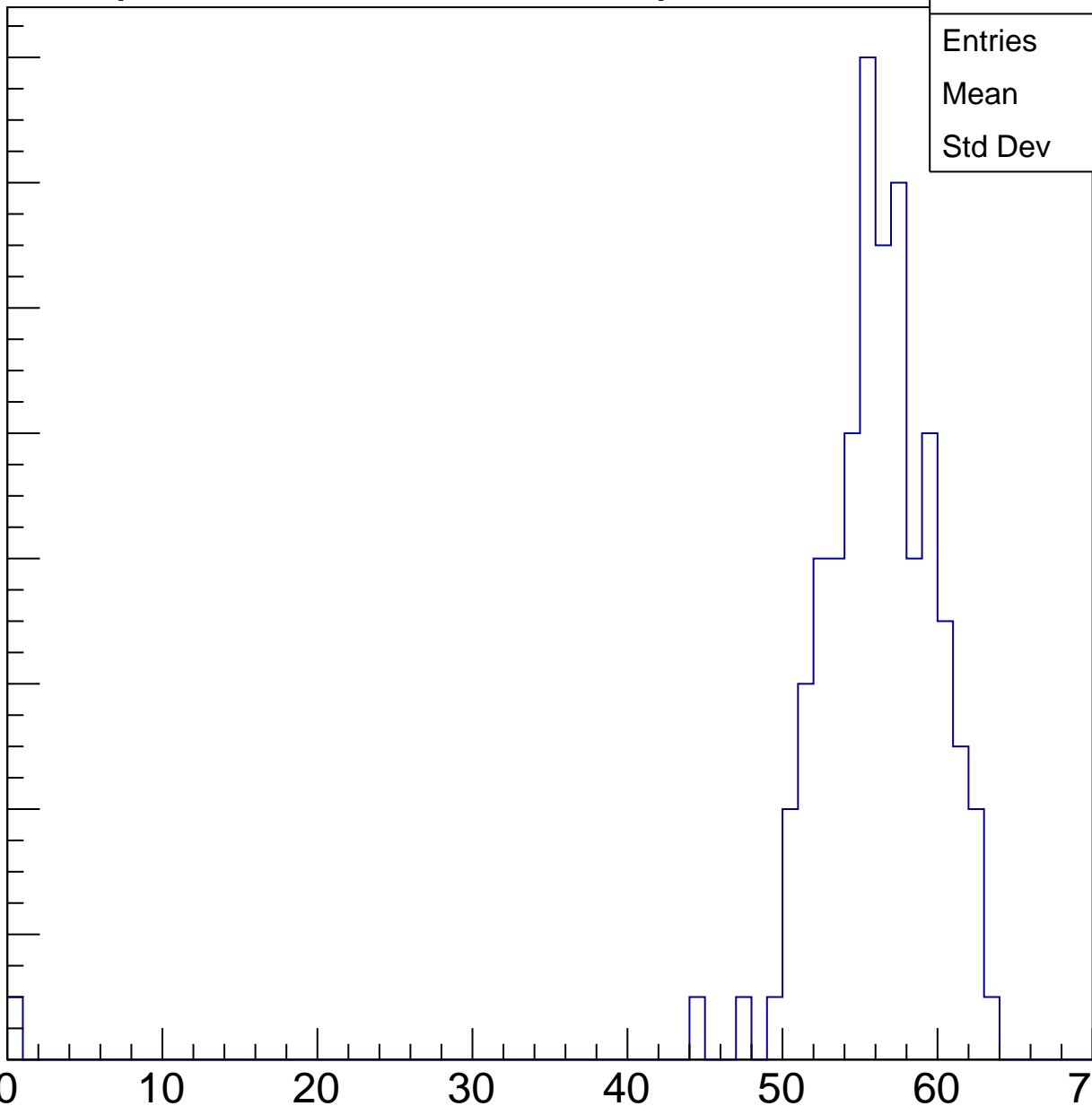
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	55.27
Std Dev	6.15

ampl



B0L101S, U14-ch68, adc5

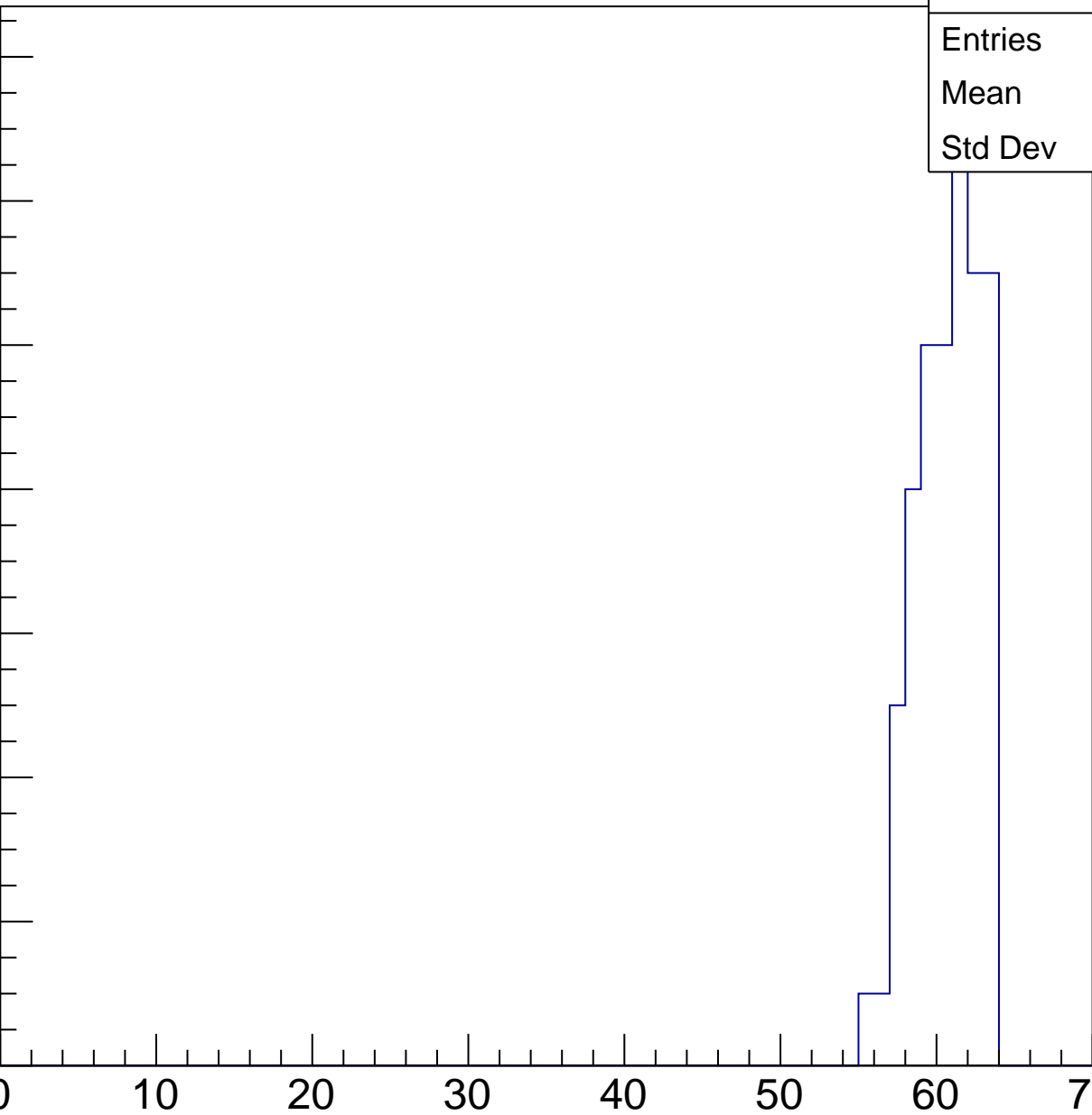
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	60.27
Std Dev	1.986

ampl

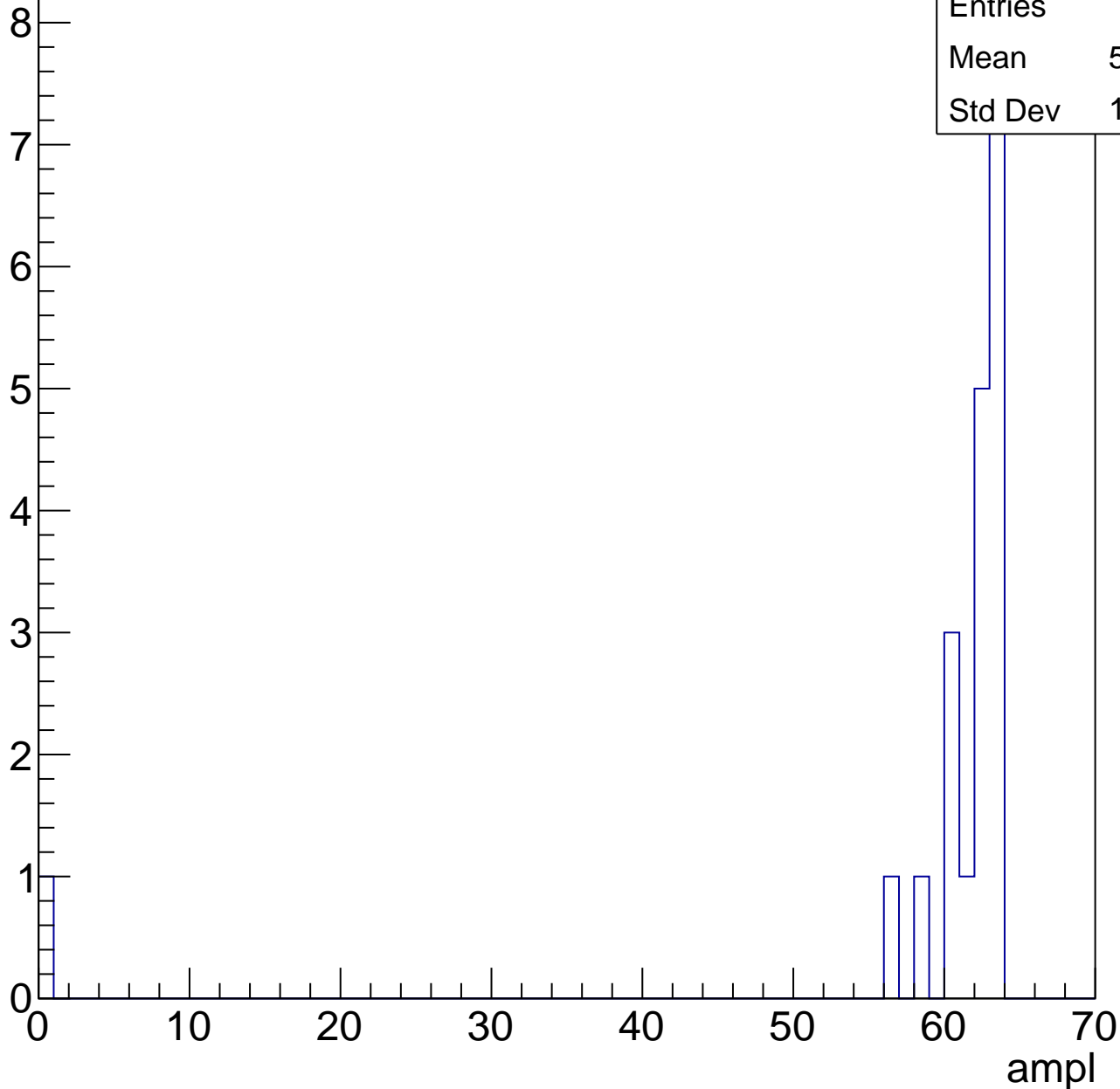


B0L101S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	58.45
Std Dev	13.54



B0L101S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch69, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	29.04
Std Dev	4.95

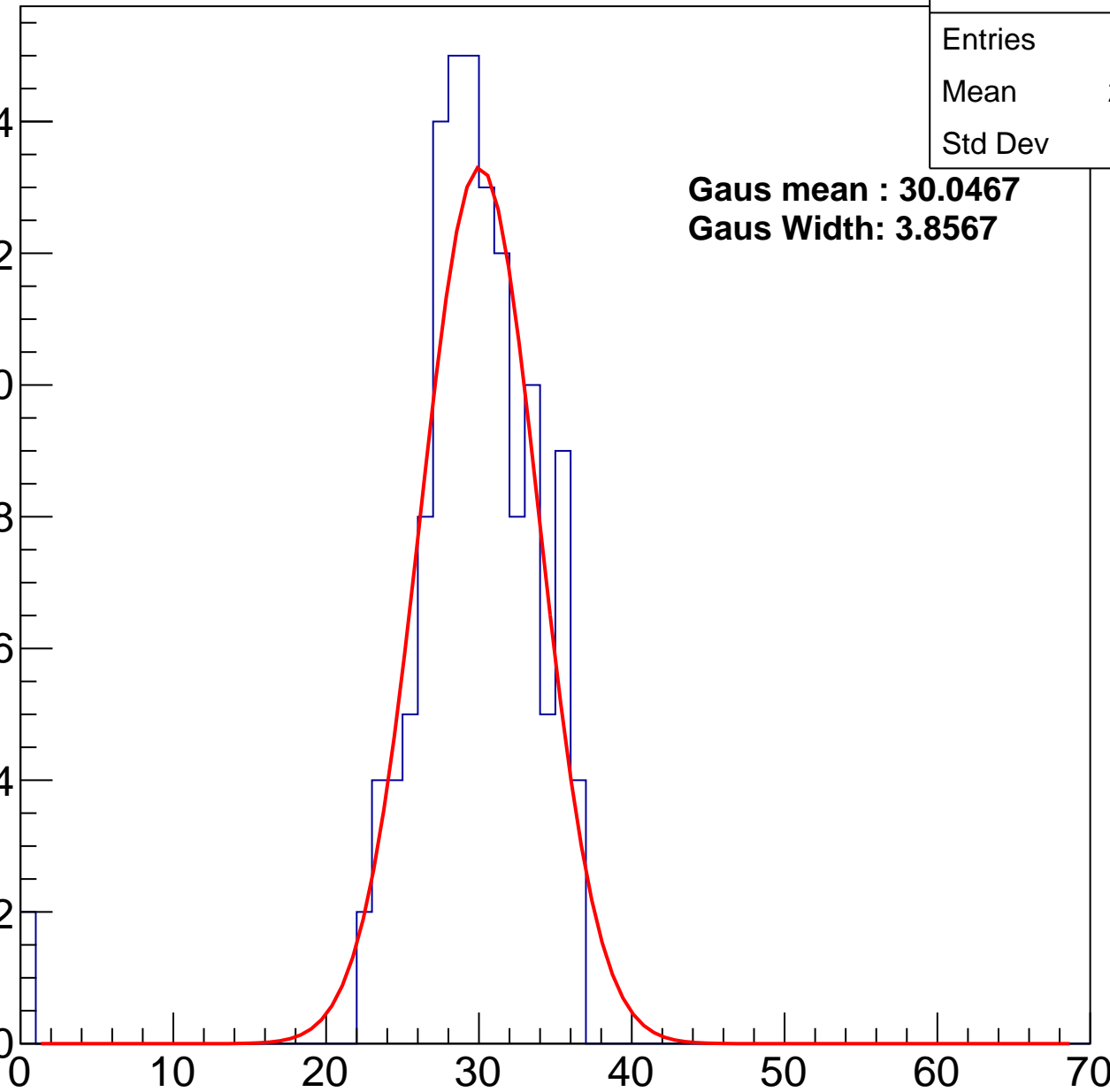
Gaus mean : 30.0467

Gaus Width: 3.8567

Entry

14
12
10
8
6
4
2
0

ampl



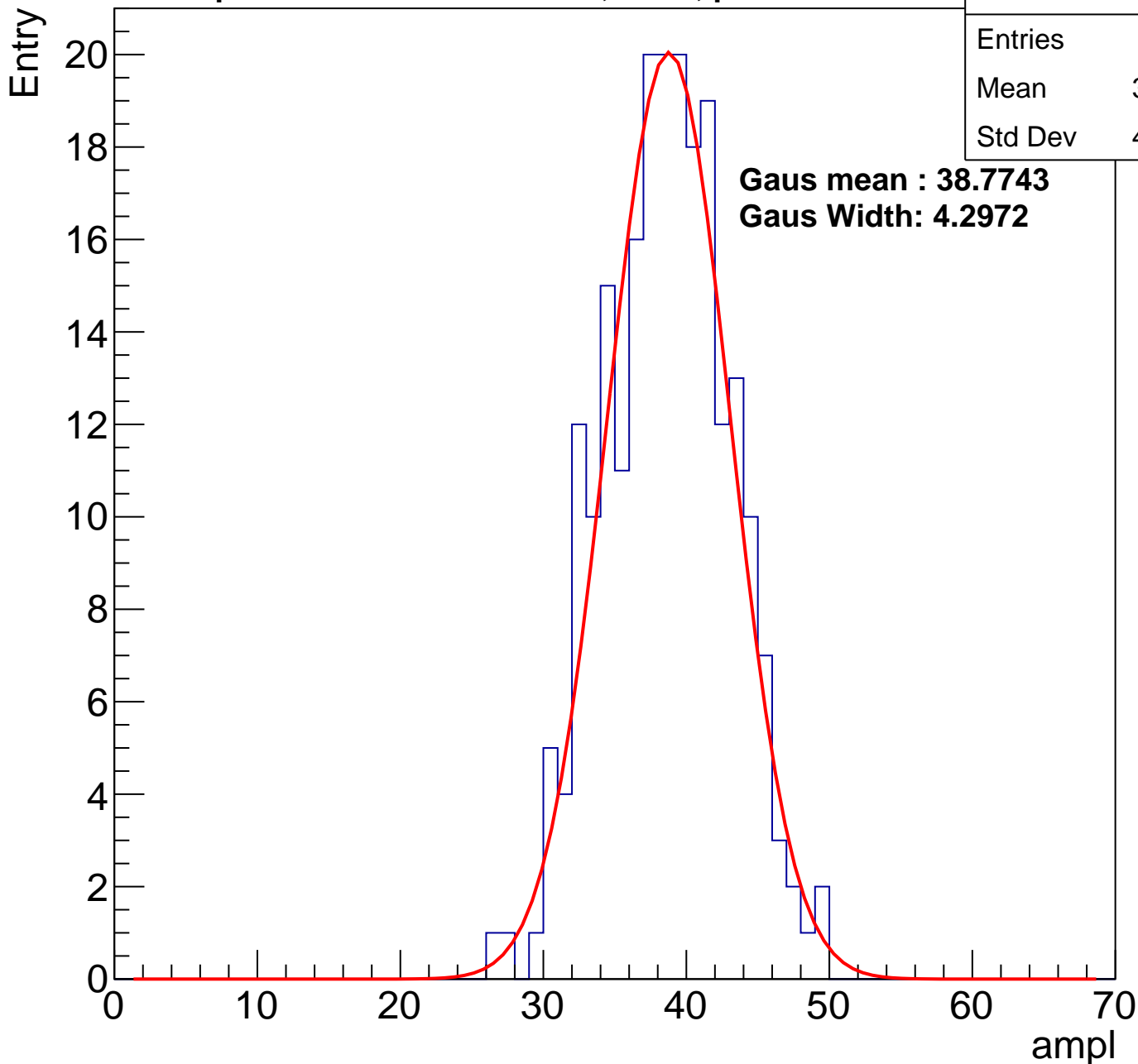
B0L101S, U14-ch69, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	223
Mean	38.19
Std Dev	4.308

Gaus mean : 38.7743

Gaus Width: 4.2972



B0L101S, U14-ch69, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

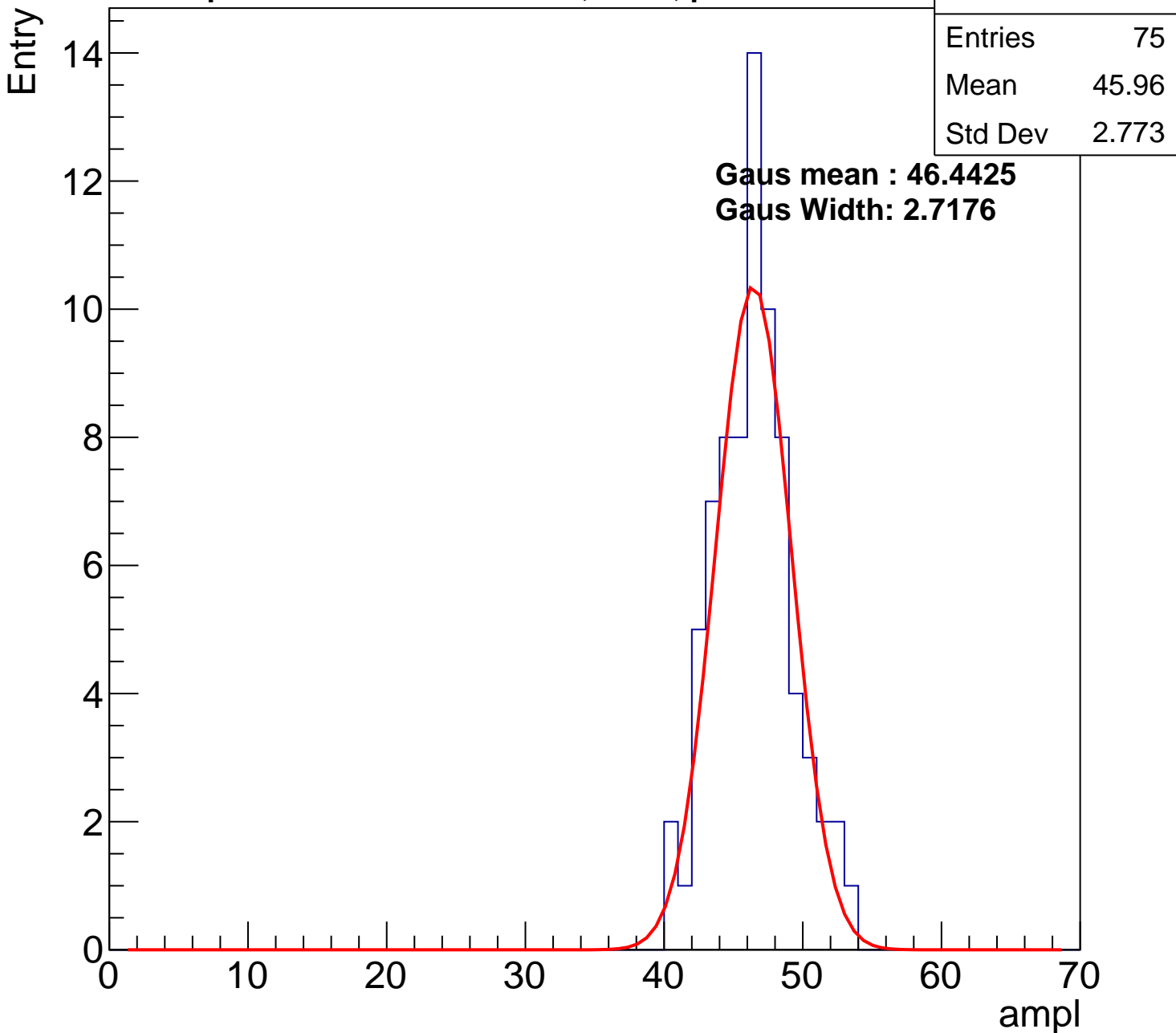
14
12
10
8
6
4
2
0

Entries	75
Mean	45.96
Std Dev	2.773

Gaus mean : 46.4425
Gaus Width: 2.7176

ampl

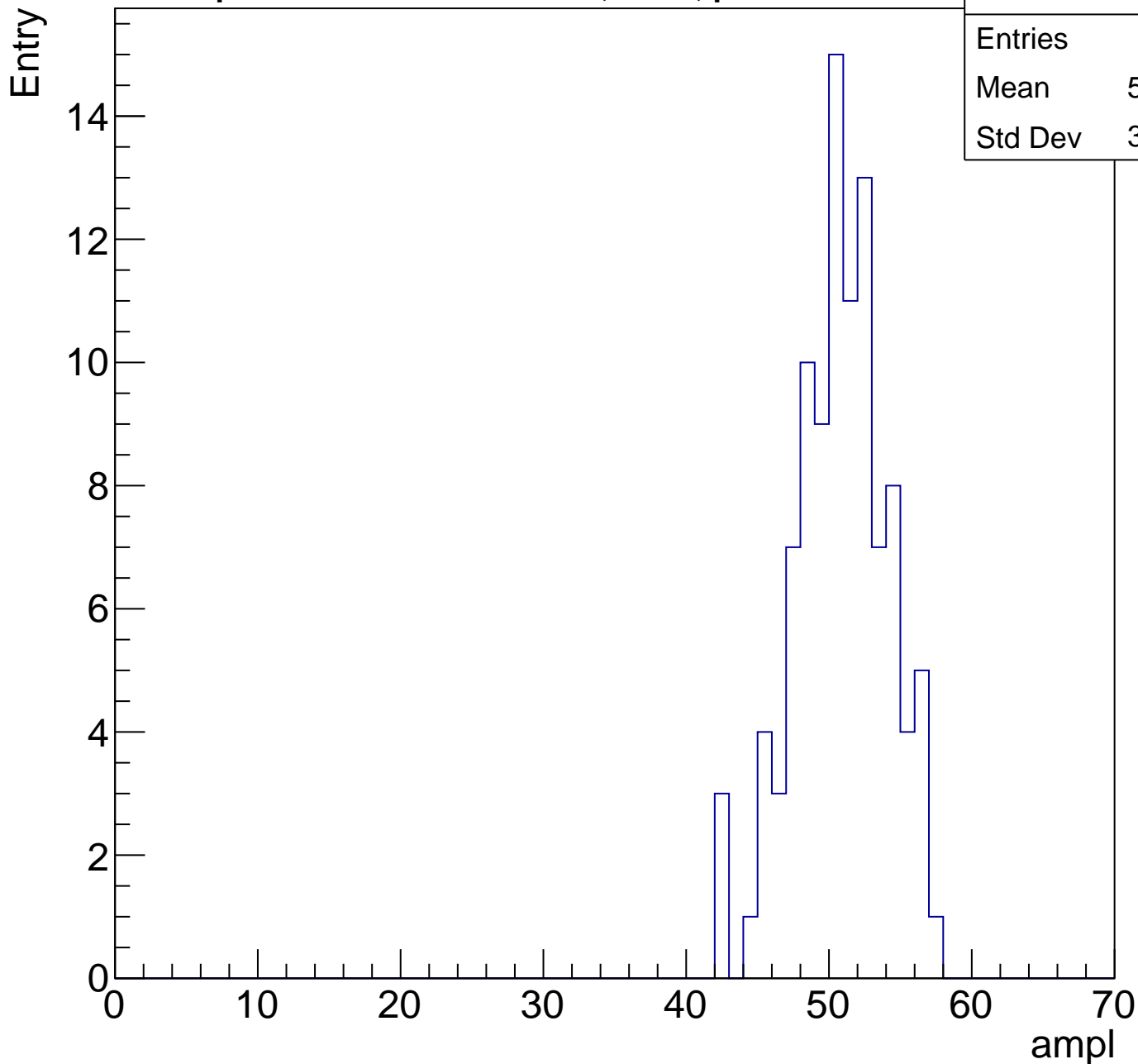
0 10 20 30 40 50 60 70



B0L101S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

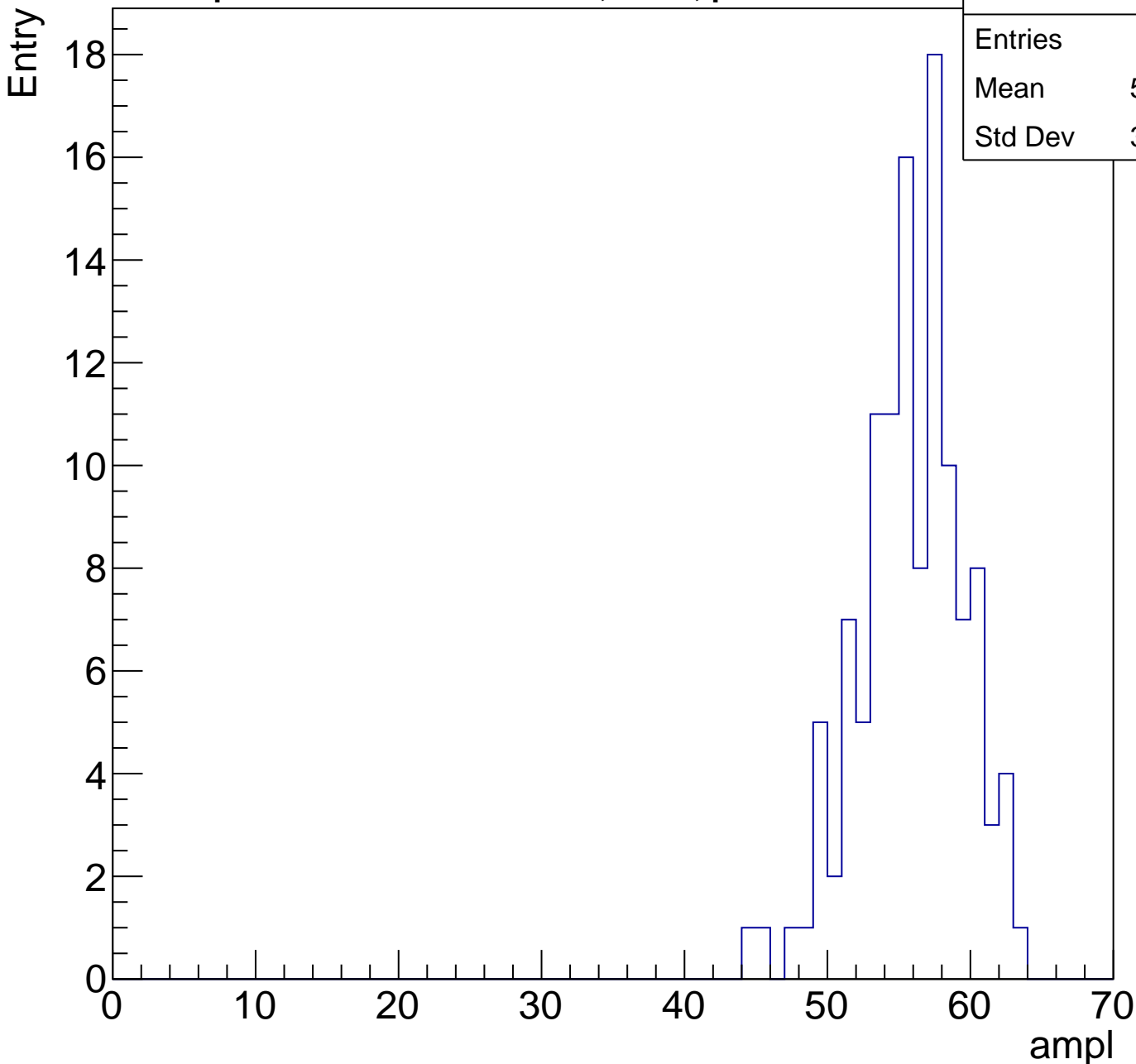
Entries	101
Mean	50.35
Std Dev	3.238



B0L101S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.37
Std Dev	3.663



B0L101S, U14-ch69, adc5

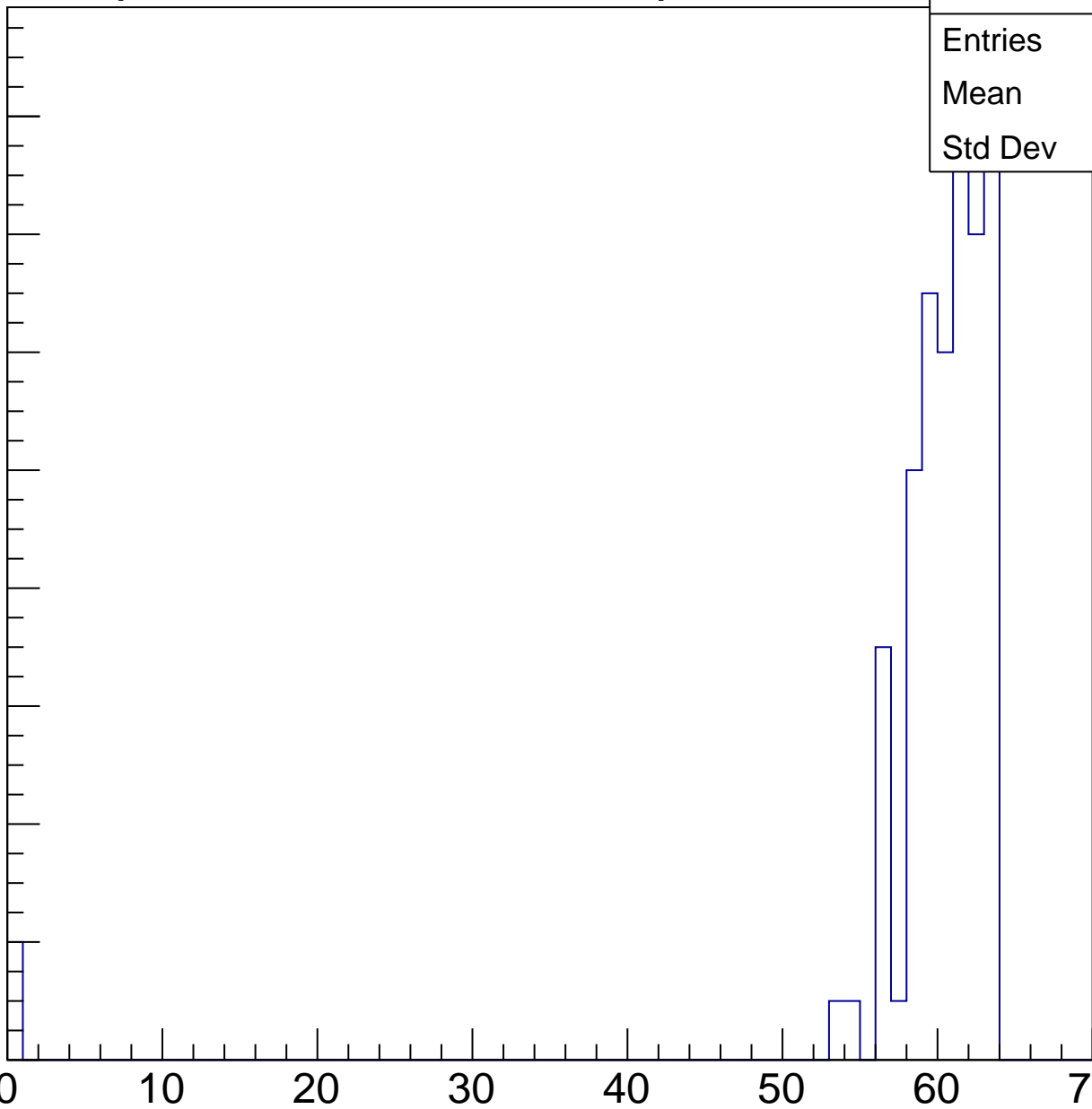
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.89
Std Dev	8.969

ampl



B0L101S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

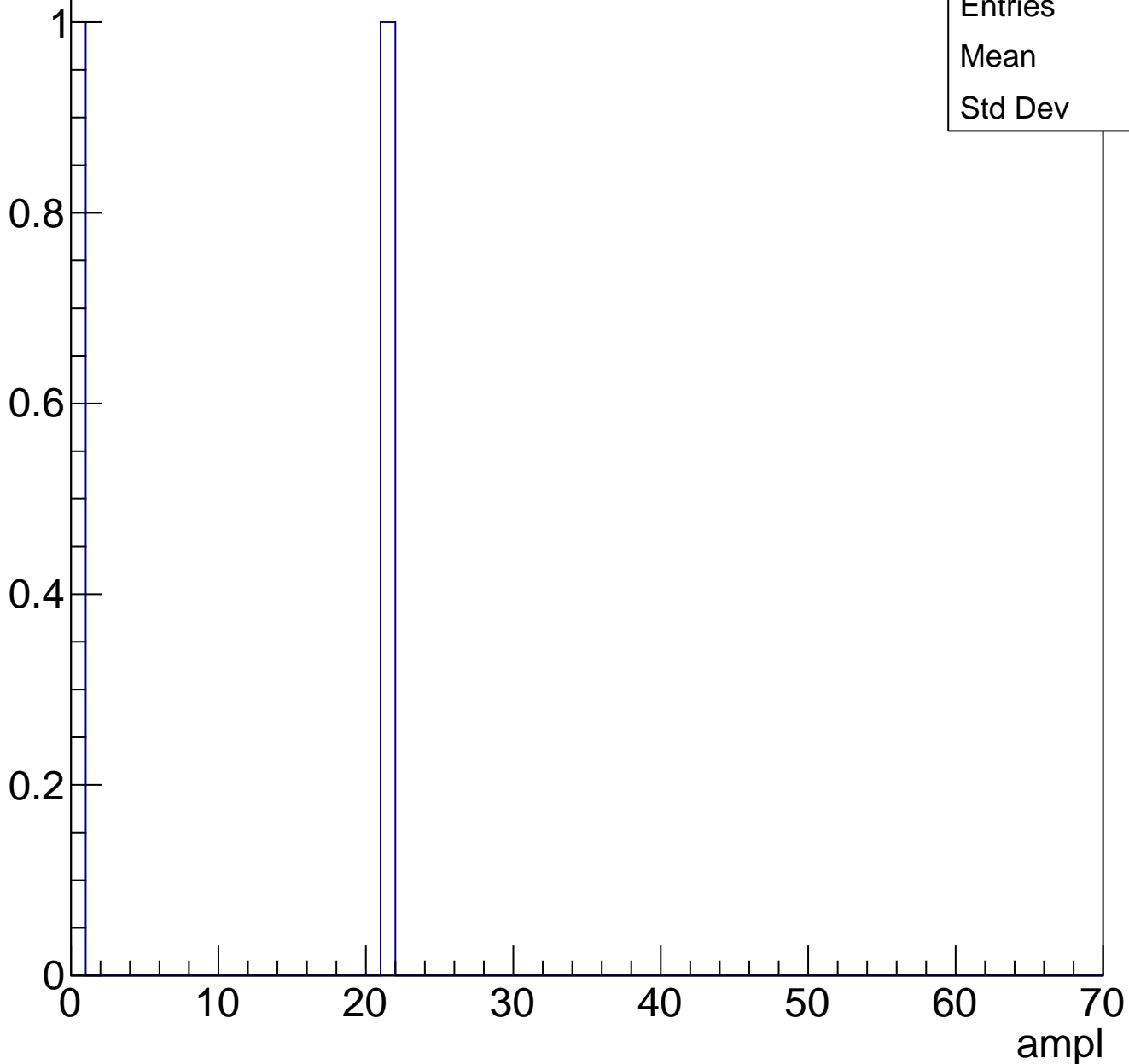
Entry



B0L101S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch70, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	29.45
Std Dev	5.091

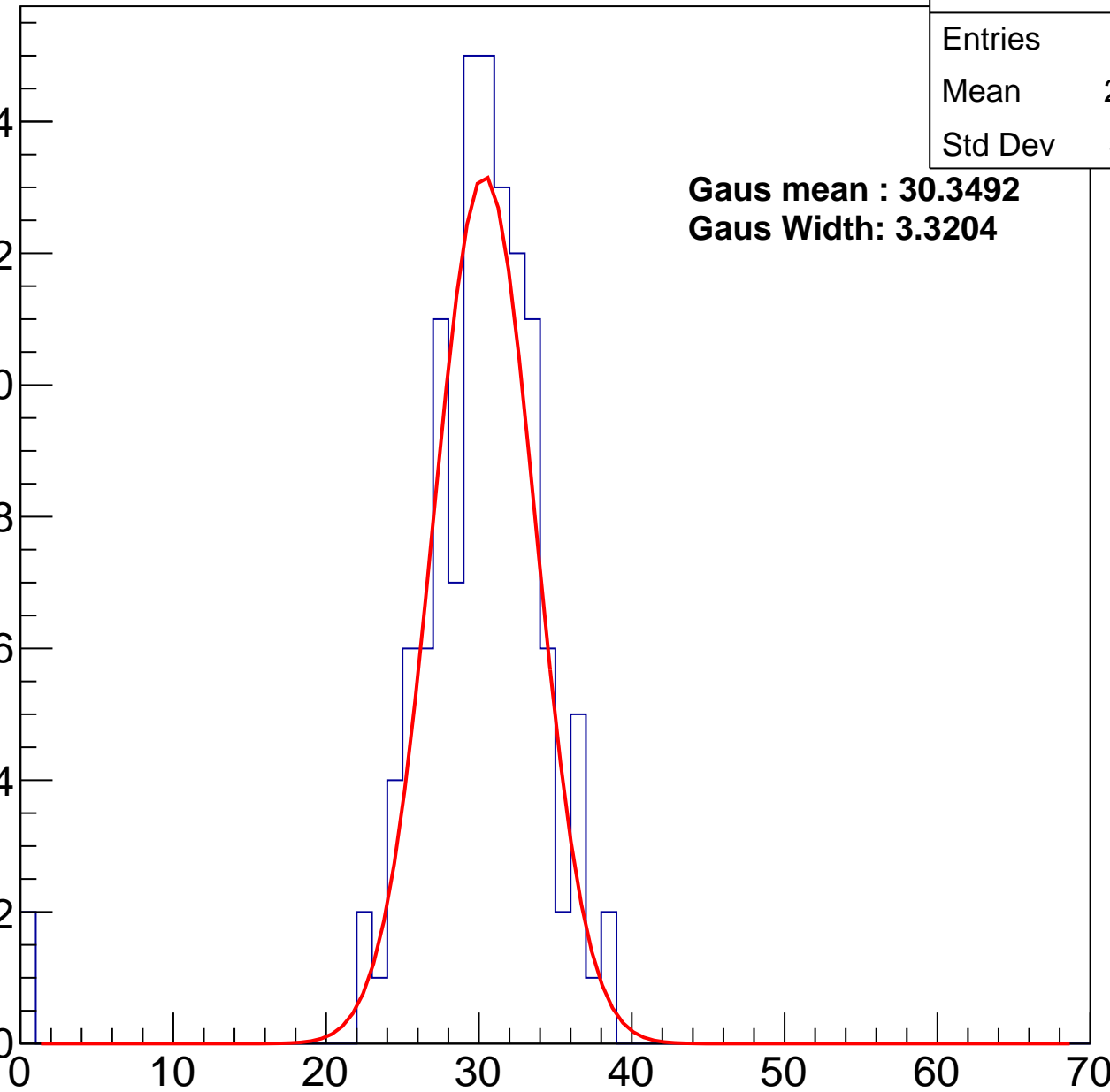
Gaus mean : 30.3492

Gaus Width: 3.3204

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch70, adc1

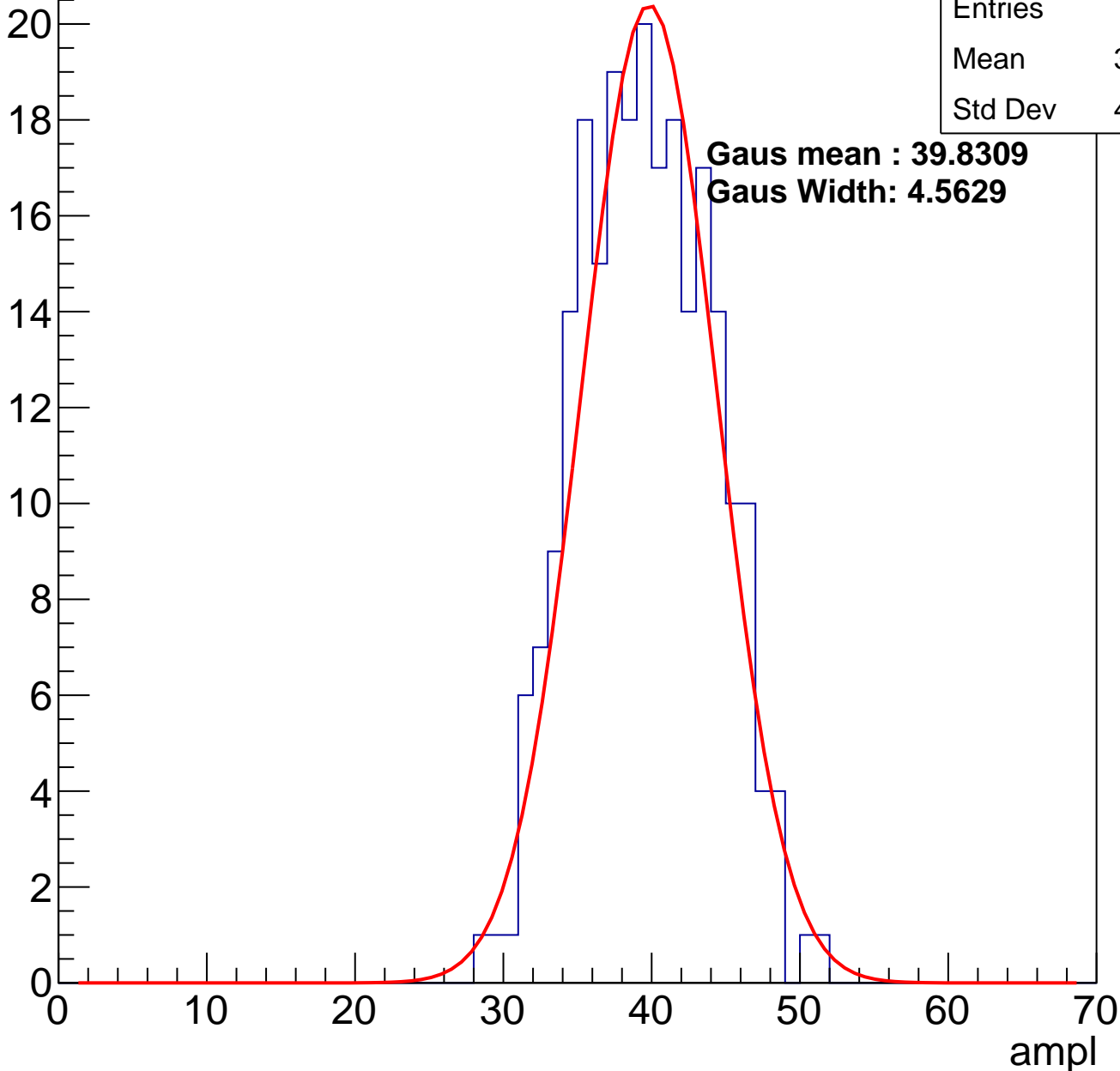
calib_packv5_042523_0143.root, FC#1, port C1

Entries	239
Mean	39.13
Std Dev	4.457

Gaus mean : 39.8309

Gaus Width: 4.5629

Entry



B0L101S, U14-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	46.22
Std Dev	3.19

Gaus mean : 46.5723

Gaus Width: 3.4458

10

8

6

4

2

0

0

10

20

30

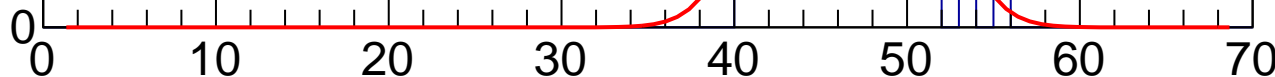
40

50

60

70

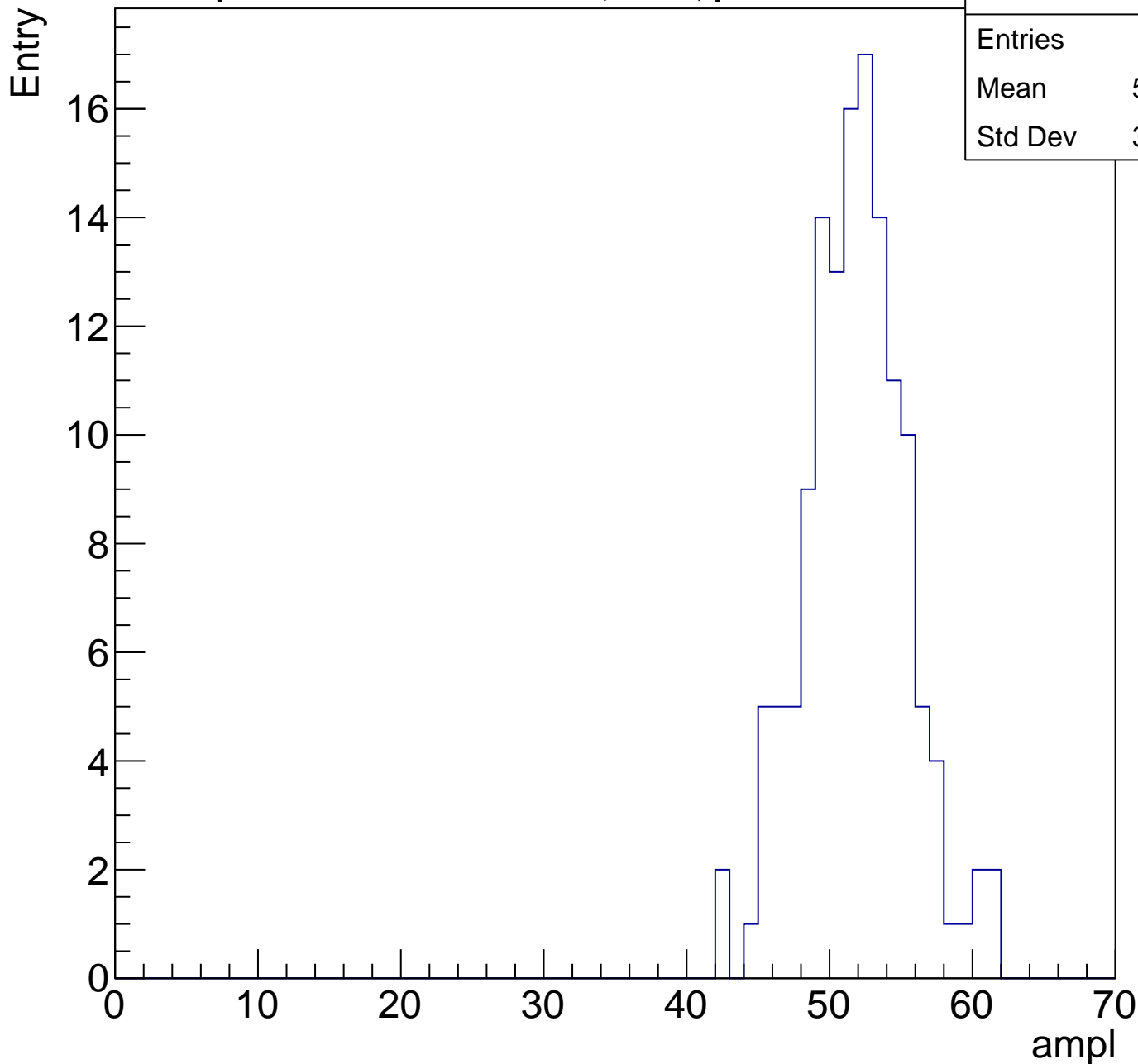
ampl



B0L101S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	51.38
Std Dev	3.627



B0L101S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

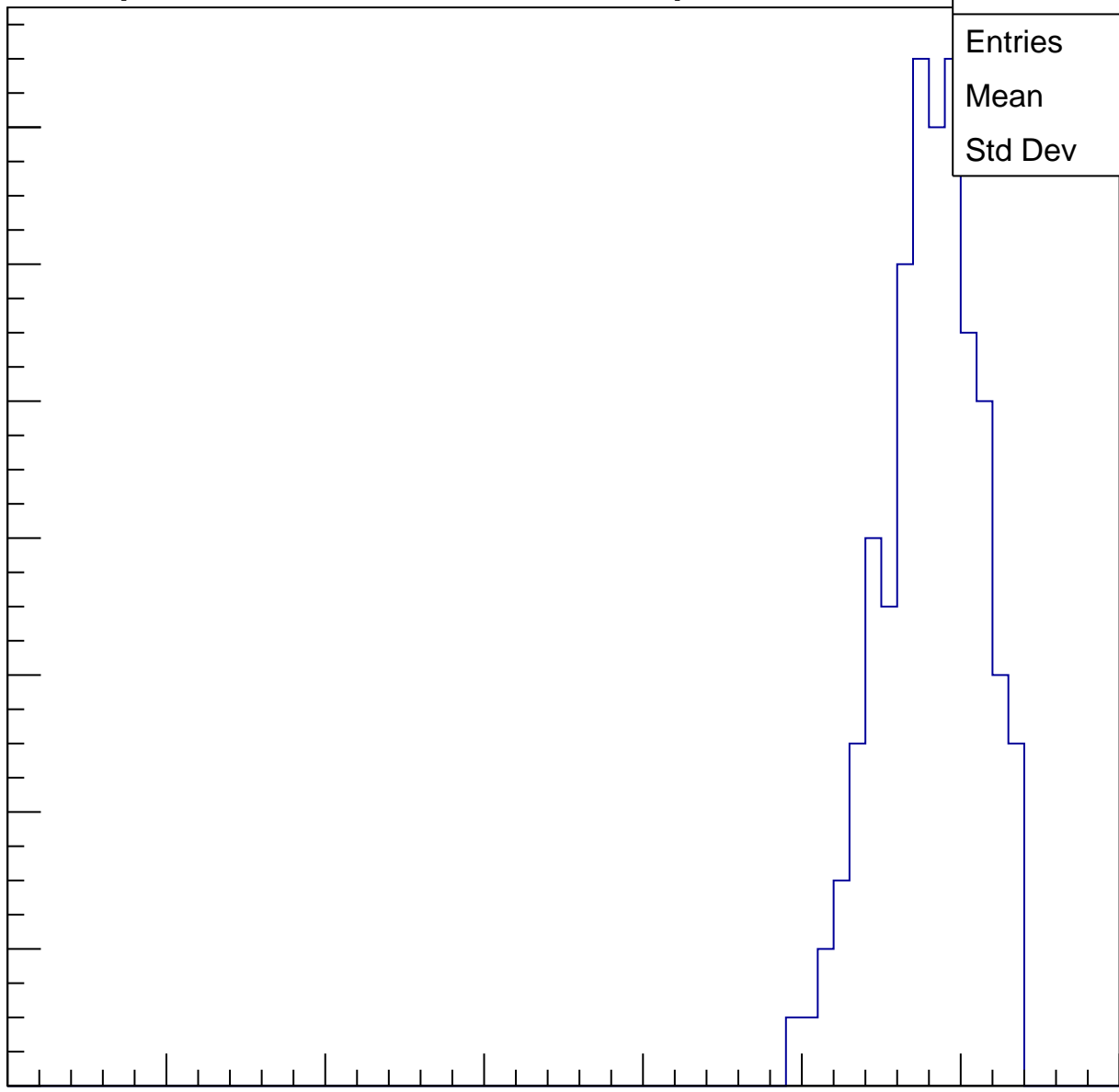
50

60

70

ampl

Entries	115
Mean	57.57
Std Dev	3.062



B0L101S, U14-ch70, adc5

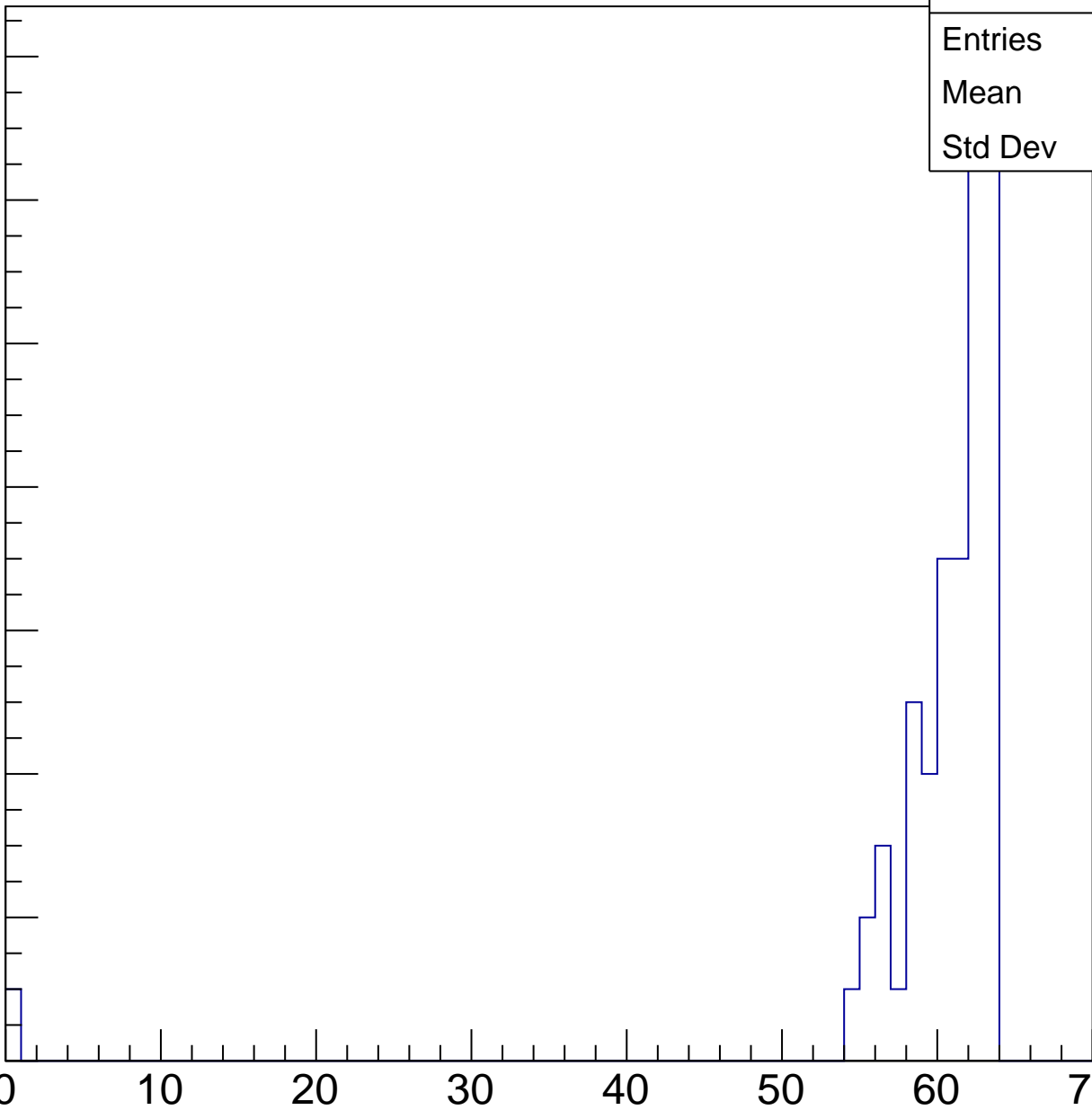
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	59.47
Std Dev	8.232

ampl



B0L101S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U14-ch71, adc0

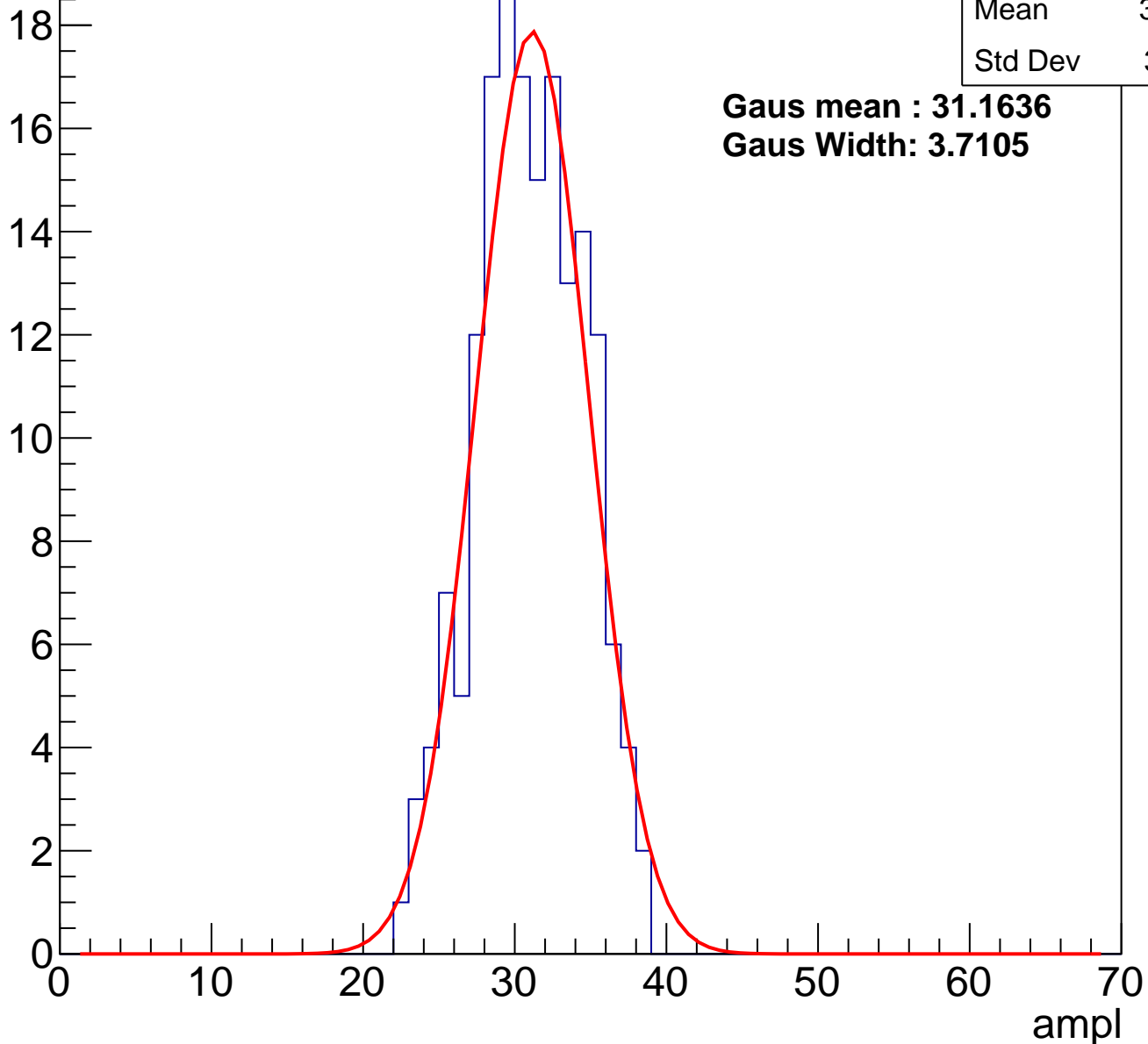
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	30.52
Std Dev	3.471

Gaus mean : 31.1636

Gaus Width: 3.7105

Entry



B0L101S, U14-ch71, adc1

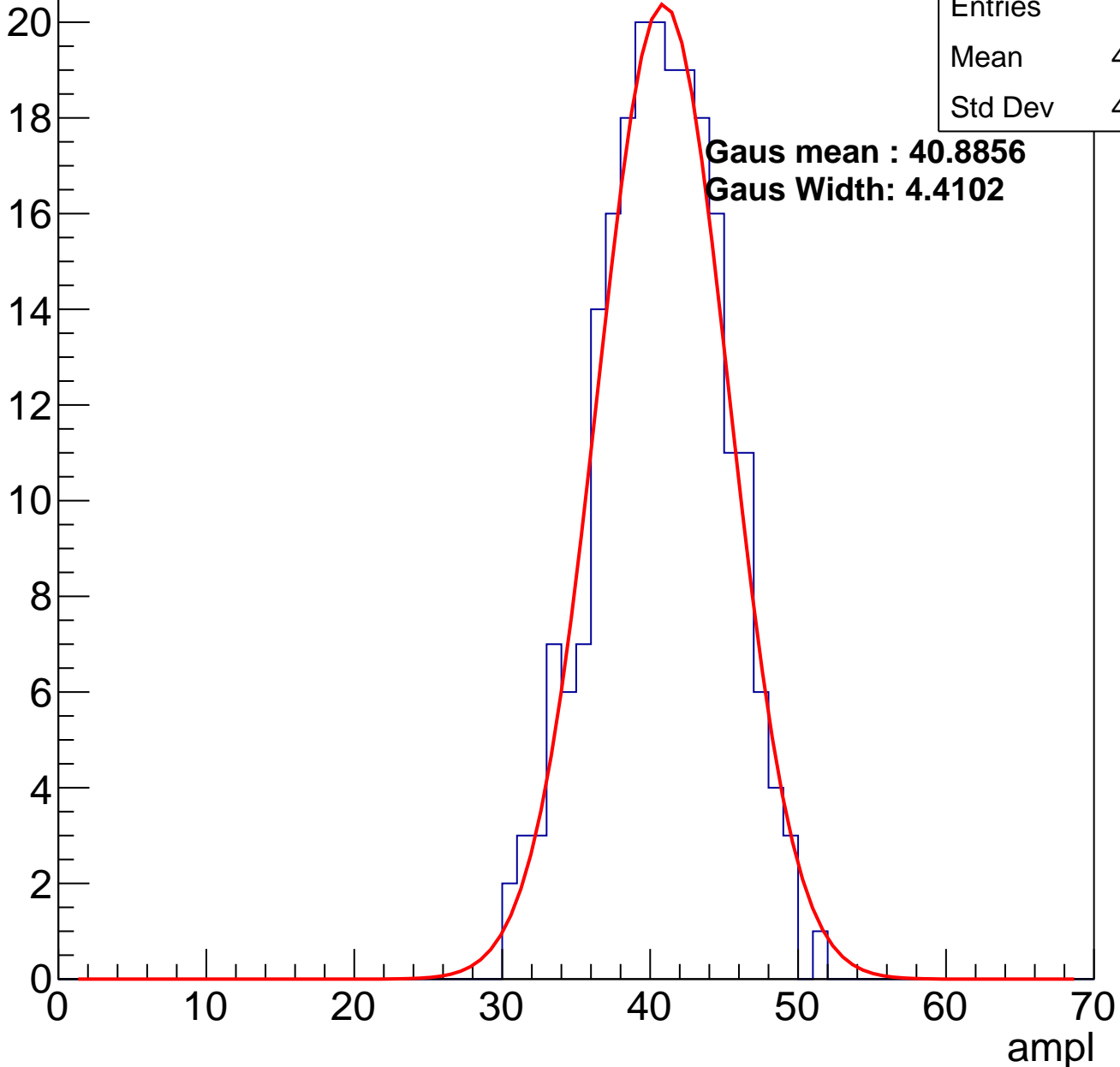
calib_packv5_042523_0143.root, FC#1, port C1

Entries	224
Mean	40.25
Std Dev	4.179

Gaus mean : 40.8856

Gaus Width: 4.4102

Entry



B0L101S, U14-ch71, adc2

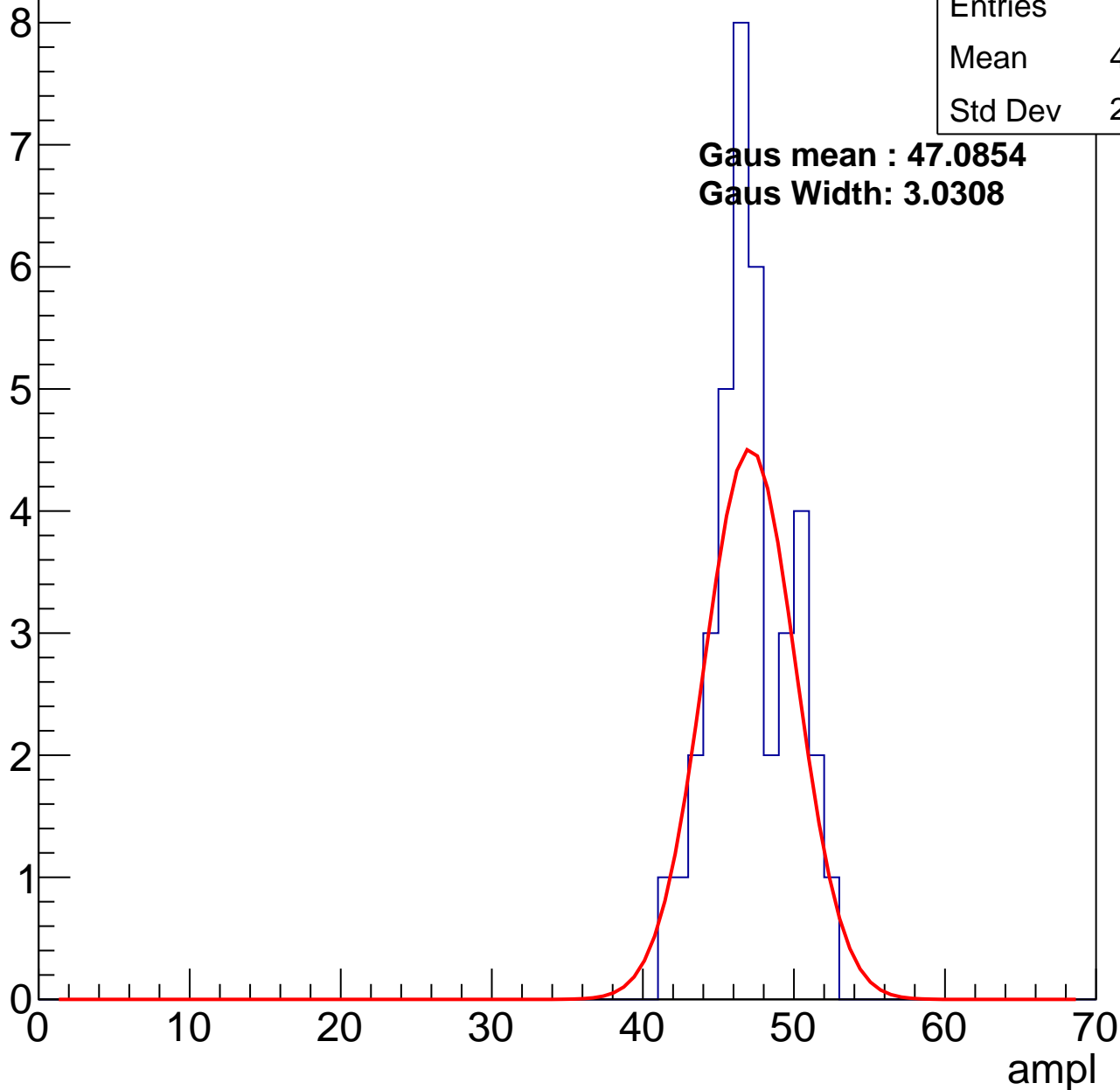
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	38
Mean	46.66
Std Dev	2.568

Gaus mean : 47.0854

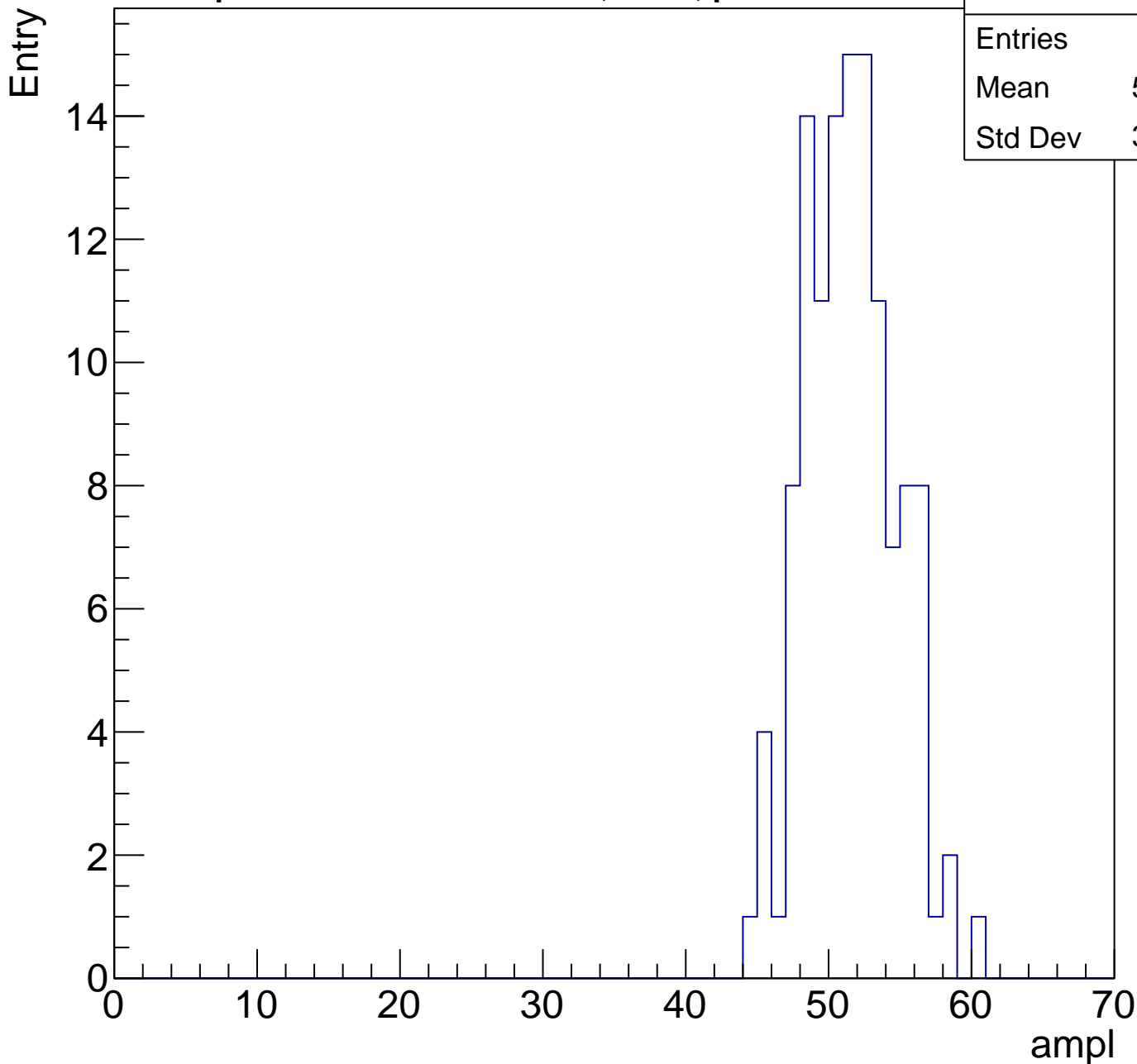
Gaus Width: 3.0308



B0L101S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	51.11
Std Dev	3.141

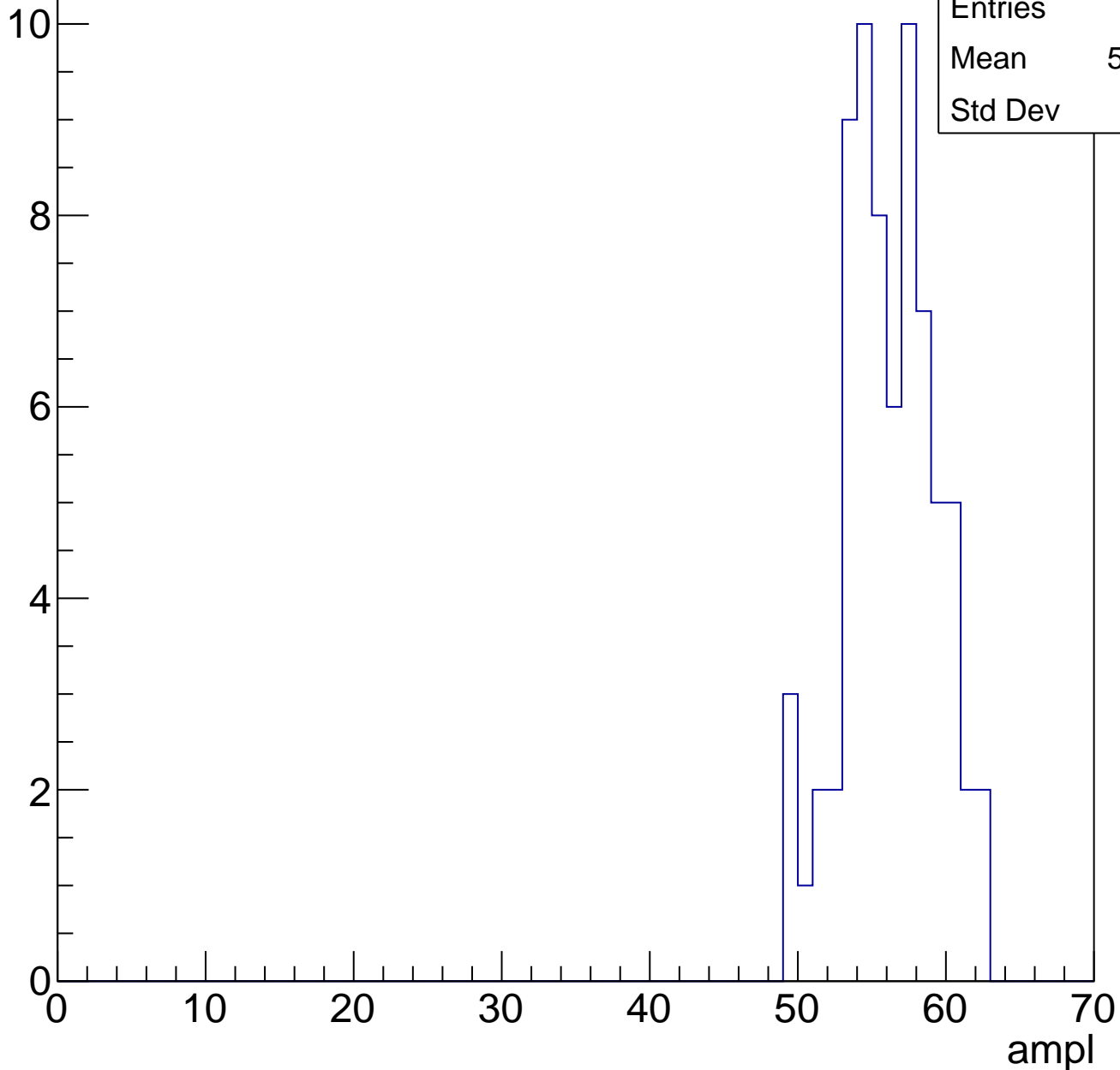


B0L101S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	72
Mean	55.74
Std Dev	3.06

Entry



B0L101S, U14-ch71, adc5

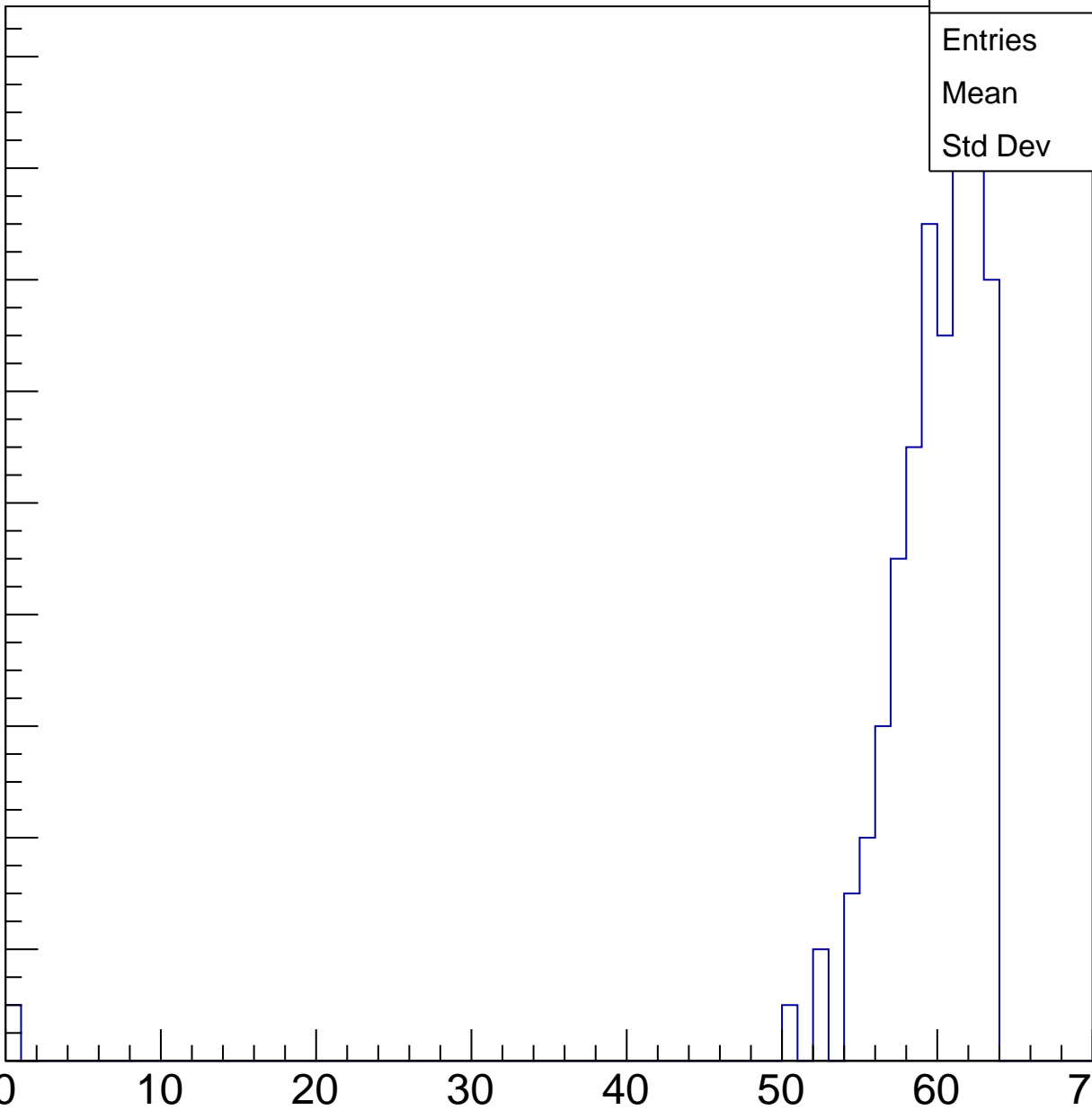
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	115
Mean	58.99
Std Dev	6.165

ampl

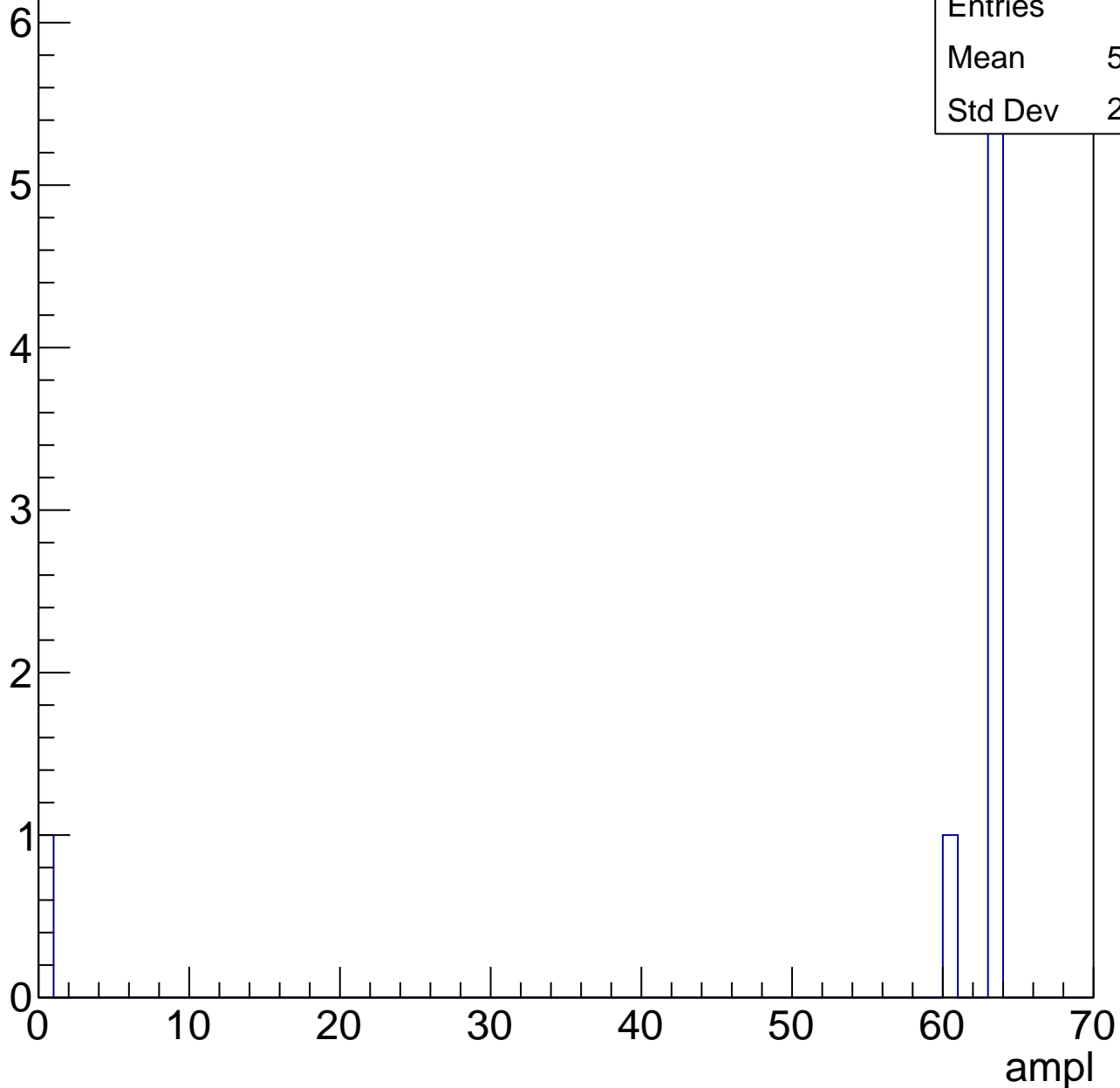


B0L101S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	8
Mean	54.75
Std Dev	20.72



B0L101S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

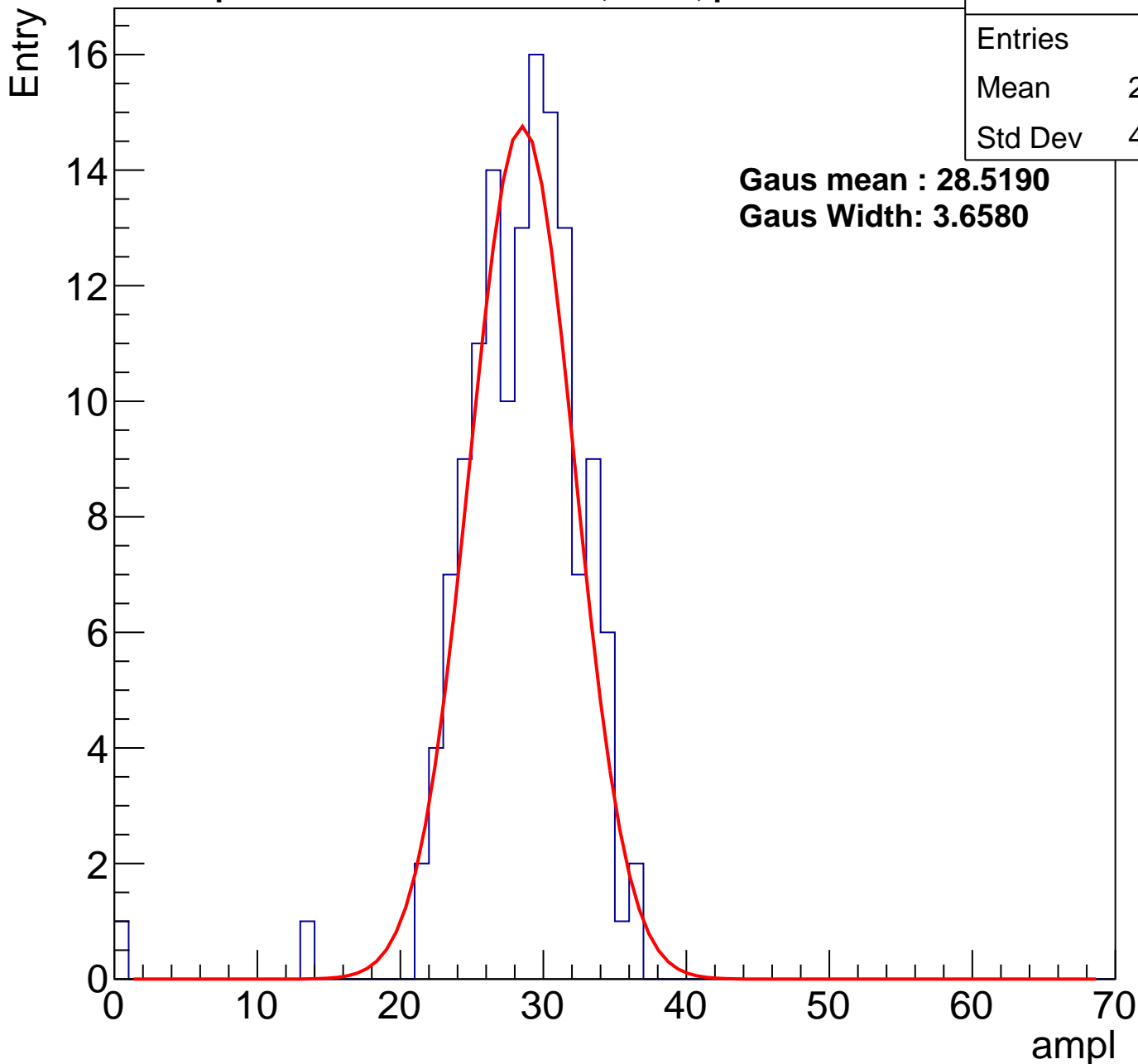
B0L101S, U14-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.96
Std Dev	4.342

Gaus mean : 28.5190

Gaus Width: 3.6580



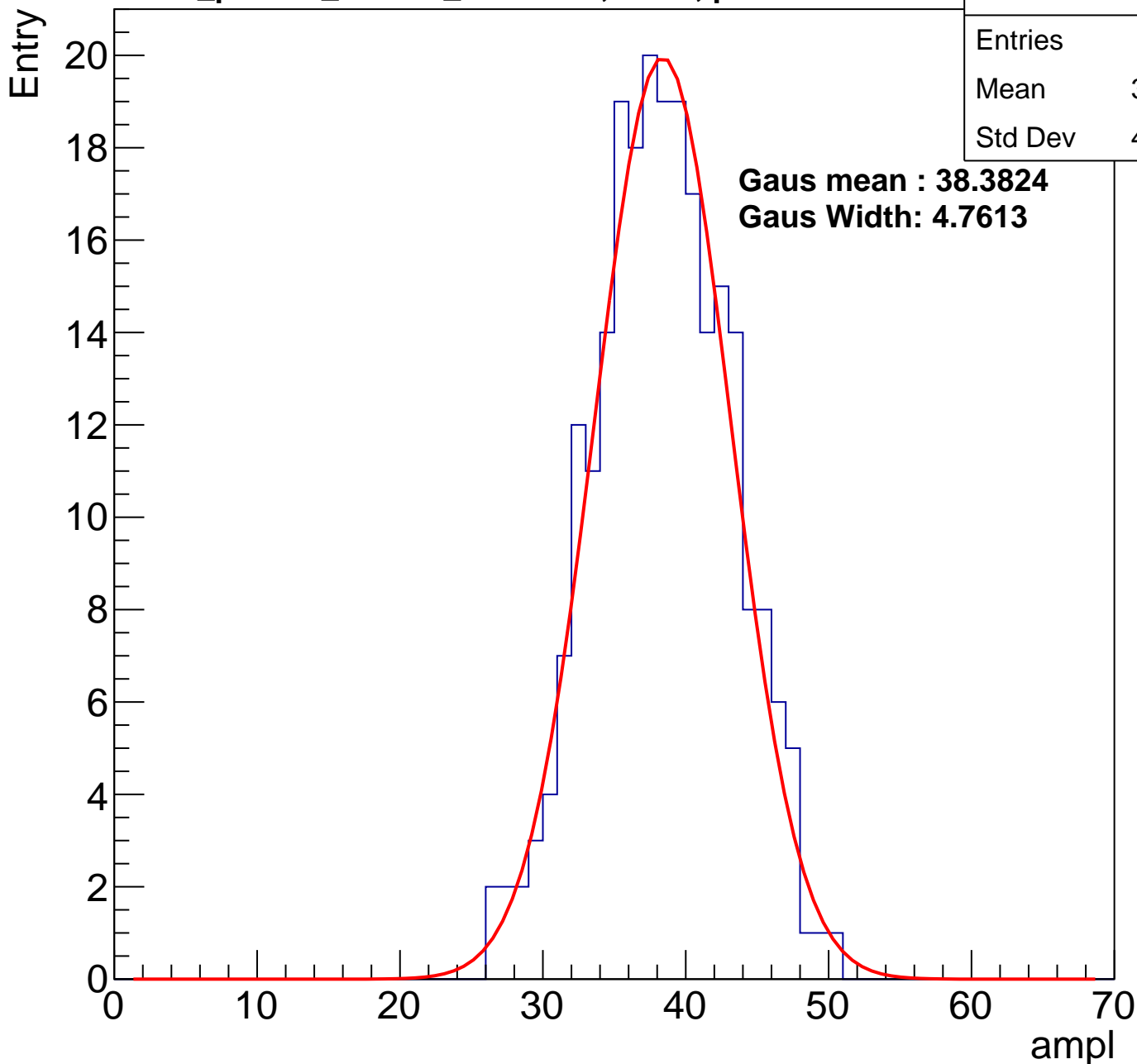
B0L101S, U14-ch72, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	242
Mean	37.94
Std Dev	4.725

Gaus mean : 38.3824

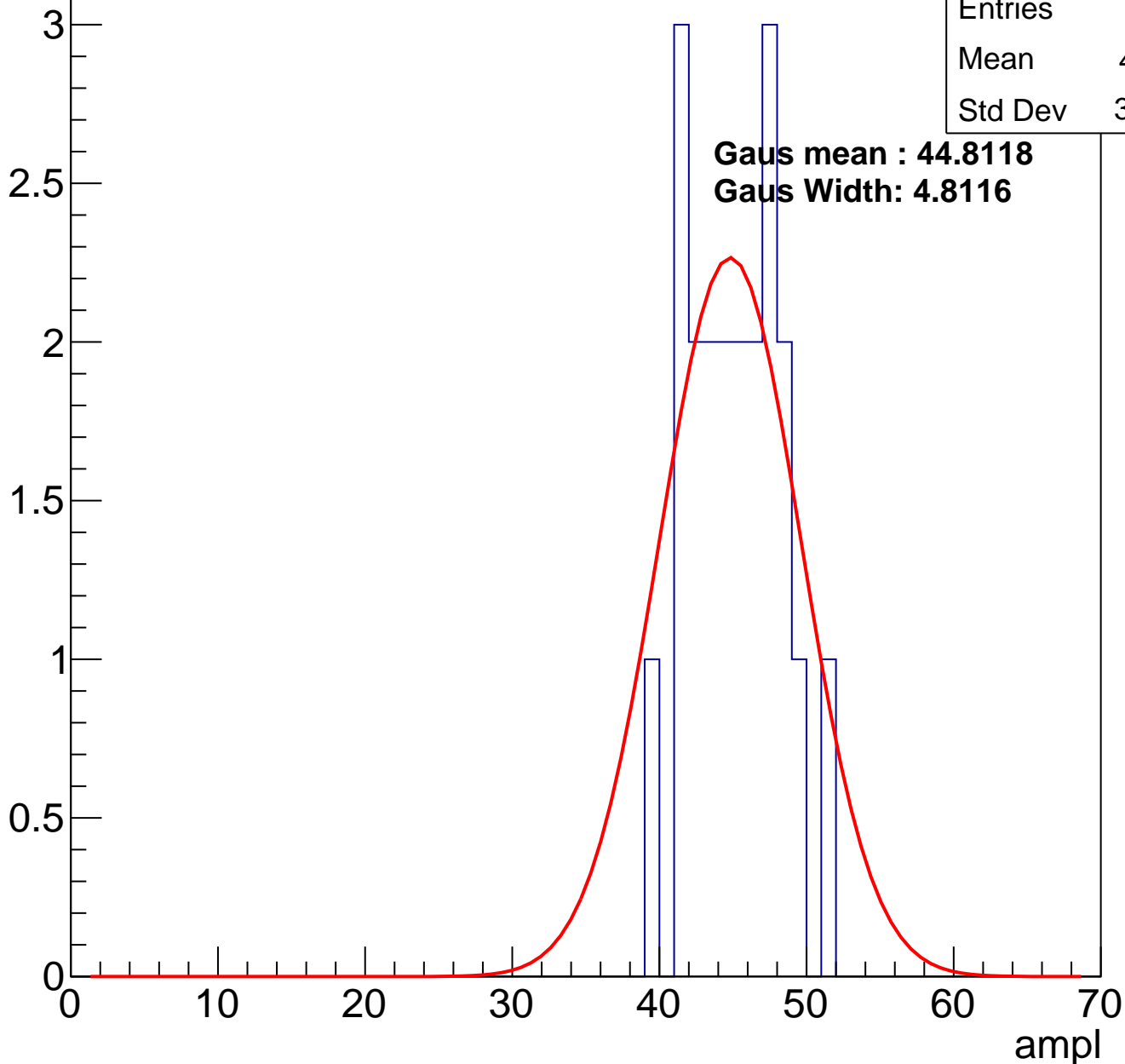
Gaus Width: 4.7613



B0L101S, U14-ch72, adc2

calib_packv5_042523_0143.root, FC#1, port C1

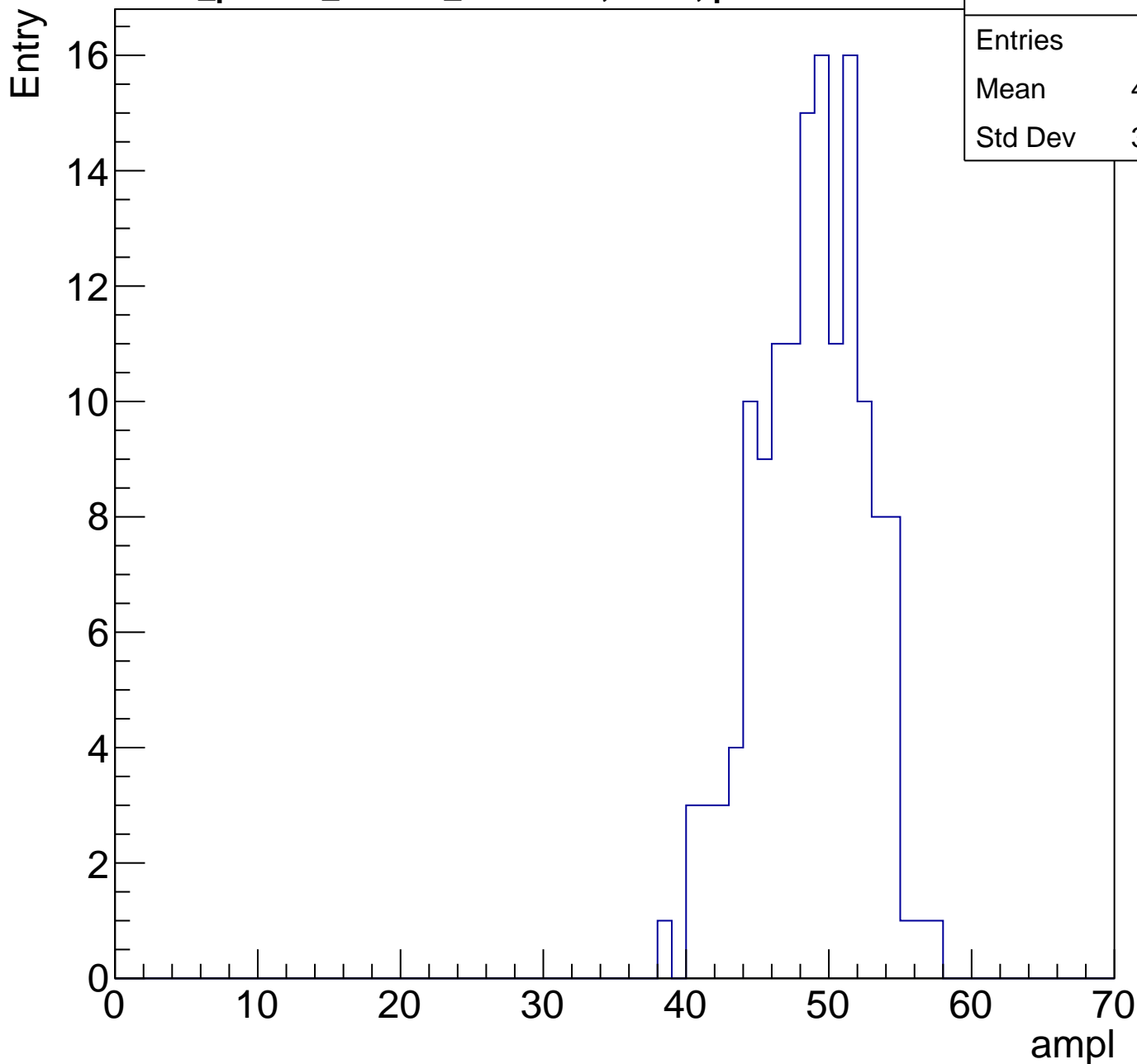
Entry



B0L101S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

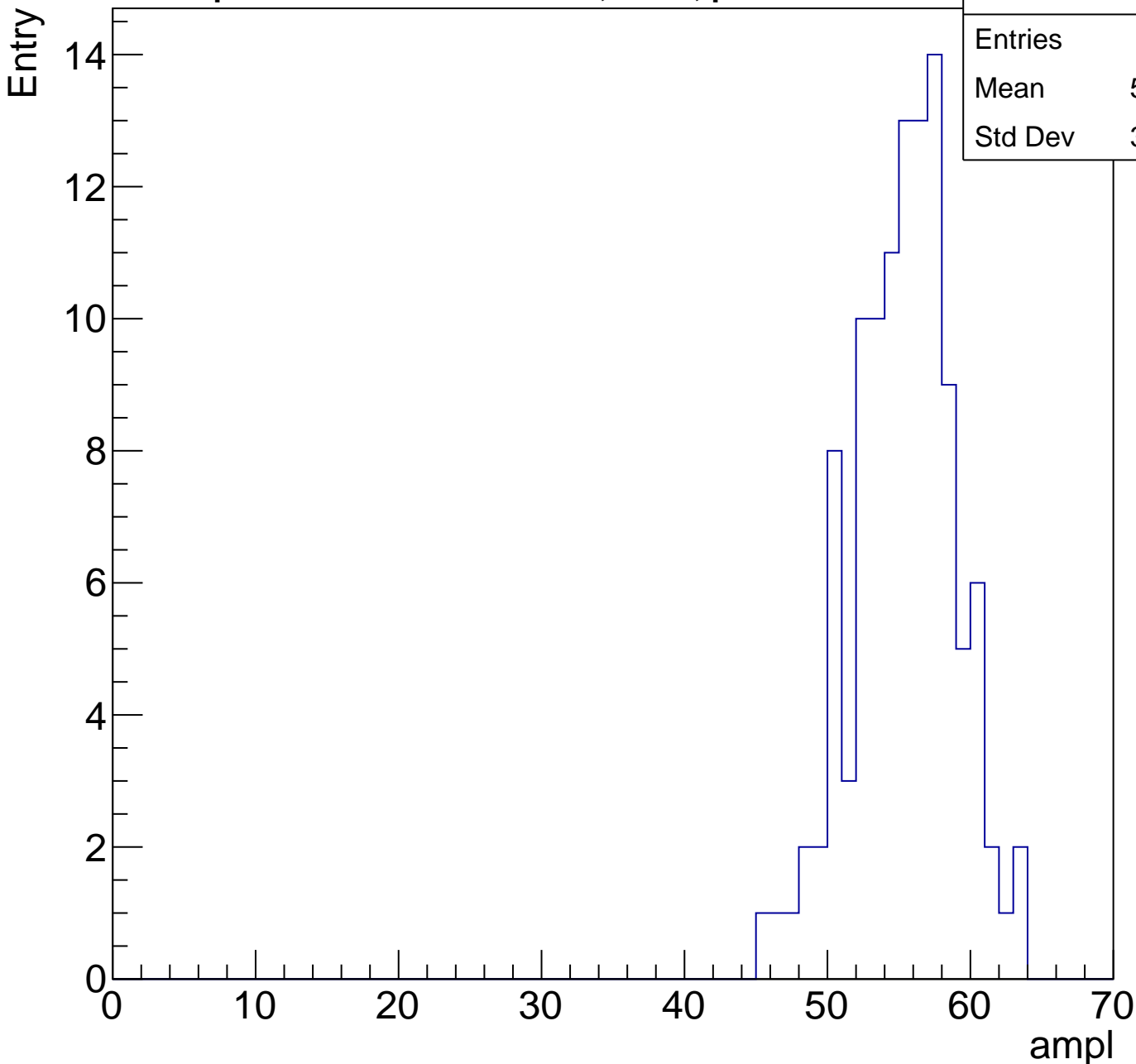
Entries	142
Mean	48.32
Std Dev	3.729



B0L101S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

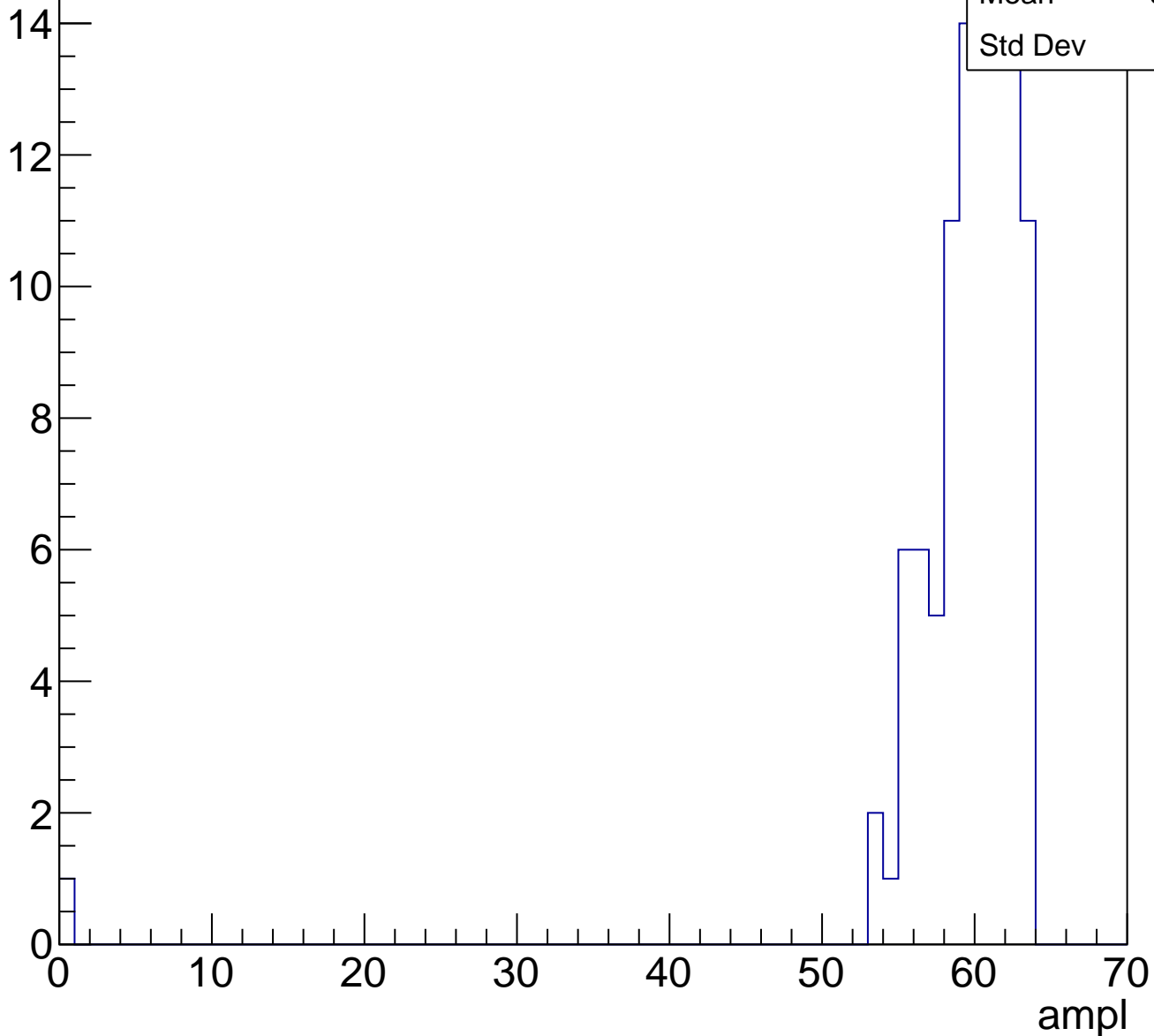
Entries	114
Mean	54.89
Std Dev	3.544



B0L101S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

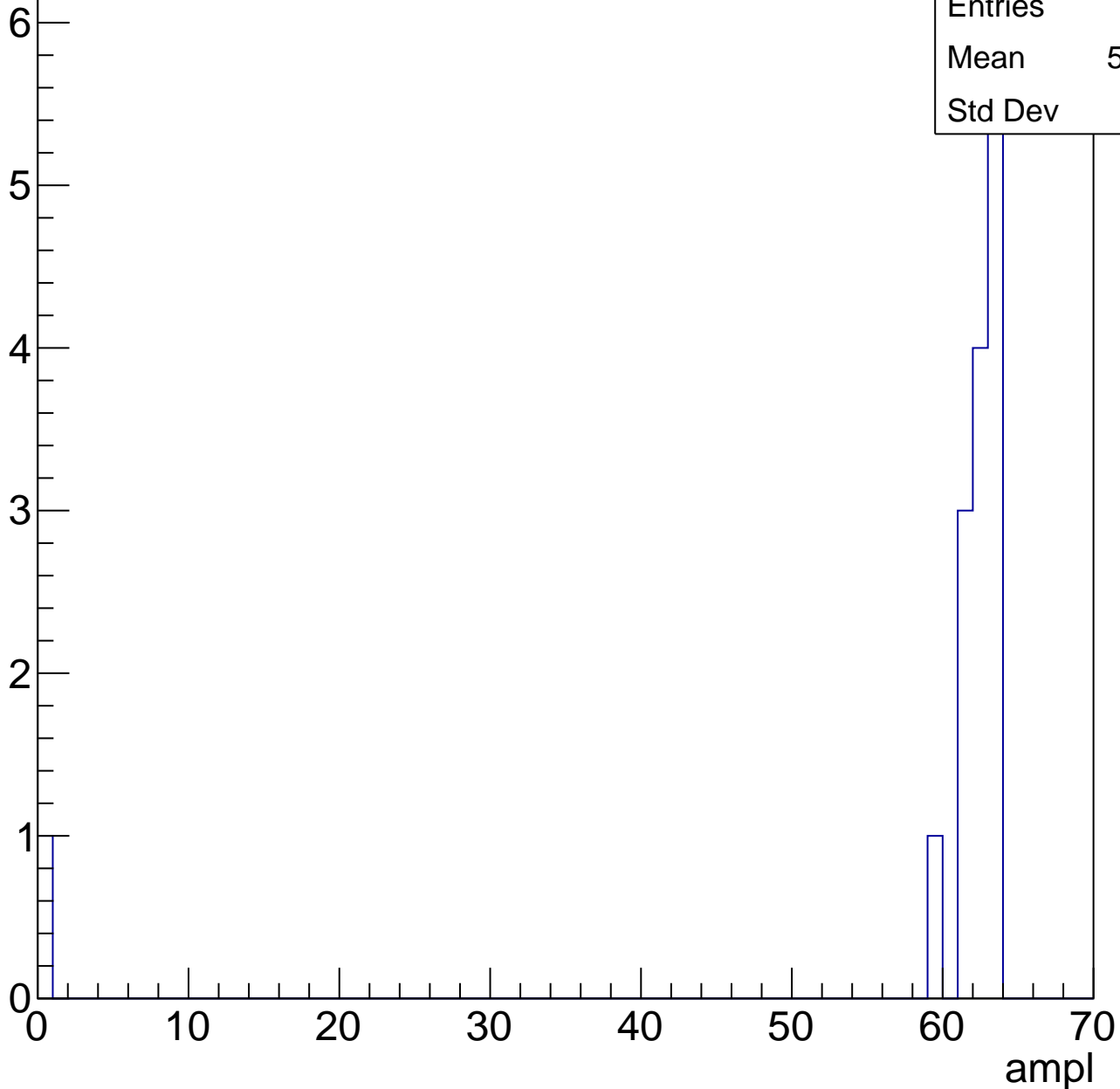


B0L101S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.87
Std Dev	15.5



B0L101S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

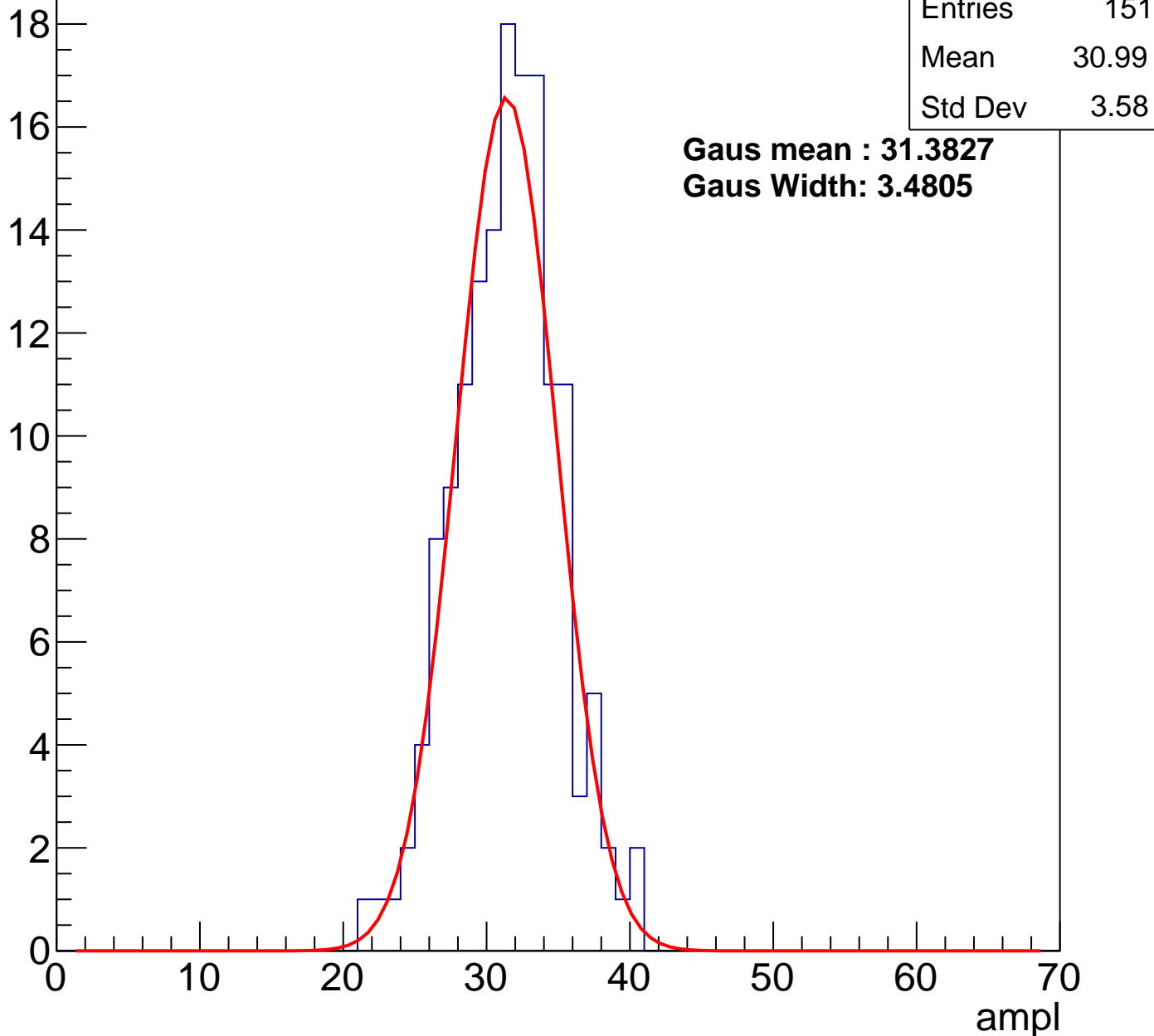


Entries	4
Mean	31.25
Std Dev	31.25

B0L101S, U14-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



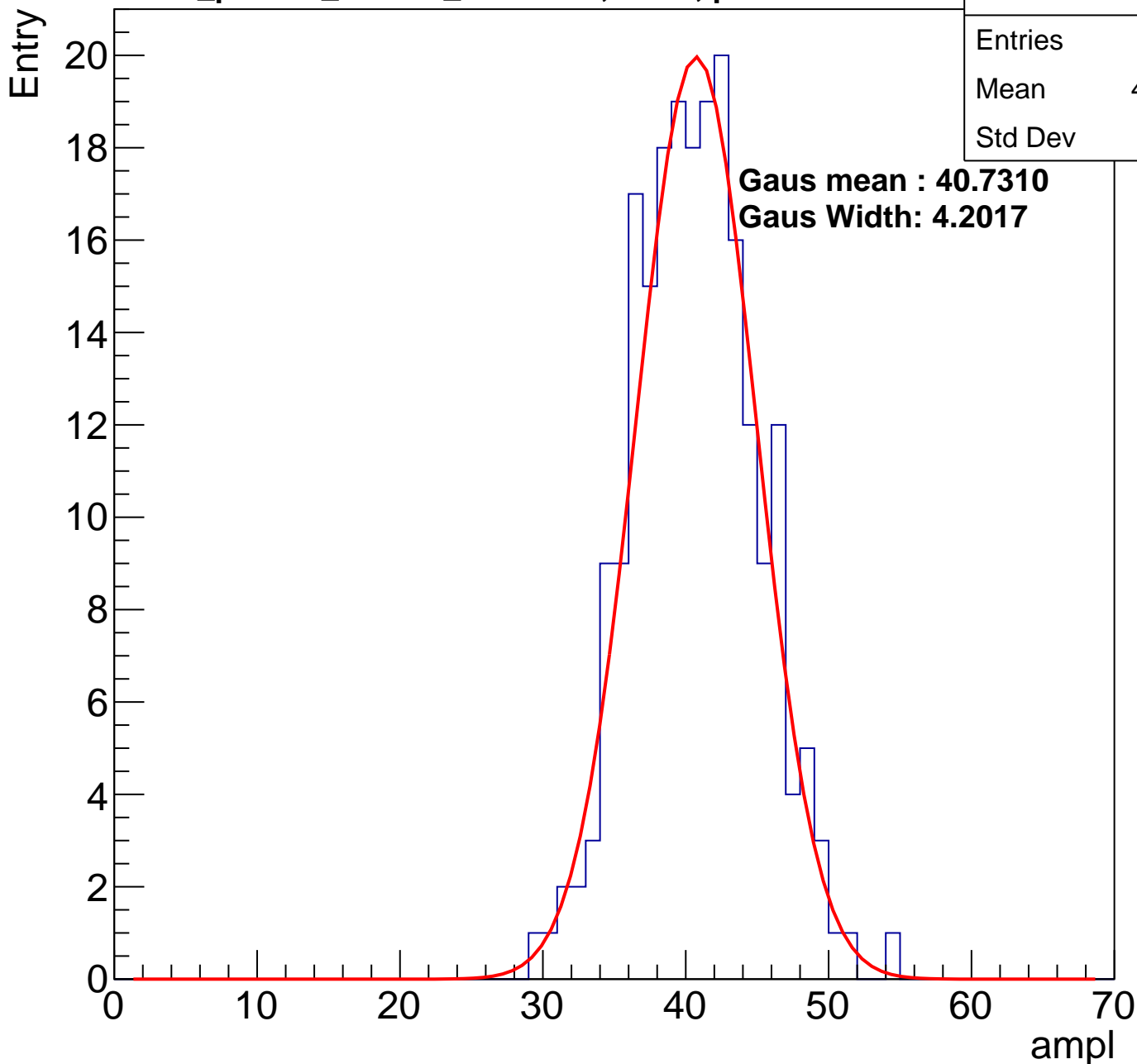
B0L101S, U14-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	217
Mean	40.27
Std Dev	4.27

Gaus mean : 40.7310

Gaus Width: 4.2017



B0L101S, U14-ch73, adc2

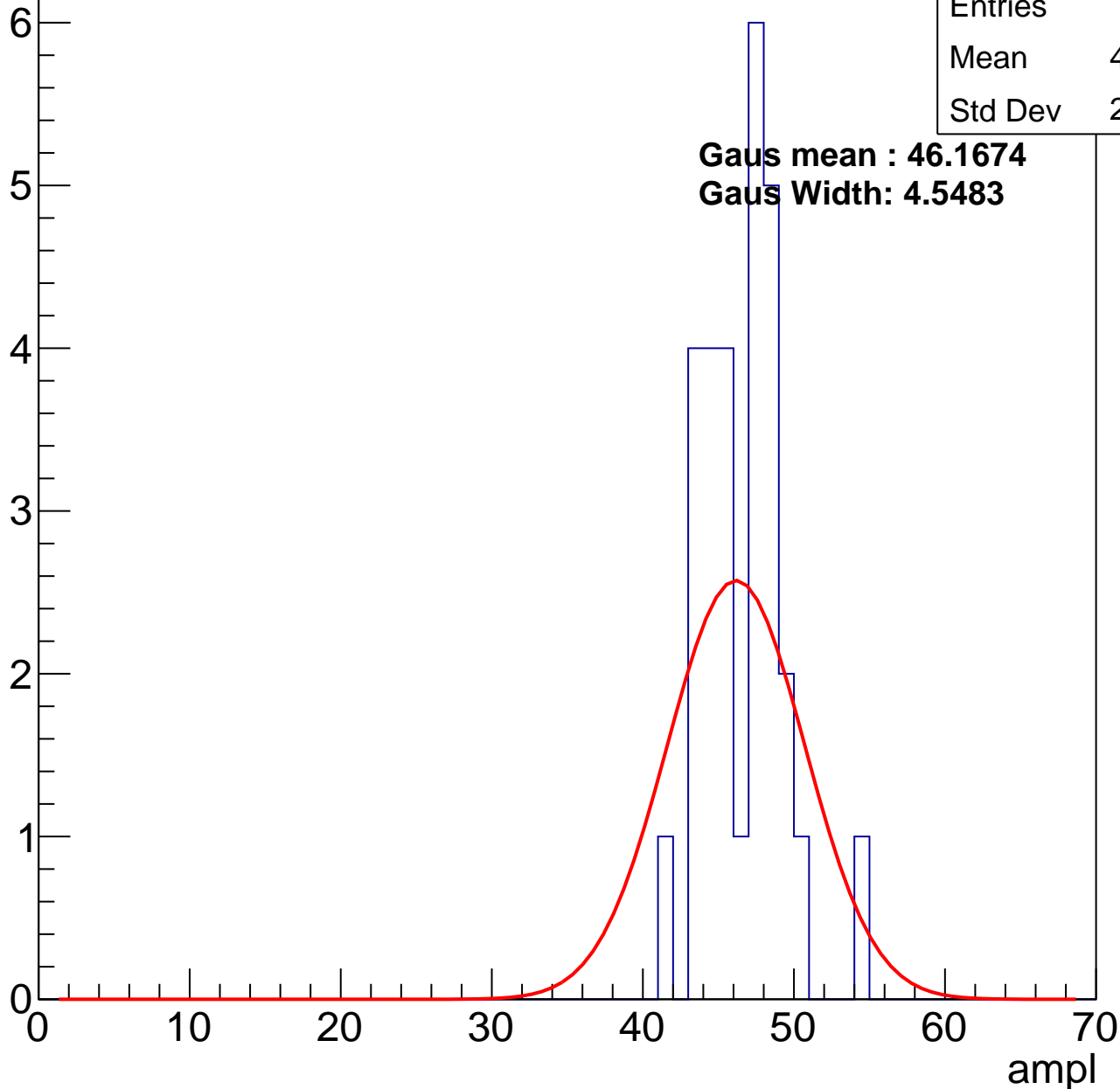
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	29
Mean	46.17
Std Dev	2.653

Gaus mean : 46.1674

Gaus Width: 4.5483

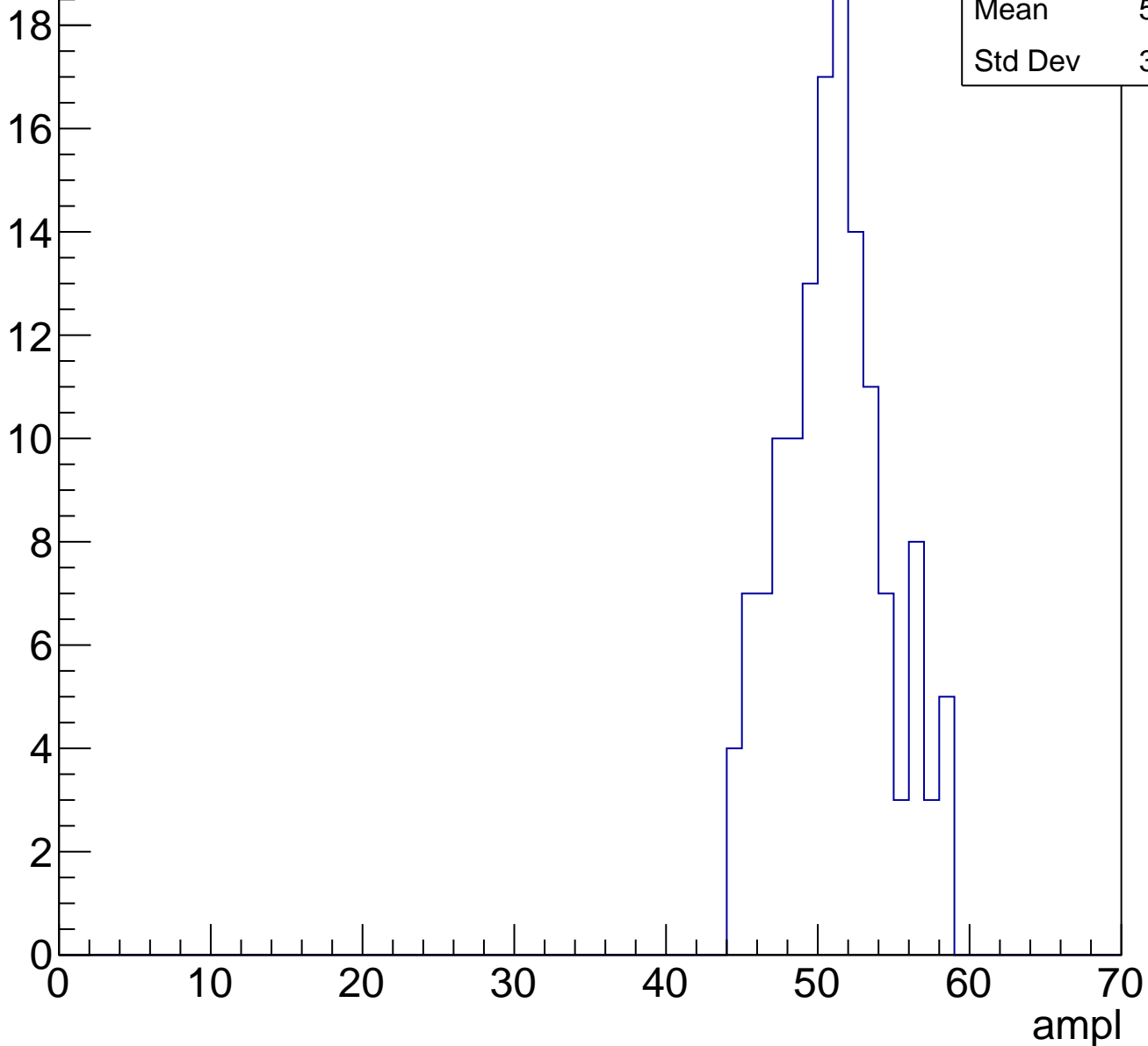


B0L101S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	50.59
Std Dev	3.449

Entry



B0L101S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

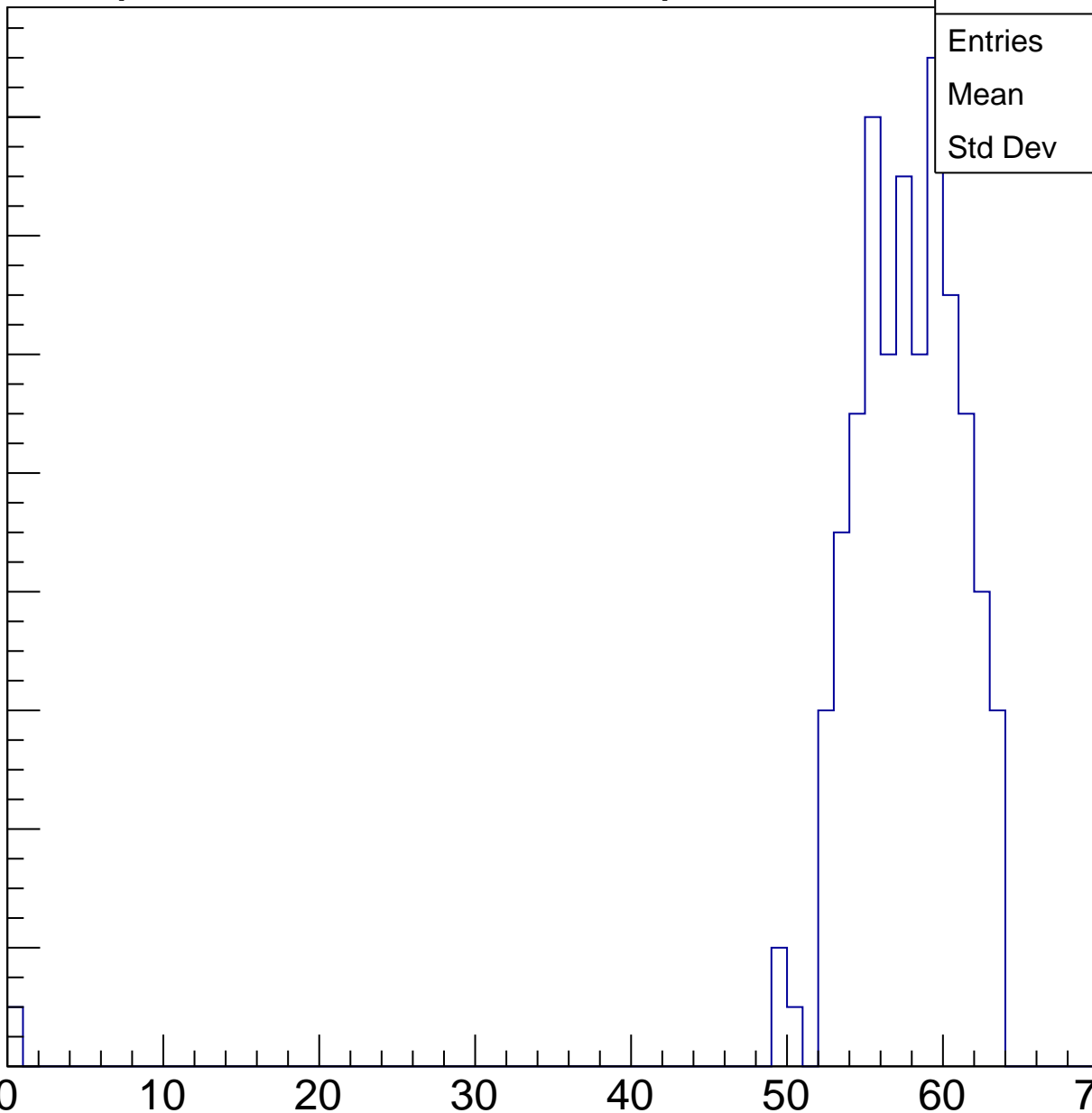
Entries	140
Mean	56.87
Std Dev	5.783

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	50
Mean	59.8
Std Dev	8.748

ampl

0

10

20

30

40

50

60

70

B0L101S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch74, adc0

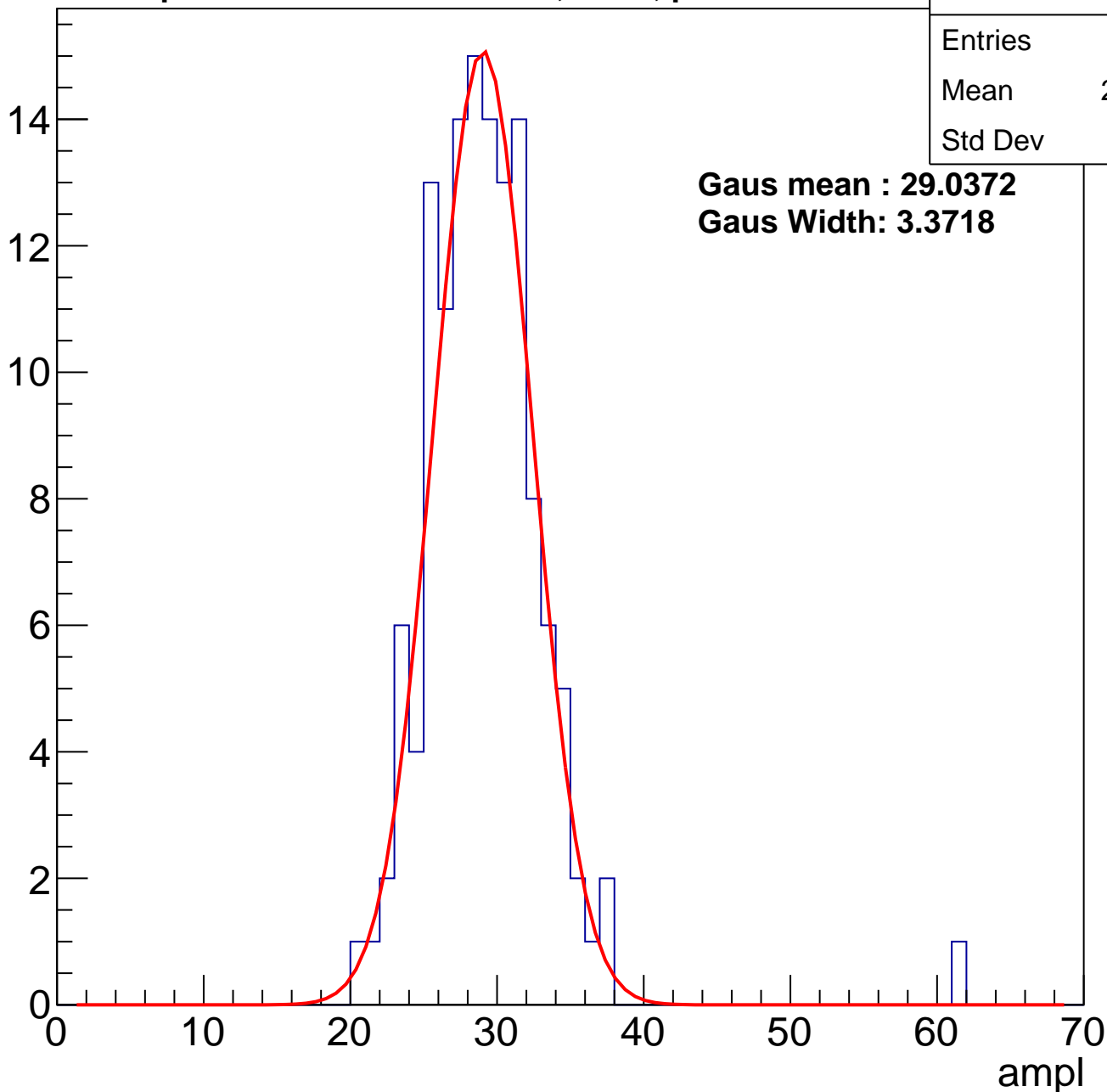
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	28.74
Std Dev	4.39

Gaus mean : 29.0372

Gaus Width: 3.3718

Entry



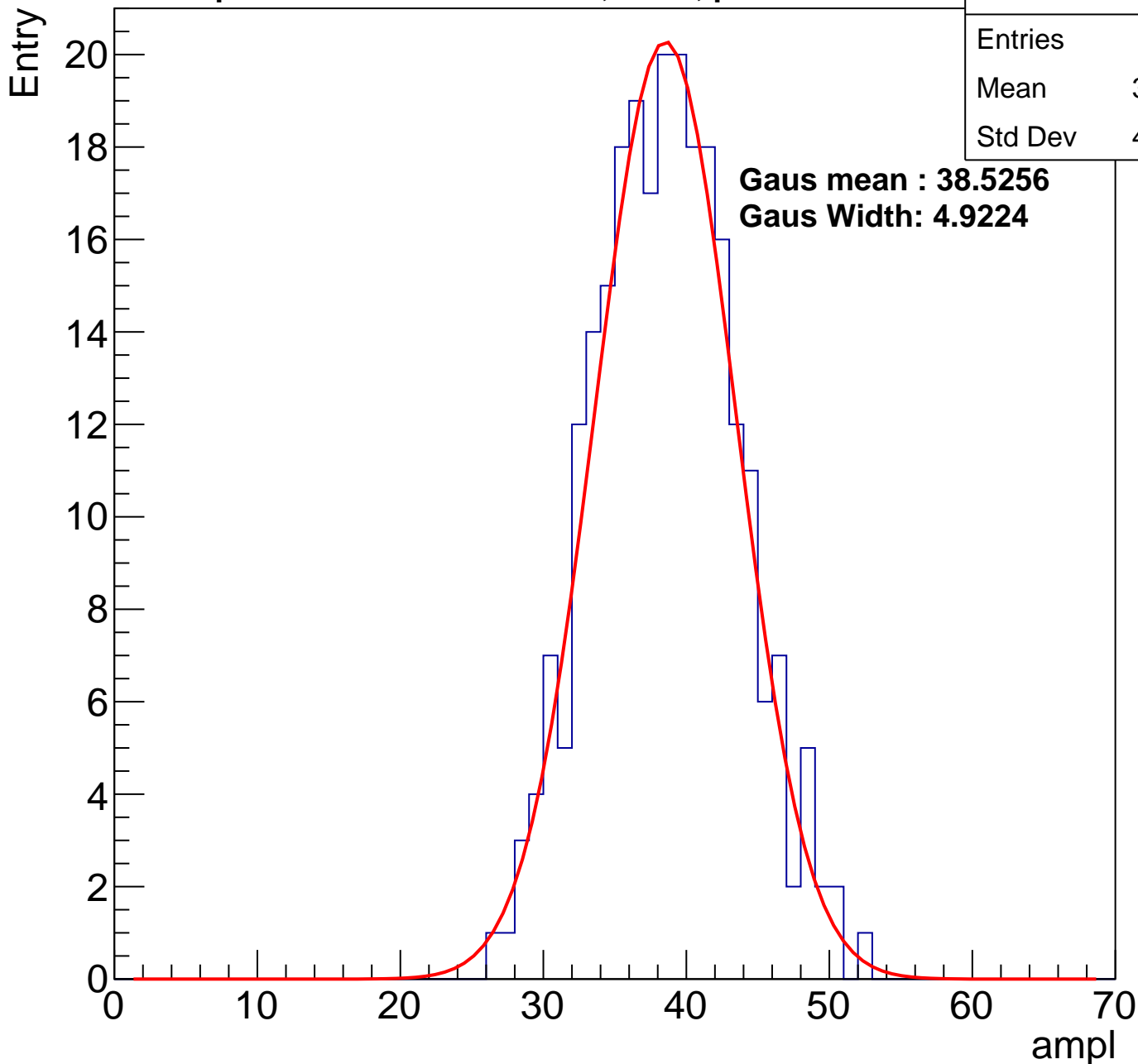
B0L101S, U14-ch74, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	256
Mean	38.14
Std Dev	4.917

Gaus mean : 38.5256

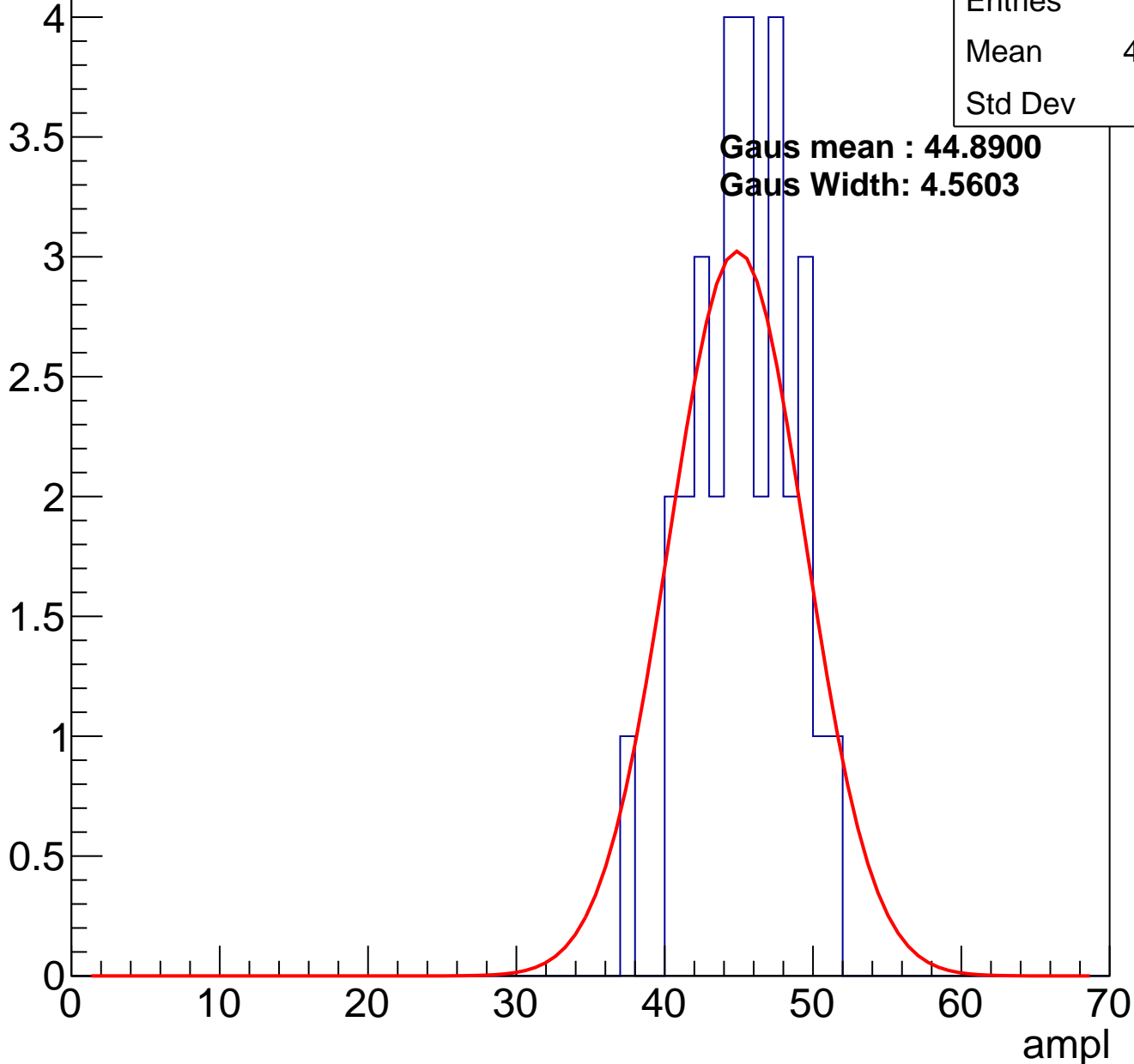
Gaus Width: 4.9224



B0L101S, U14-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

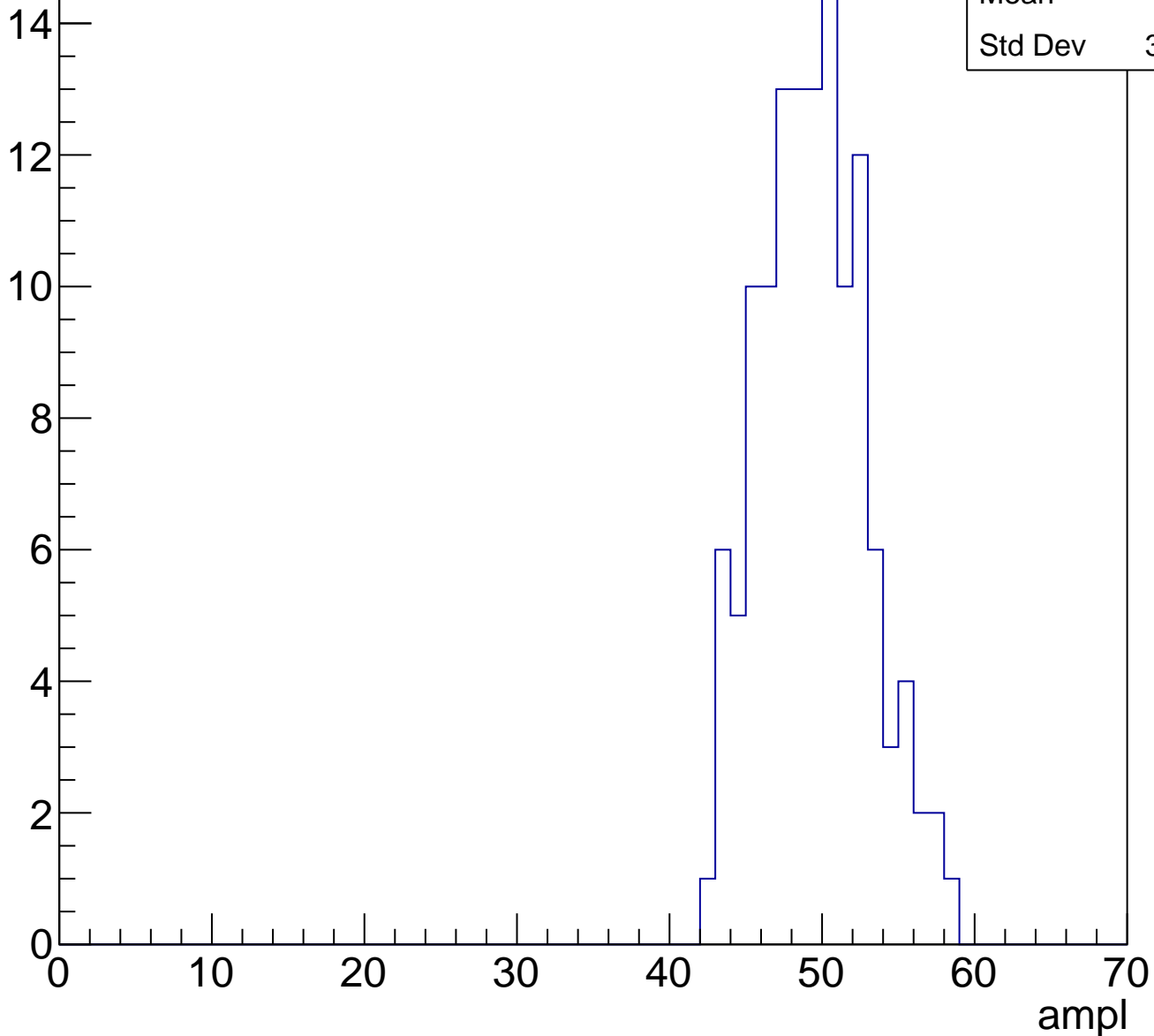


B0L101S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	48.97
Std Dev	3.439

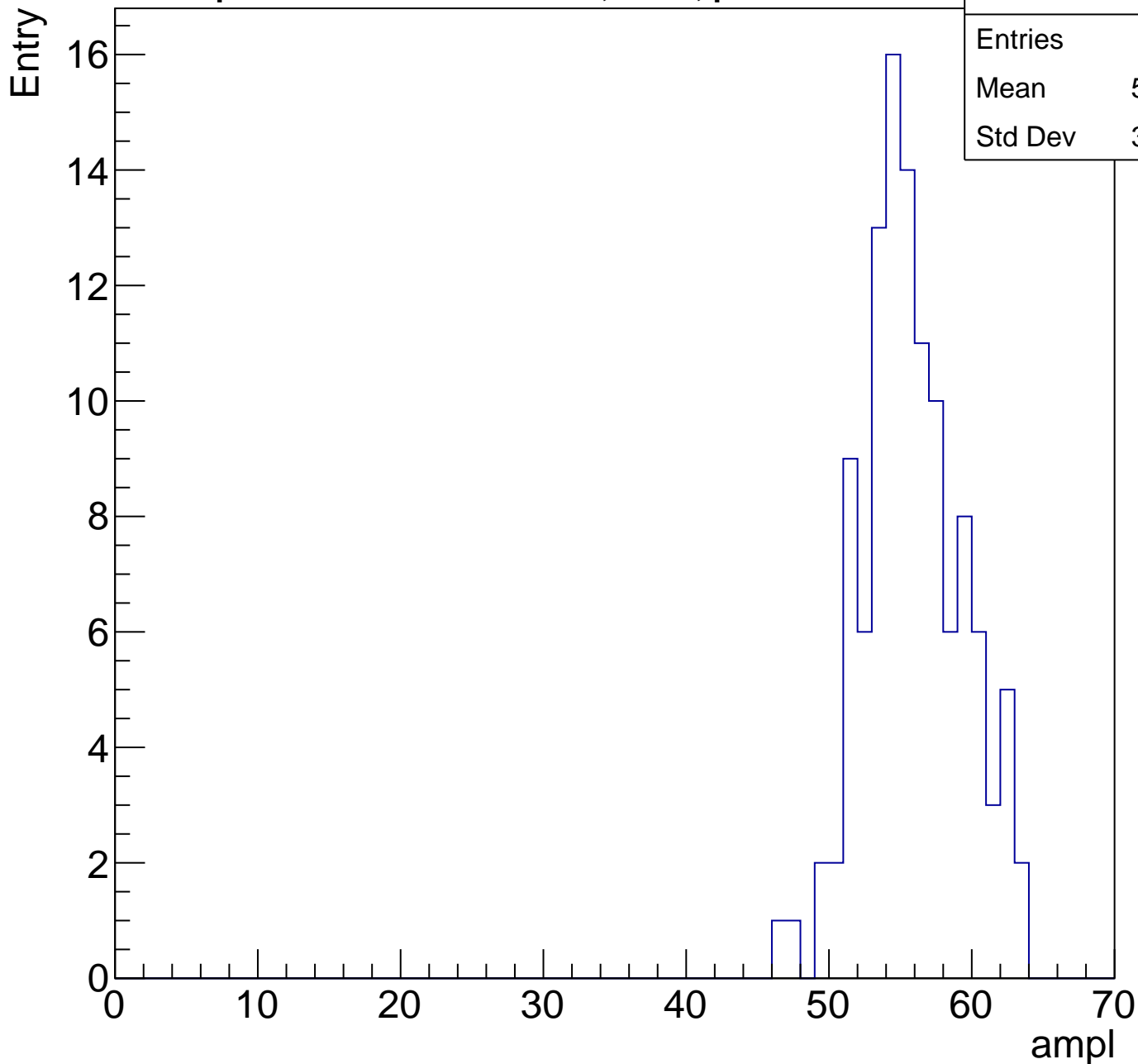
Entry



B0L101S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	55.39
Std Dev	3.463



B0L101S, U14-ch74, adc5

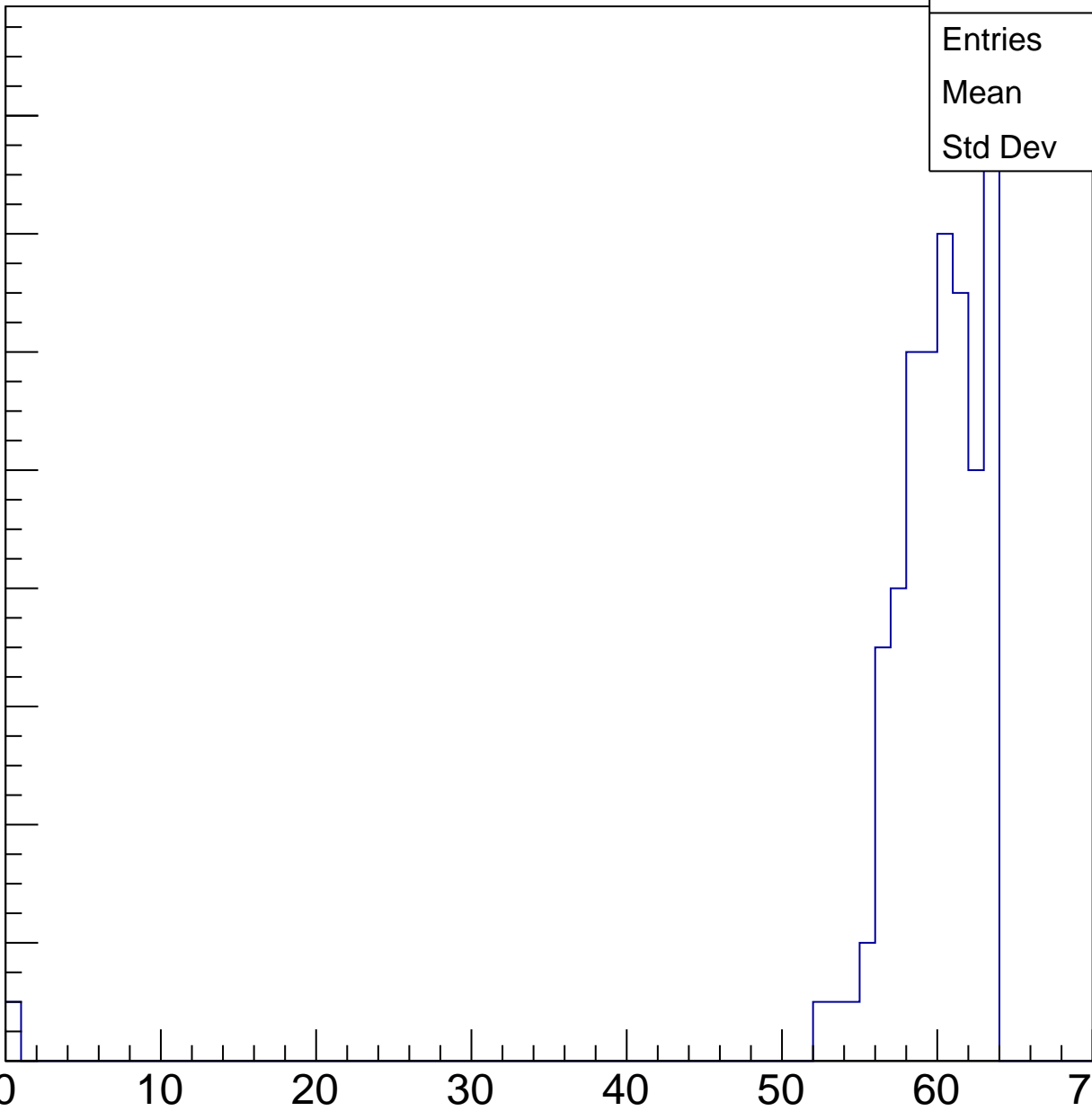
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.04
Std Dev	6.481

ampl

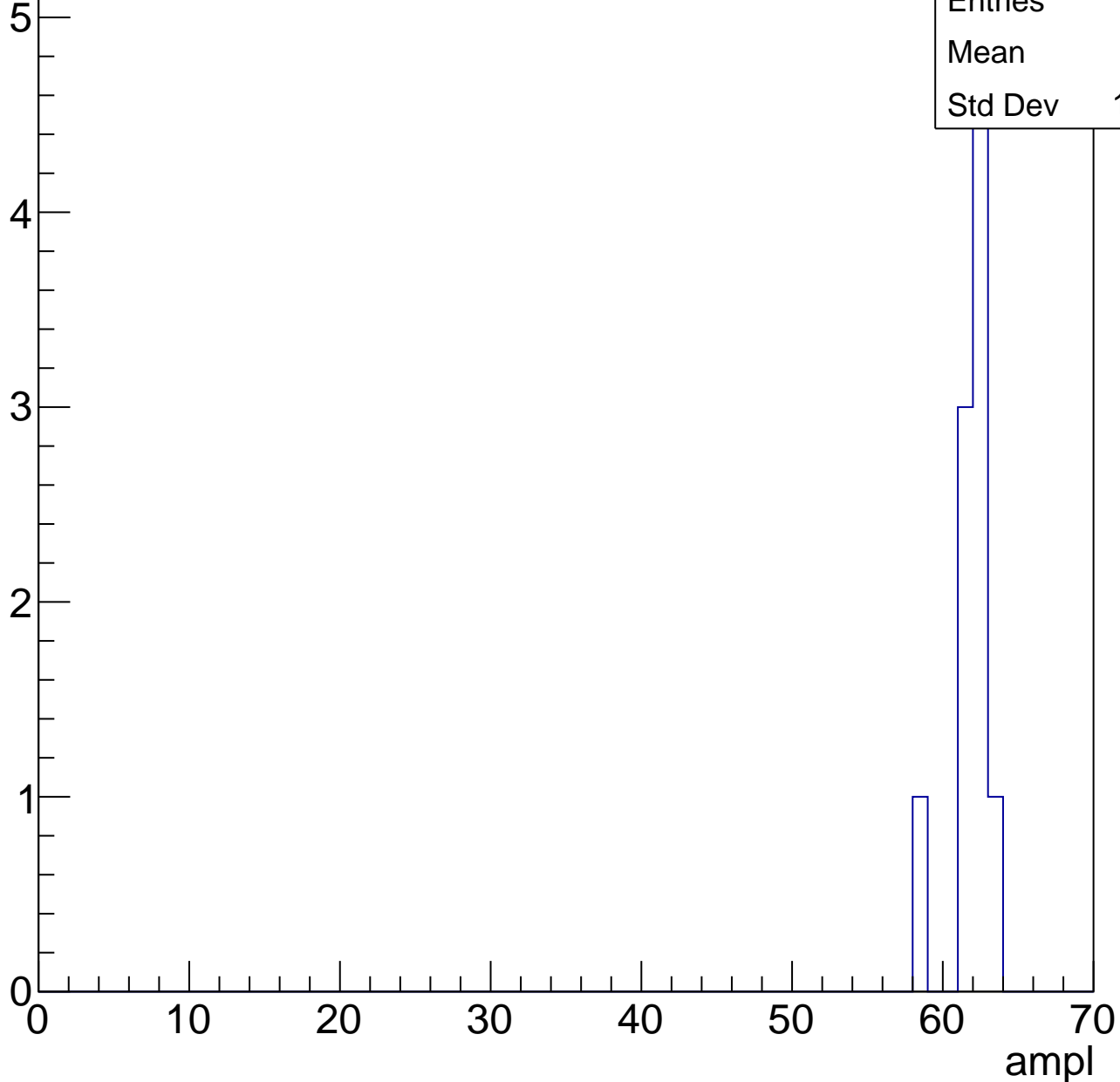


B0L101S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

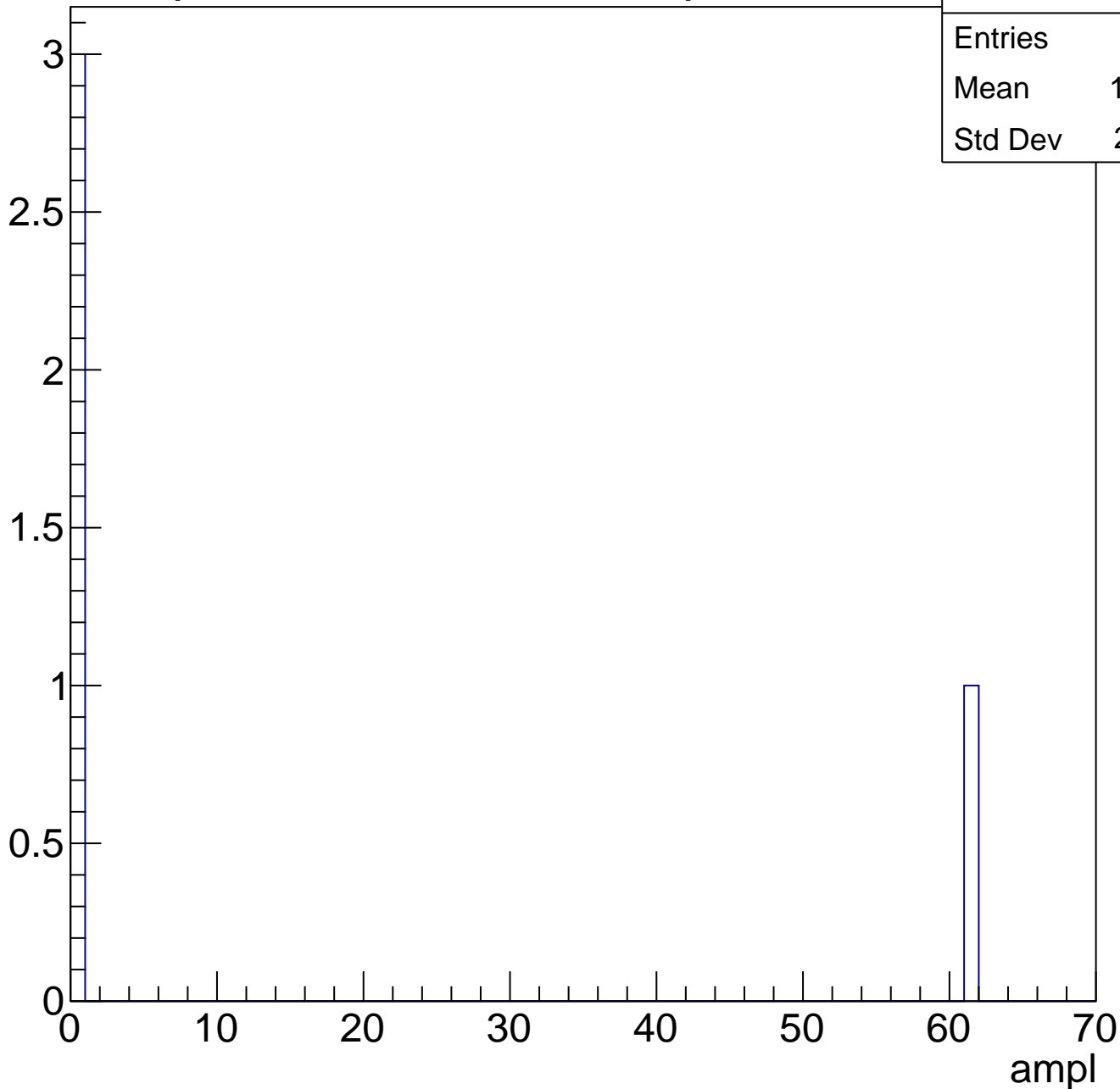
Entries	10
Mean	61.4
Std Dev	1.281



B0L101S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

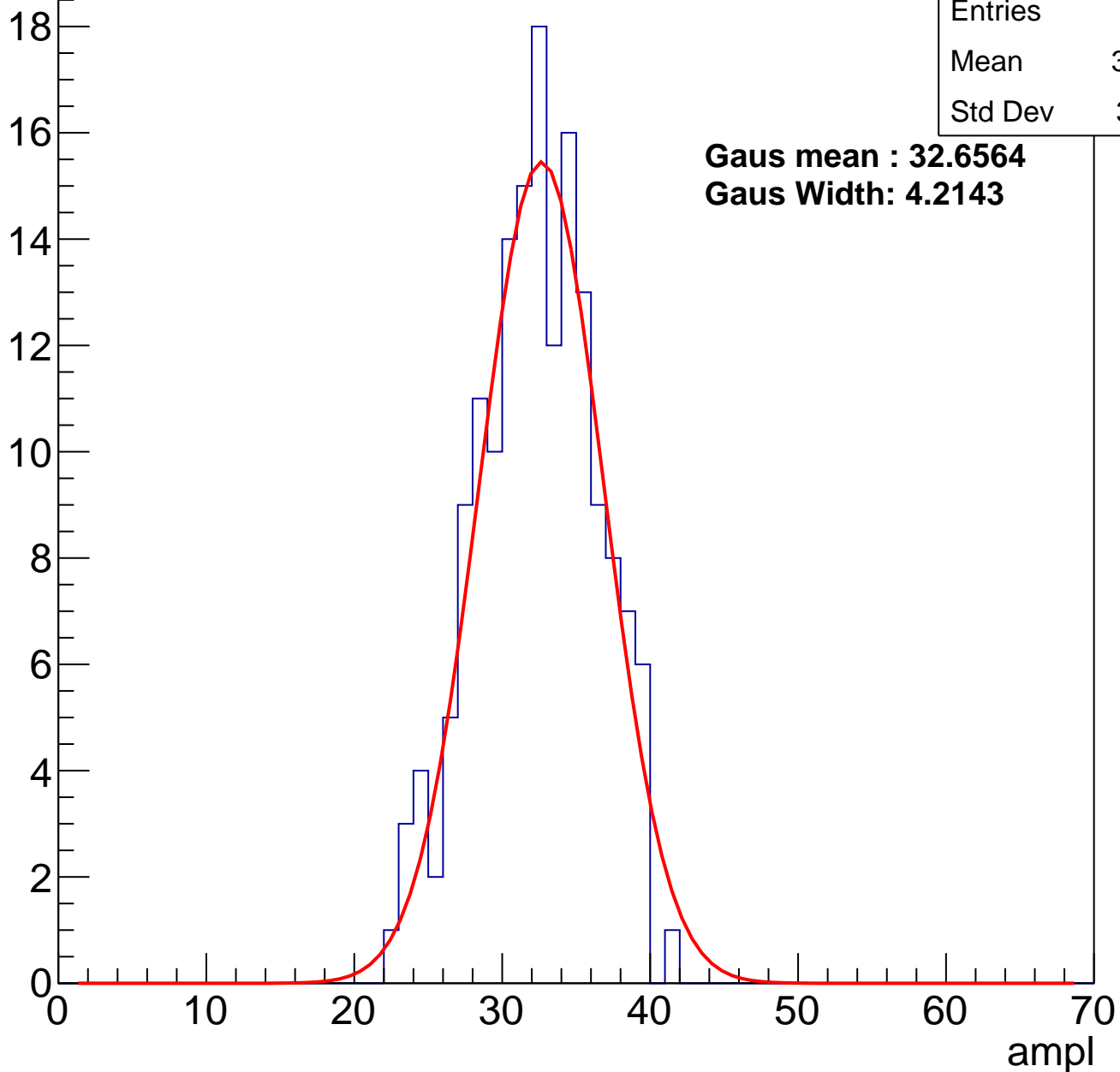
Entry



B0L101S, U14-ch75, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch75, adc1

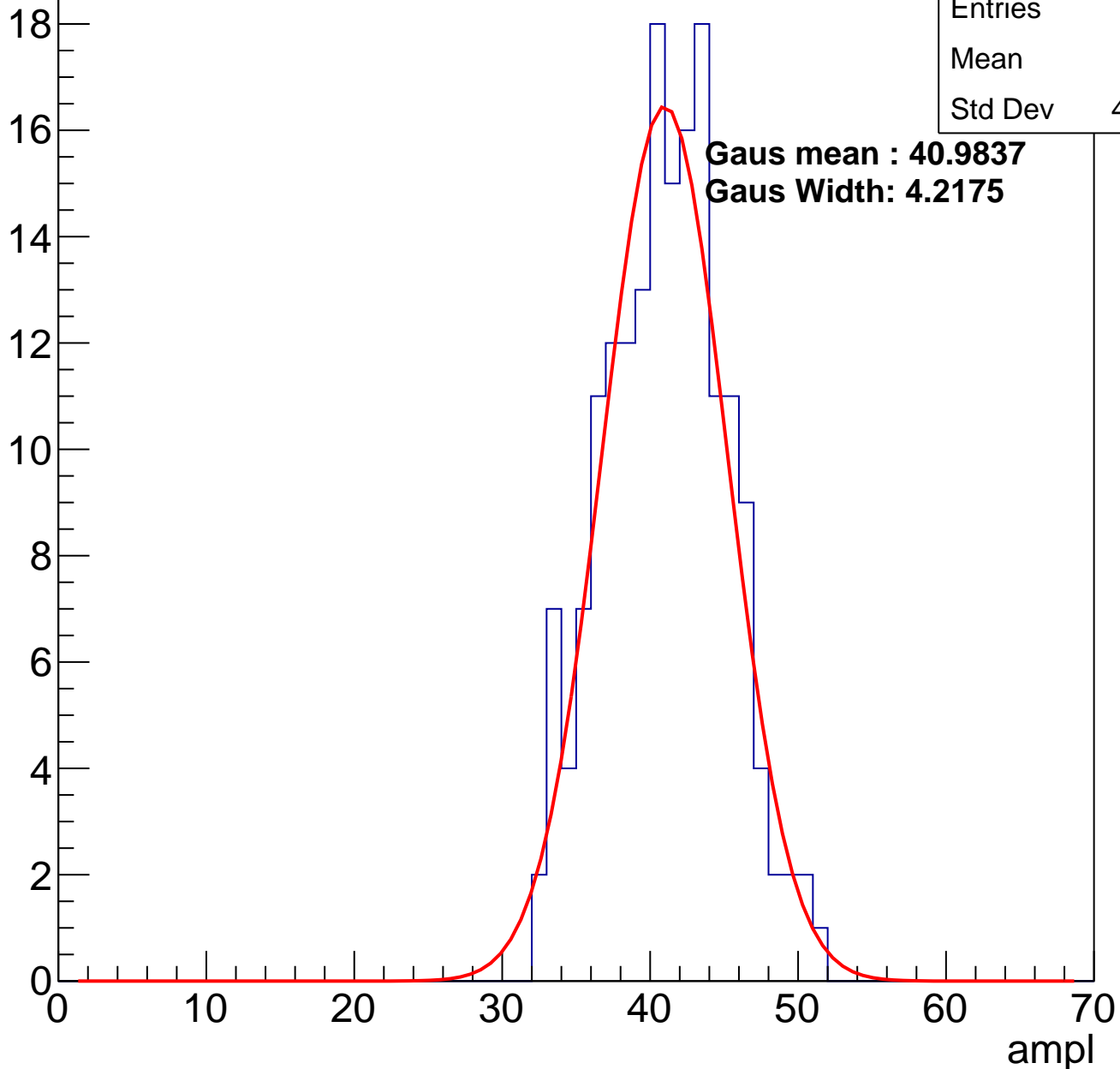
calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	40.6
Std Dev	4.044

Gaus mean : 40.9837

Gaus Width: 4.2175

Entry



B0L101S, U14-ch75, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	76
Mean	46.26
Std Dev	3.13

Gaus mean : 47.2563

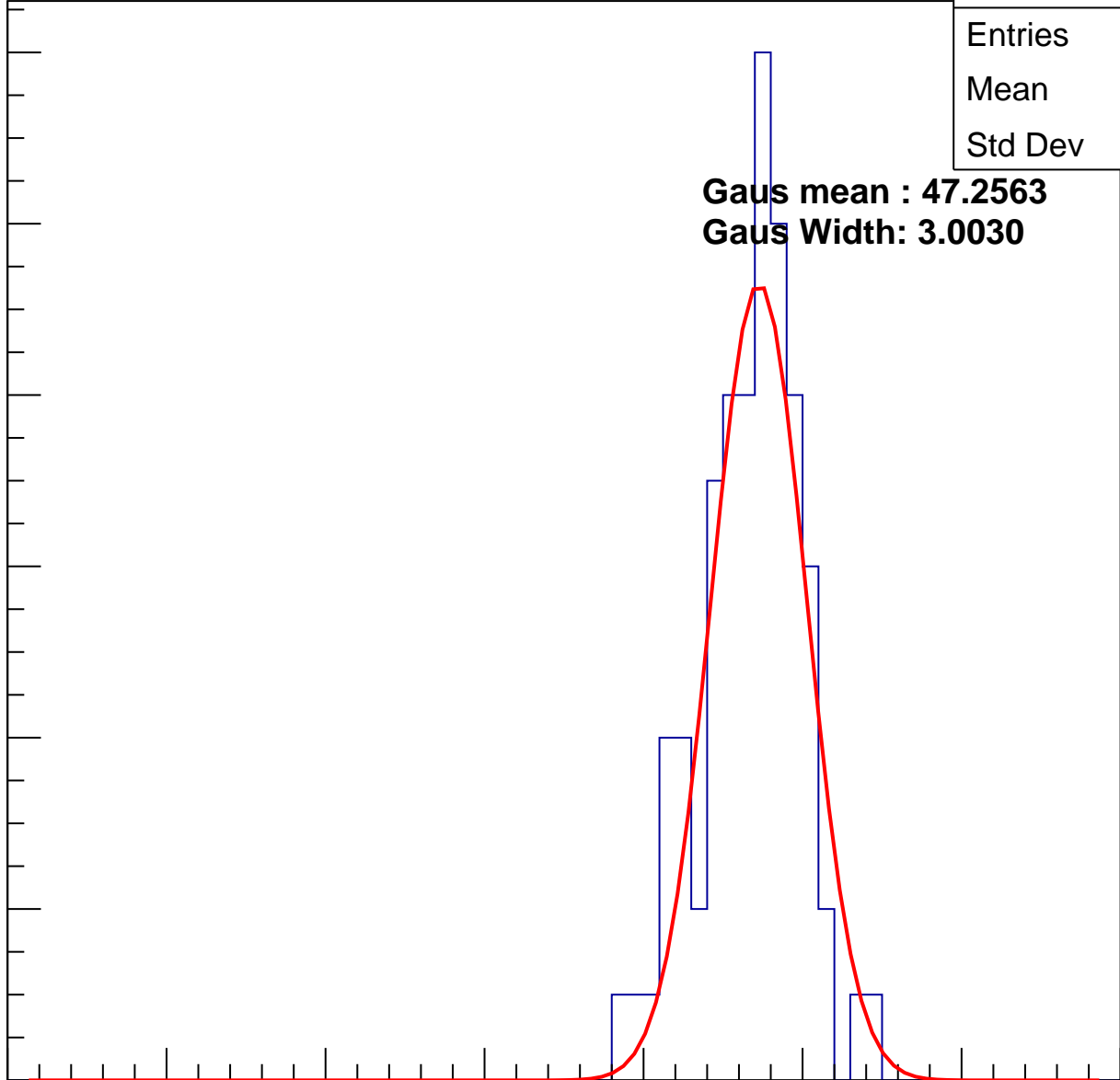
Gaus Width: 3.0030

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	51.03
Std Dev	3.443

Entry

10

8

6

4

2

0

0

10

20

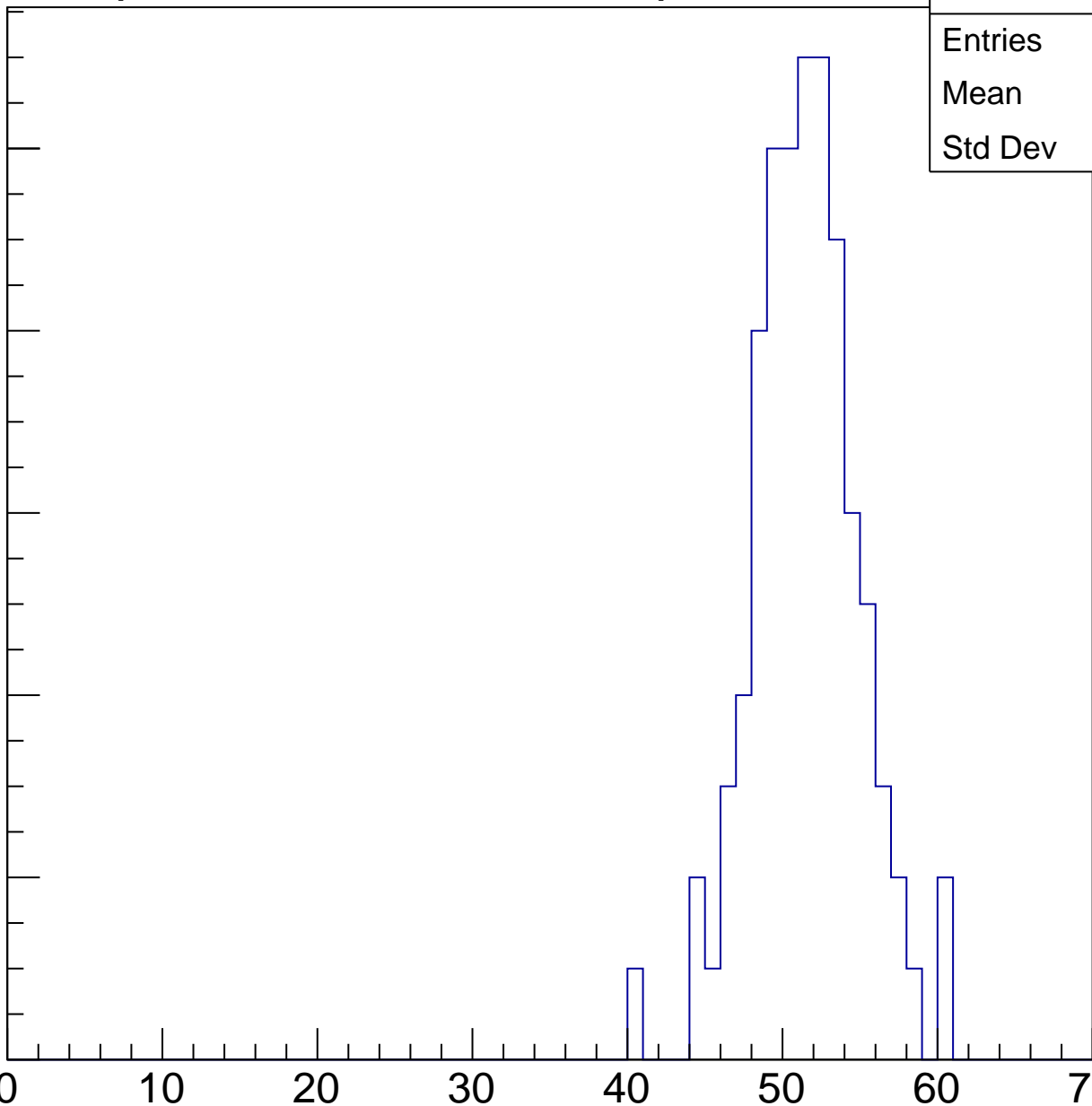
30

40

50

60

ampl



B0L101S, U14-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

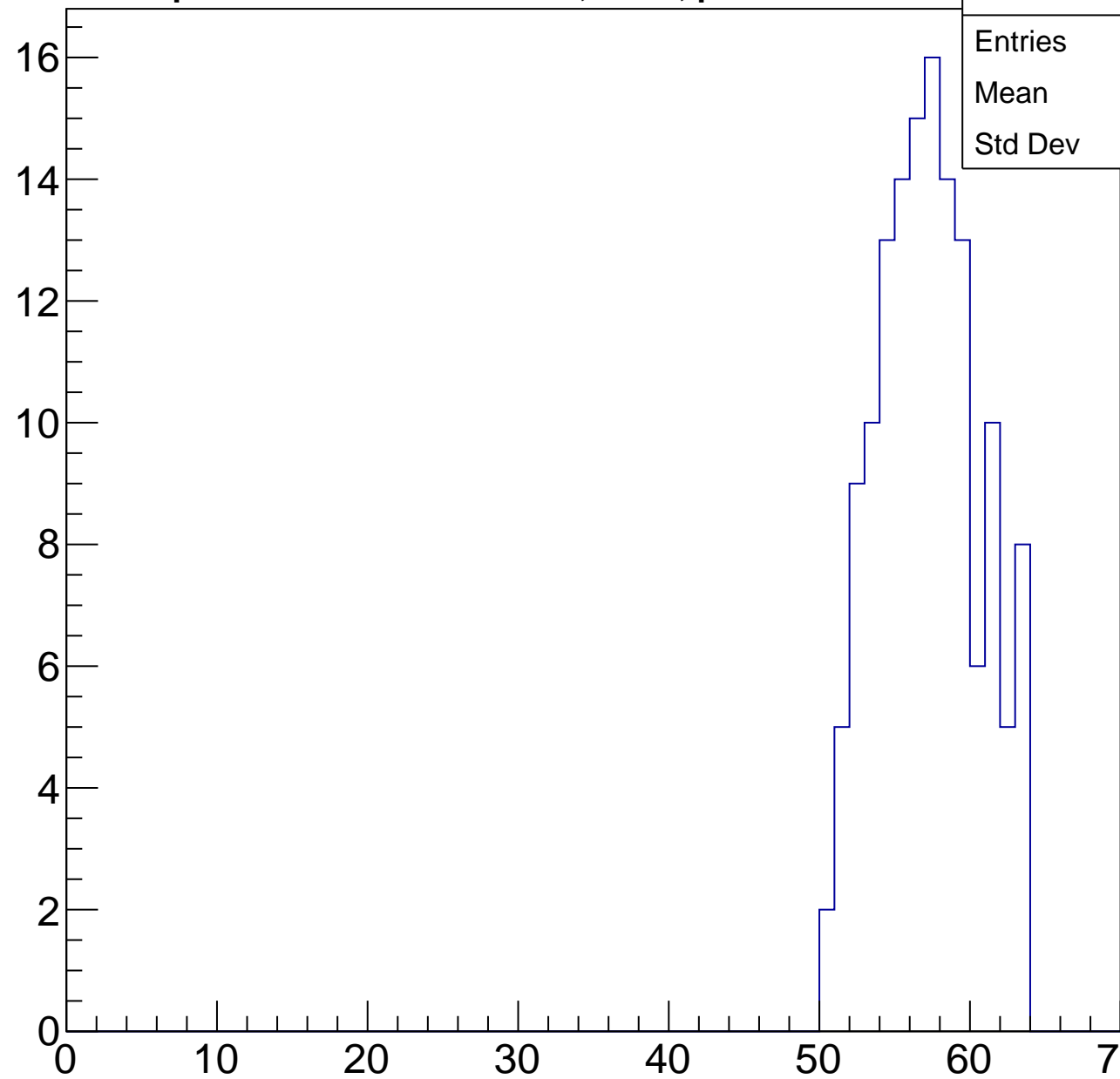
Entries	140
Mean	56.71
Std Dev	3.309

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch75, adc5

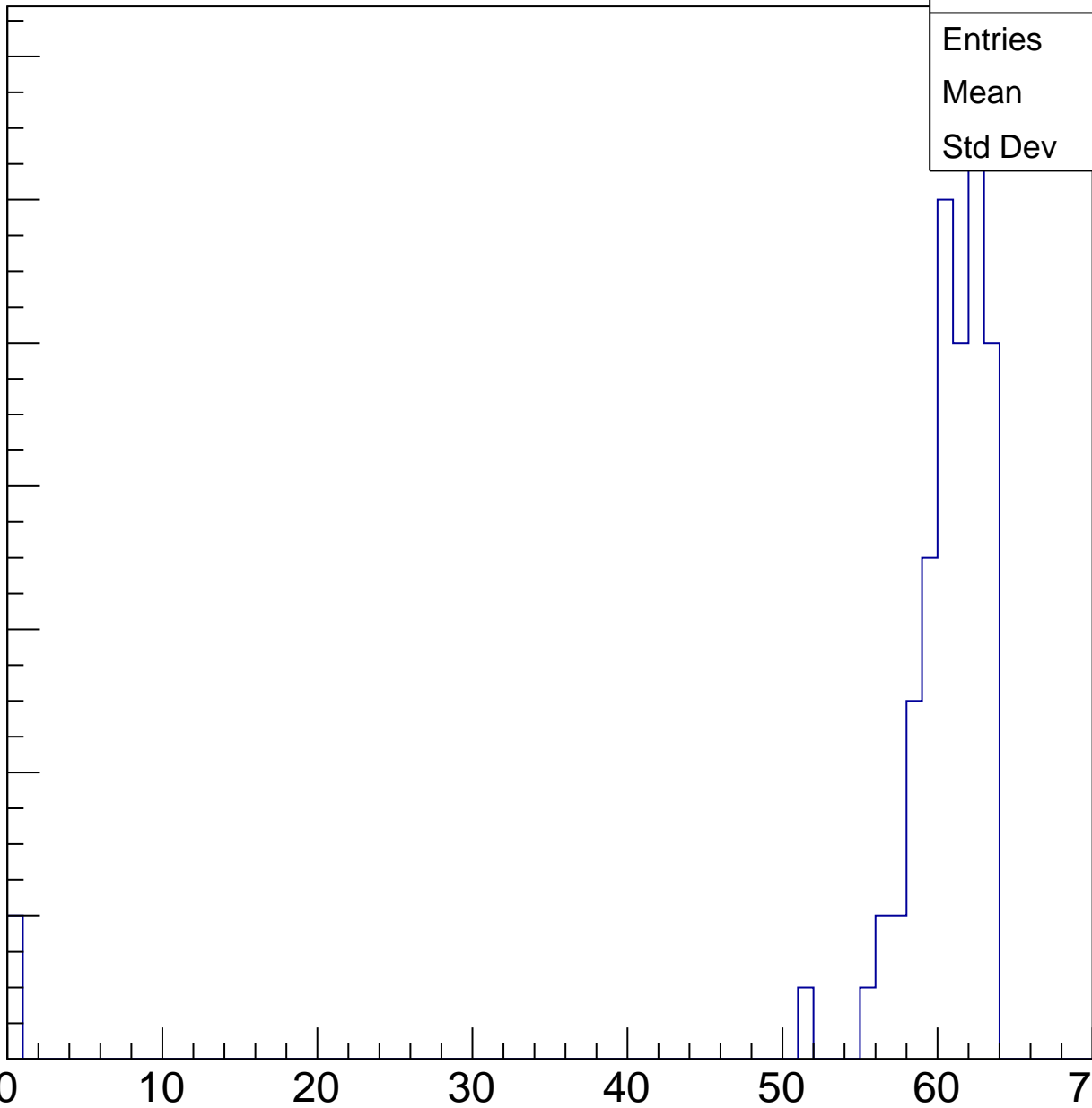
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	58.53
Std Dev	10.59

ampl



B0L101S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch76, adc0

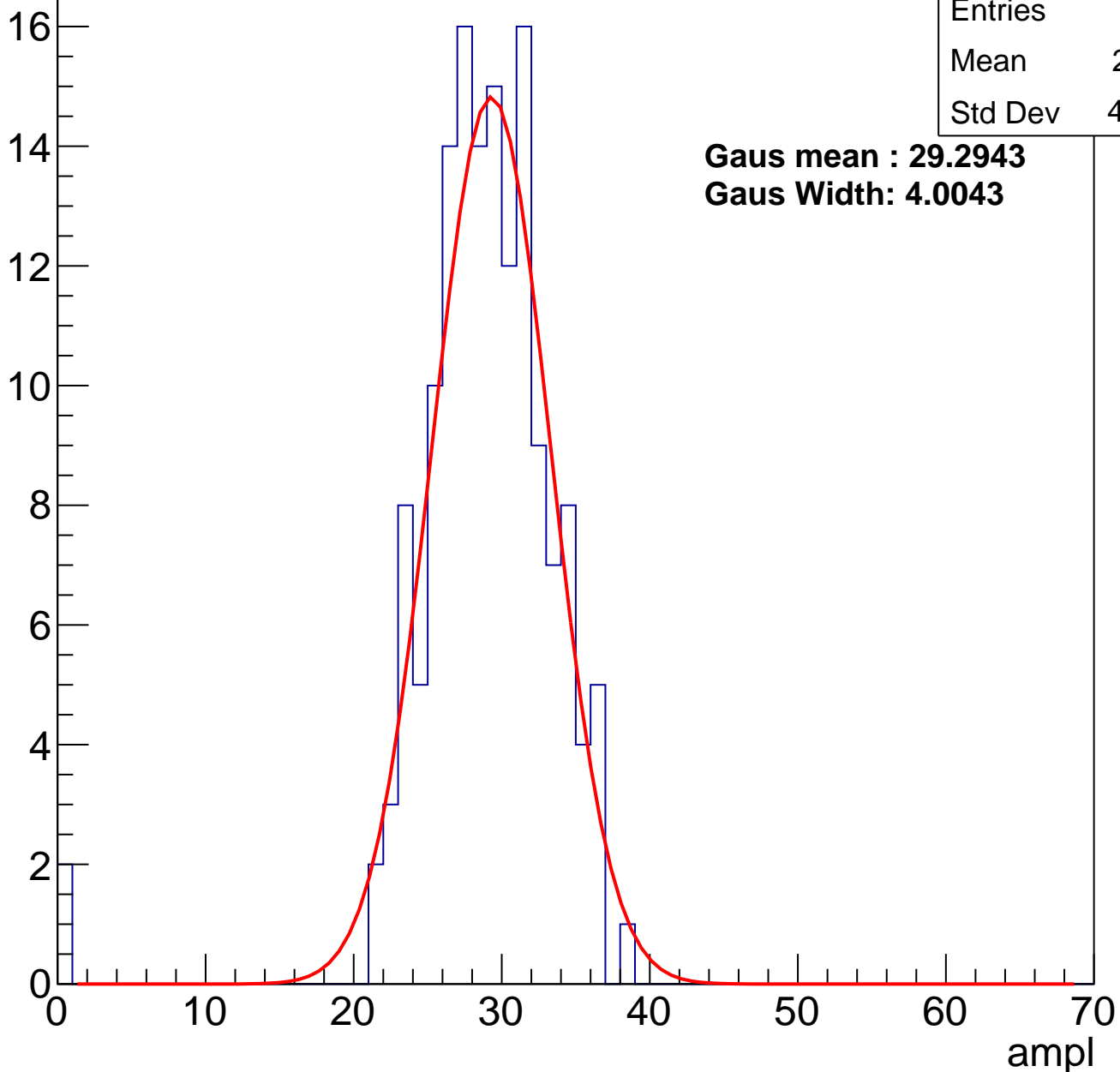
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.41
Std Dev	4.897

Gaus mean : 29.2943

Gaus Width: 4.0043

Entry



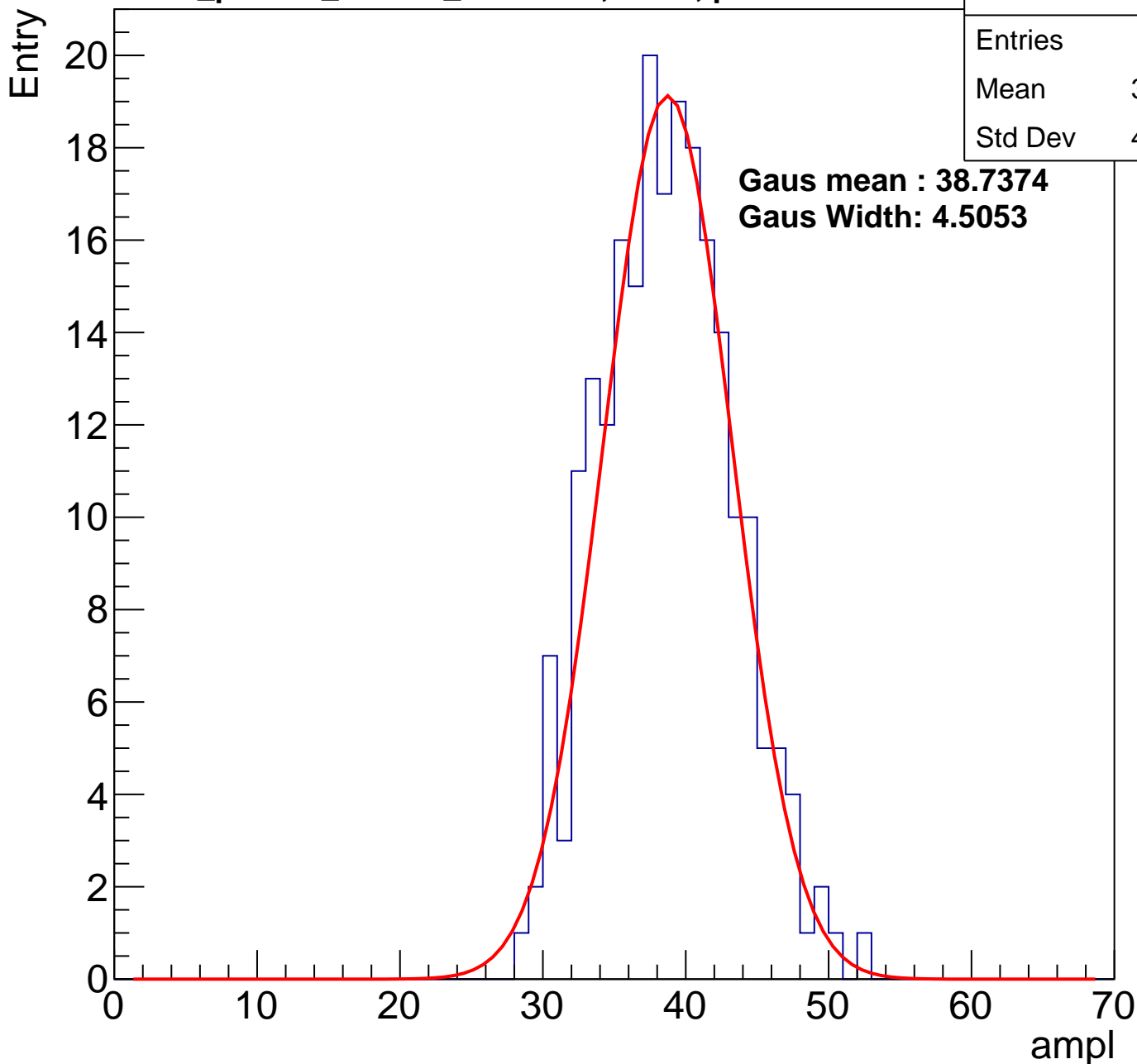
B0L101S, U14-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	223
Mean	38.25
Std Dev	4.552

Gaus mean : 38.7374

Gaus Width: 4.5053



B0L101S, U14-ch76, adc2

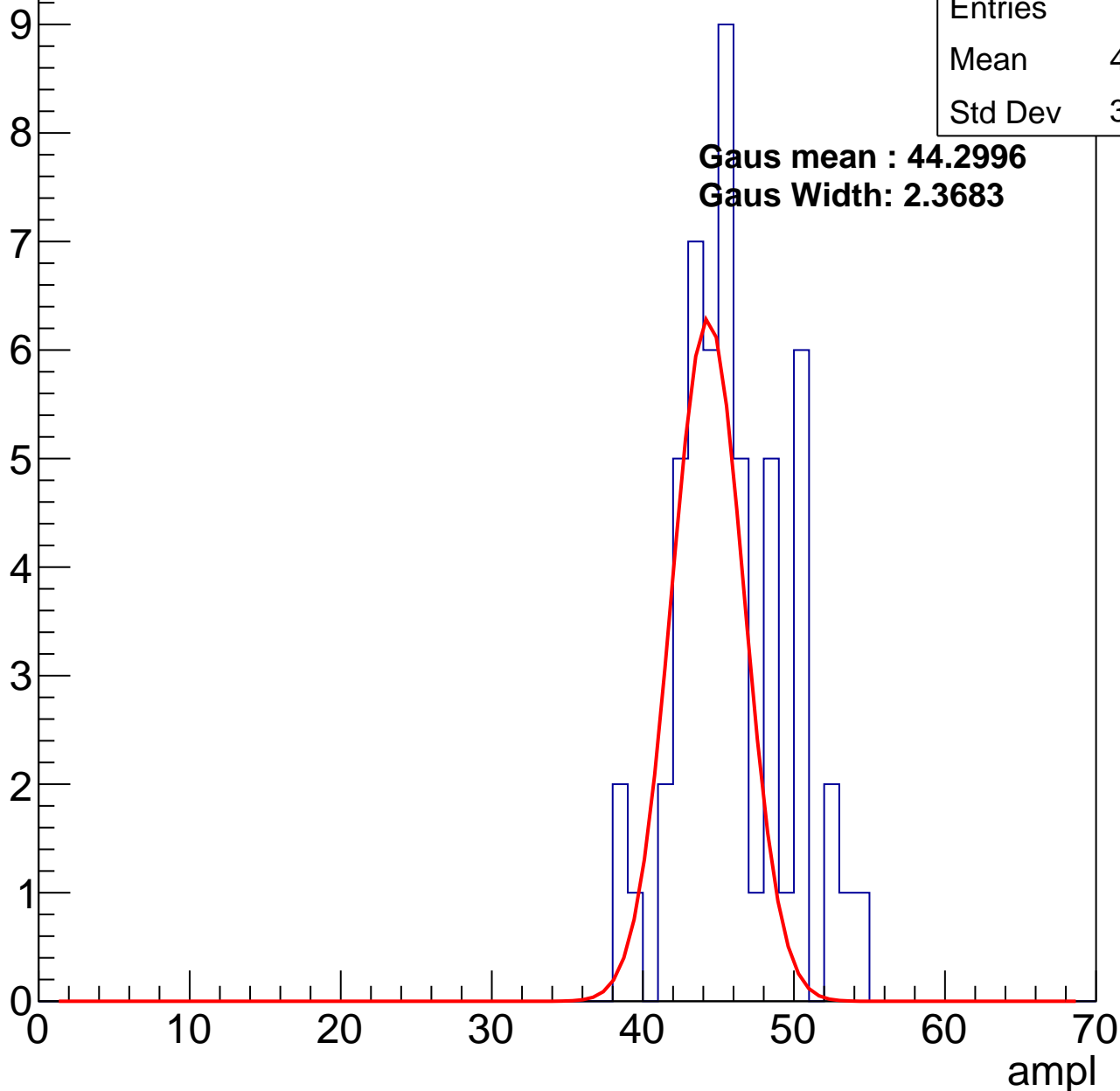
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	54
Mean	45.44
Std Dev	3.594

Gaus mean : 44.2996

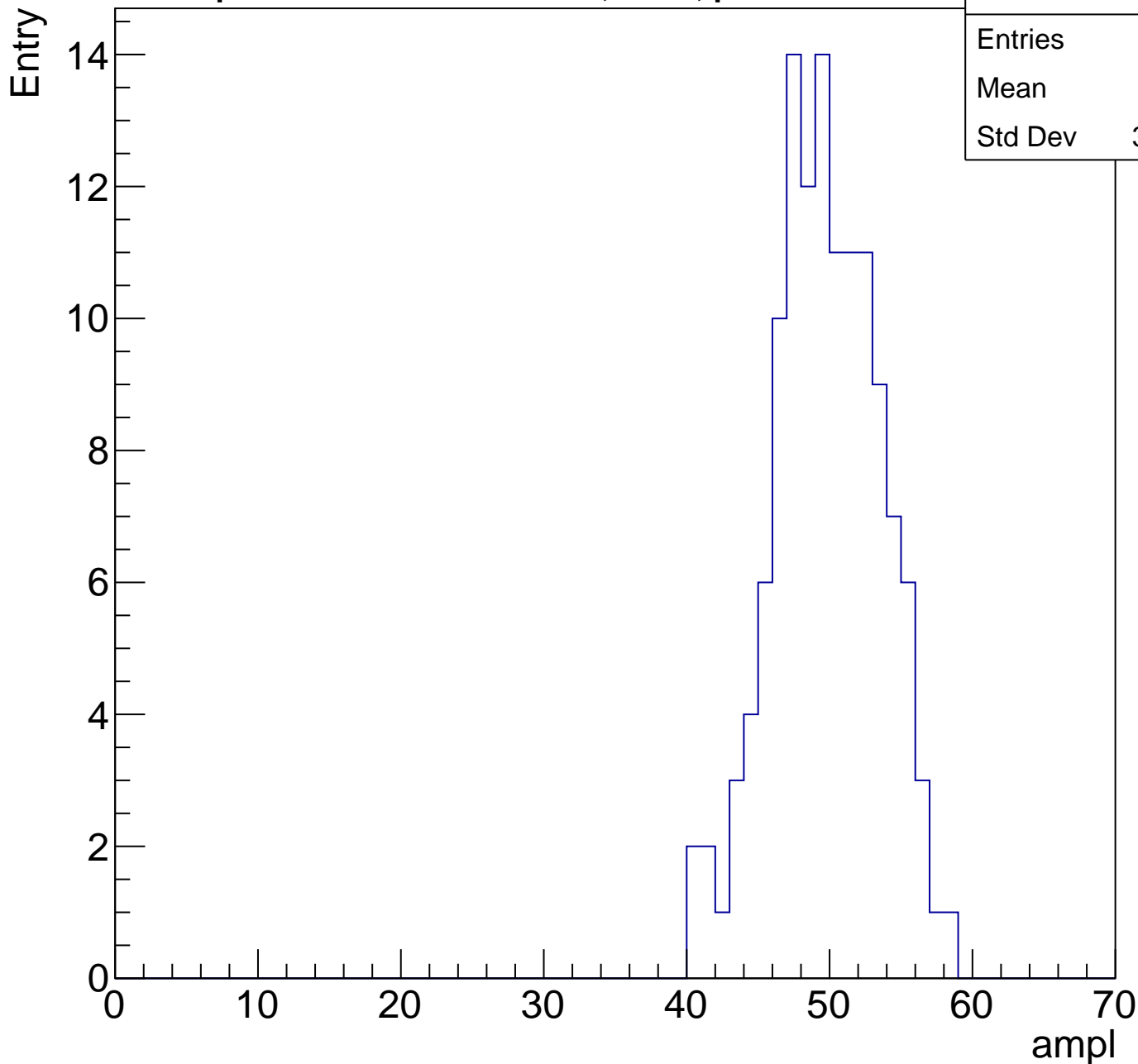
Gaus Width: 2.3683



B0L101S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	49.3
Std Dev	3.719



B0L101S, U14-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.33
Std Dev	3.501

Entry

12

10

8

6

4

2

0

0

10

20

30

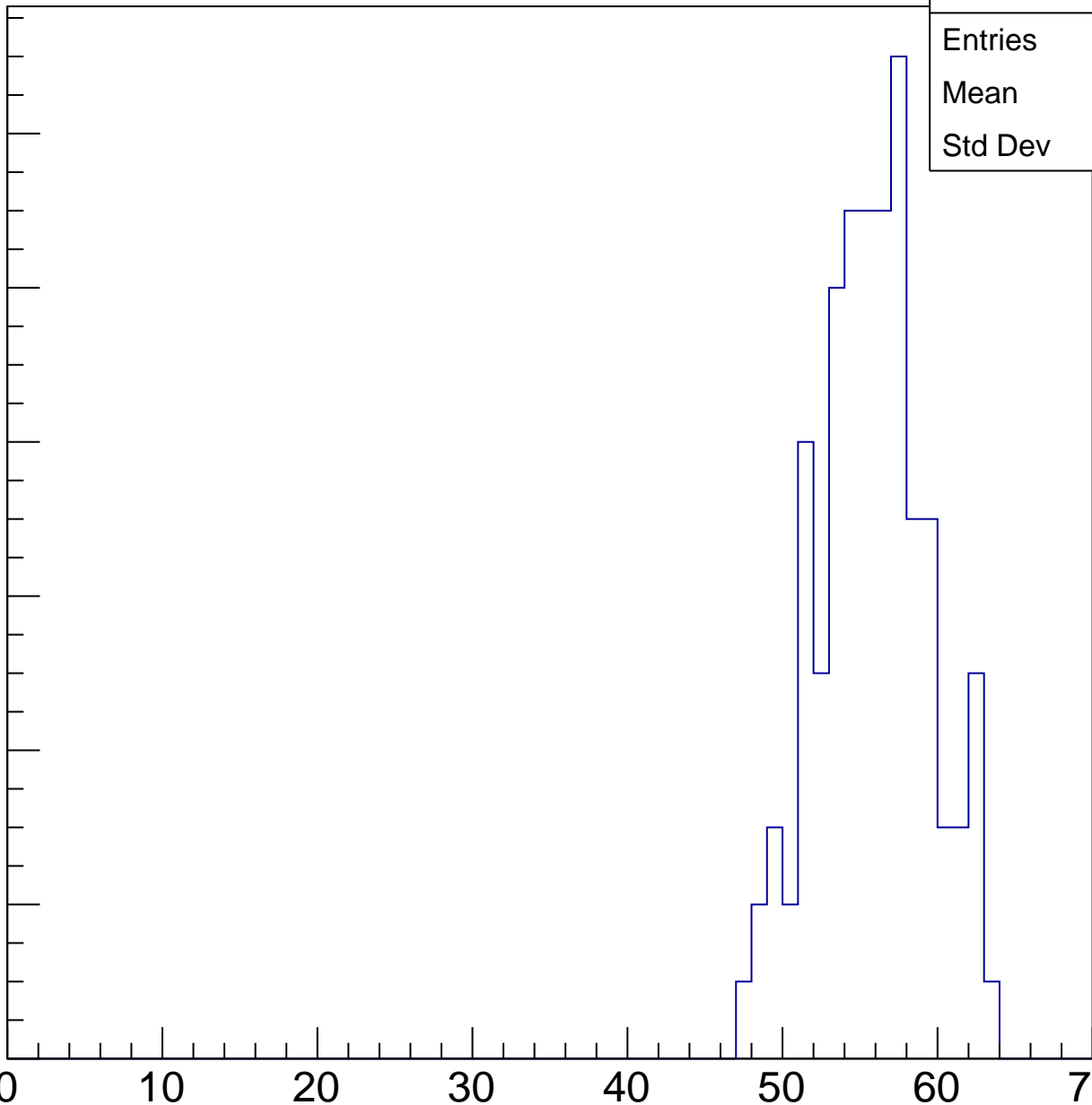
40

50

60

70

ampl



B0L101S, U14-ch76, adc5

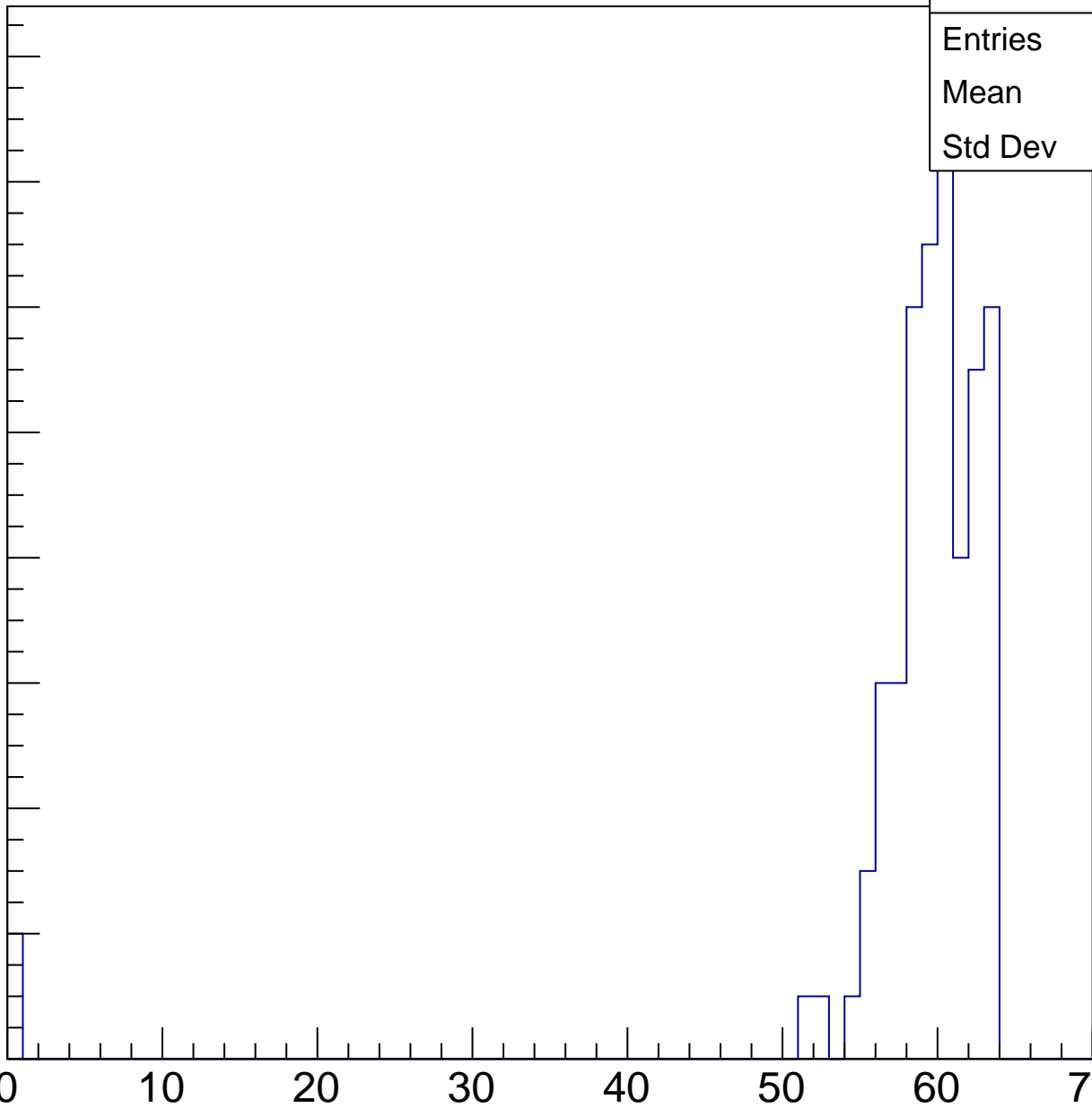
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.14
Std Dev	9.034

ampl

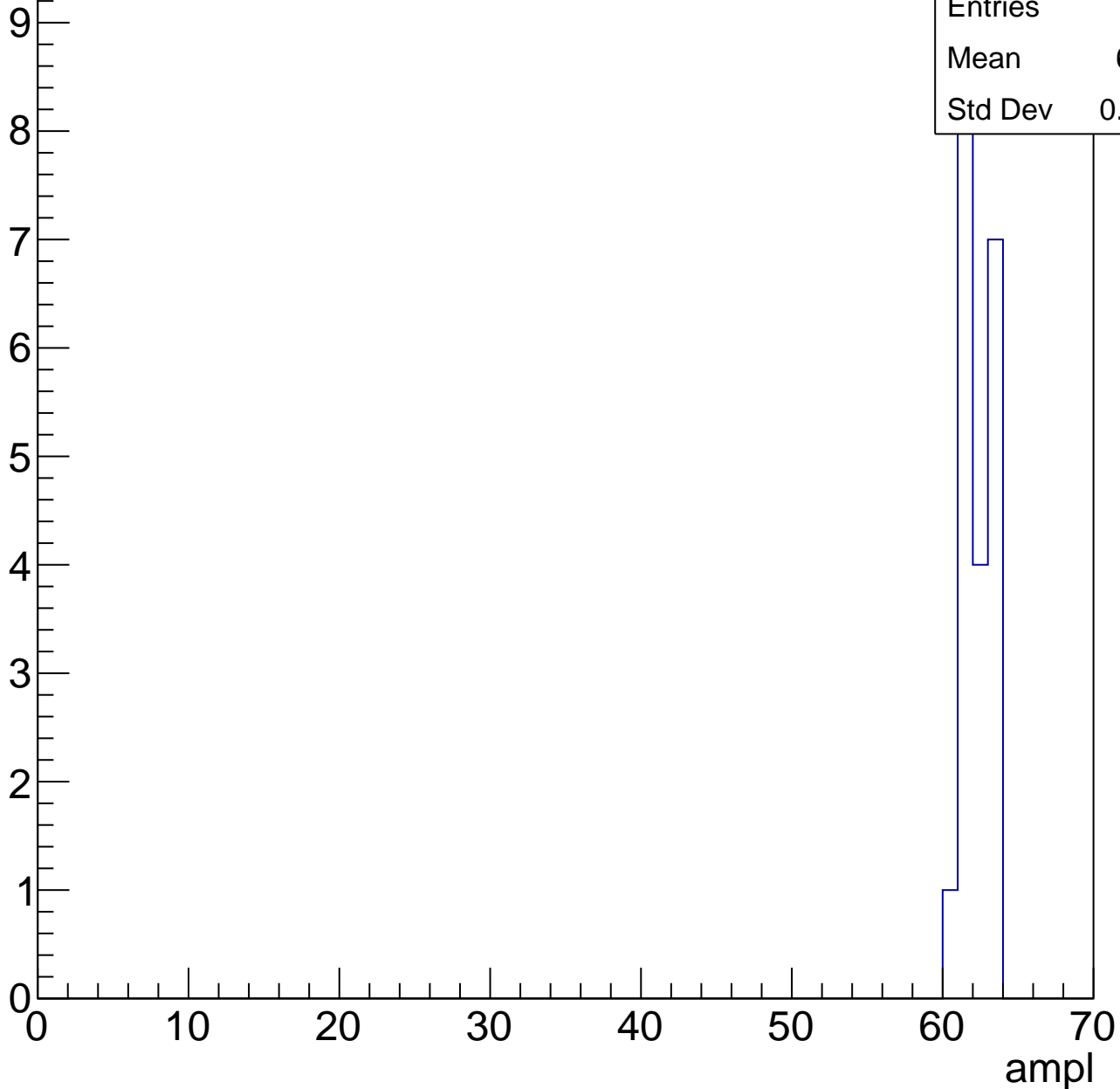


B0L101S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61.81
Std Dev	0.9571



B0L101S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



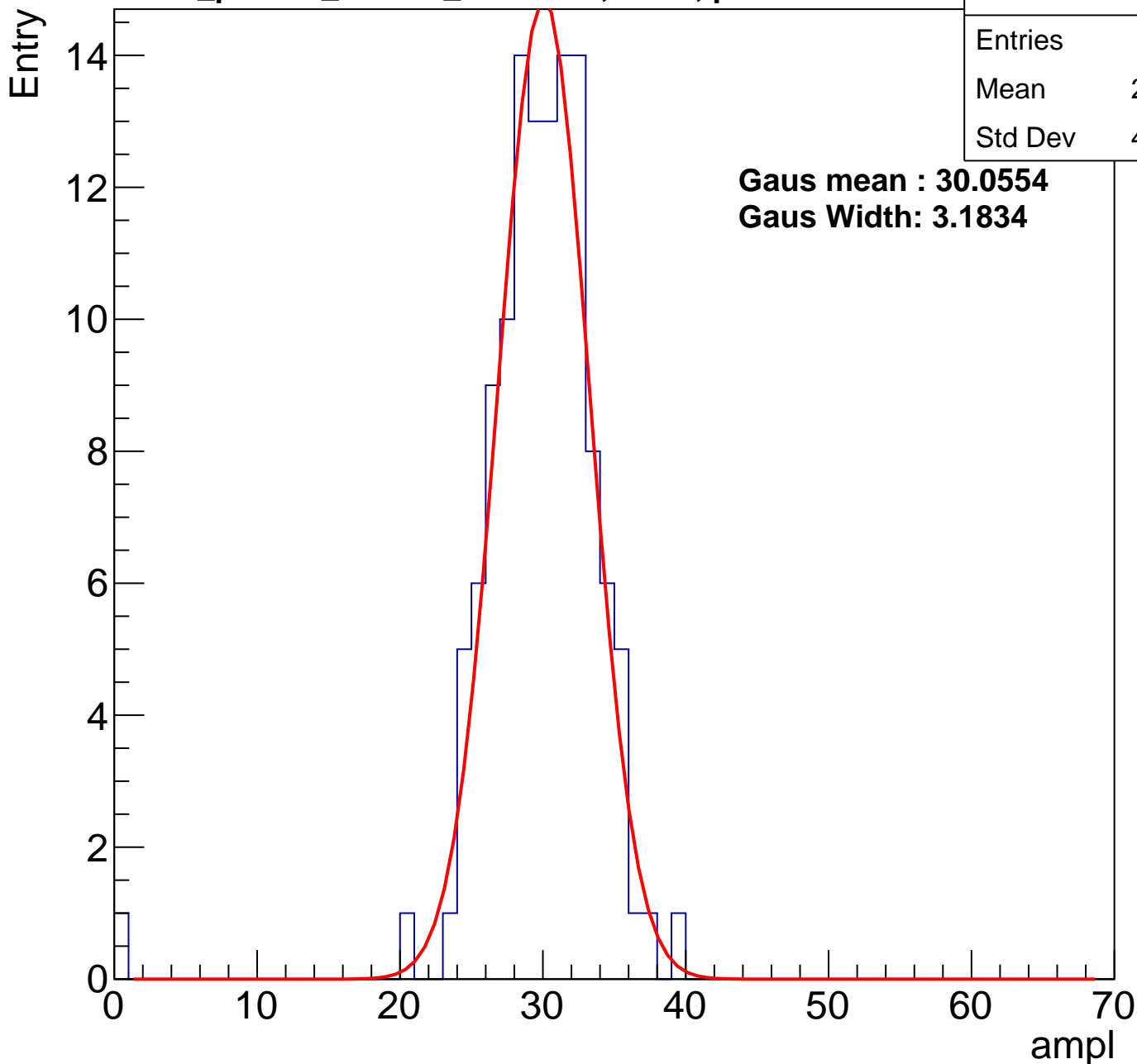
B0L101S, U14-ch77, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	29.37
Std Dev	4.193

Gaus mean : 30.0554

Gaus Width: 3.1834

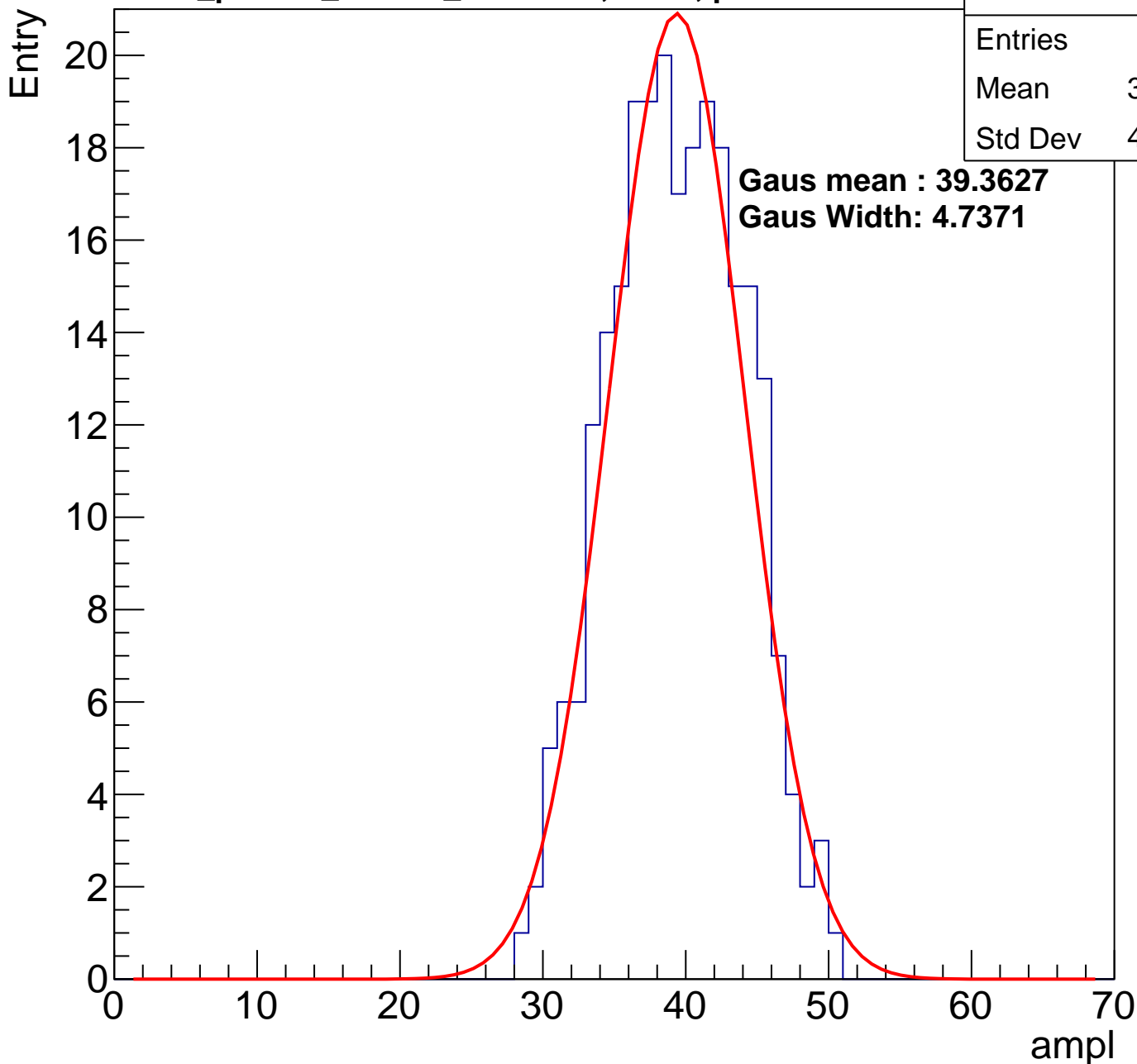


B0L101S, U14-ch77, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	251
Mean	38.92
Std Dev	4.557

Gaus mean : 39.3627
Gaus Width: 4.7371



B0L101S, U14-ch77, adc2

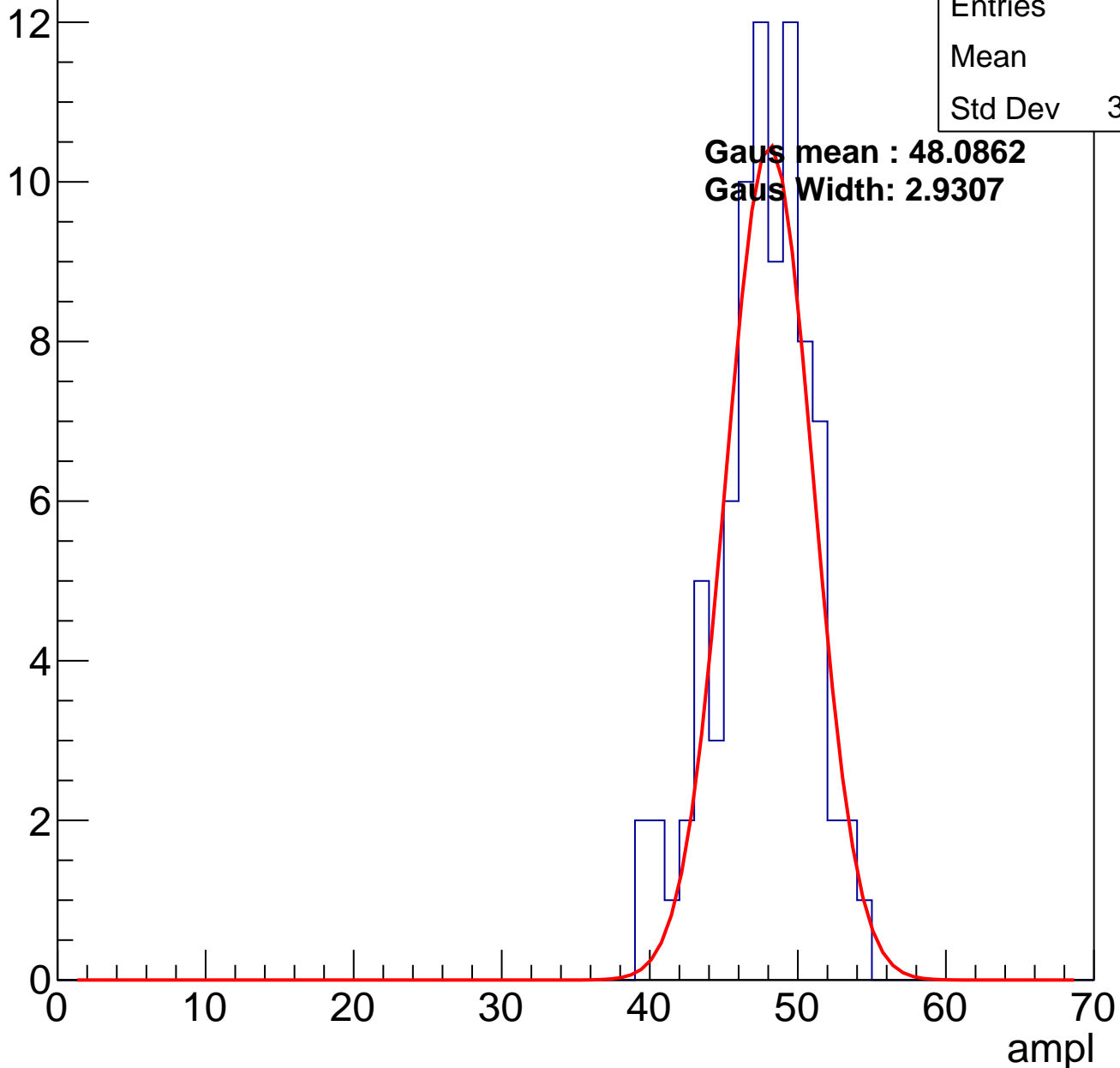
calib_packv5_042523_0143.root, FC#1, port C1

Entries	84
Mean	47.2
Std Dev	3.203

Gaus mean : 48.0862

Gaus Width: 2.9307

Entry



B0L101S, U14-ch77, adc3

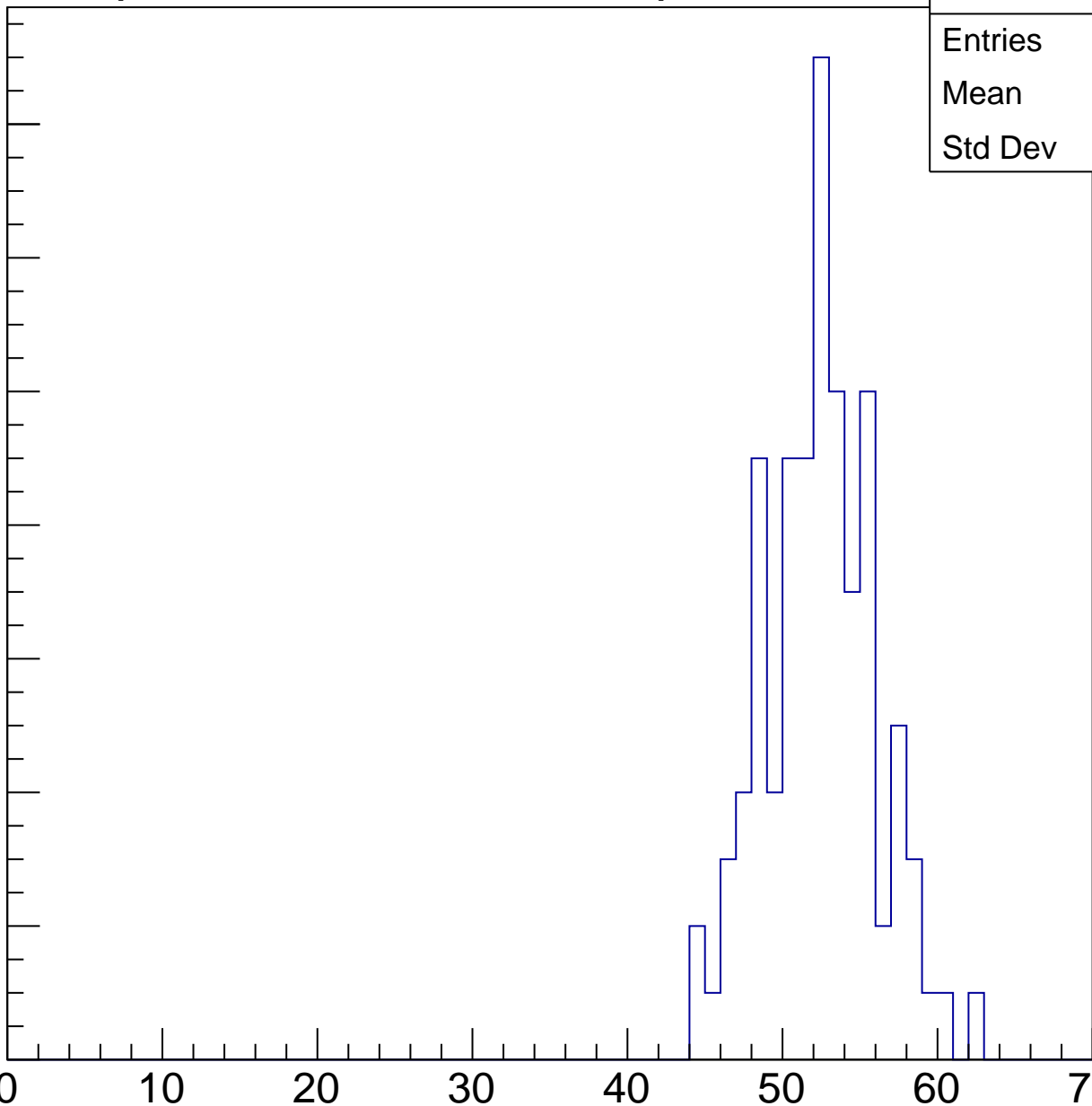
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	51.94
Std Dev	3.558

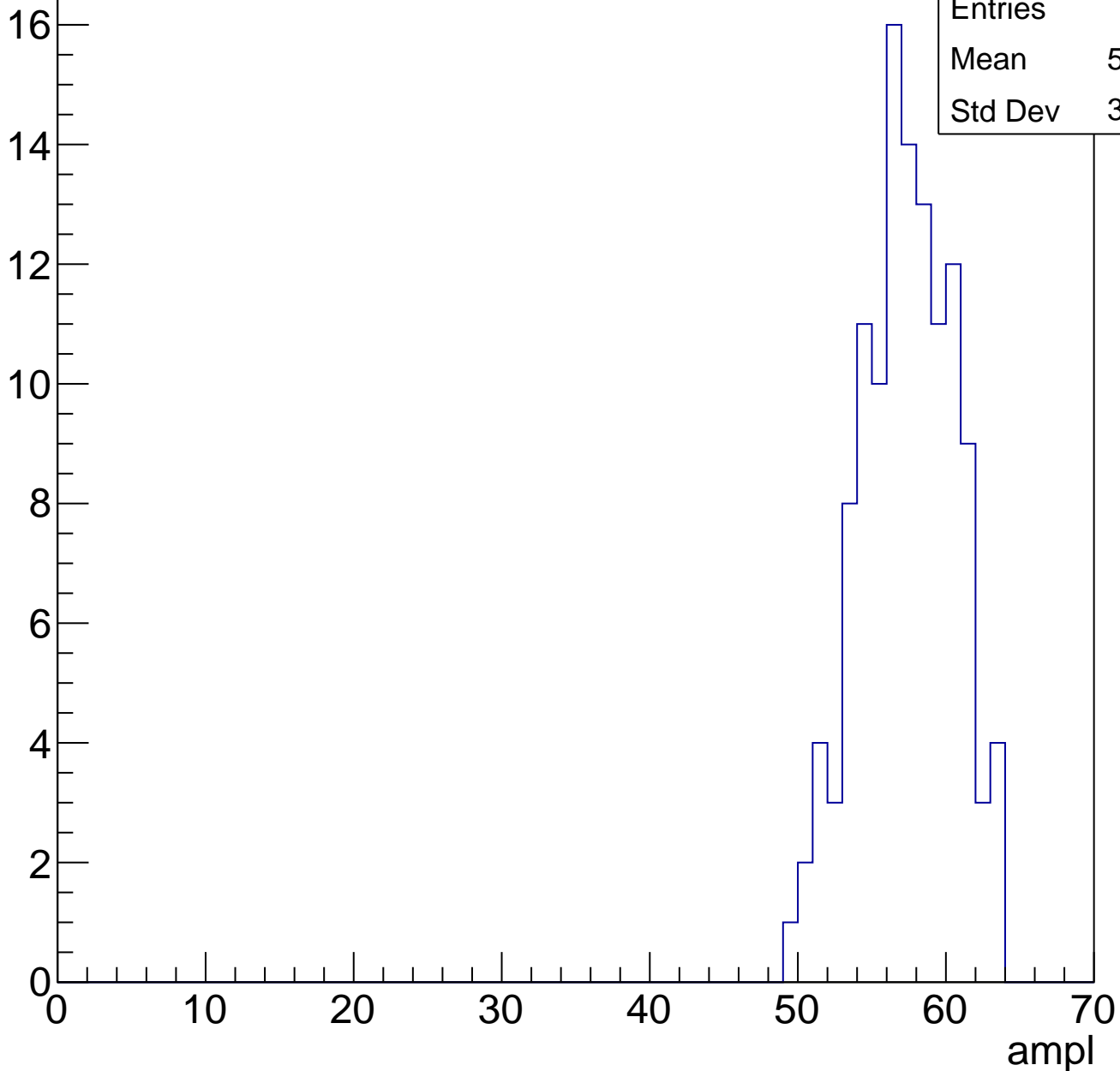
ampl



B0L101S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch77, adc5

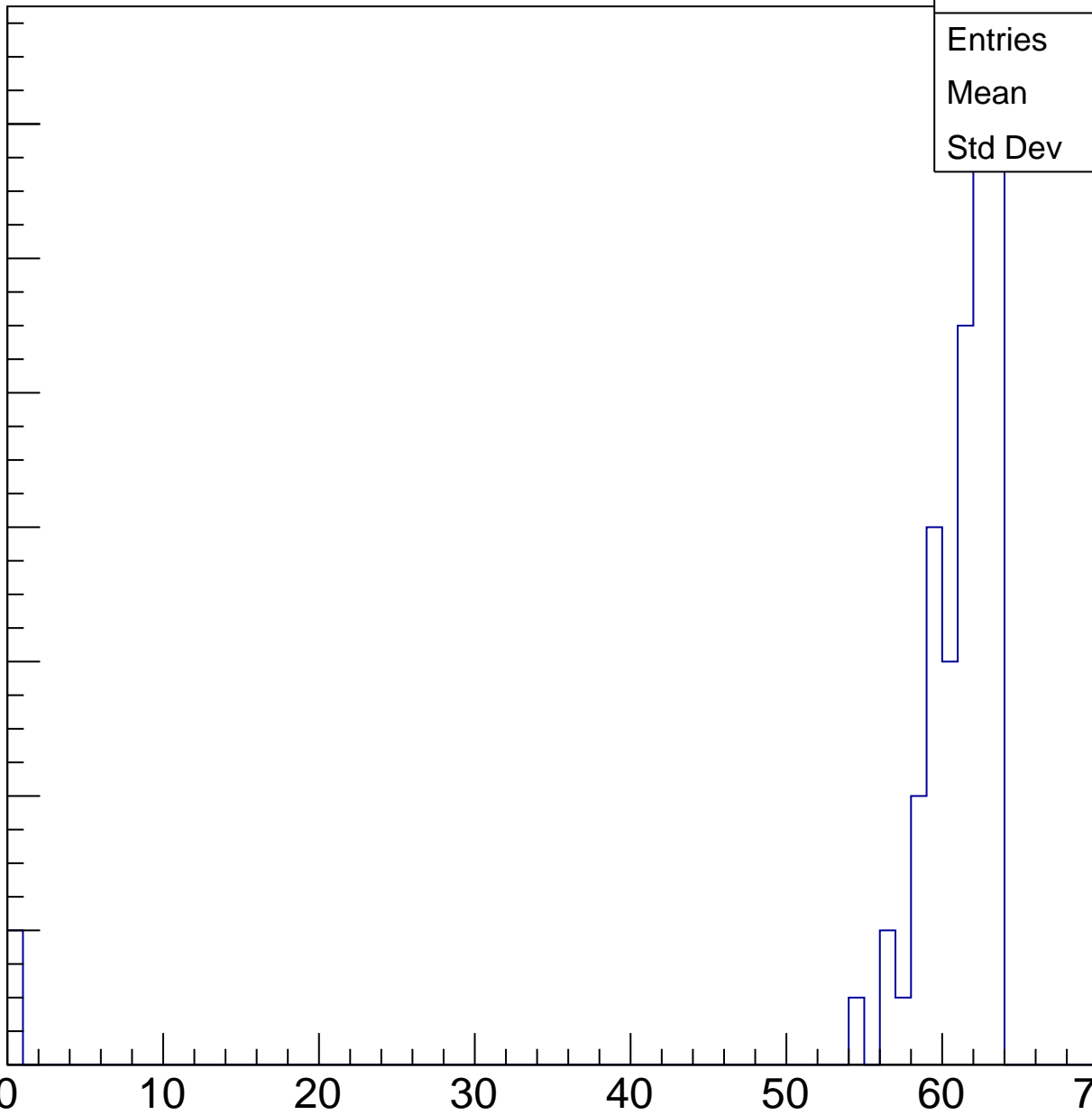
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.97
Std Dev	10.7

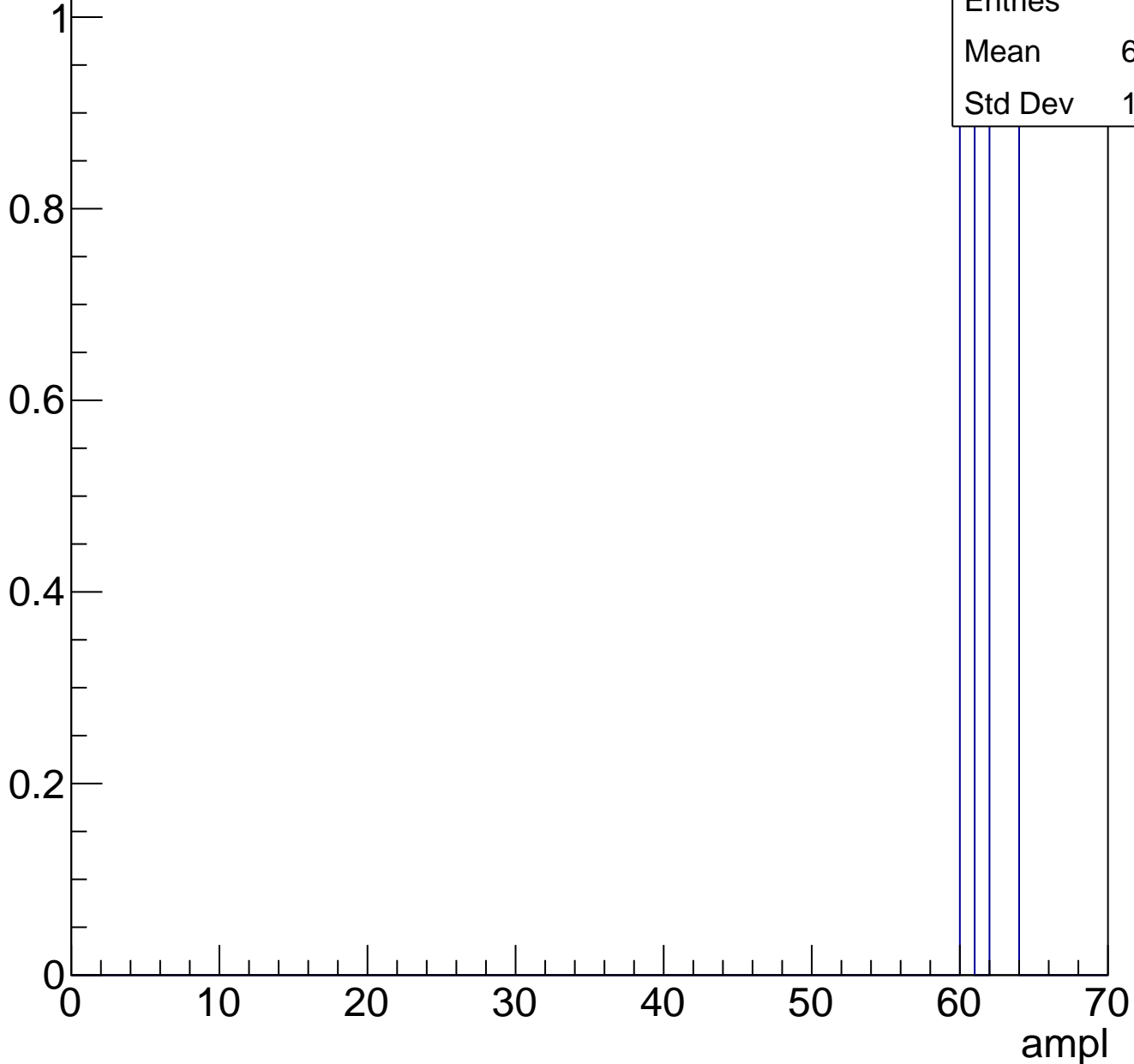
ampl



B0L101S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch78, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	27.57
Std Dev	4.082

Gaus mean : 27.9804

Gaus Width: 3.3424

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

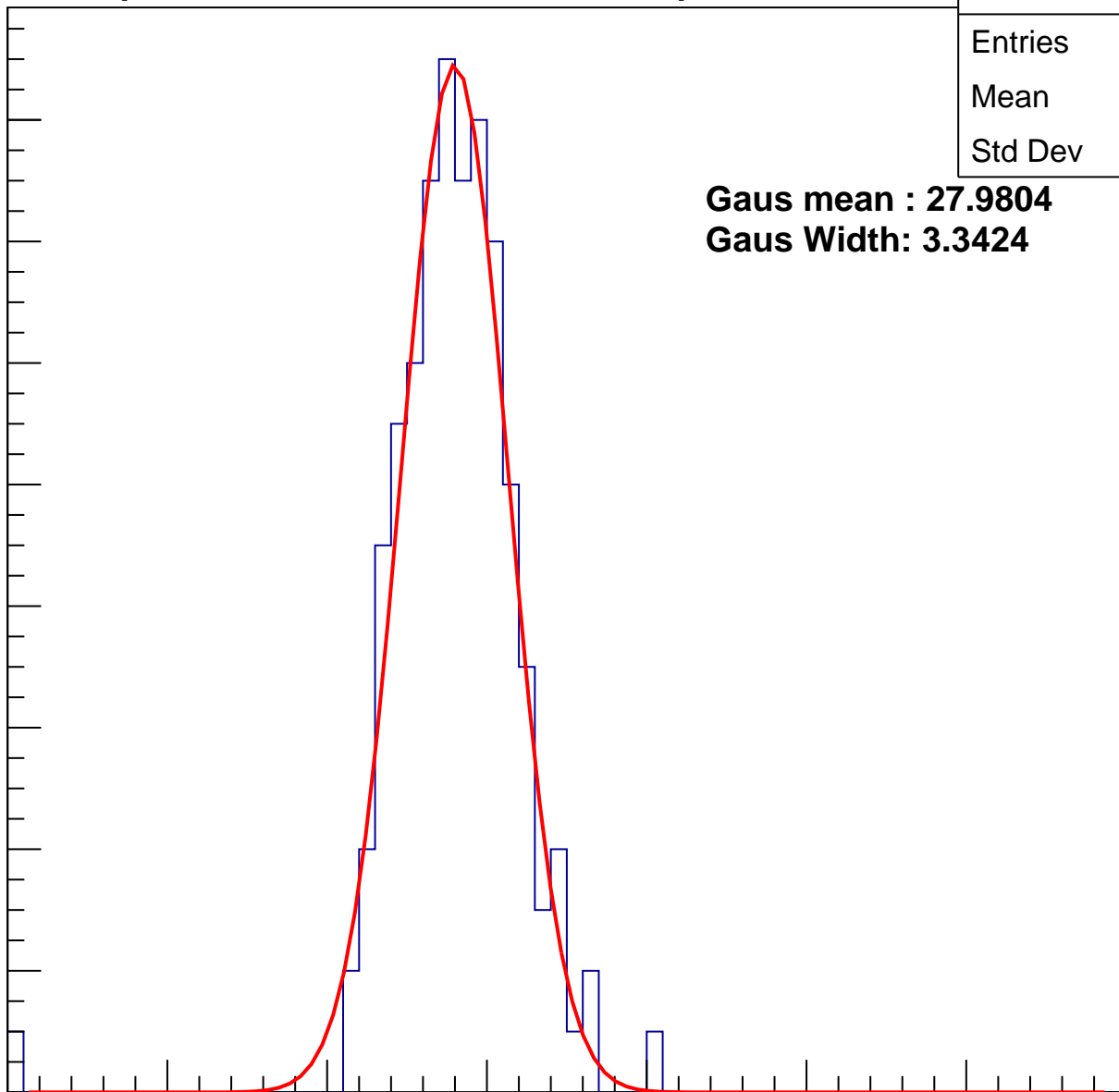
40

50

60

70

ampl



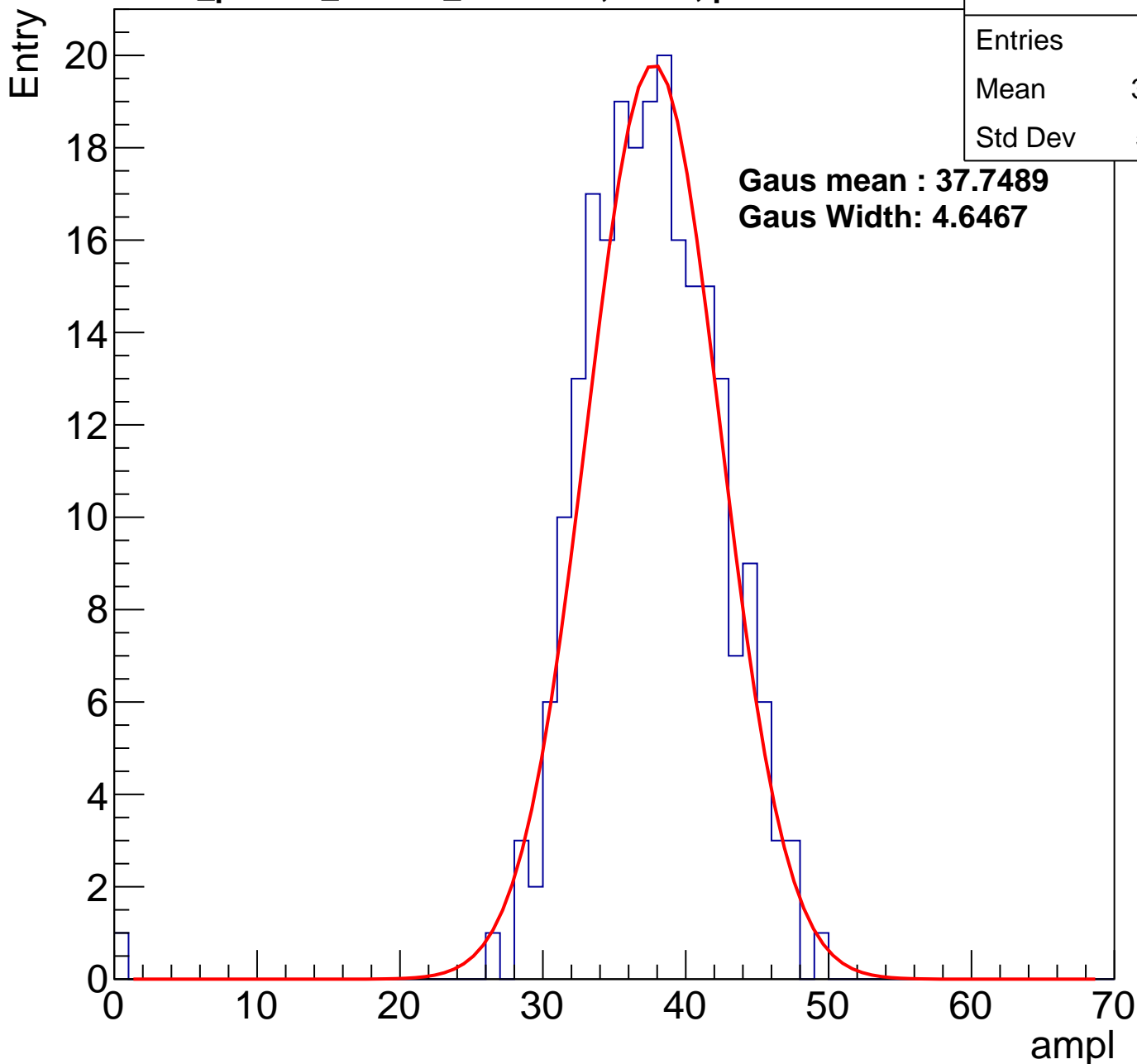
B0L101S, U14-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	233
Mean	37.06
Std Dev	5.031

Gaus mean : 37.7489

Gaus Width: 4.6467



B0L101S, U14-ch78, adc2

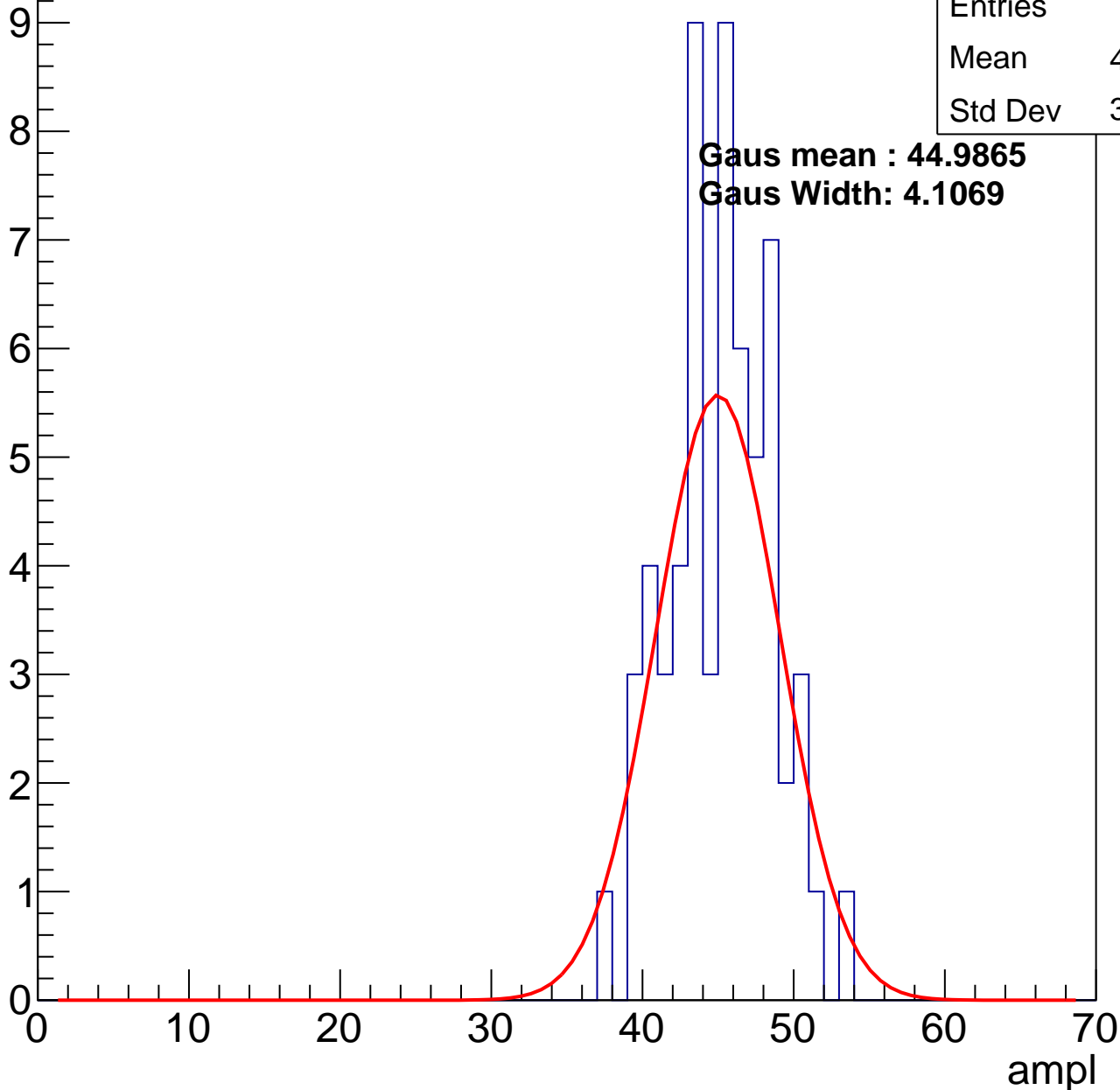
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	61
Mean	44.72
Std Dev	3.354

Gaus mean : 44.9865

Gaus Width: 4.1069

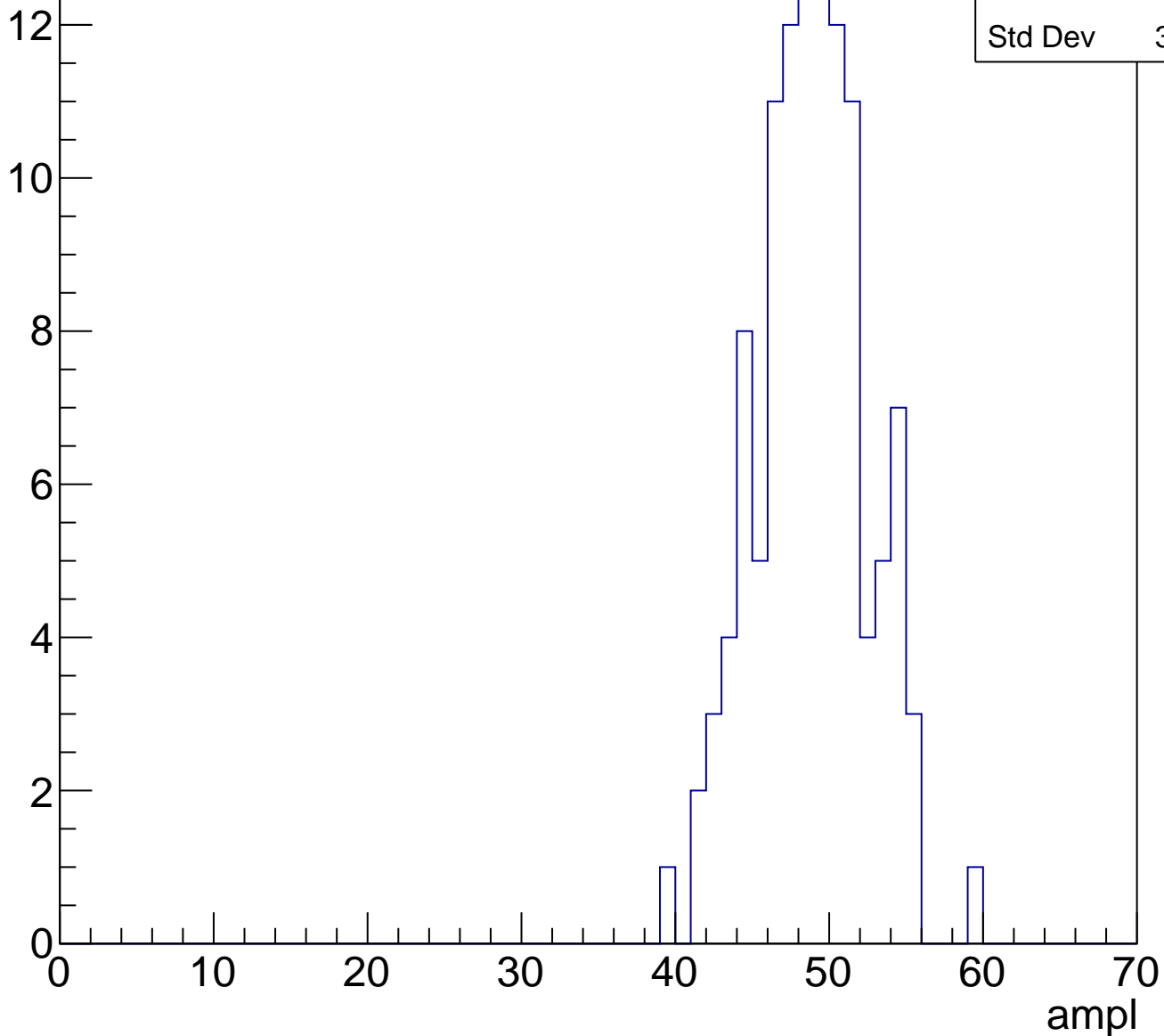


B0L101S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	48.37
Std Dev	3.574

Entry



B0L101S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	55.22
Std Dev	3.279

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B0L101S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

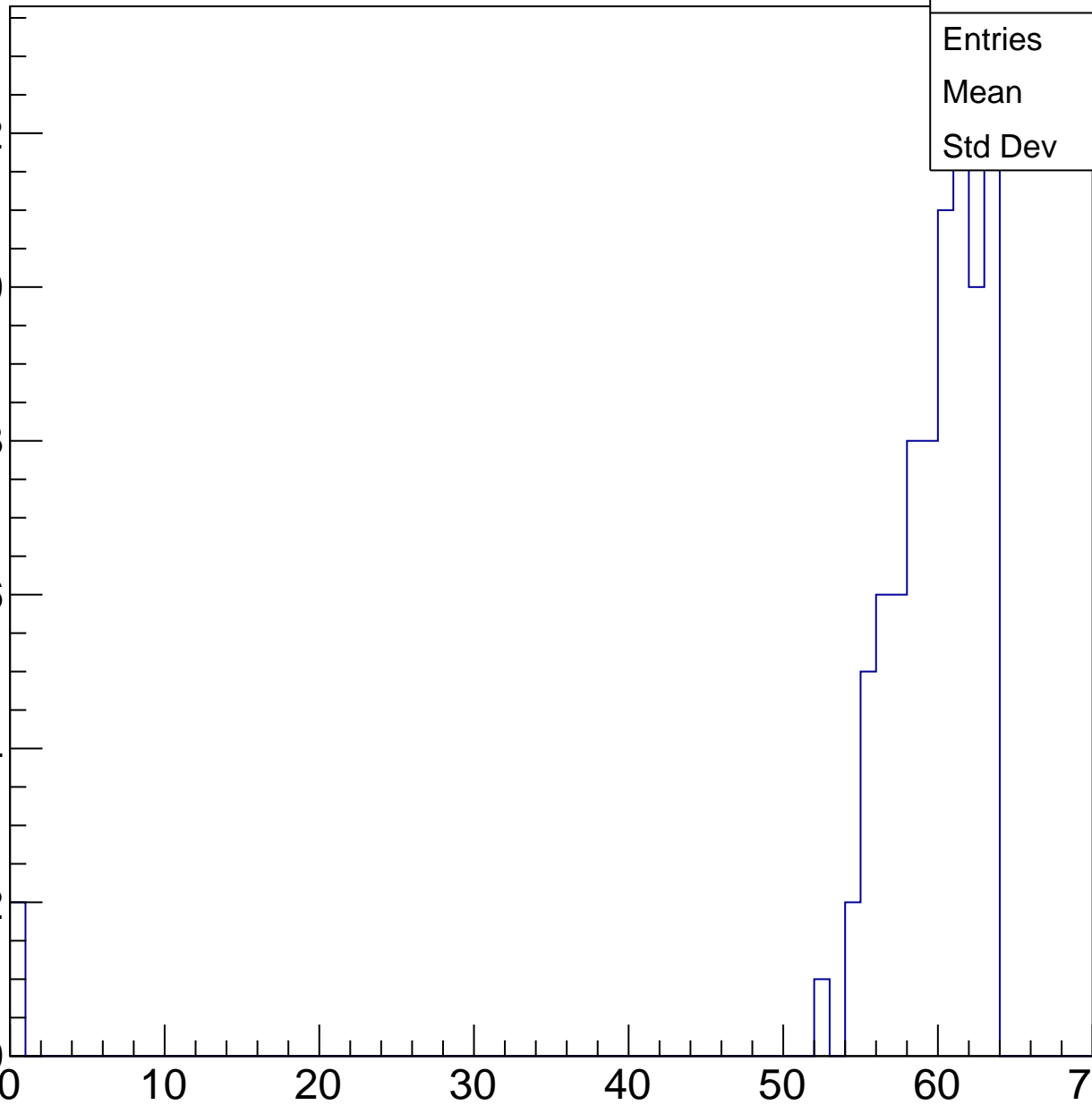
4

2

0

Entries	85
Mean	58.13
Std Dev	9.408

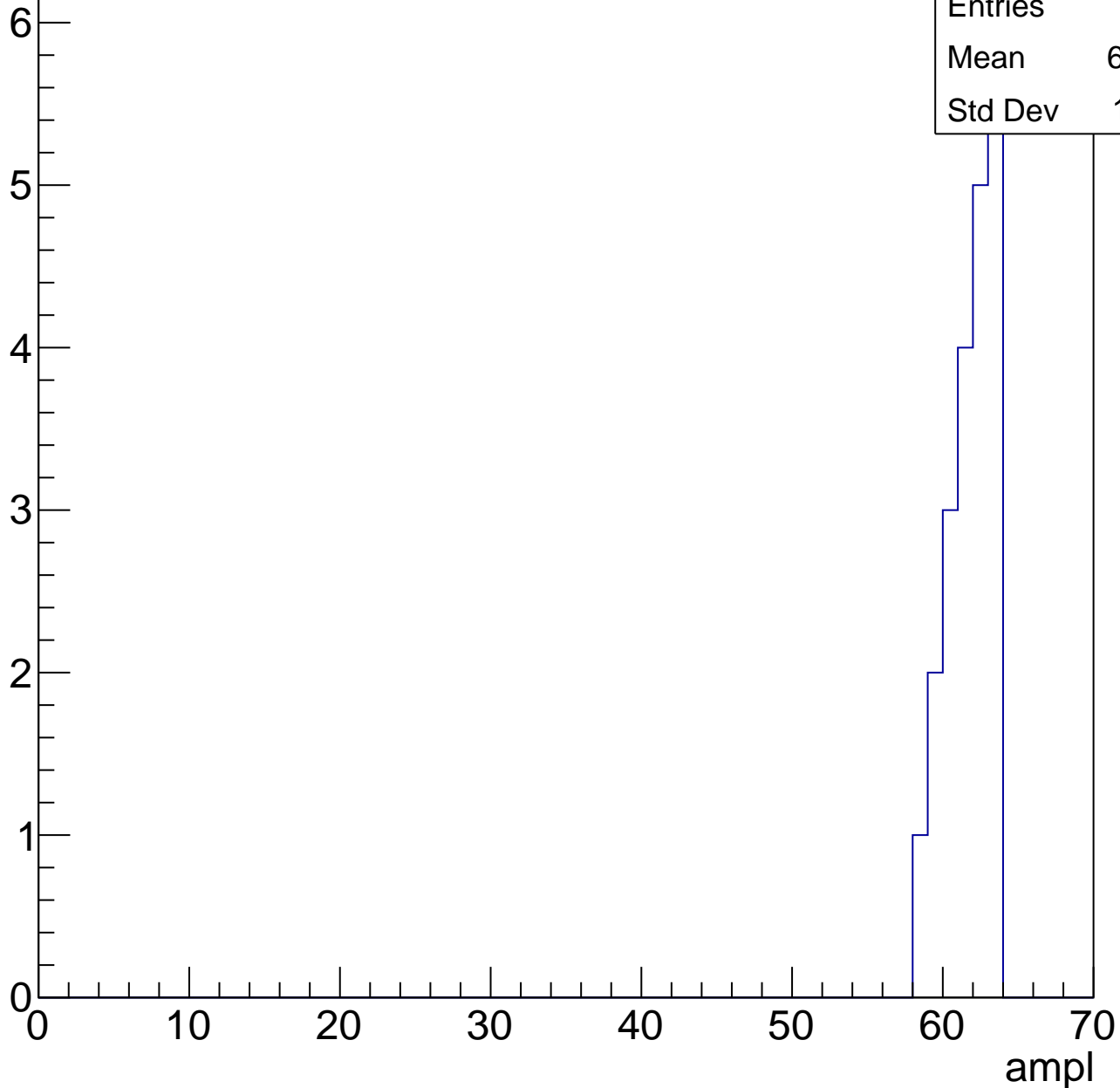
ampl



B0L101S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

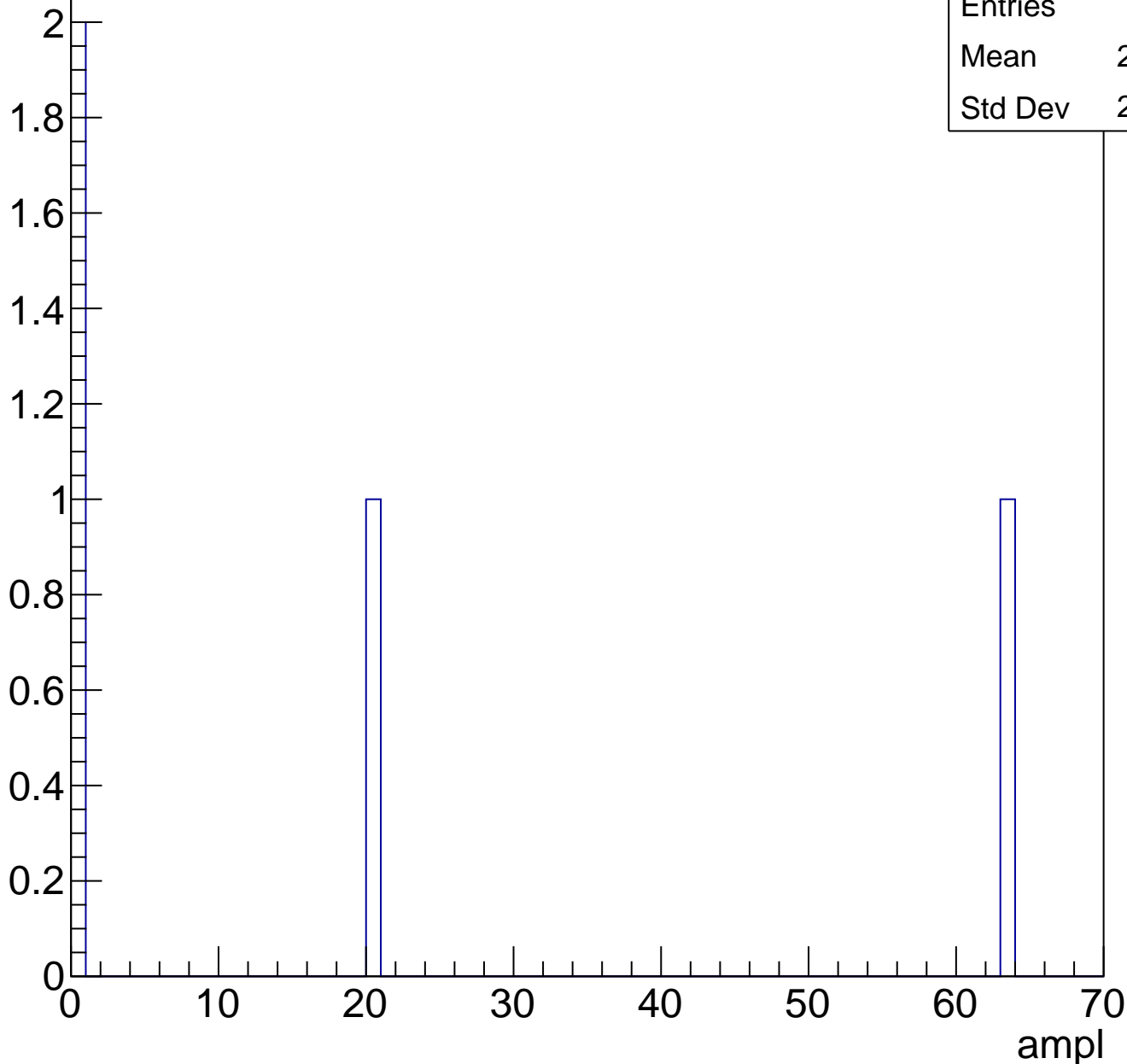
Entry



B0L101S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	20.75
Std Dev	25.72

B0L101S, U14-ch79, adc0

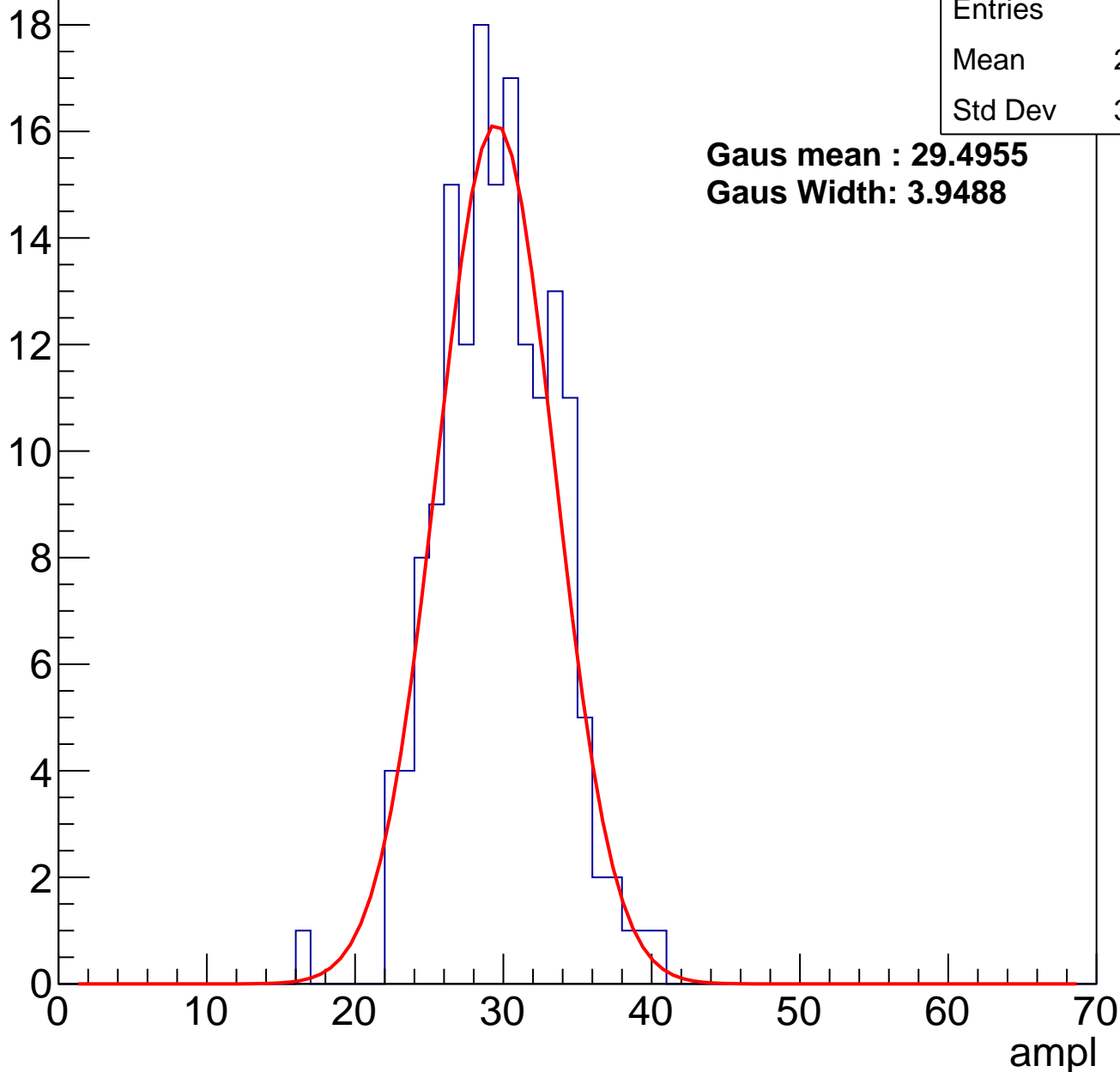
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	29.27
Std Dev	3.847

Gaus mean : 29.4955

Gaus Width: 3.9488

Entry

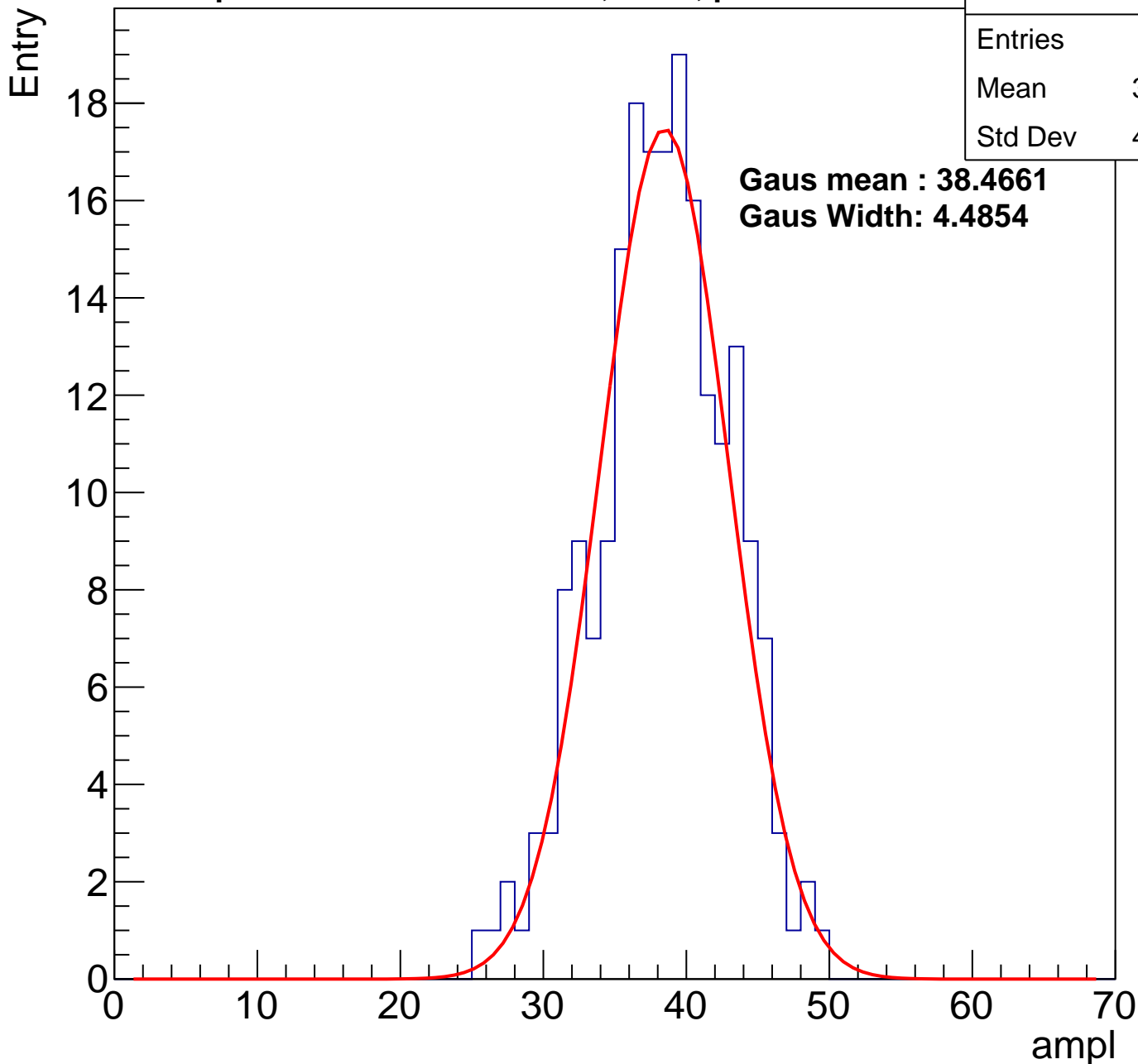


B0L101S, U14-ch79, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	205
Mean	37.88
Std Dev	4.579

Gaus mean : 38.4661
Gaus Width: 4.4854



B0L101S, U14-ch79, adc2

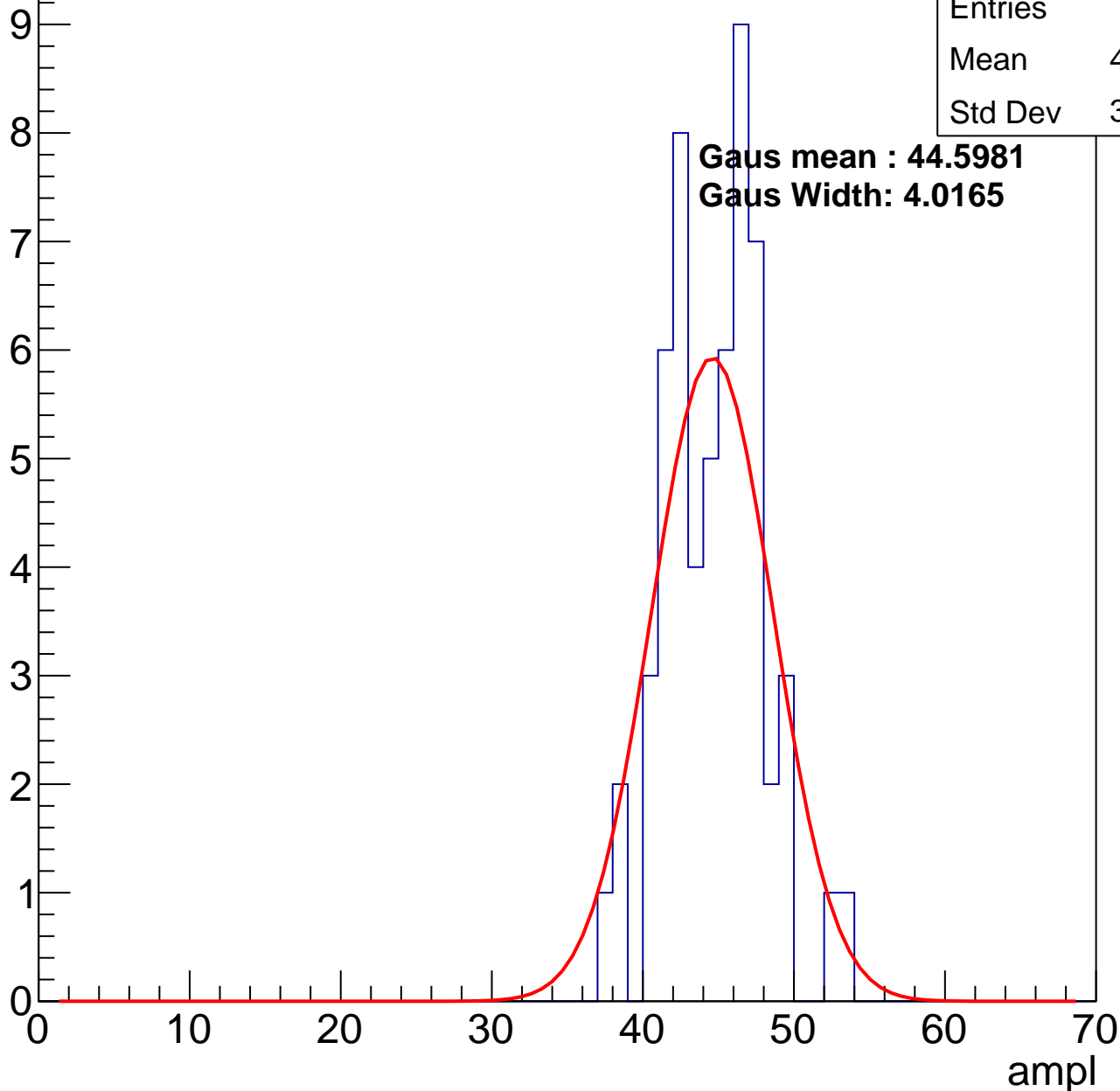
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	58
Mean	44.28
Std Dev	3.247

Gaus mean : 44.5981

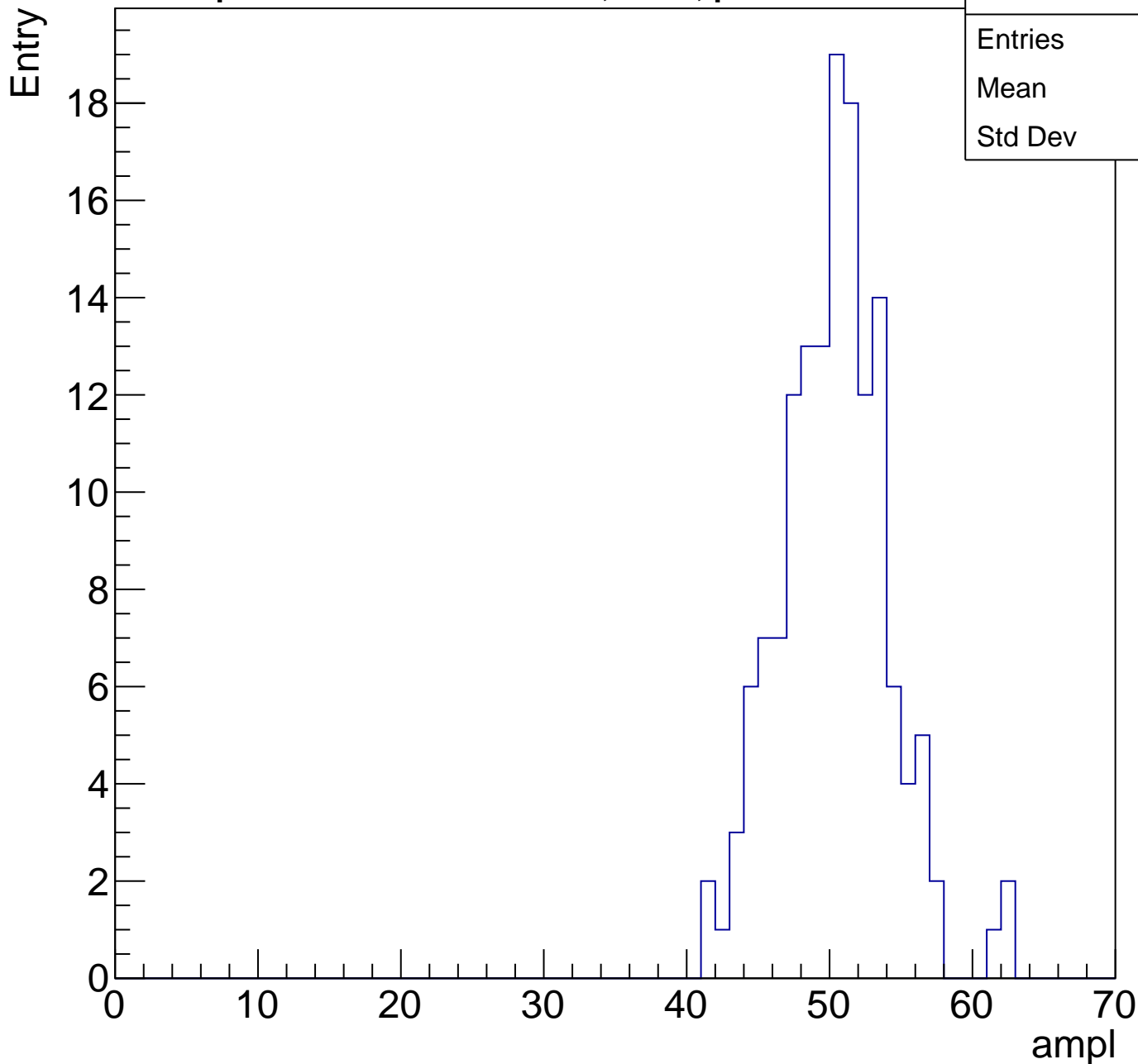
Gaus Width: 4.0165



B0L101S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	49.9
Std Dev	3.8



B0L101S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

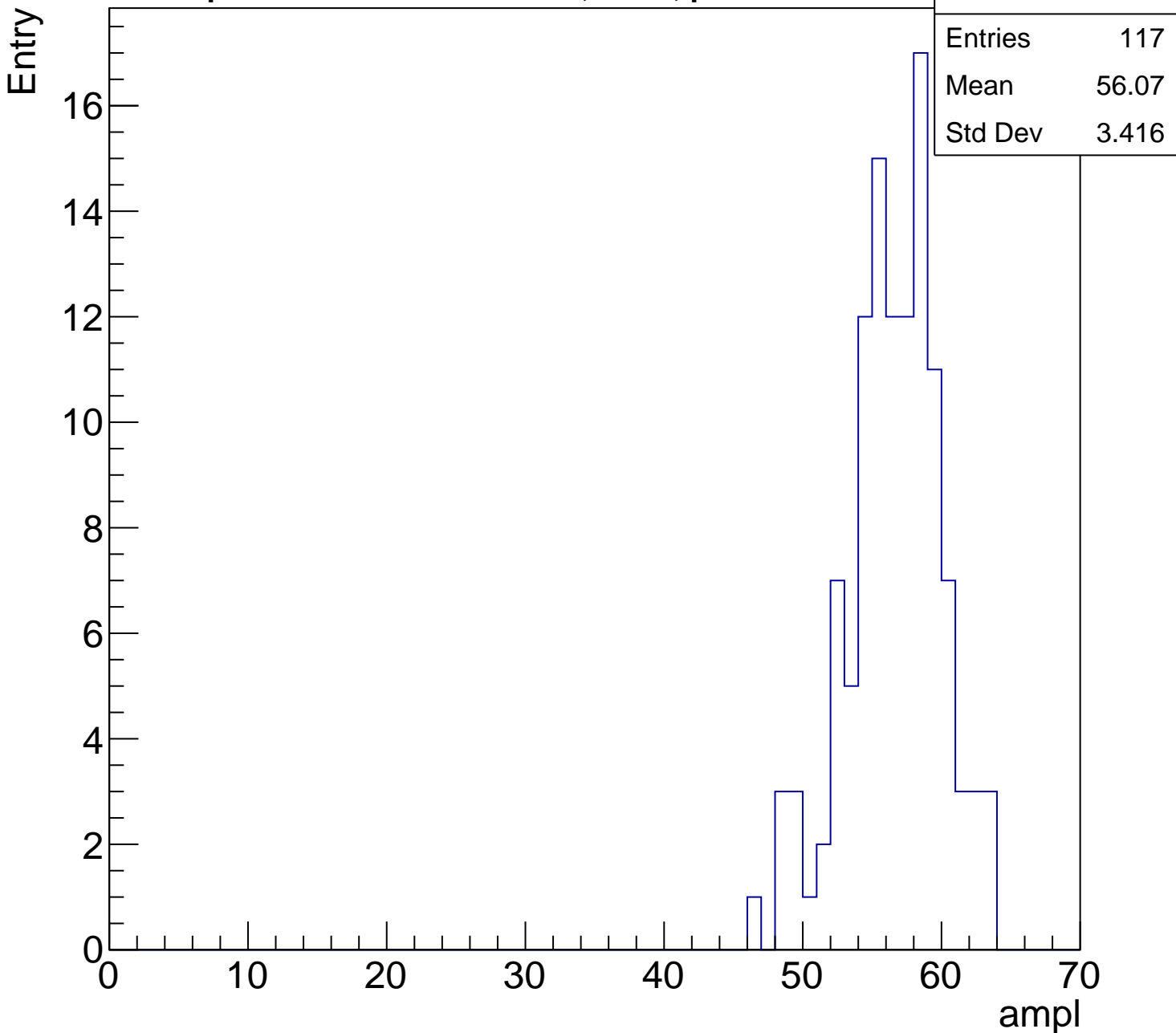
Entries	117
Mean	56.07
Std Dev	3.416

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#1, port C1

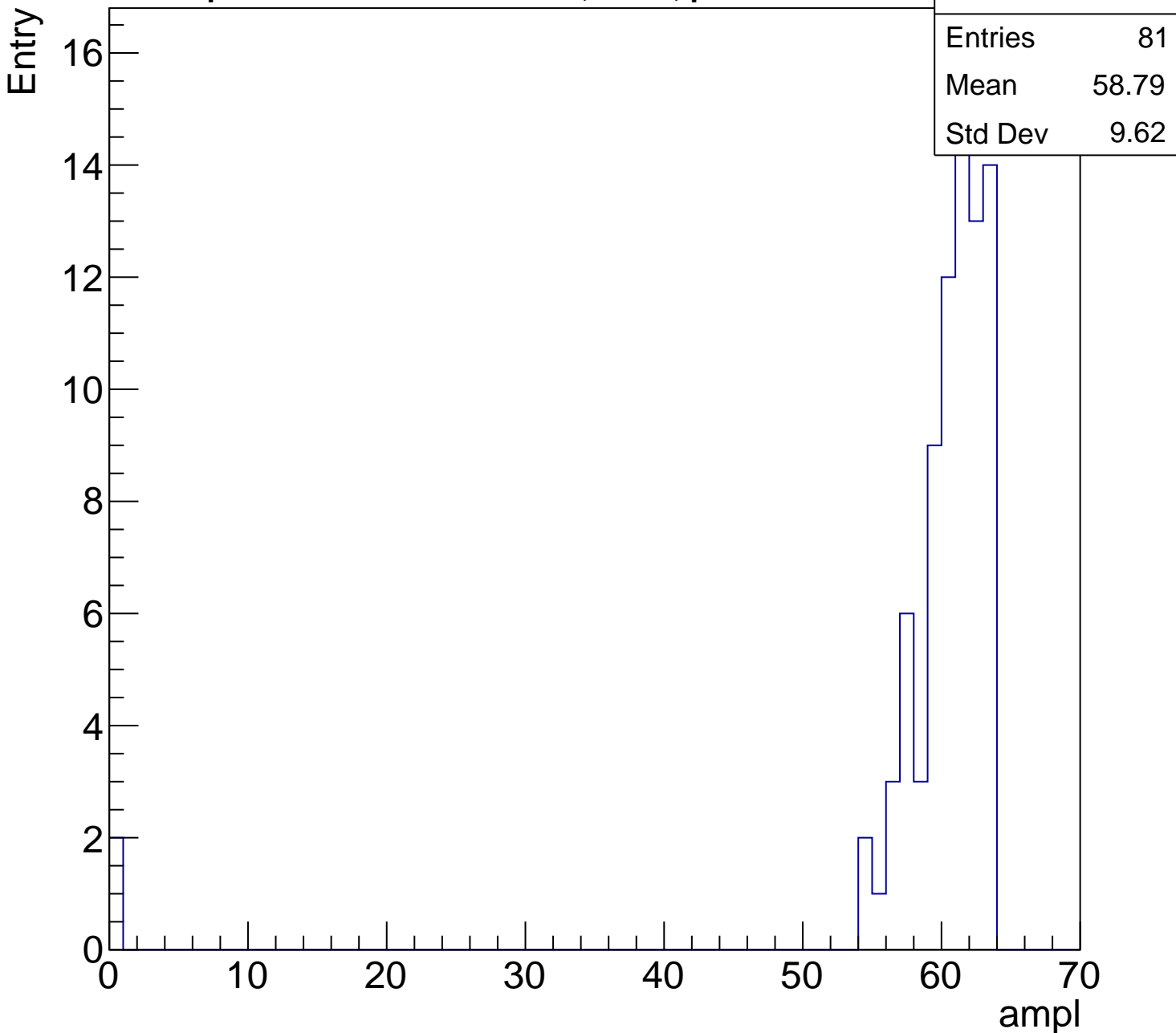
Entries	81
Mean	58.79
Std Dev	9.62

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

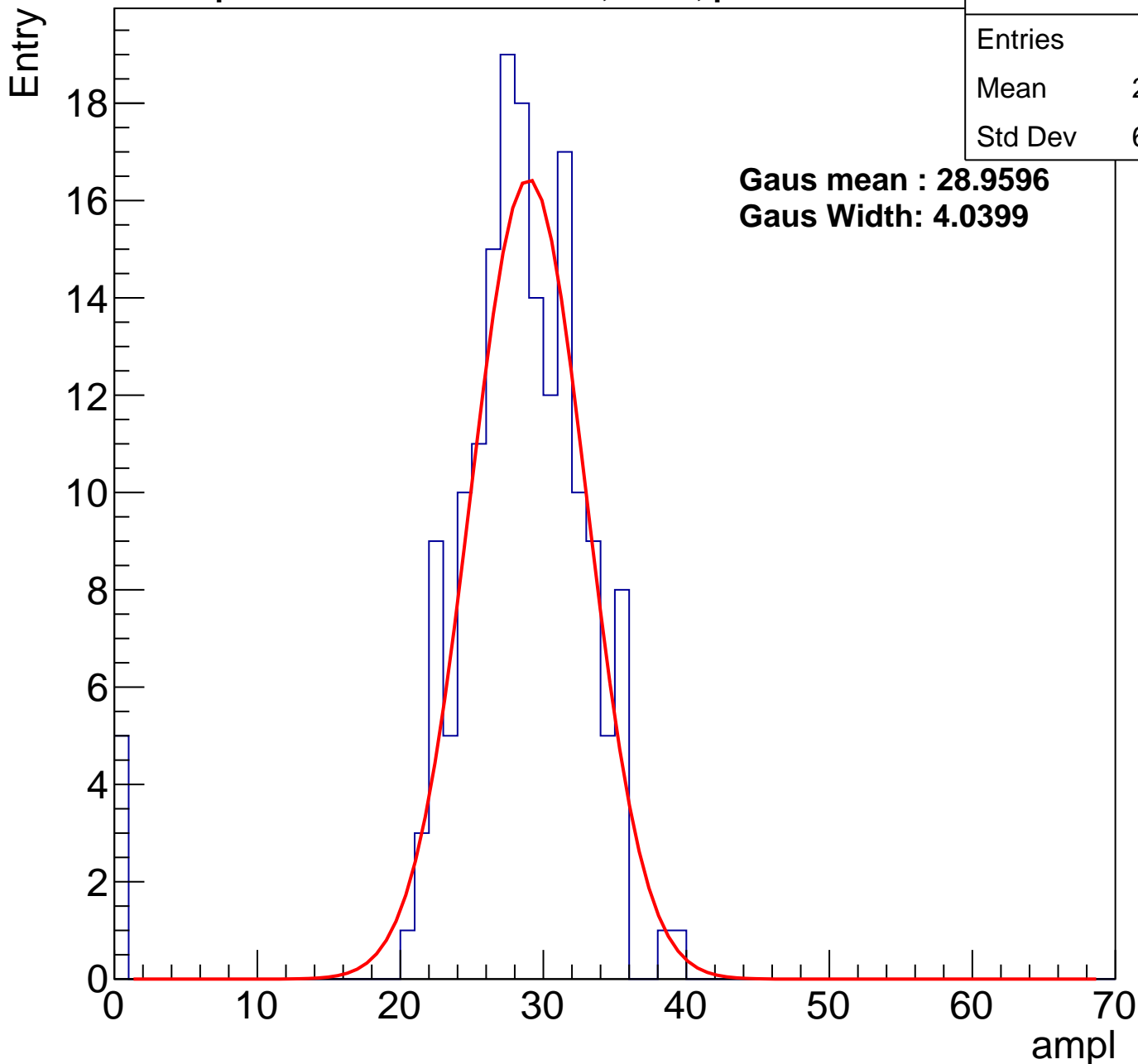
B0L101S, U14-ch80, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	27.49
Std Dev	6.018

Gaus mean : 28.9596

Gaus Width: 4.0399



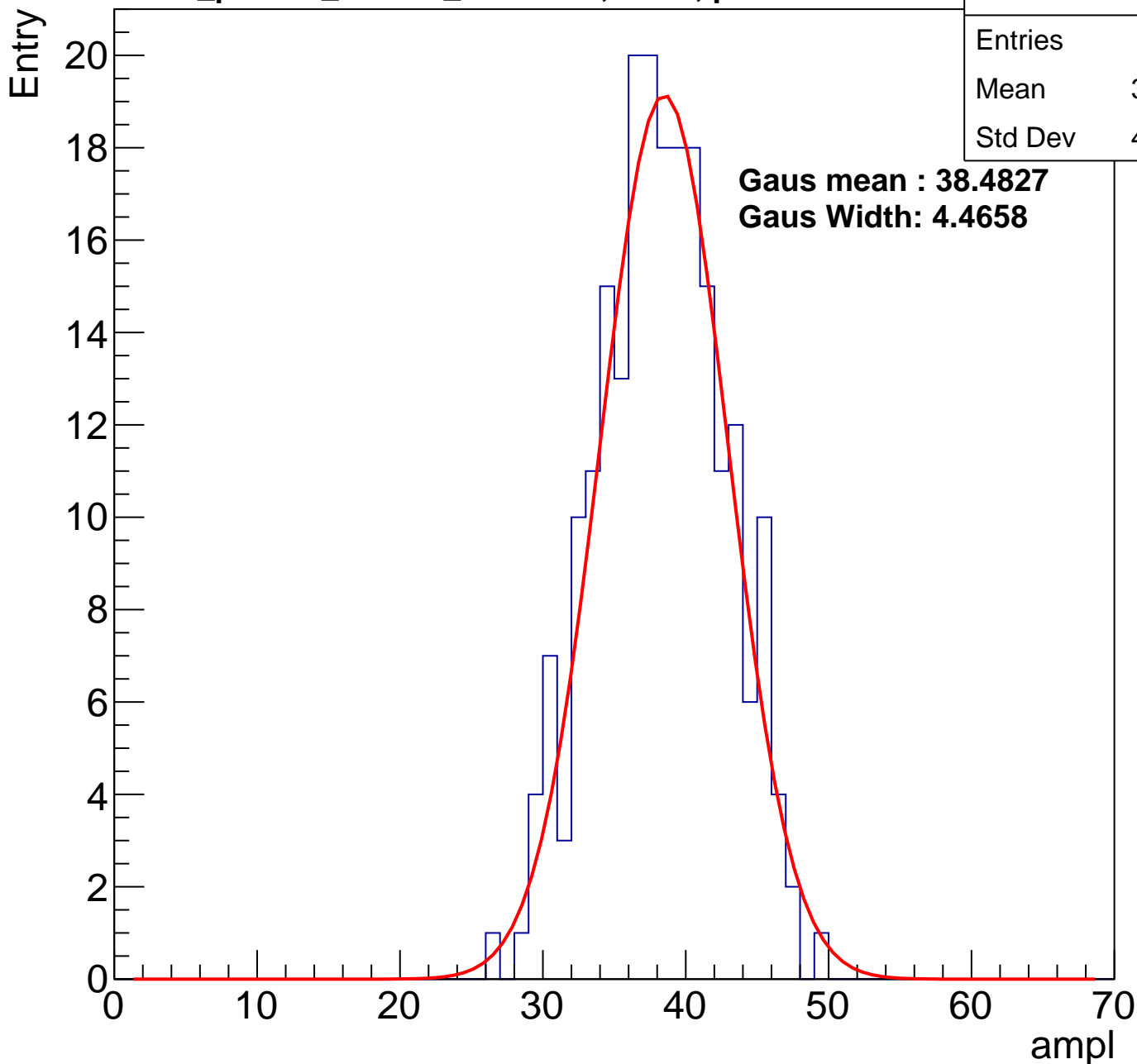
B0L101S, U14-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	220
Mean	37.82
Std Dev	4.366

Gaus mean : 38.4827

Gaus Width: 4.4658



B0L101S, U14-ch80, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	45
Mean	44.78
Std Dev	3.306

Gaus mean : 45.0180

Gaus Width: 2.9590

Entry

10

8

6

4

2

0

0

10

20

30

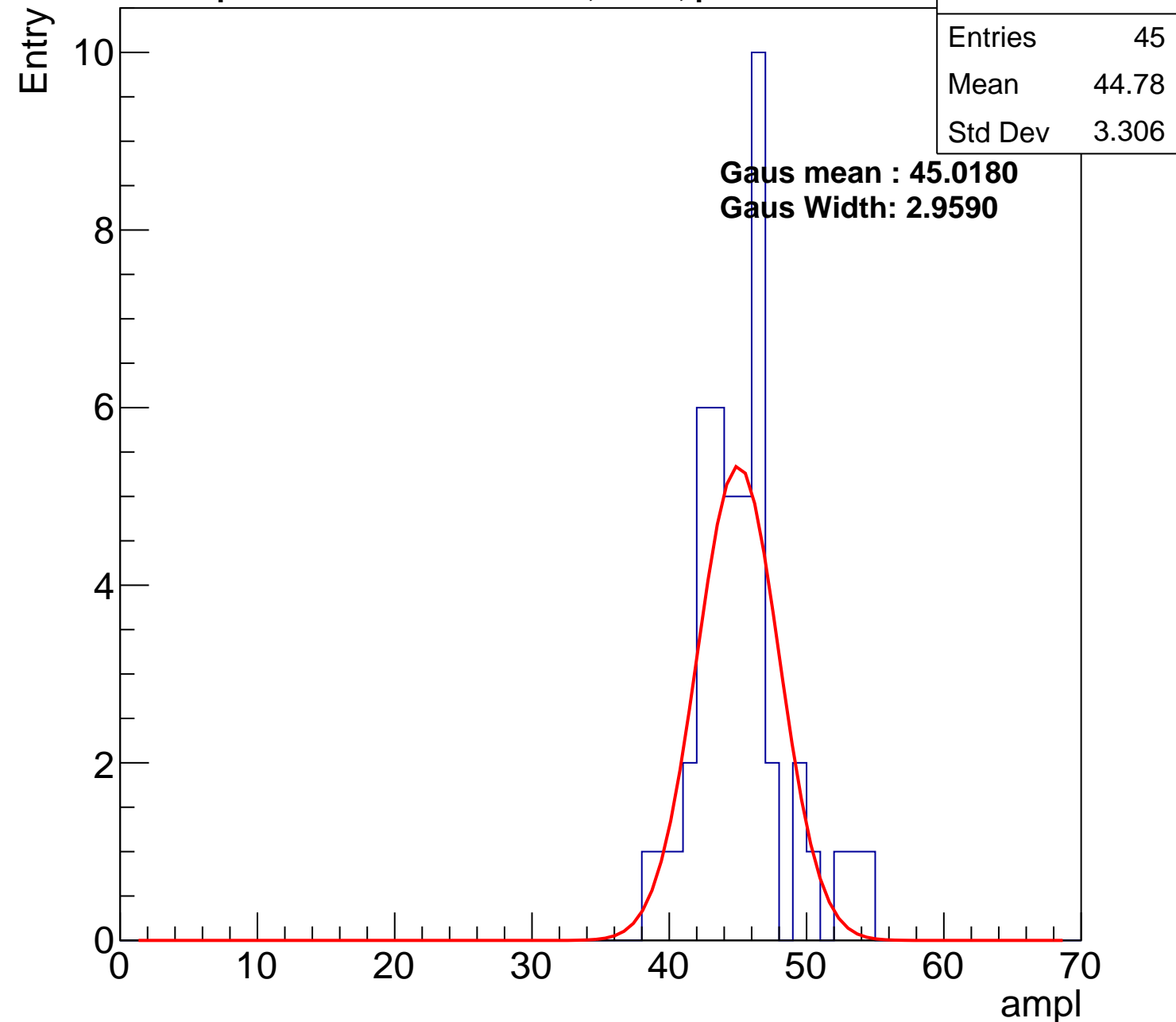
40

50

60

70

ampl

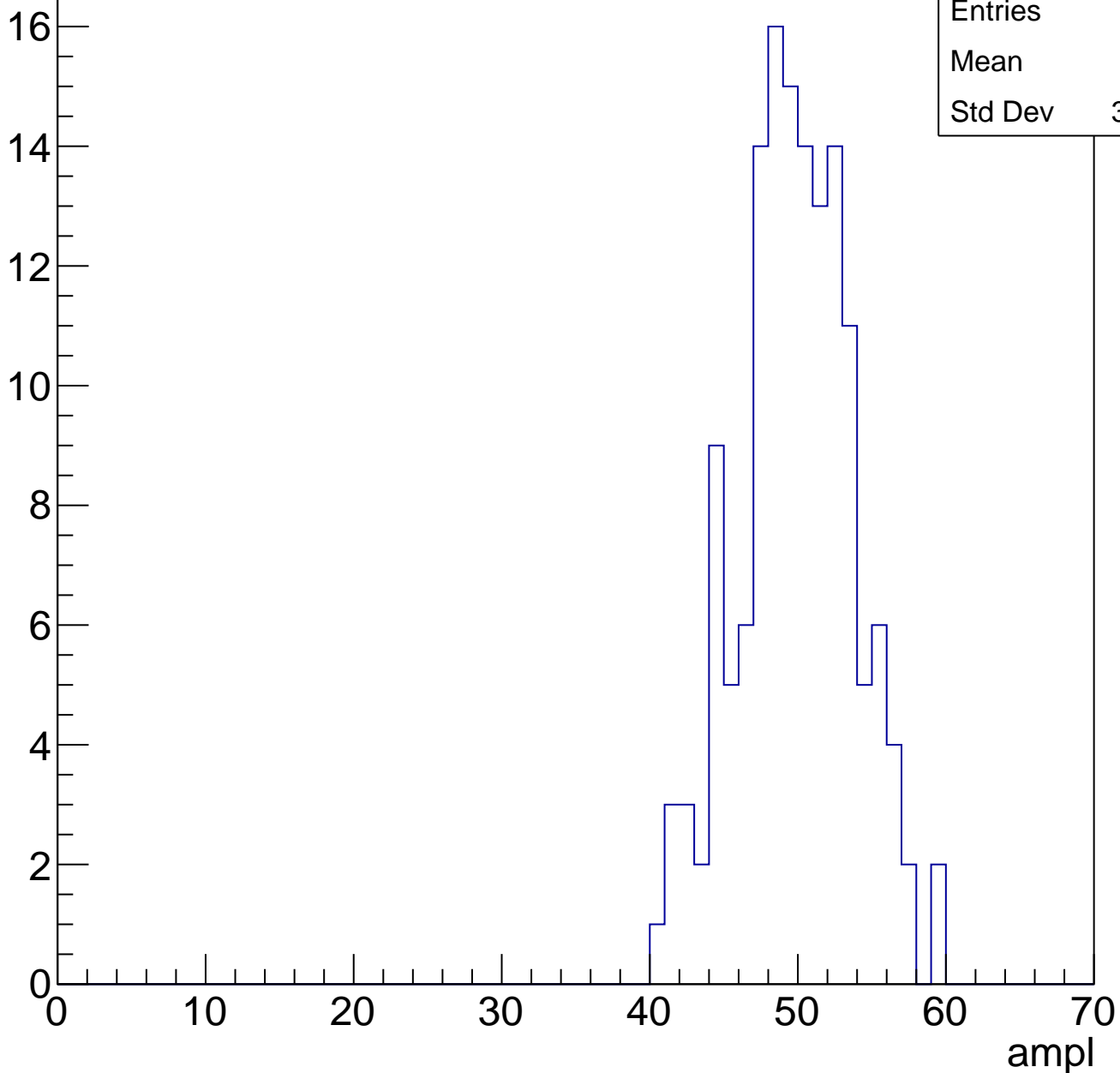


B0L101S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	49.4
Std Dev	3.835

Entry



B0L101S, U14-ch80, adc4

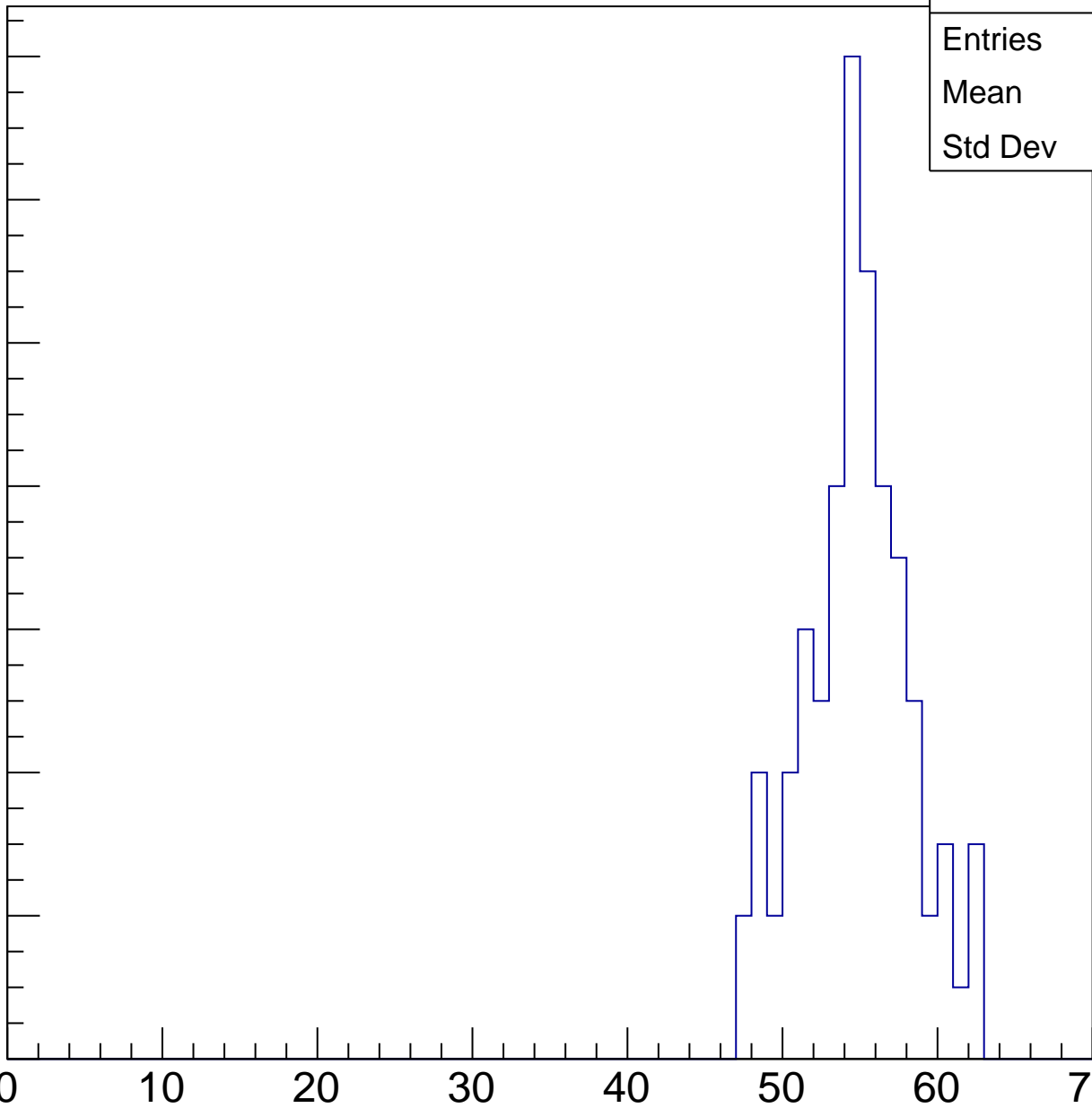
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	54.32
Std Dev	3.455

ampl



B0L101S, U14-ch80, adc5

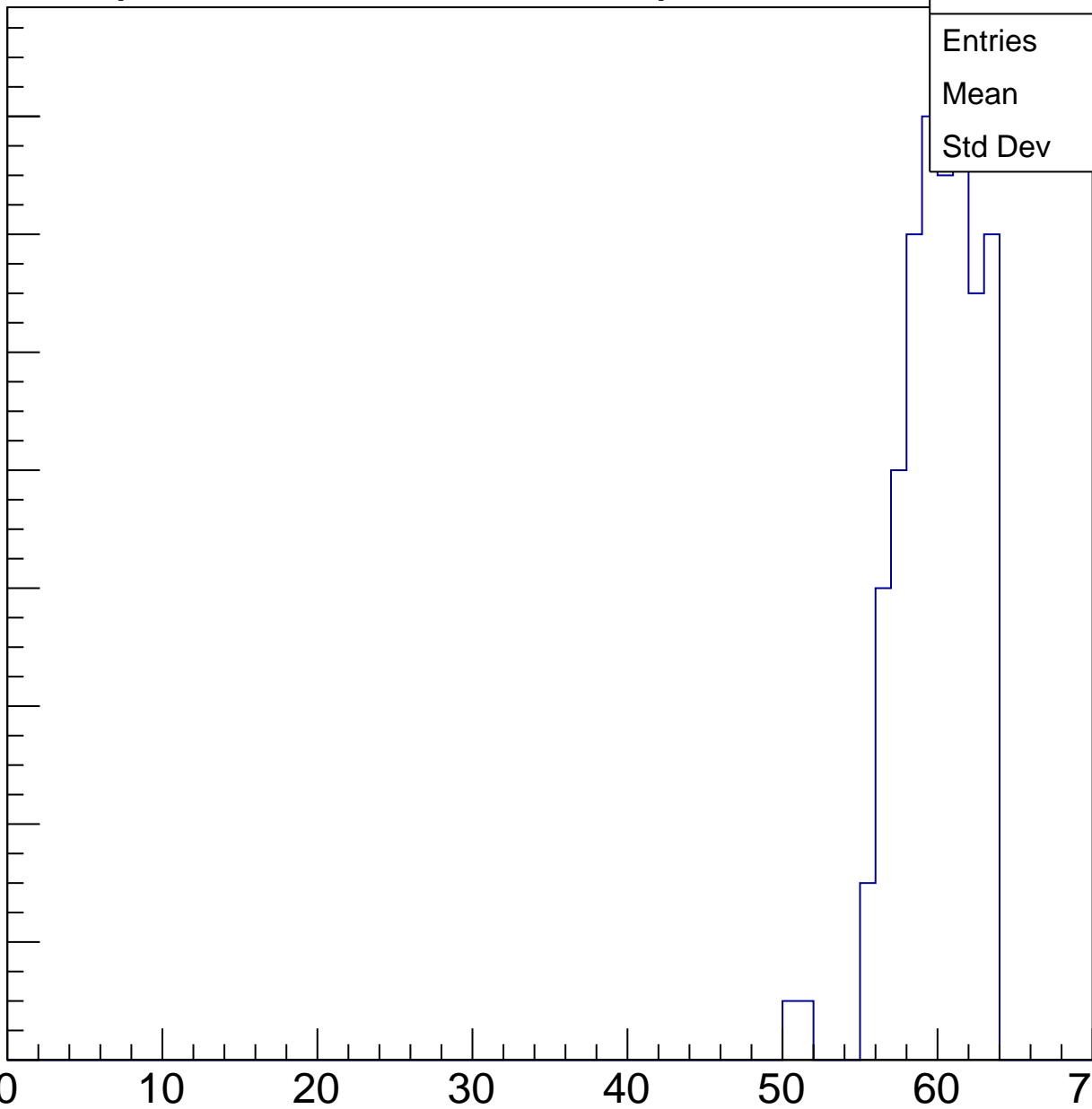
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	59.51
Std Dev	2.525

ampl

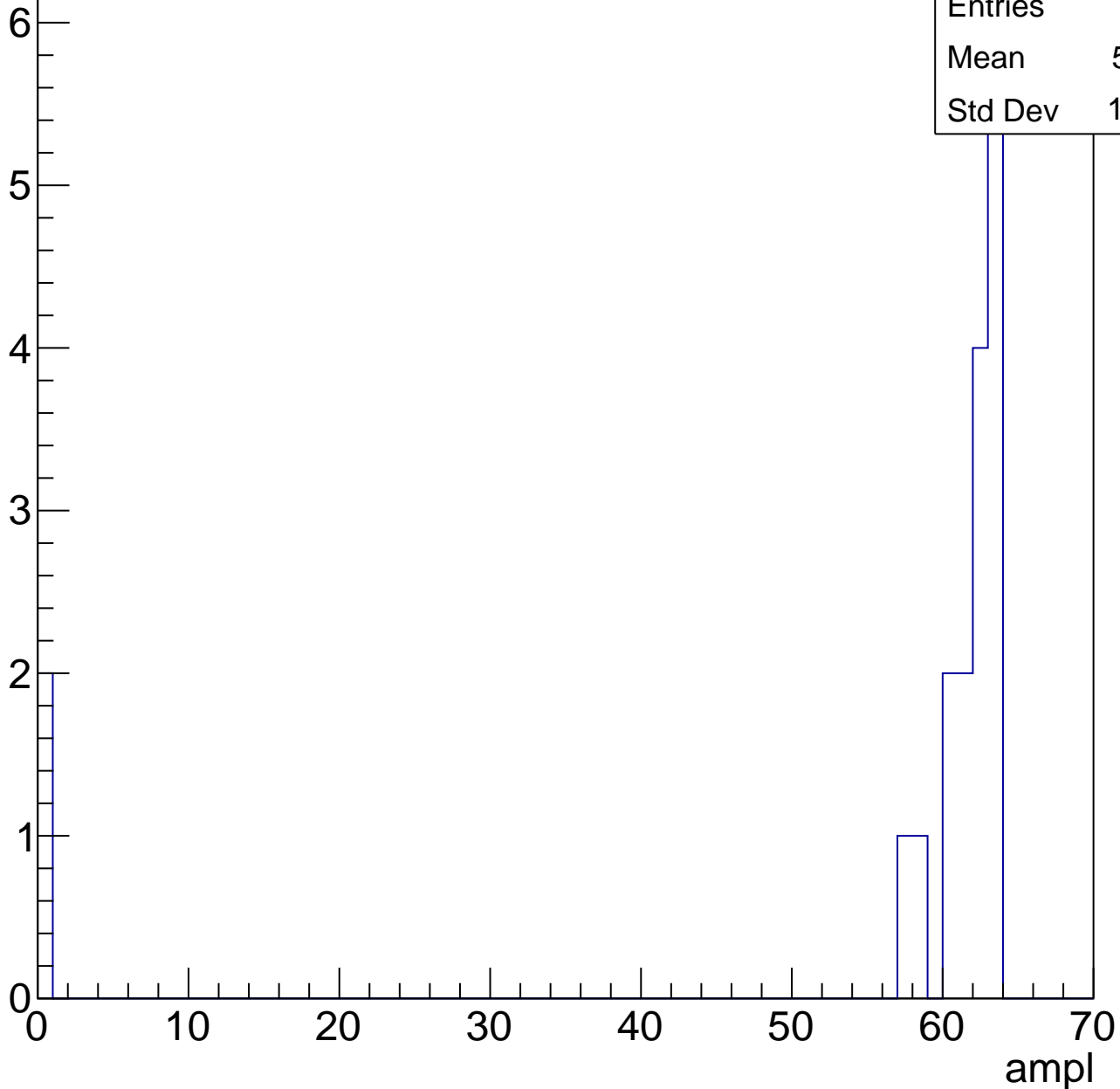


B0L101S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

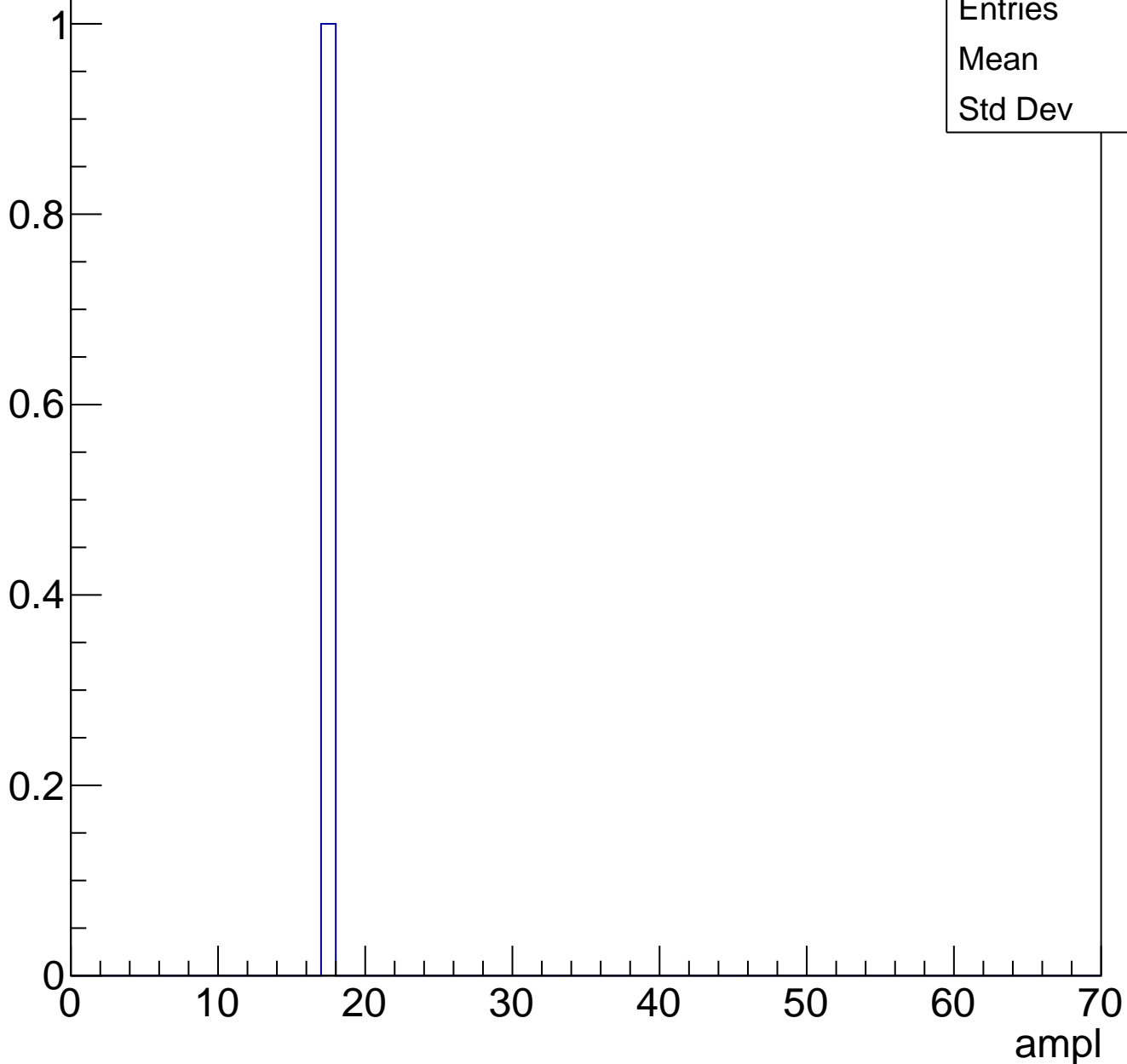
Entries	18
Mean	54.61
Std Dev	19.38



B0L101S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch81, adc0

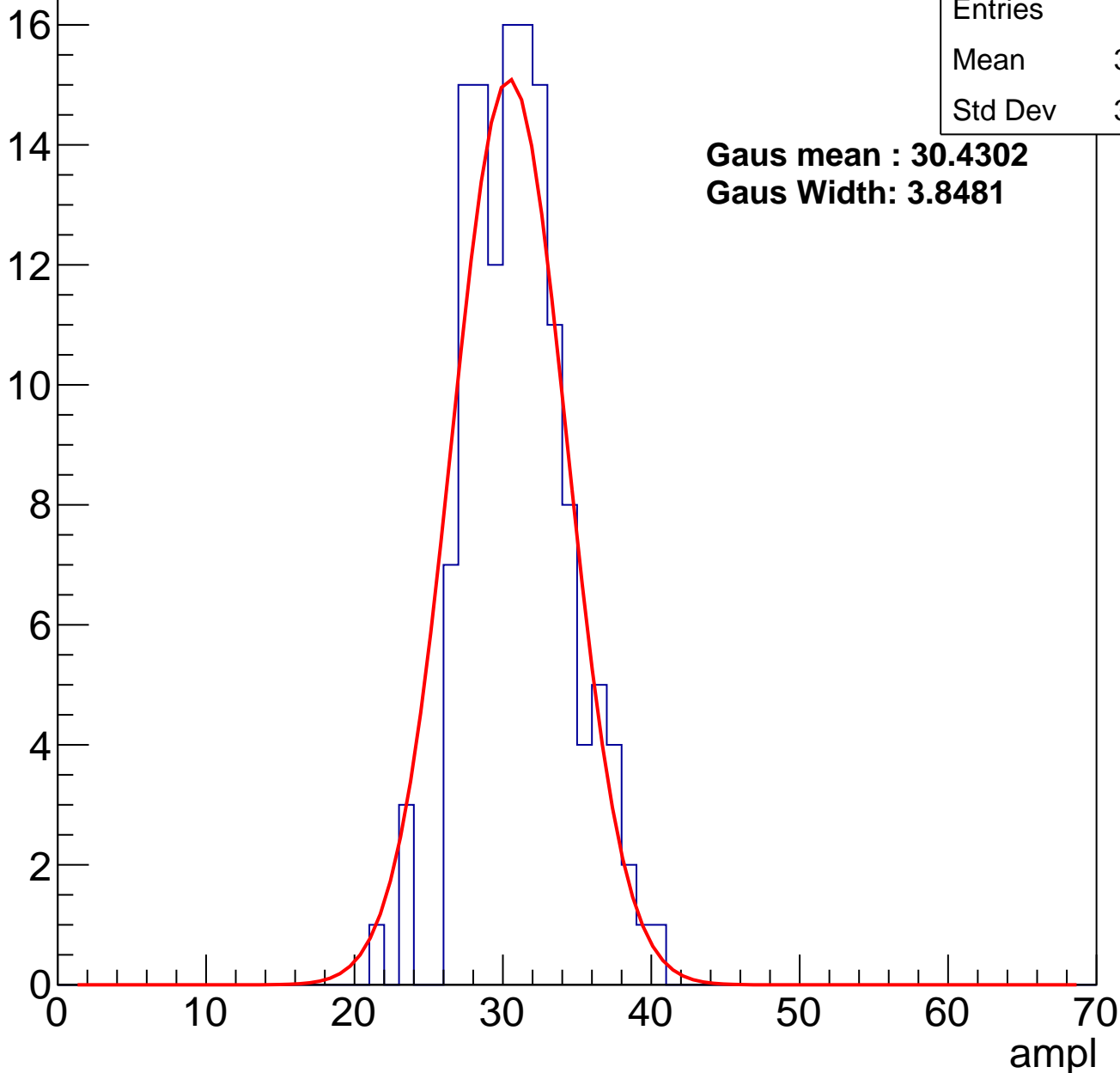
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	30.58
Std Dev	3.418

Gaus mean : 30.4302

Gaus Width: 3.8481

Entry



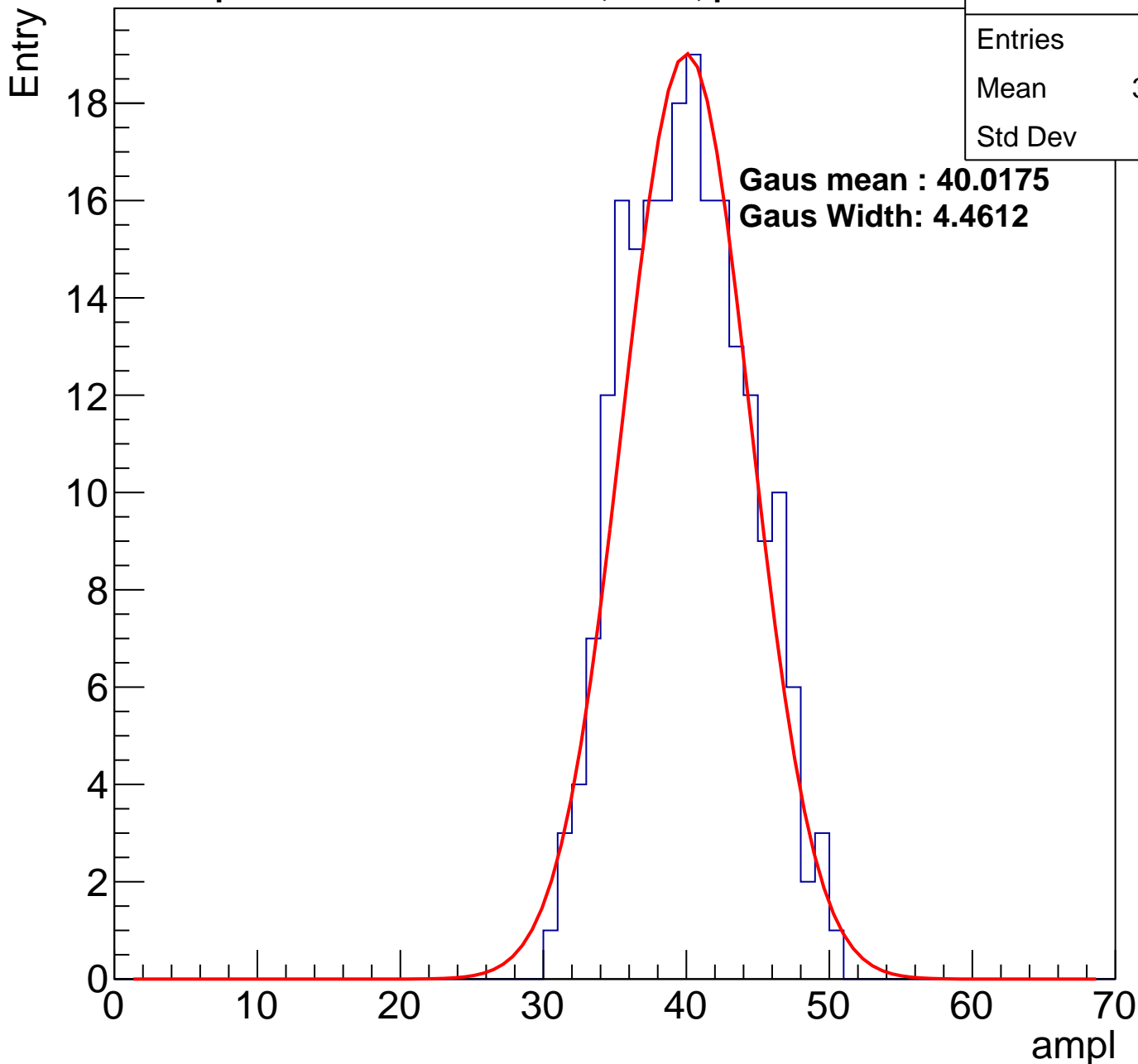
B0L101S, U14-ch81, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	215
Mean	39.57
Std Dev	4.28

Gaus mean : 40.0175

Gaus Width: 4.4612



B0L101S, U14-ch81, adc2

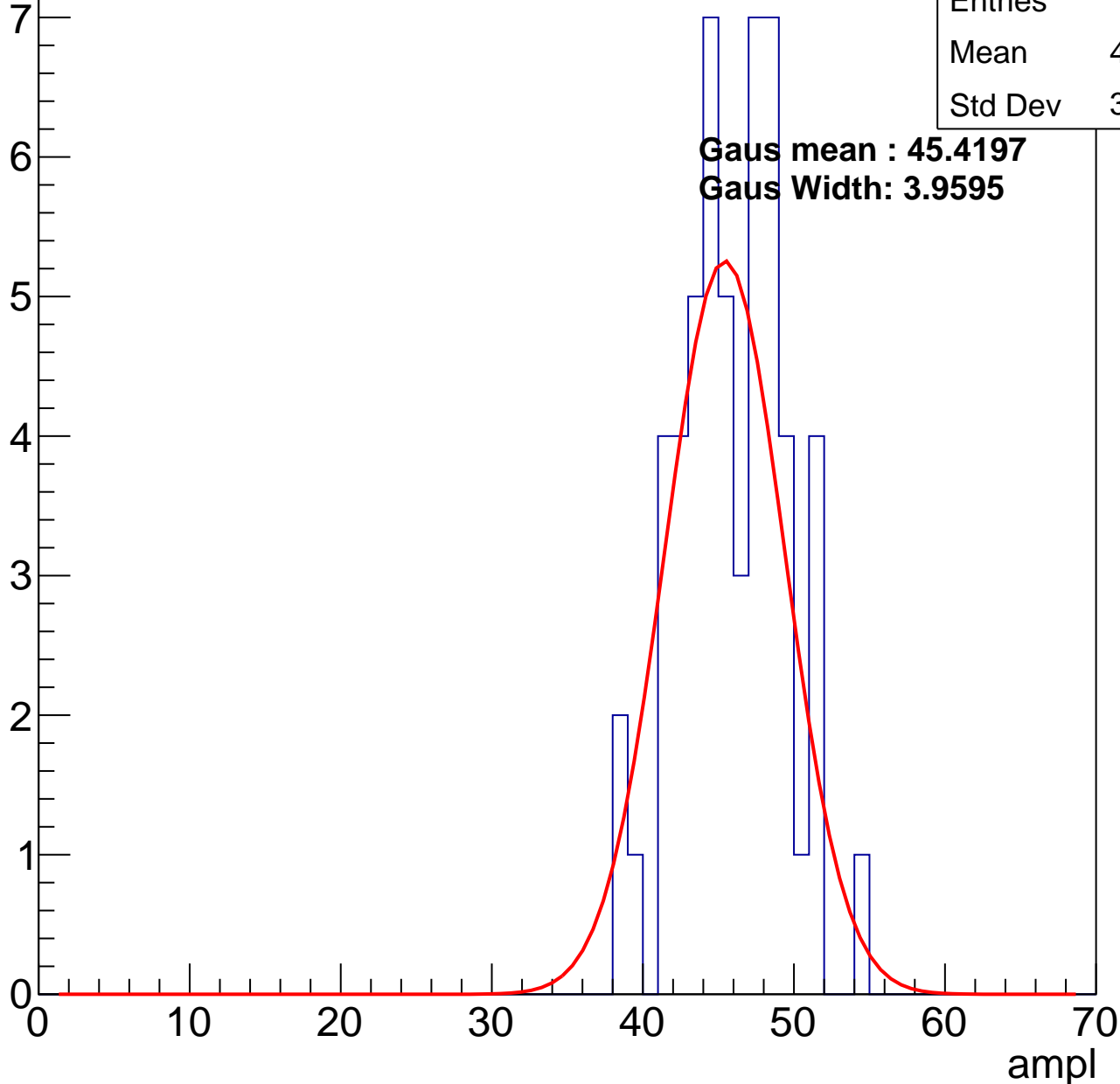
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	55
Mean	45.49
Std Dev	3.453

Gaus mean : 45.4197

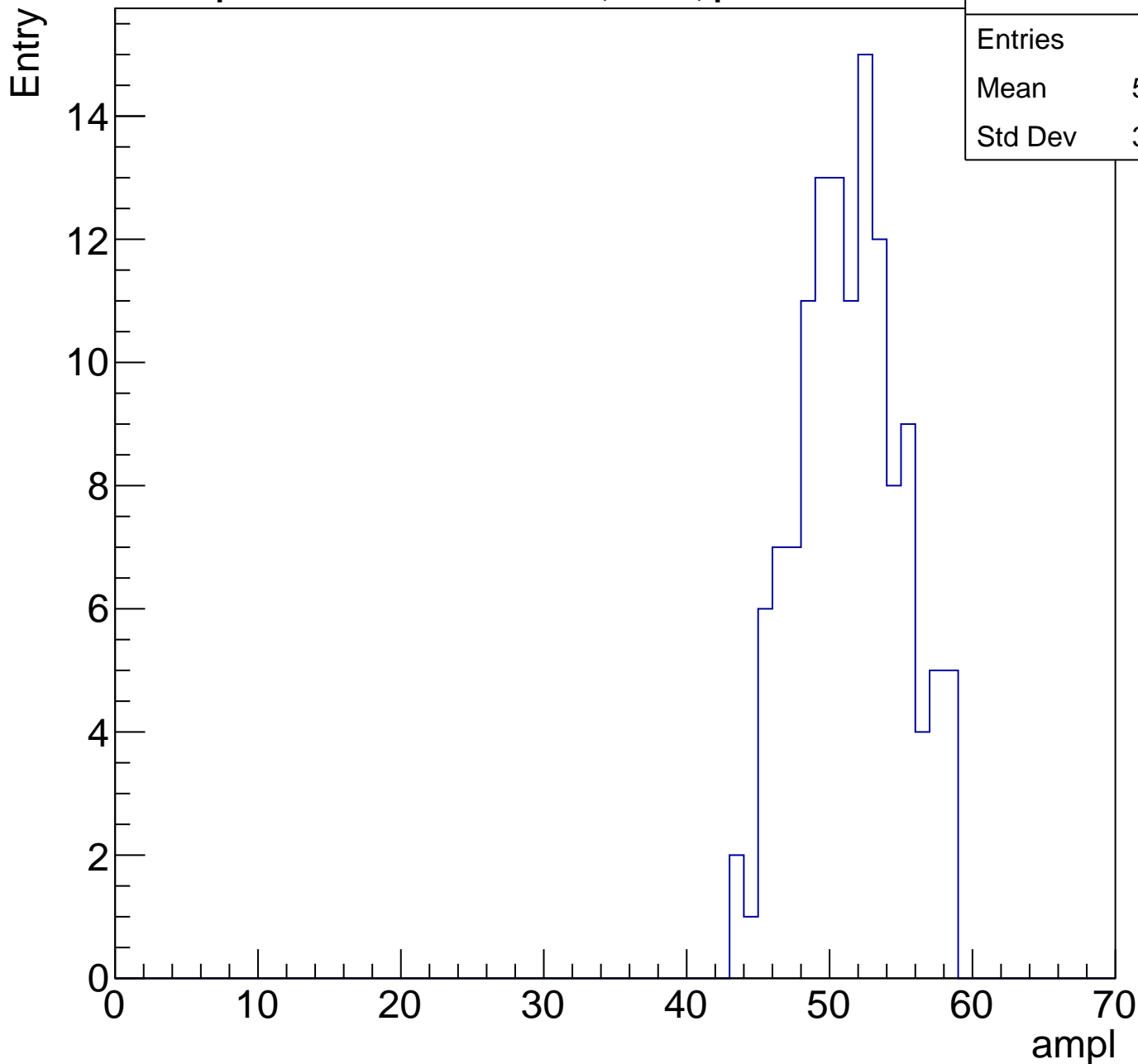
Gaus Width: 3.9595



B0L101S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	50.92
Std Dev	3.573



B0L101S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	56.49
Std Dev	3.394

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B0L101S, U14-ch81, adc5

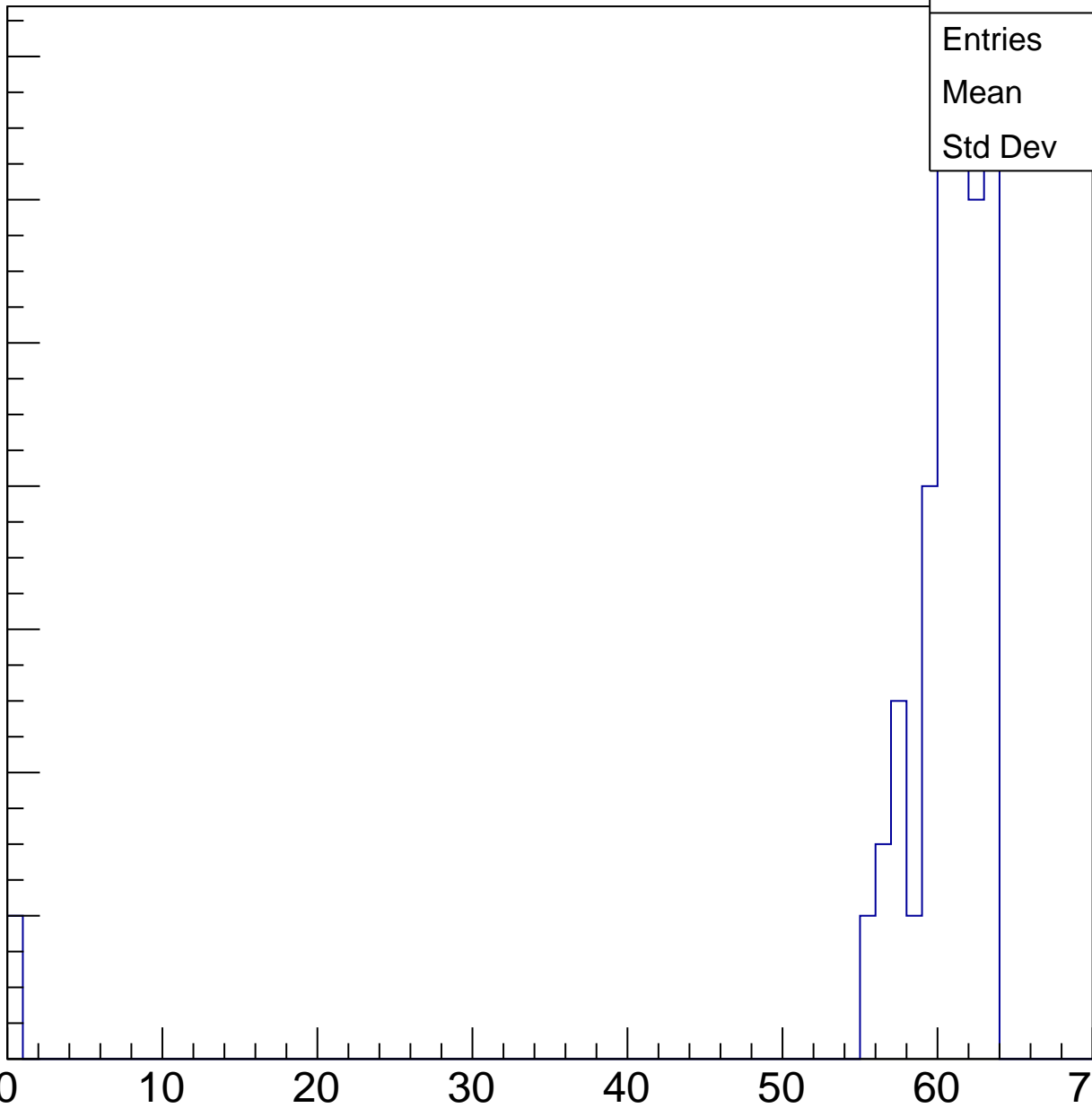
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	58.81
Std Dev	9.961

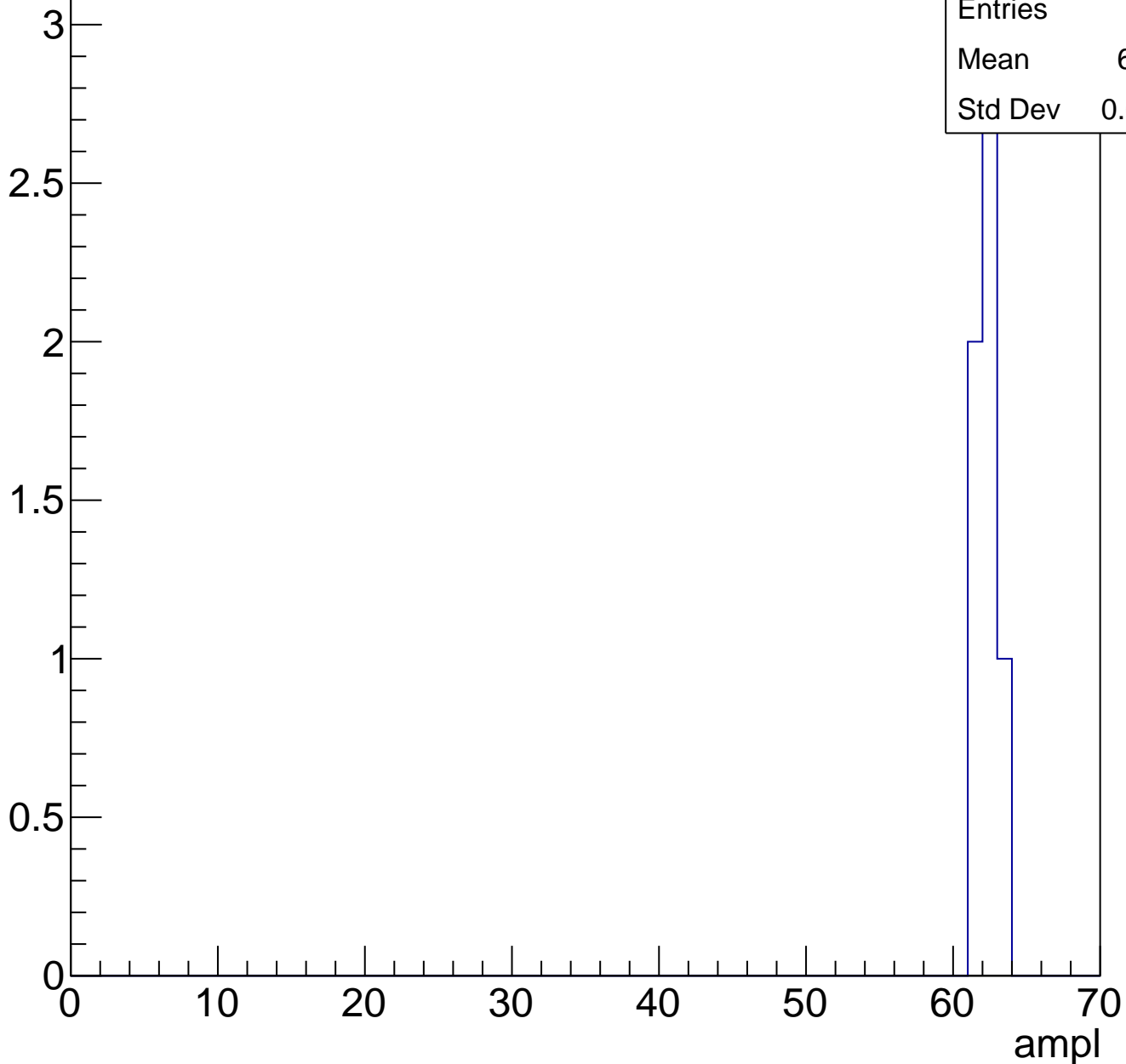
ampl



B0L101S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch82, adc0

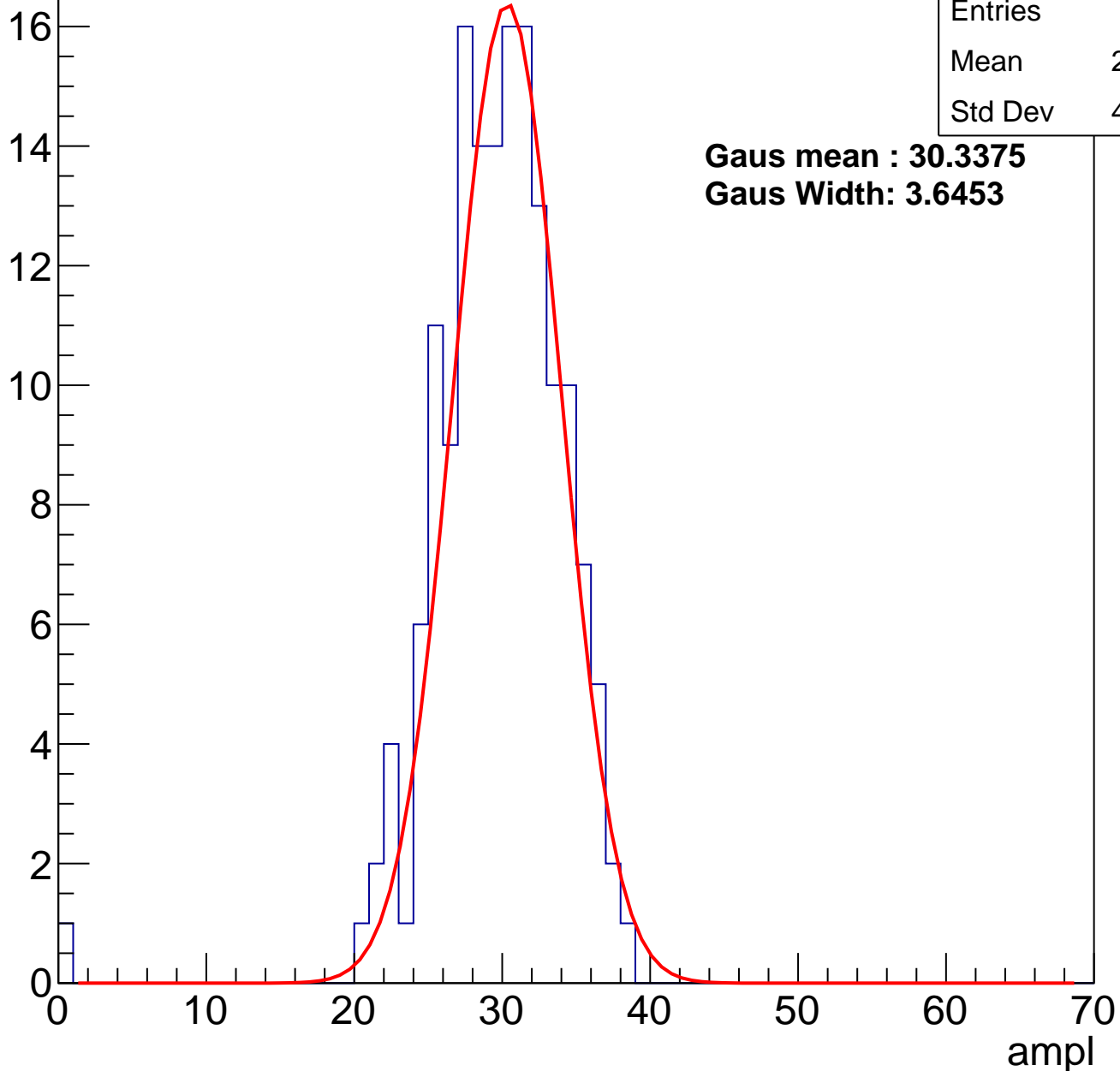
calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	29.28
Std Dev	4.393

Gaus mean : 30.3375

Gaus Width: 3.6453

Entry



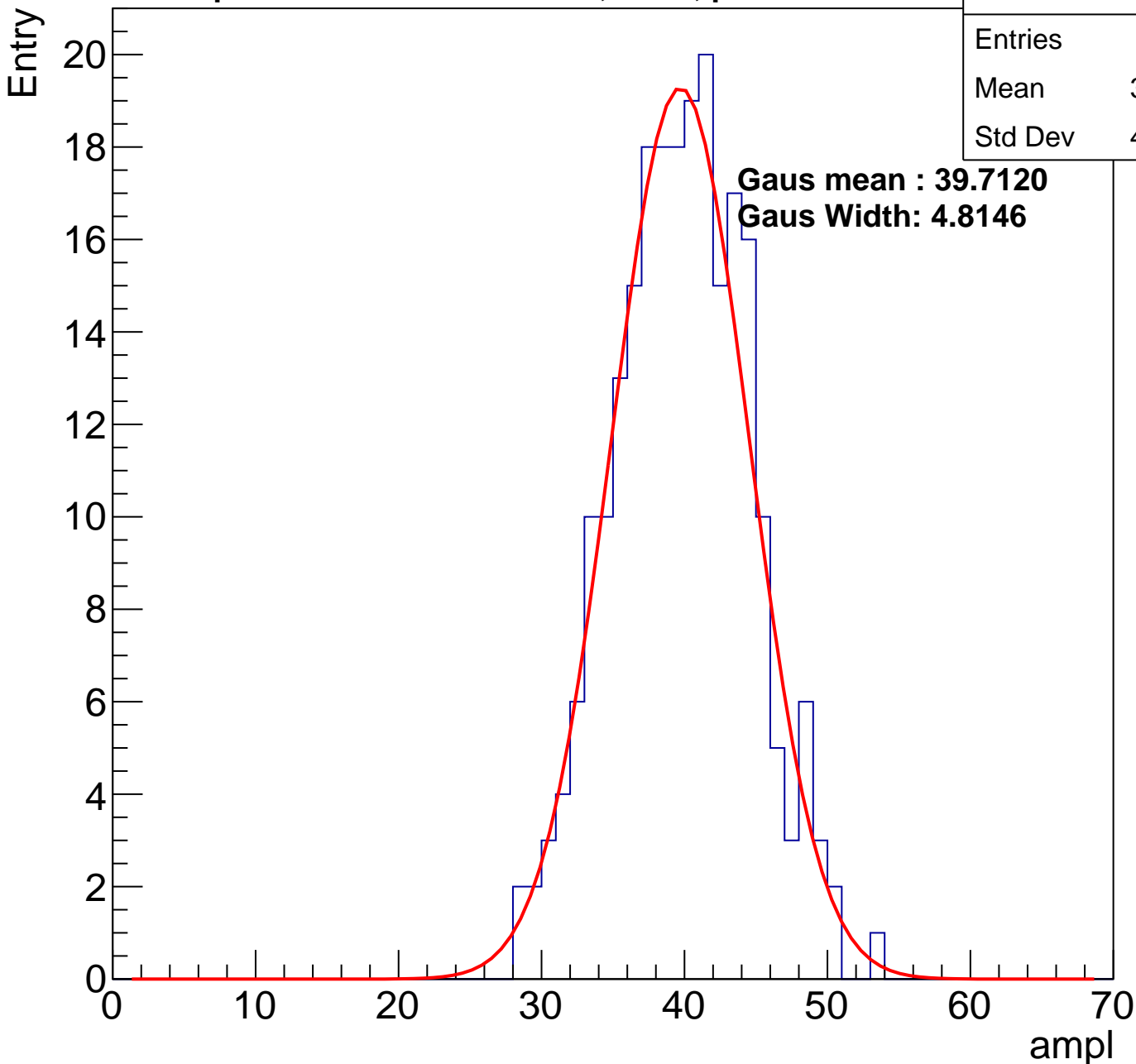
B0L101S, U14-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	236
Mean	39.37
Std Dev	4.682

Gaus mean : 39.7120

Gaus Width: 4.8146



B0L101S, U14-ch82, adc2

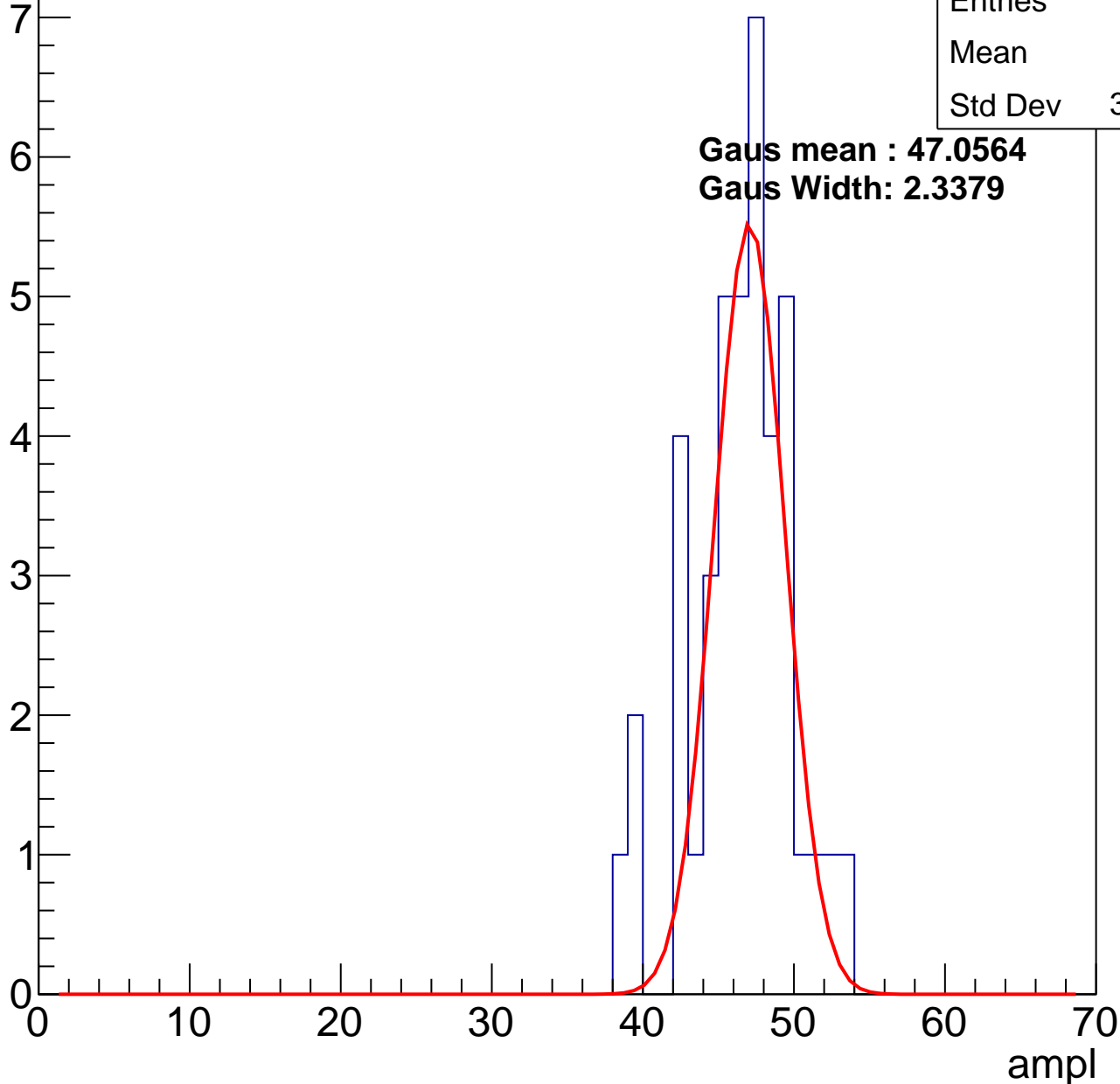
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	41
Mean	46
Std Dev	3.298

Gaus mean : 47.0564

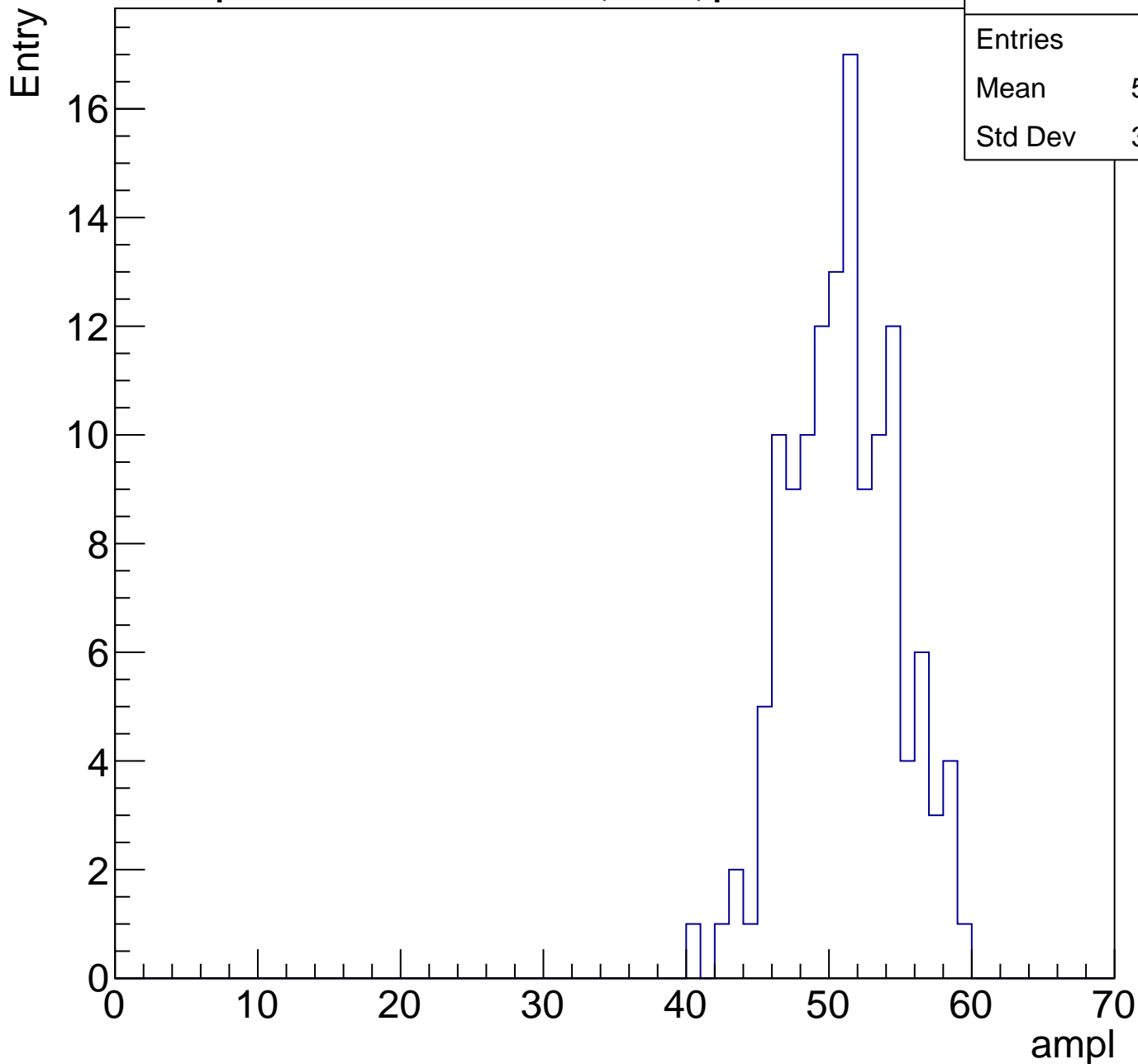
Gaus Width: 2.3379



B0L101S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	50.53
Std Dev	3.736



B0L101S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

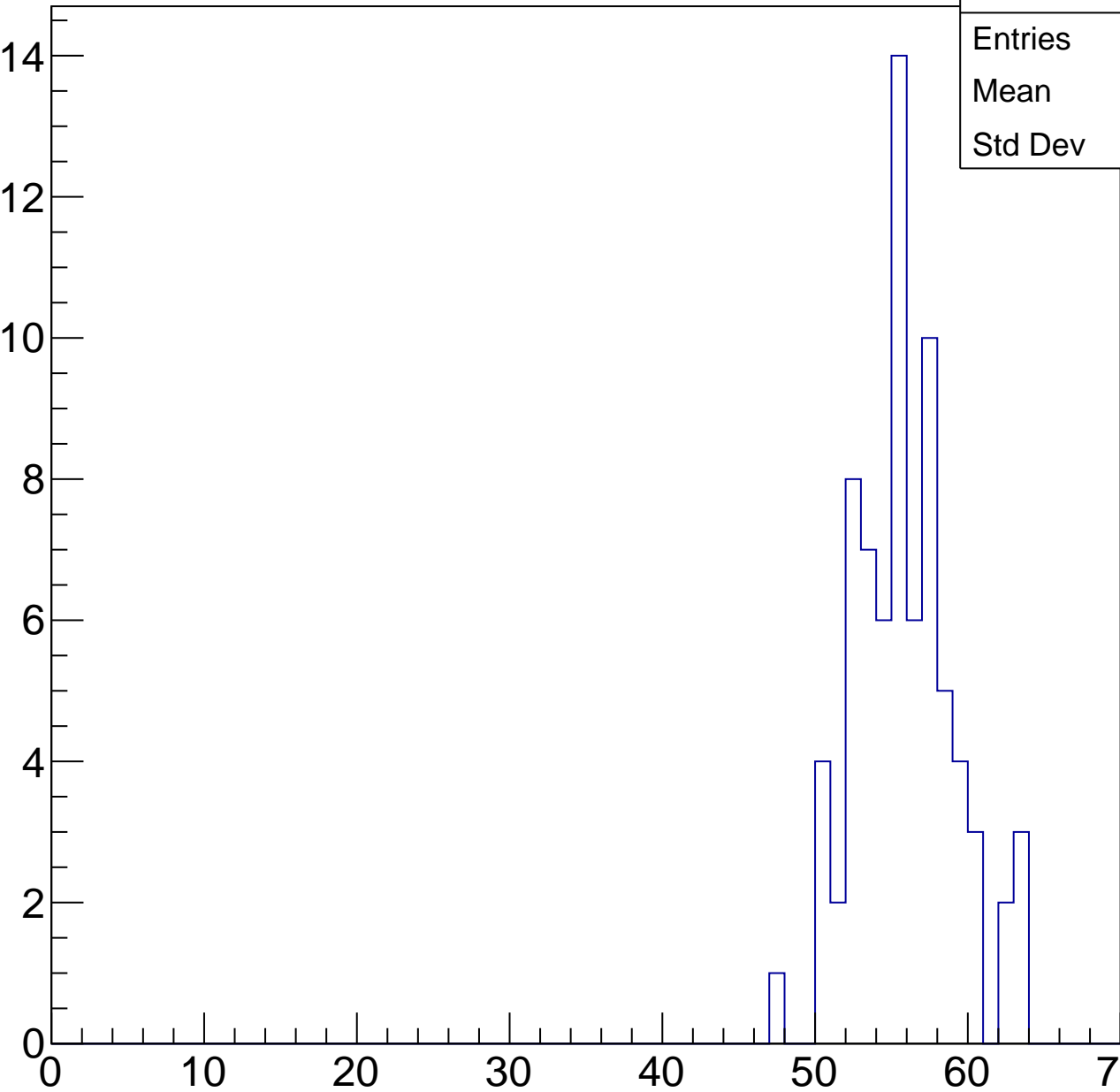
Entries	75
Mean	55.4
Std Dev	3.286

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch82, adc5

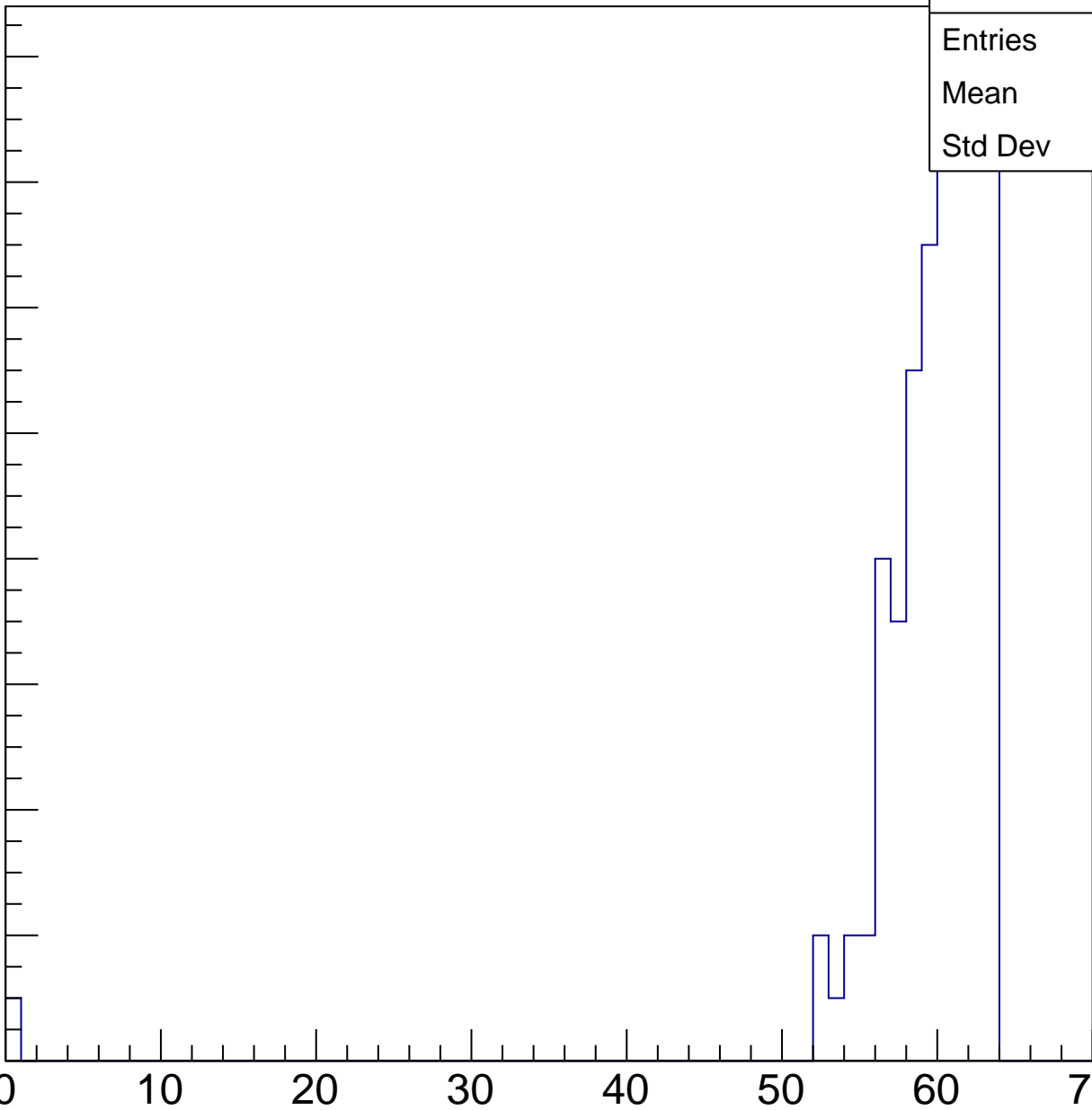
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	59.06
Std Dev	6.282

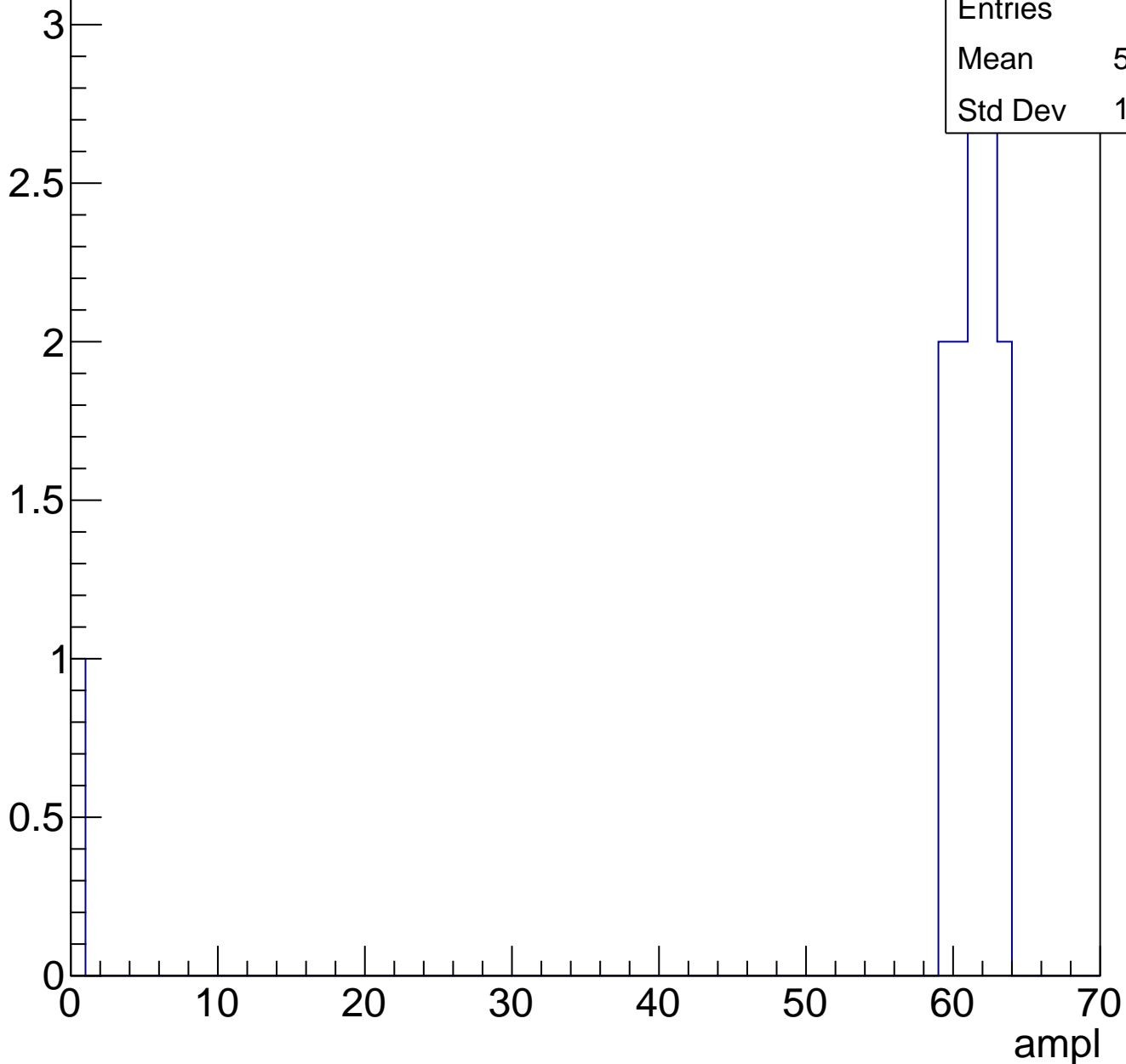
ampl



B0L101S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	20.33
Std Dev	28.76

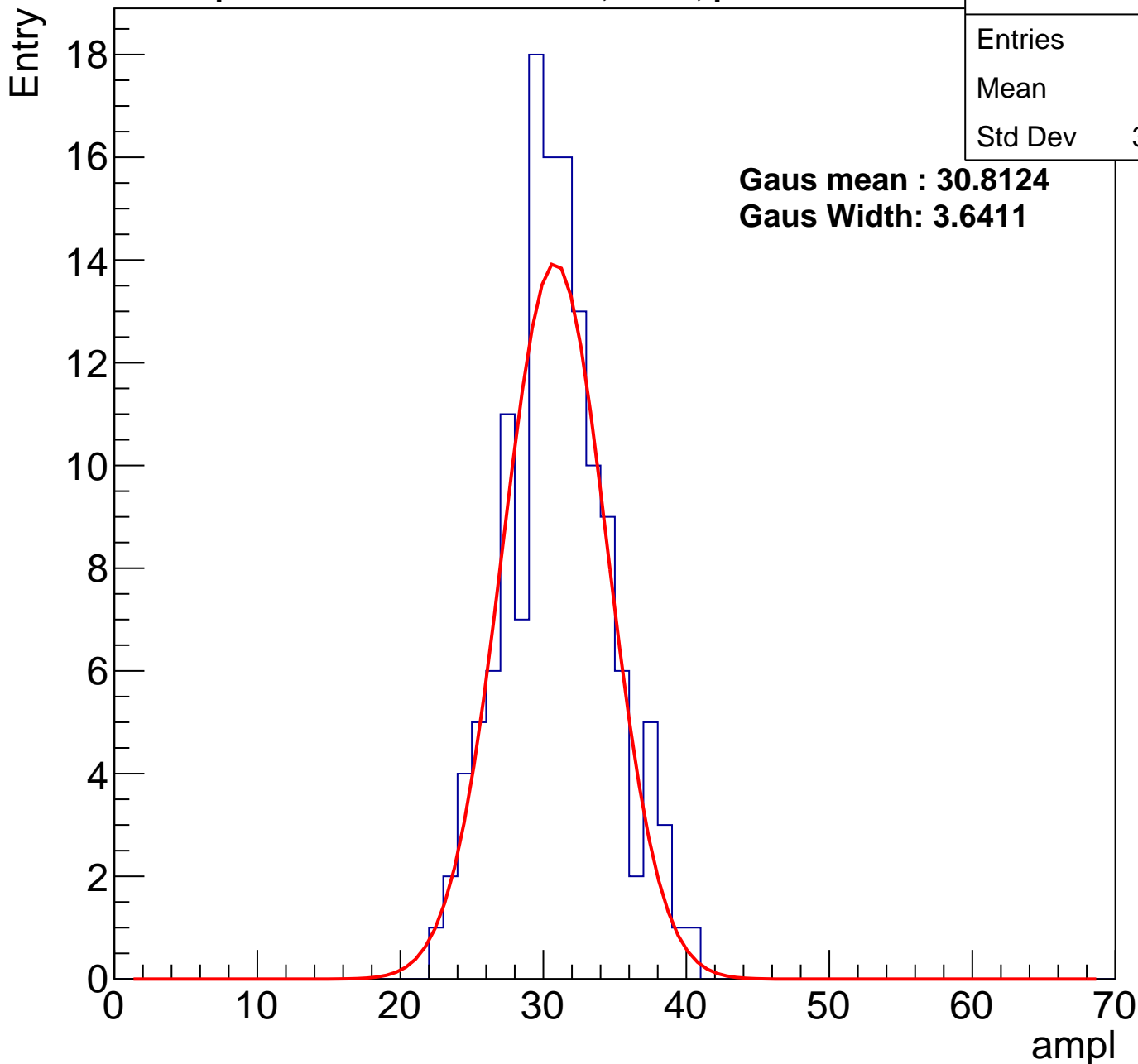
B0L101S, U14-ch83, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	30.5
Std Dev	3.618

Gaus mean : 30.8124

Gaus Width: 3.6411



B0L101S, U14-ch83, adc1

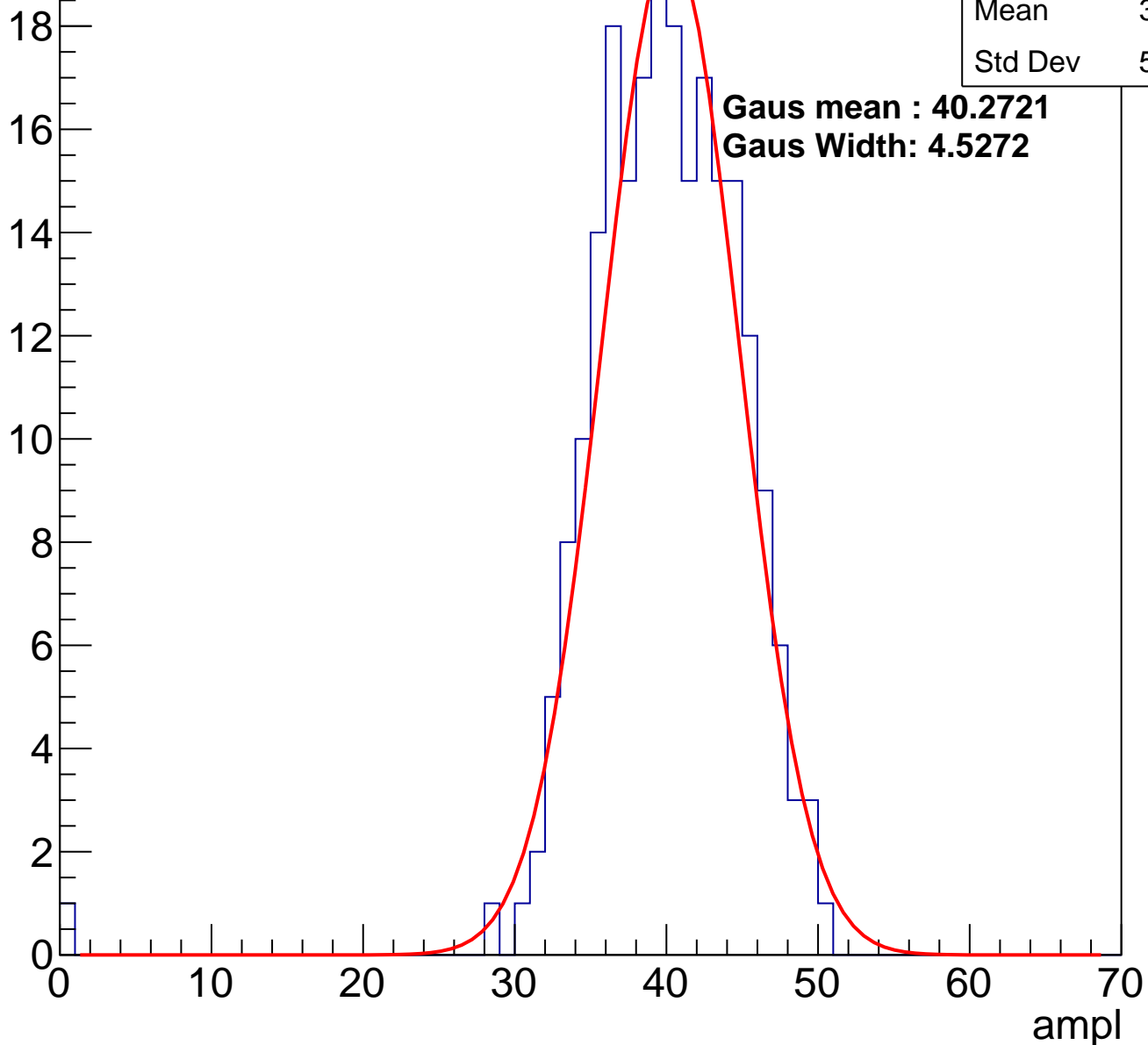
calib_packv5_042523_0143.root, FC#1, port C1

Entries	225
Mean	39.53
Std Dev	5.077

Gaus mean : 40.2721

Gaus Width: 4.5272

Entry

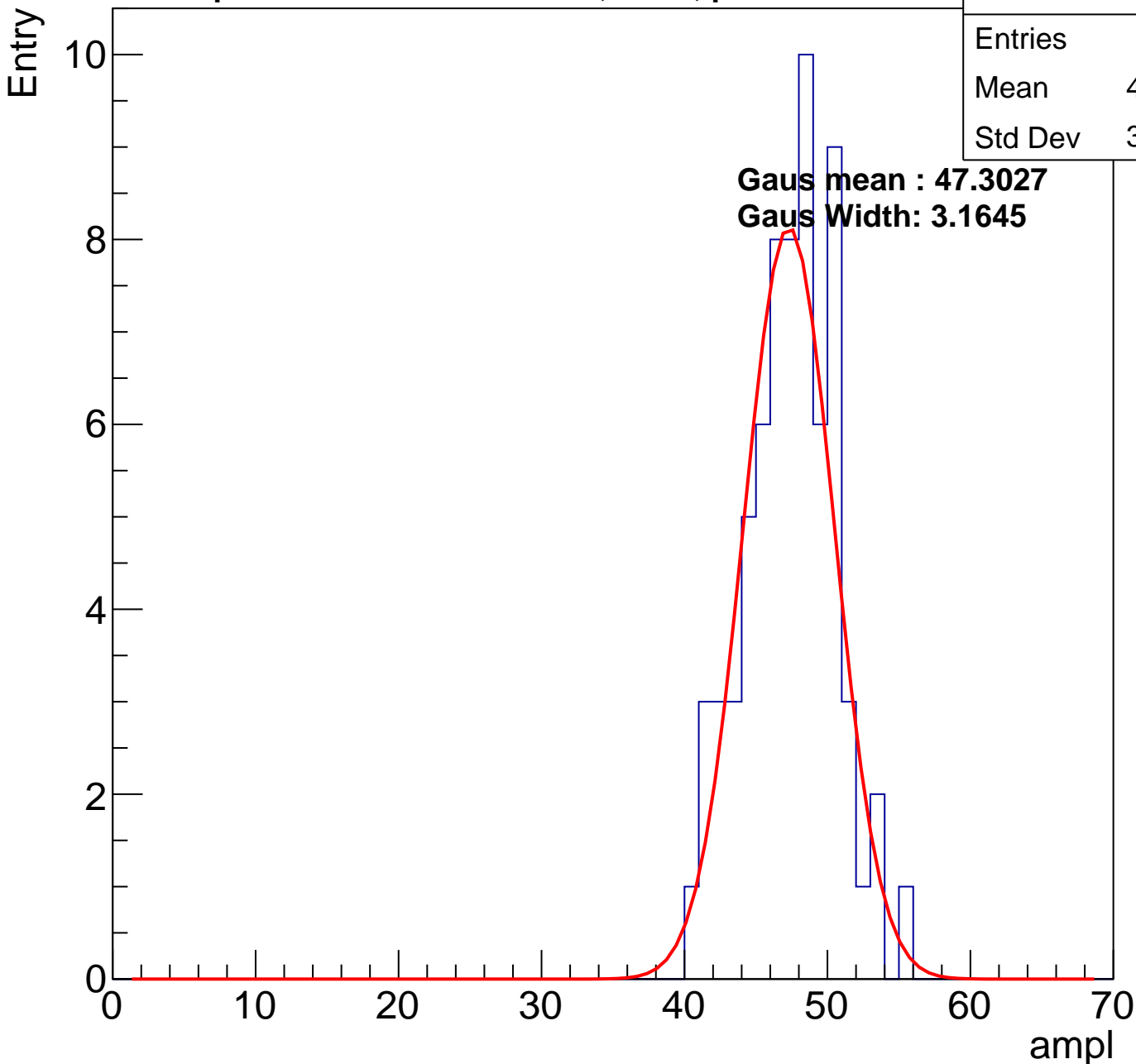


B0L101S, U14-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	69
Mean	46.99
Std Dev	3.142

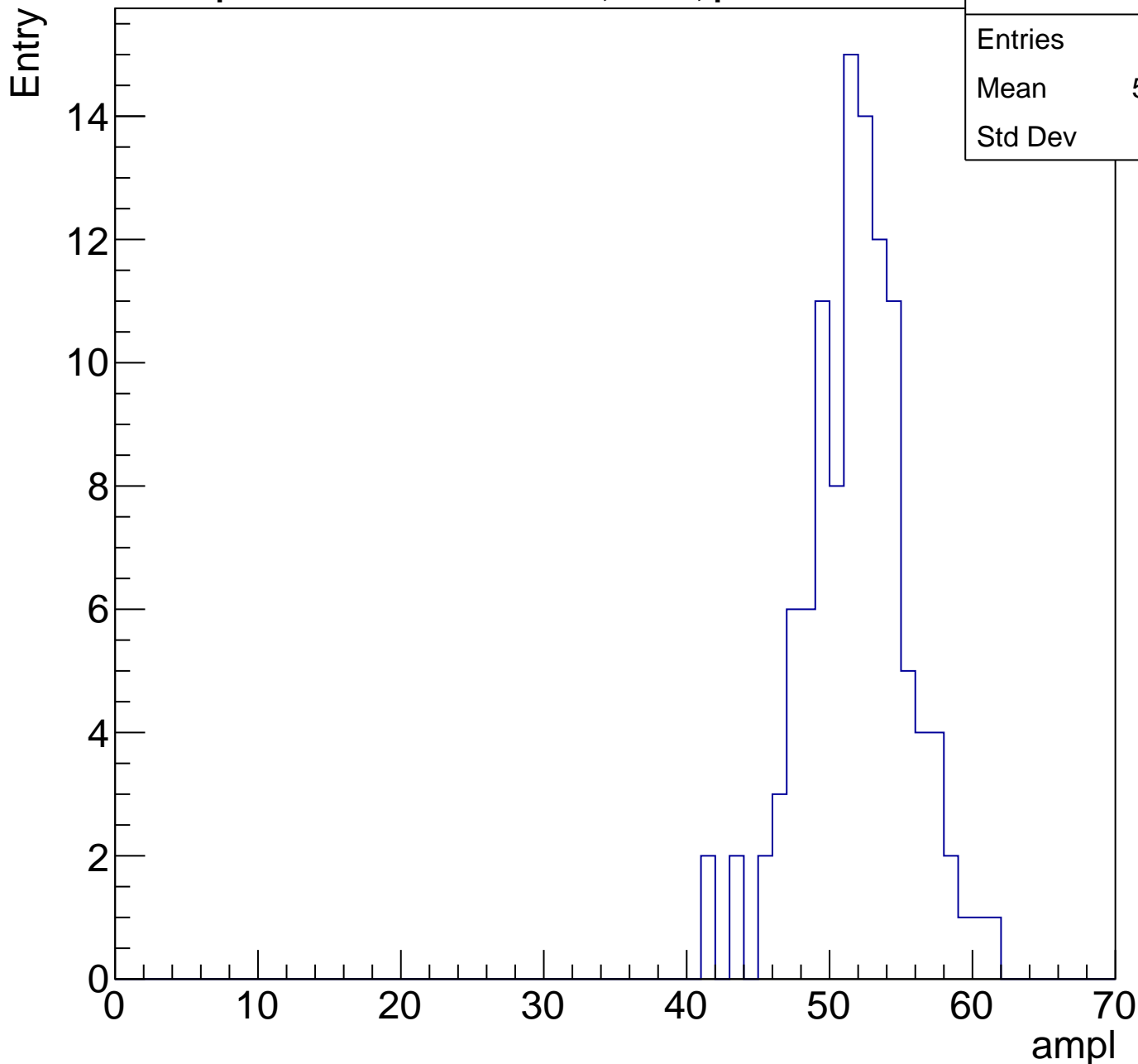
Gaus mean : 47.3027
Gaus Width: 3.1645



B0L101S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	51.37
Std Dev	3.68



B0L101S, U14-ch83, adc4

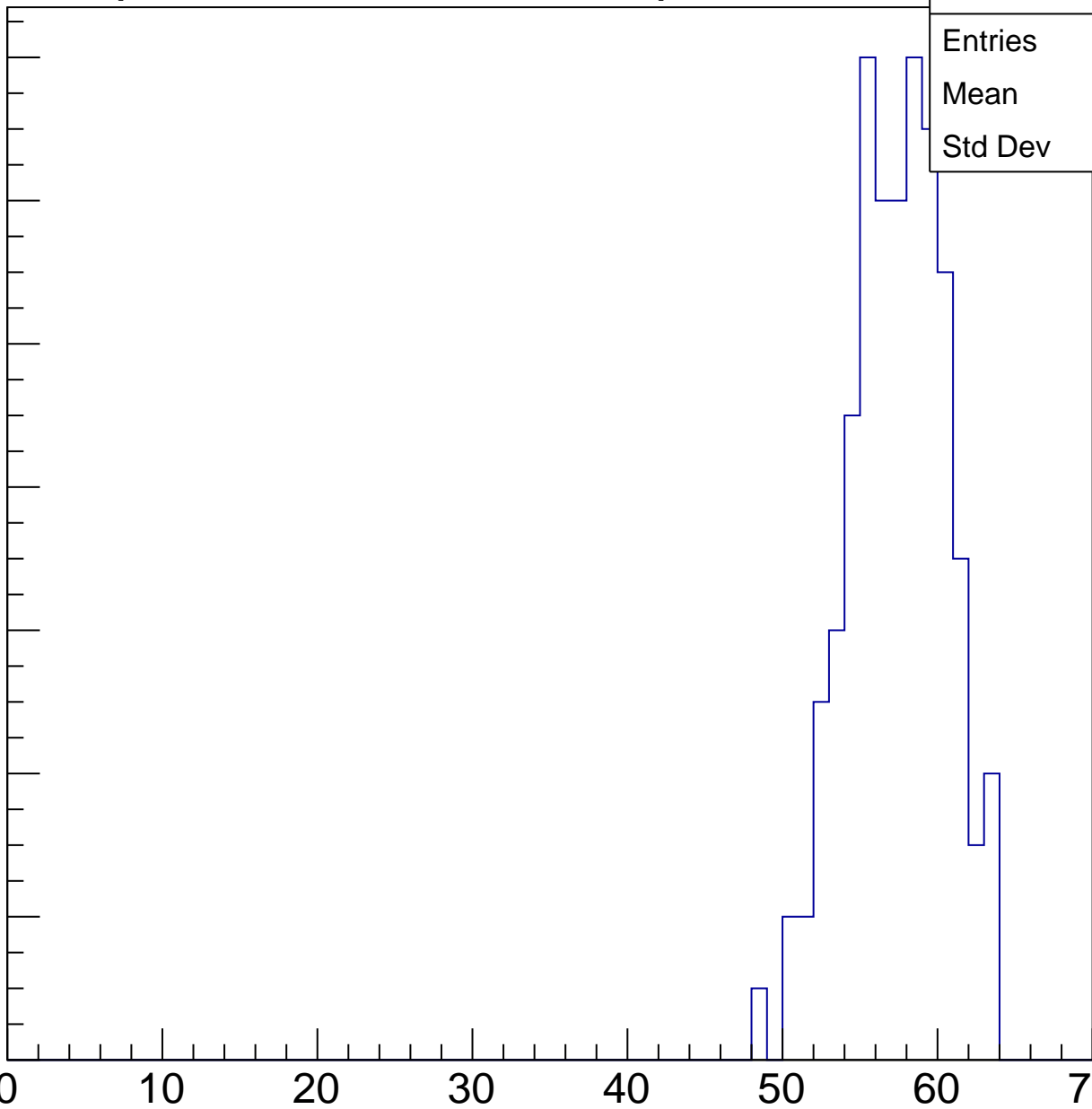
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	56.9
Std Dev	3.115

ampl



B0L101S, U14-ch83, adc5

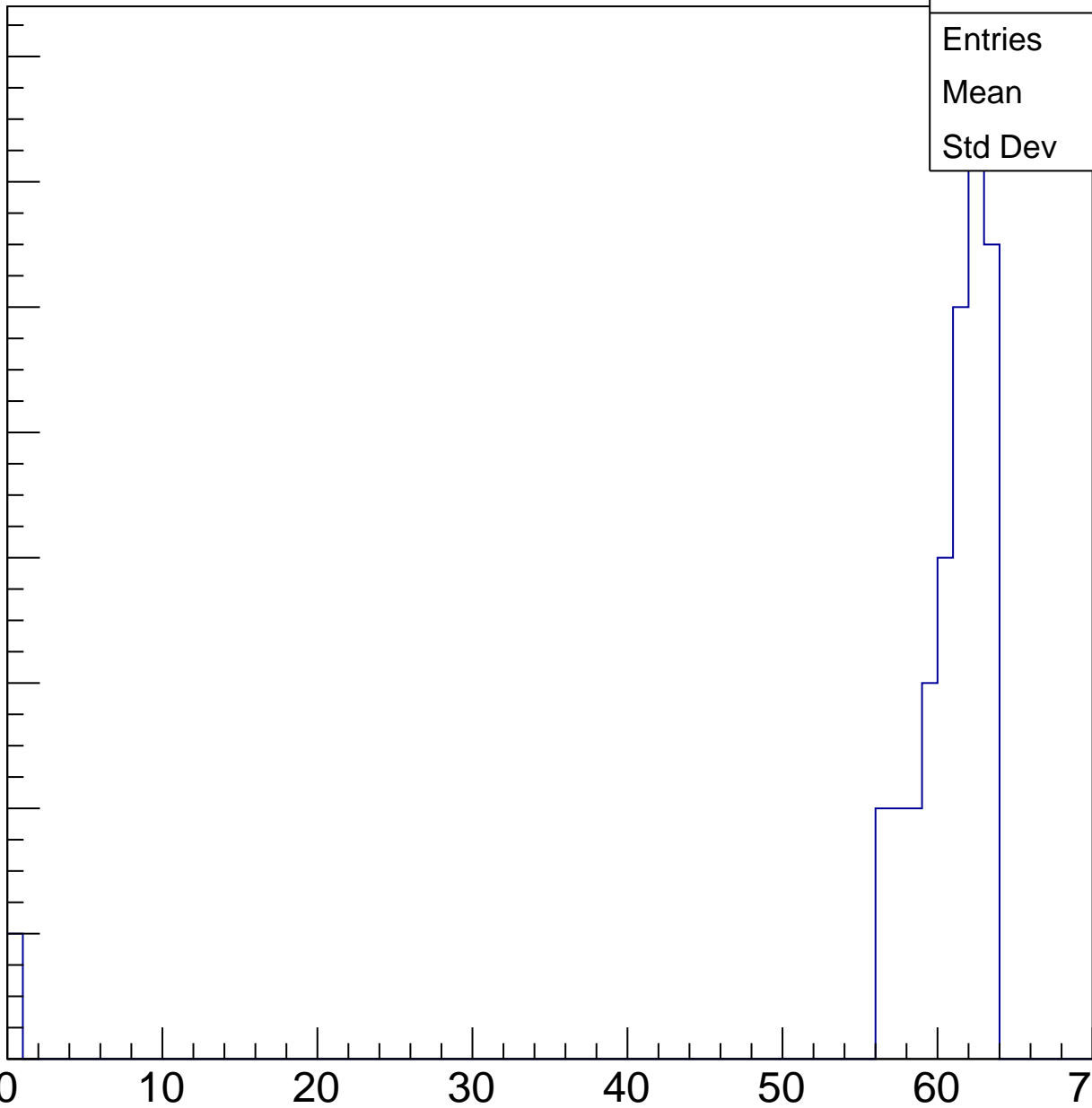
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.86
Std Dev	10.37

ampl



B0L101S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch84, adc0

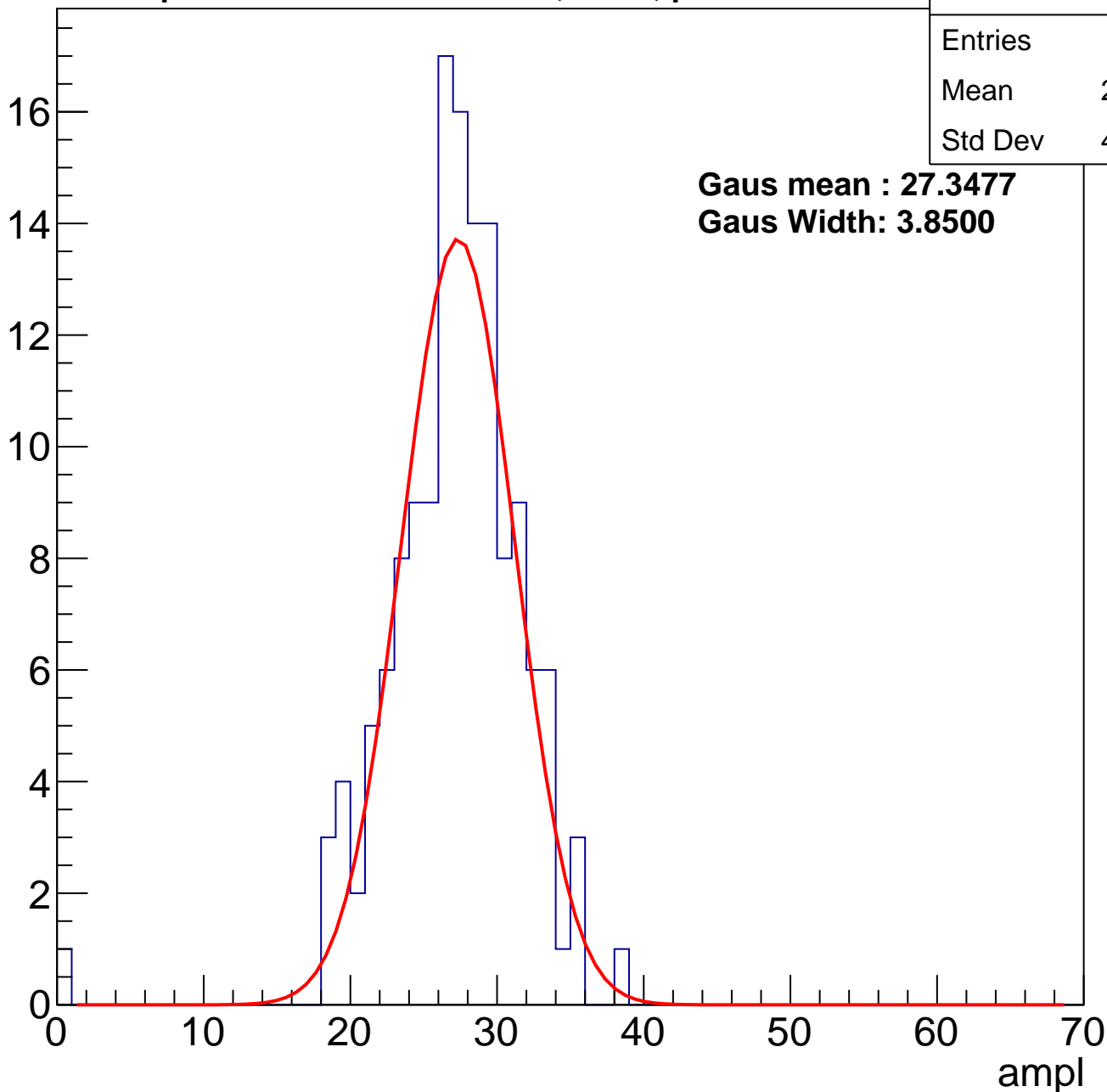
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	26.69
Std Dev	4.533

Gaus mean : 27.3477

Gaus Width: 3.8500

Entry



B0L101S, U14-ch84, adc1

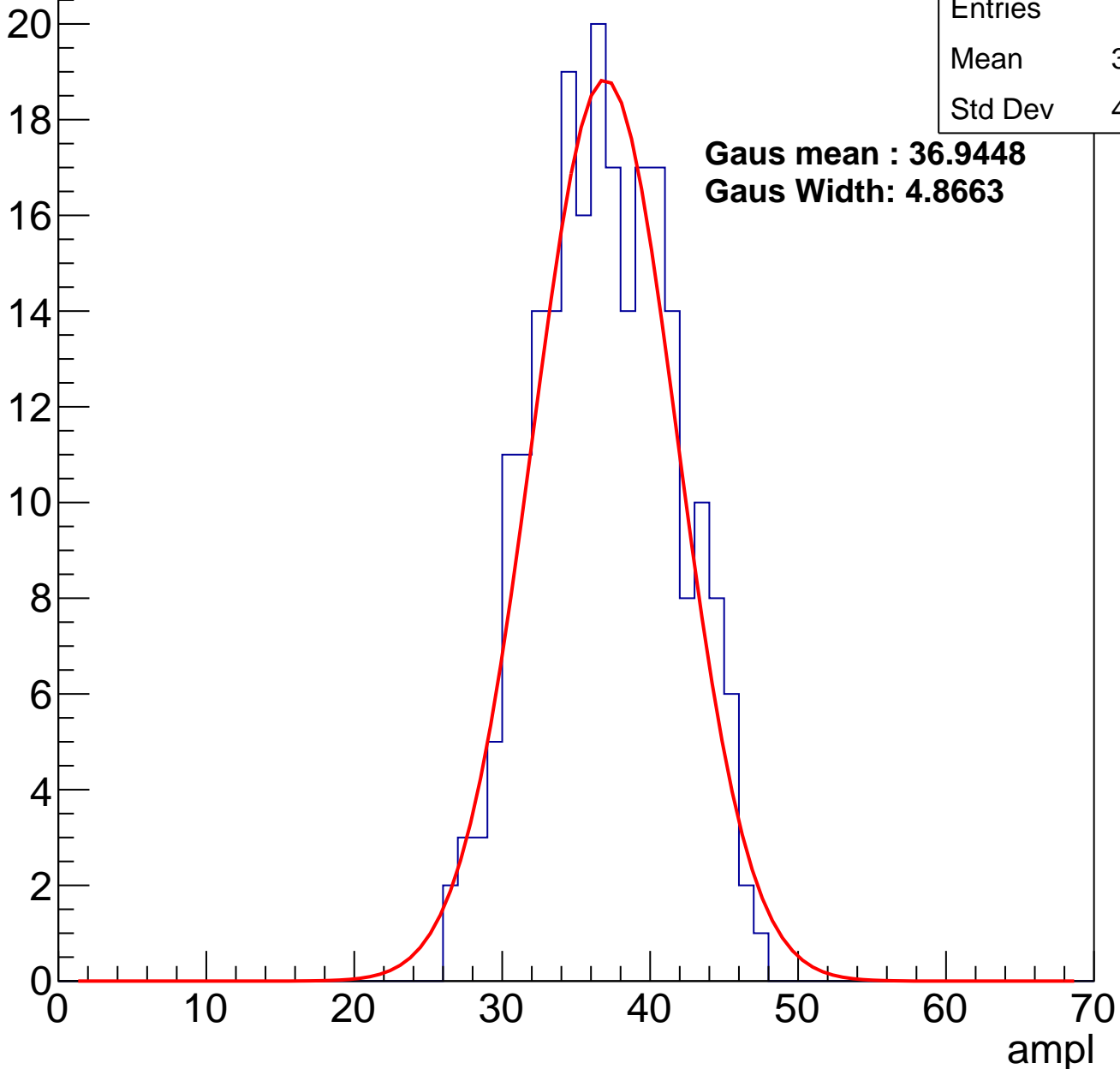
calib_packv5_042523_0143.root, FC#1, port C1

Entries	232
Mean	36.53
Std Dev	4.583

Gaus mean : 36.9448

Gaus Width: 4.8663

Entry



B0L101S, U14-ch84, adc2

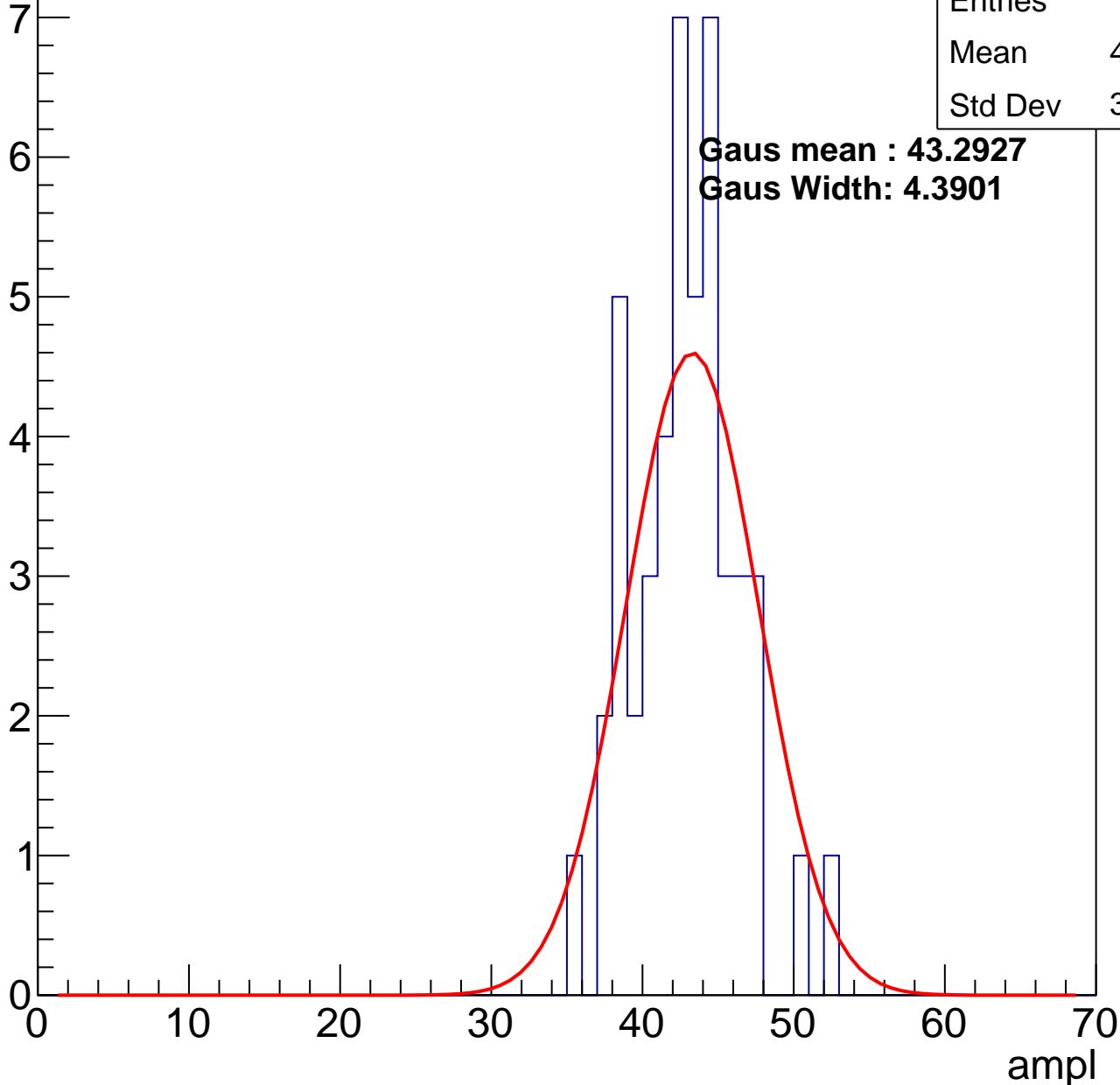
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	47
Mean	42.43
Std Dev	3.425

Gaus mean : 43.2927

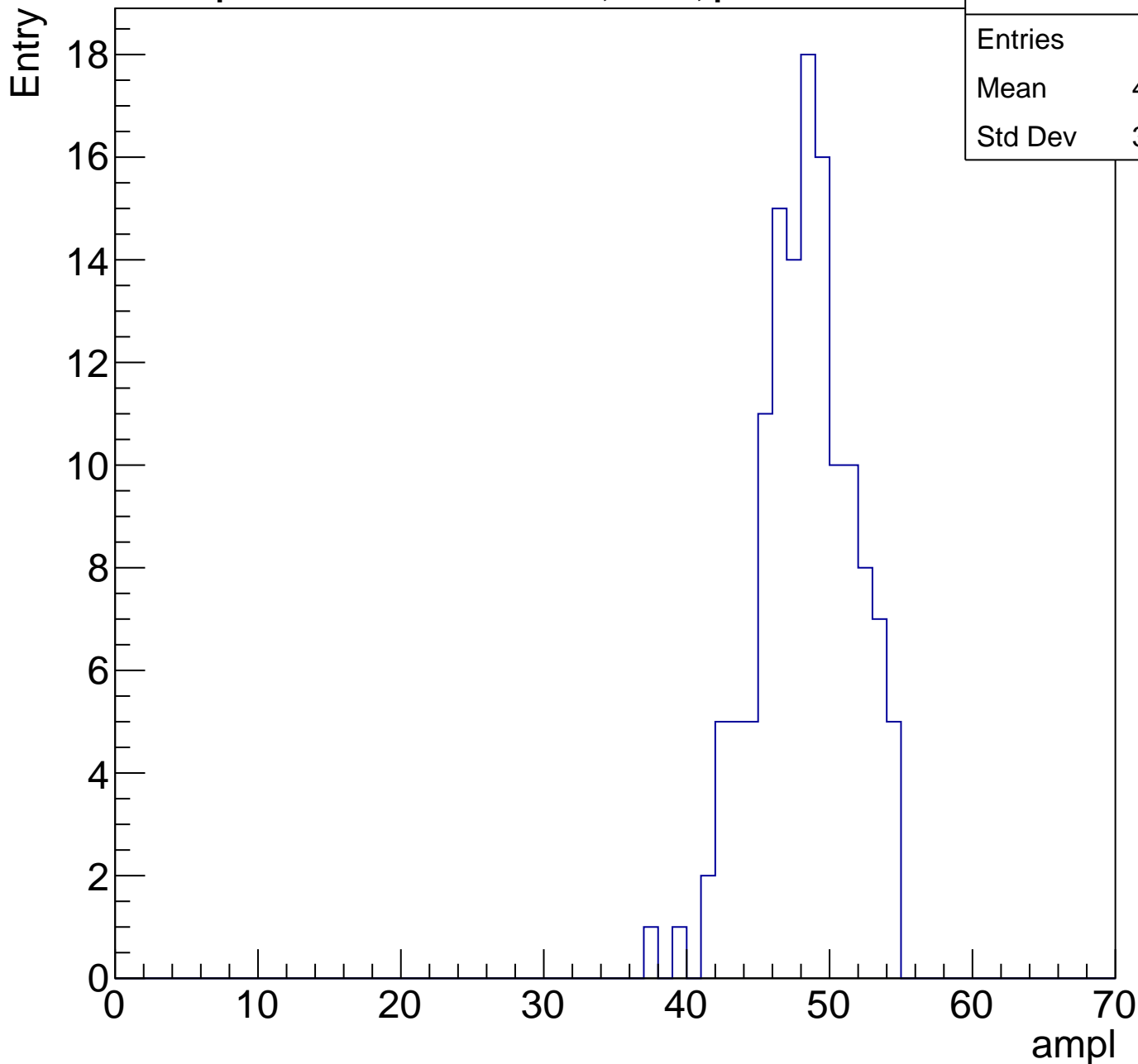
Gaus Width: 4.3901



B0L101S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	47.83
Std Dev	3.339



B0L101S, U14-ch84, adc4

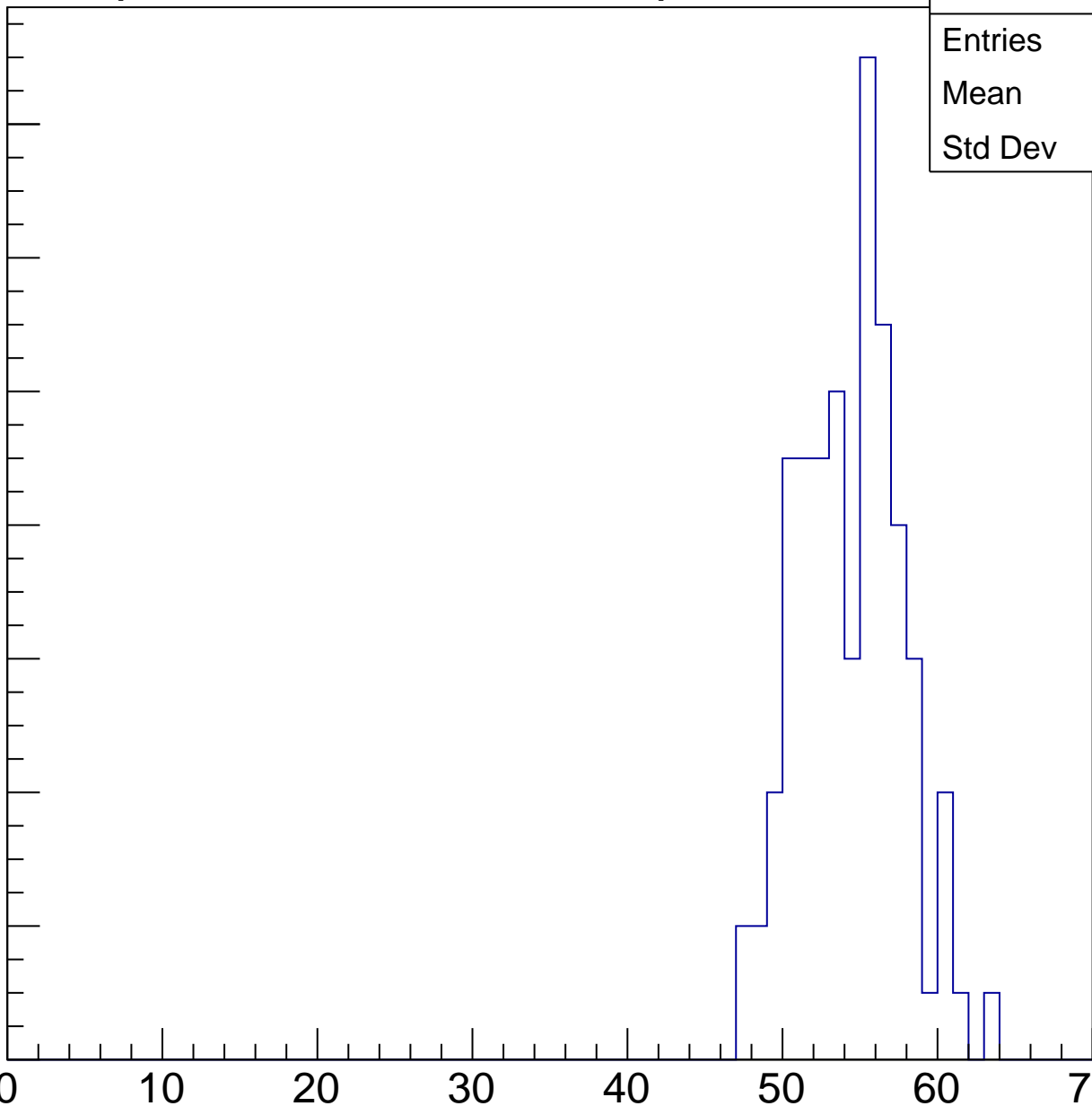
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	53.93
Std Dev	3.299

ampl



B0L101S, U14-ch84, adc5

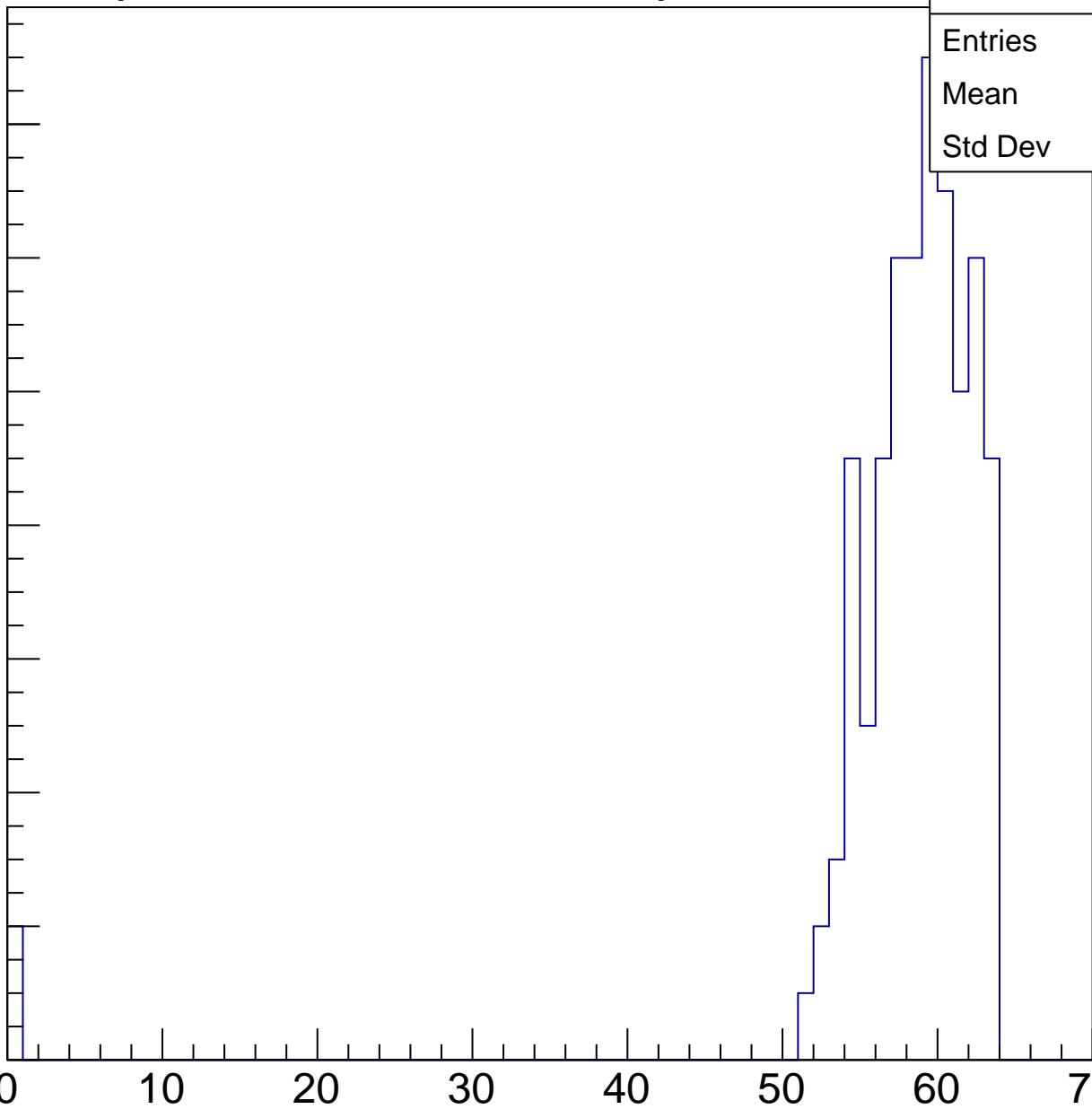
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	57.41
Std Dev	8.216

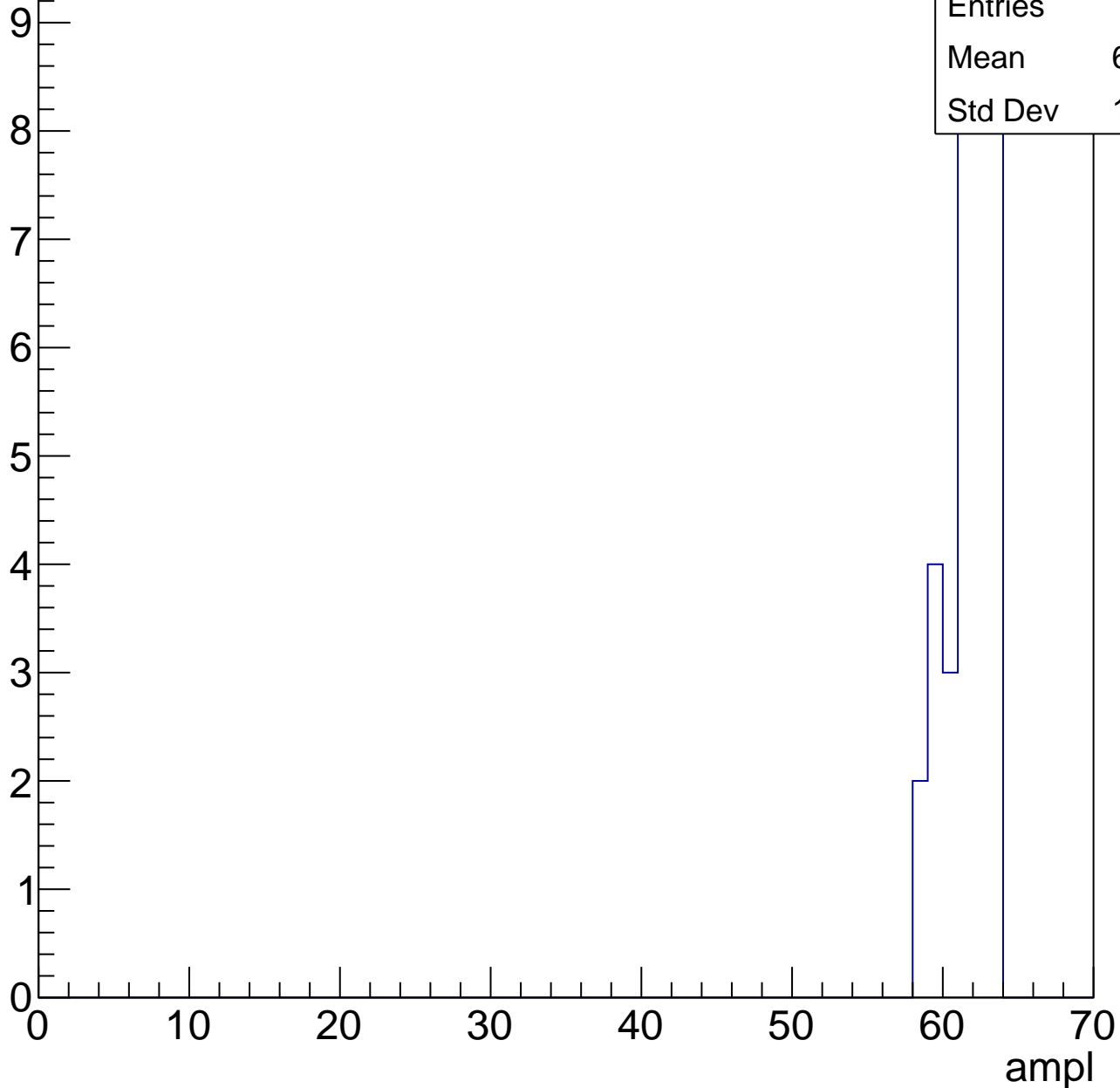
ampl



B0L101S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch85, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.32
Std Dev	4.468

Gaus mean : 29.0354

Gaus Width: 3.7443

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

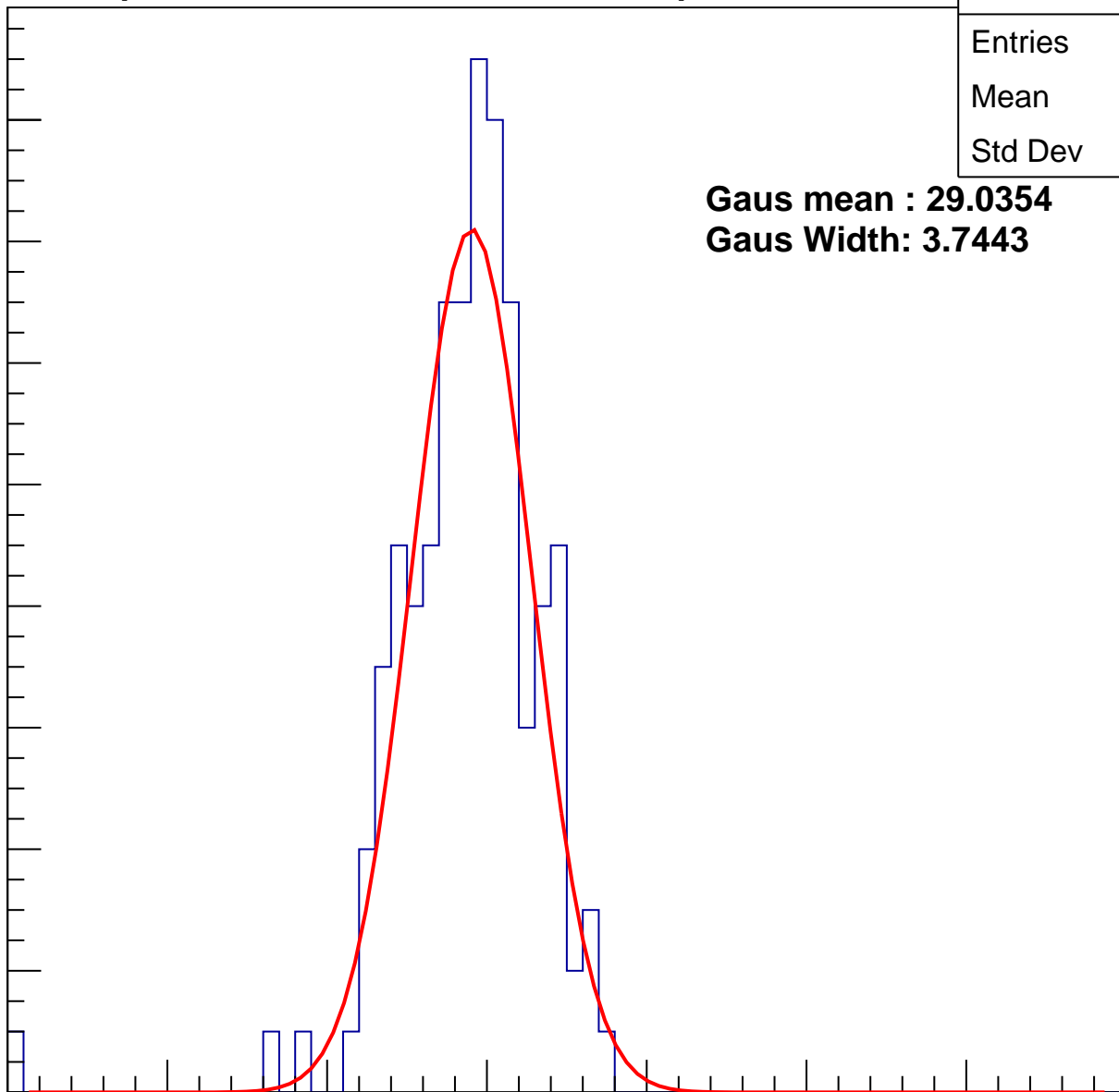
40

50

60

70

ampl



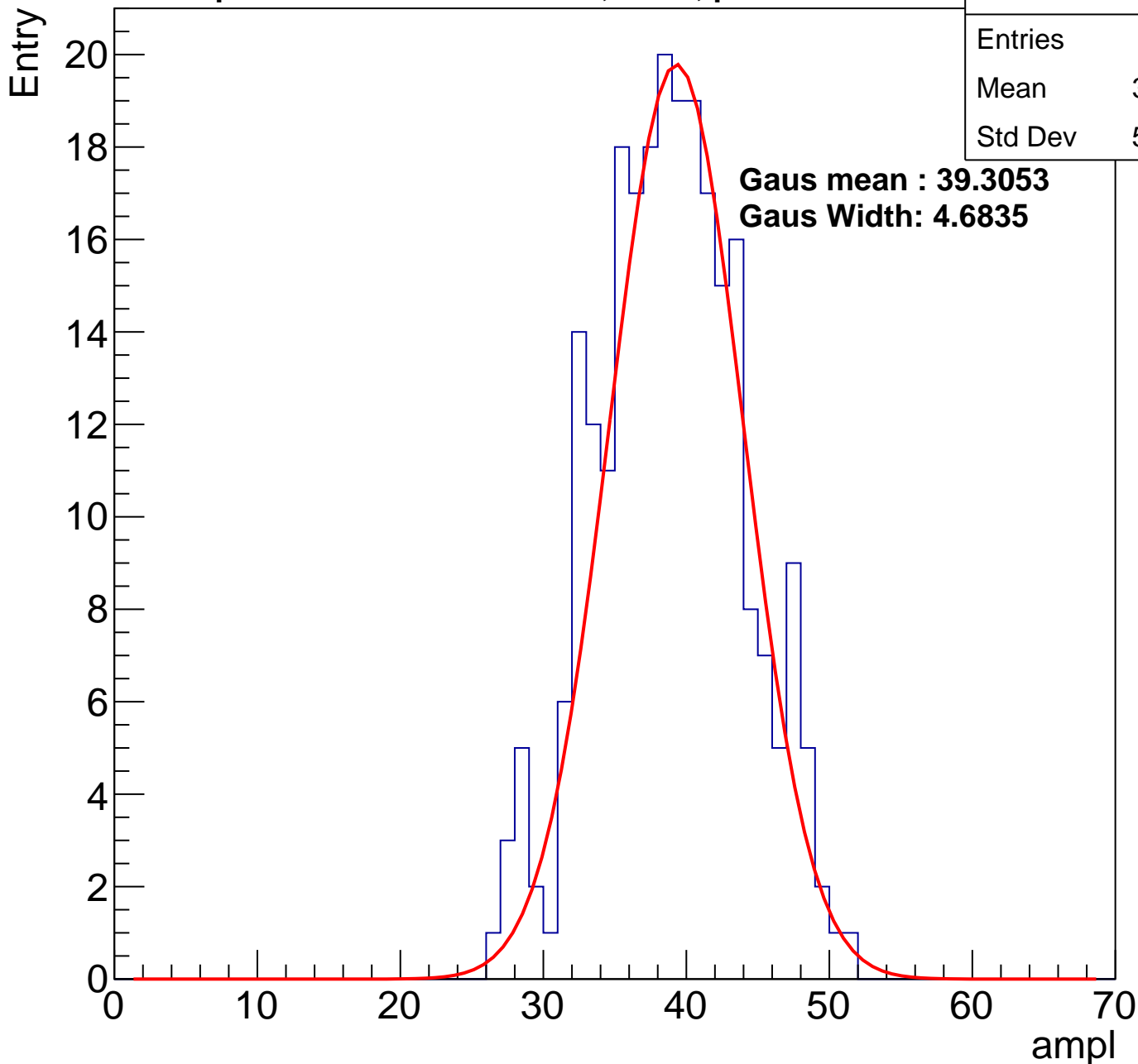
B0L101S, U14-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	252
Mean	38.42
Std Dev	5.013

Gaus mean : 39.3053

Gaus Width: 4.6835



B0L101S, U14-ch85, adc2

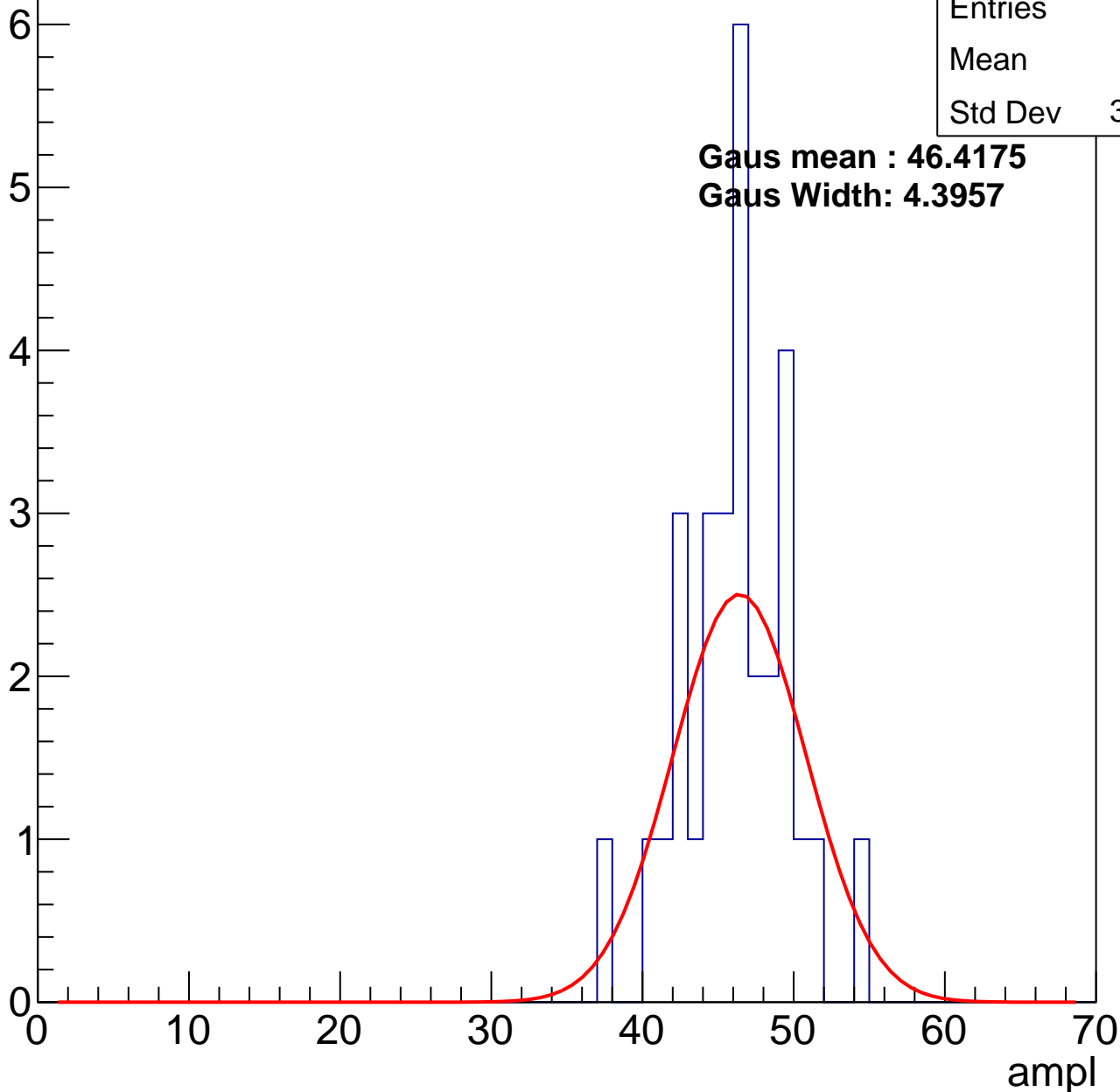
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	30
Mean	45.7
Std Dev	3.475

Gaus mean : 46.4175

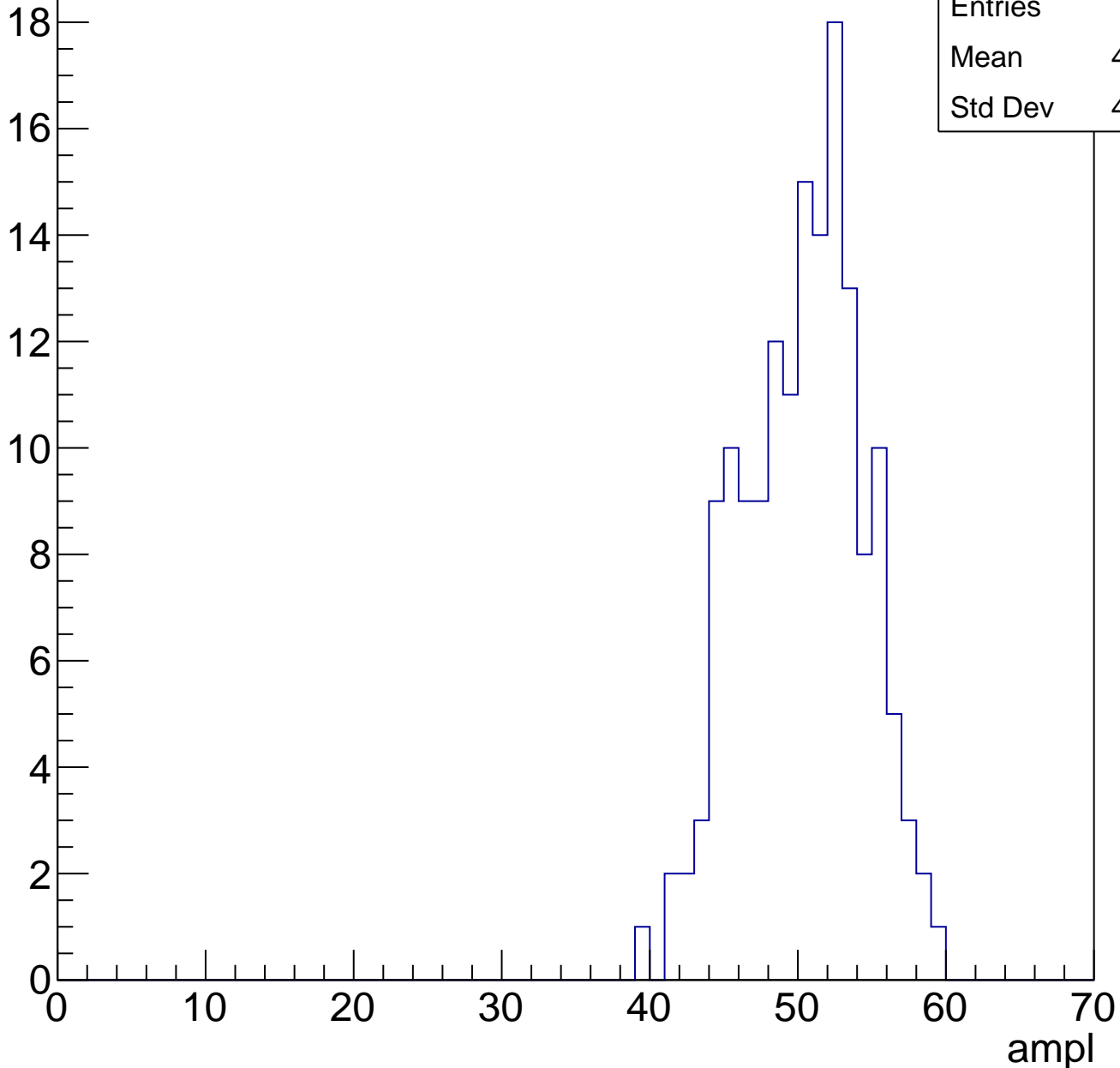
Gaus Width: 4.3957



B0L101S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch85, adc4

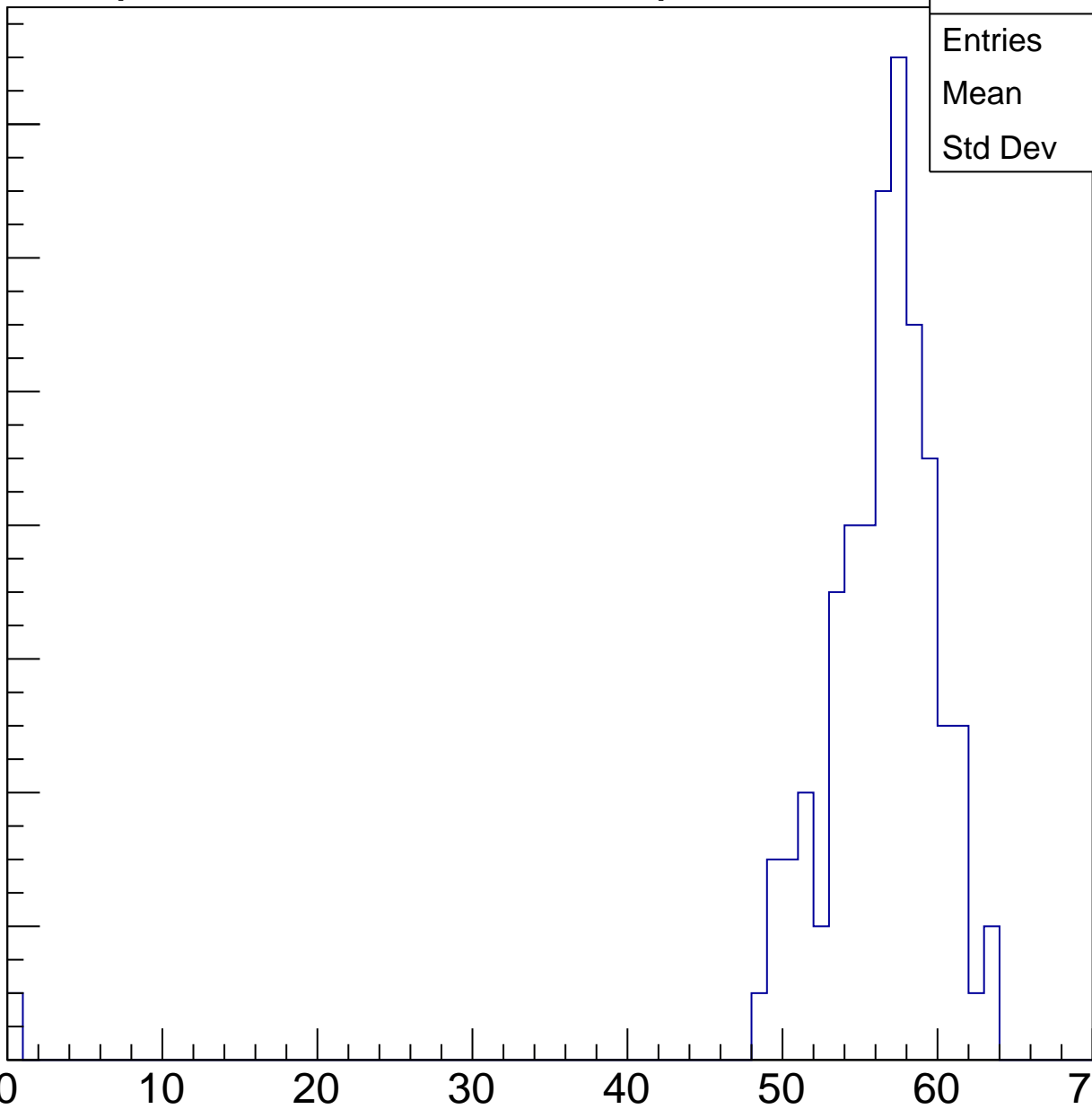
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	55.52
Std Dev	6.505

ampl



B0L101S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

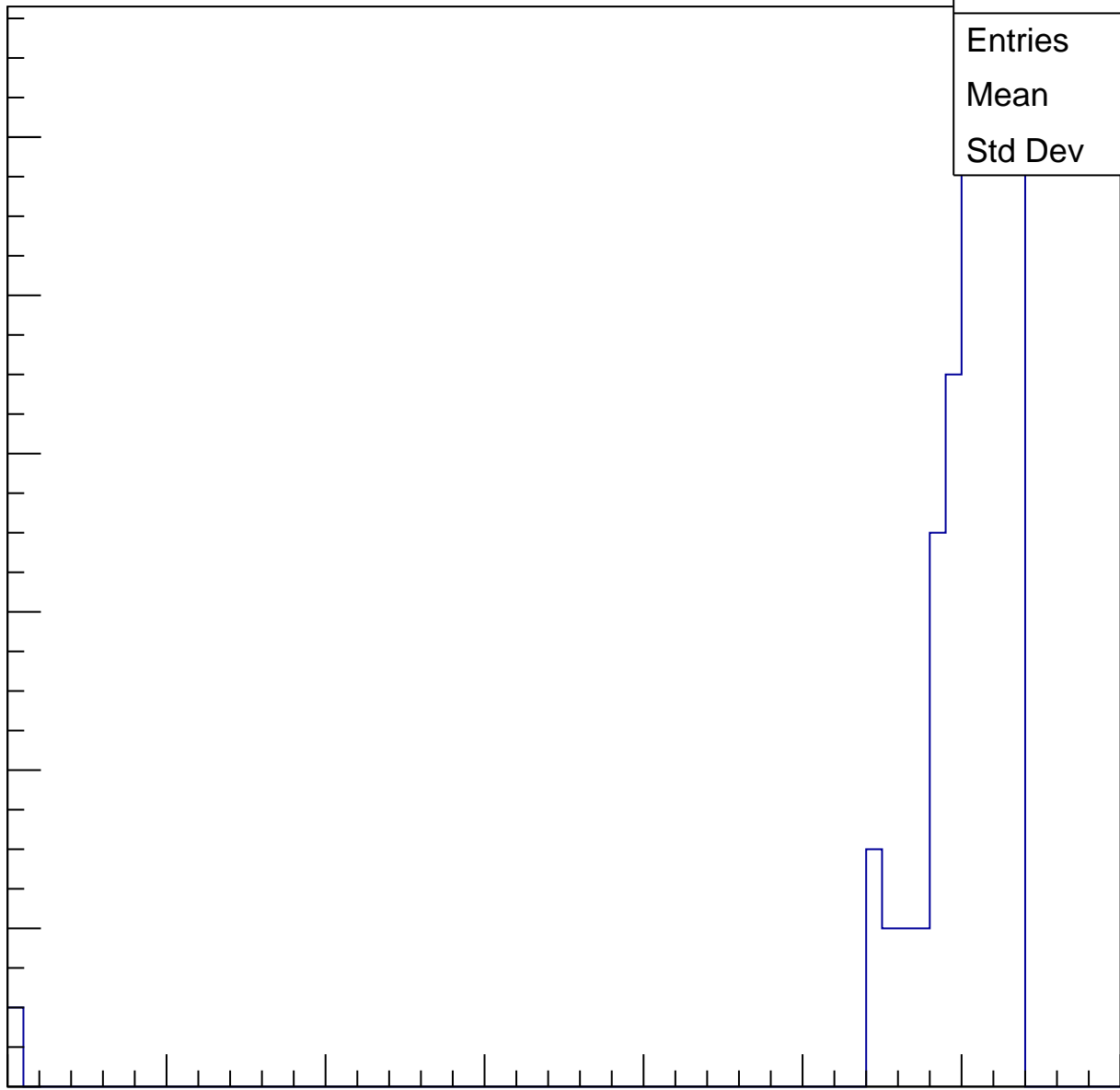
2

0

Entries	76
Mean	59.36
Std Dev	7.247

ampl

0 10 20 30 40 50 60 70

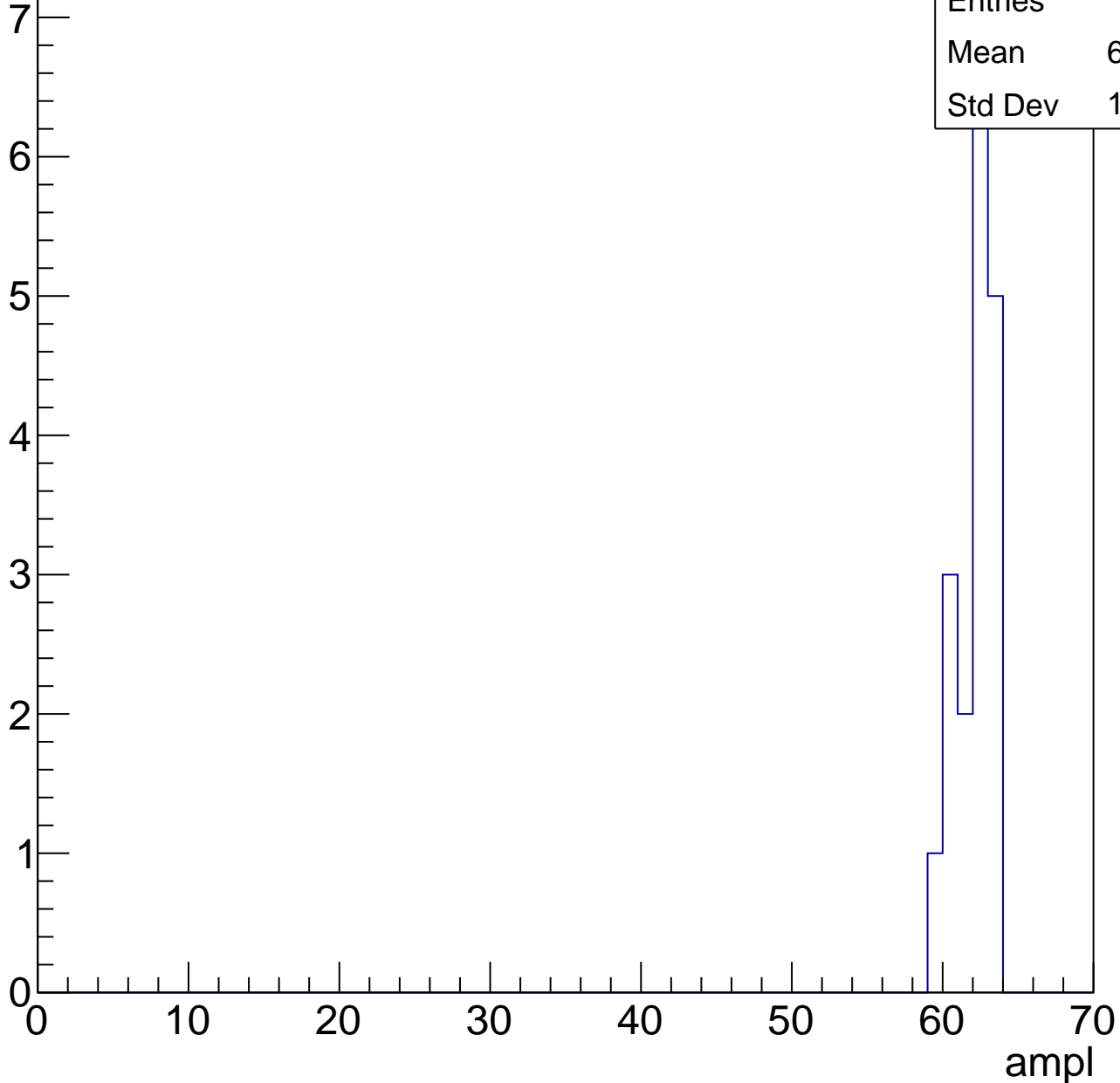


B0L101S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	61.67
Std Dev	1.202



B0L101S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

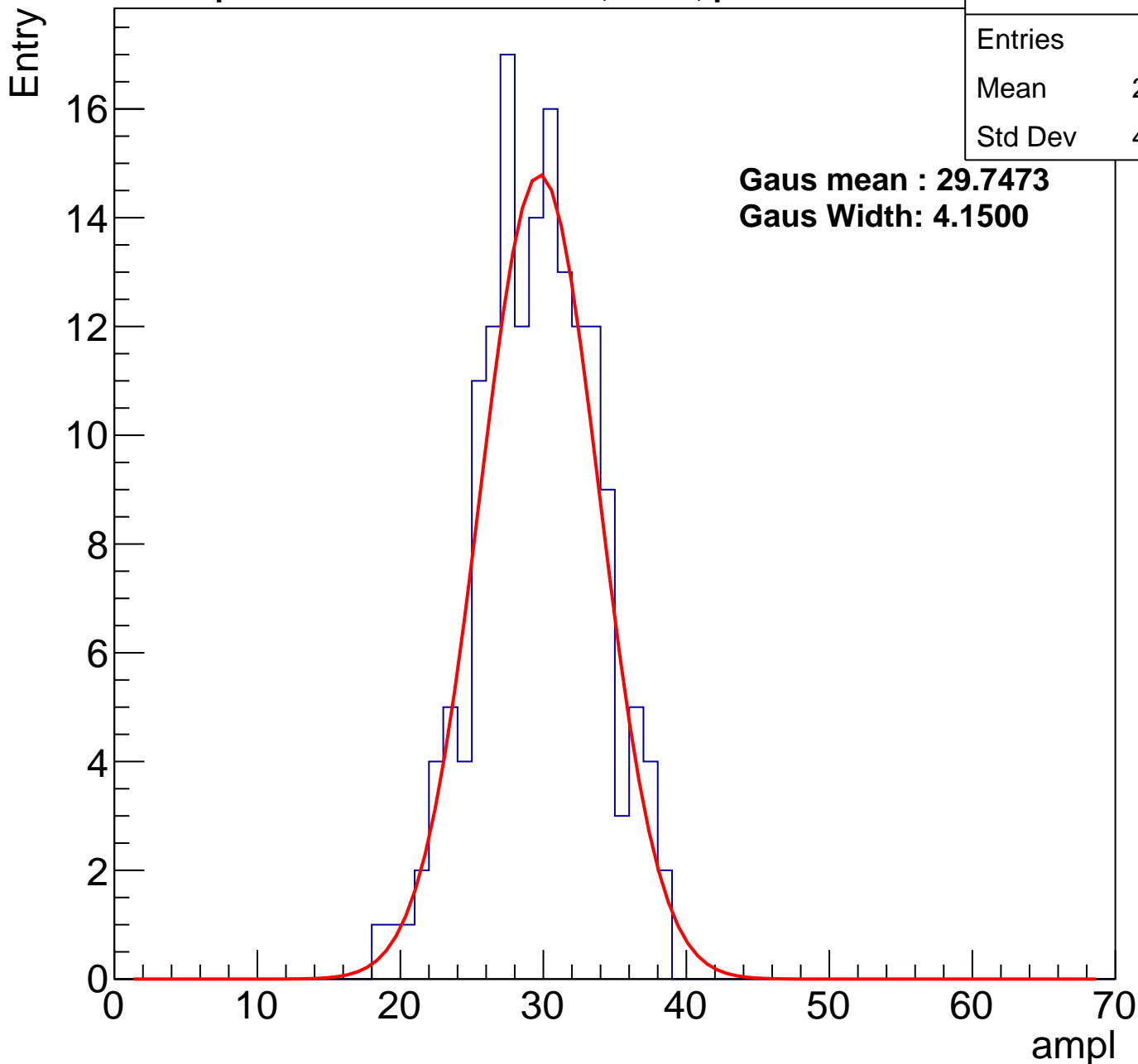
B0L101S, U14-ch86, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	29.15
Std Dev	4.055

Gaus mean : 29.7473

Gaus Width: 4.1500

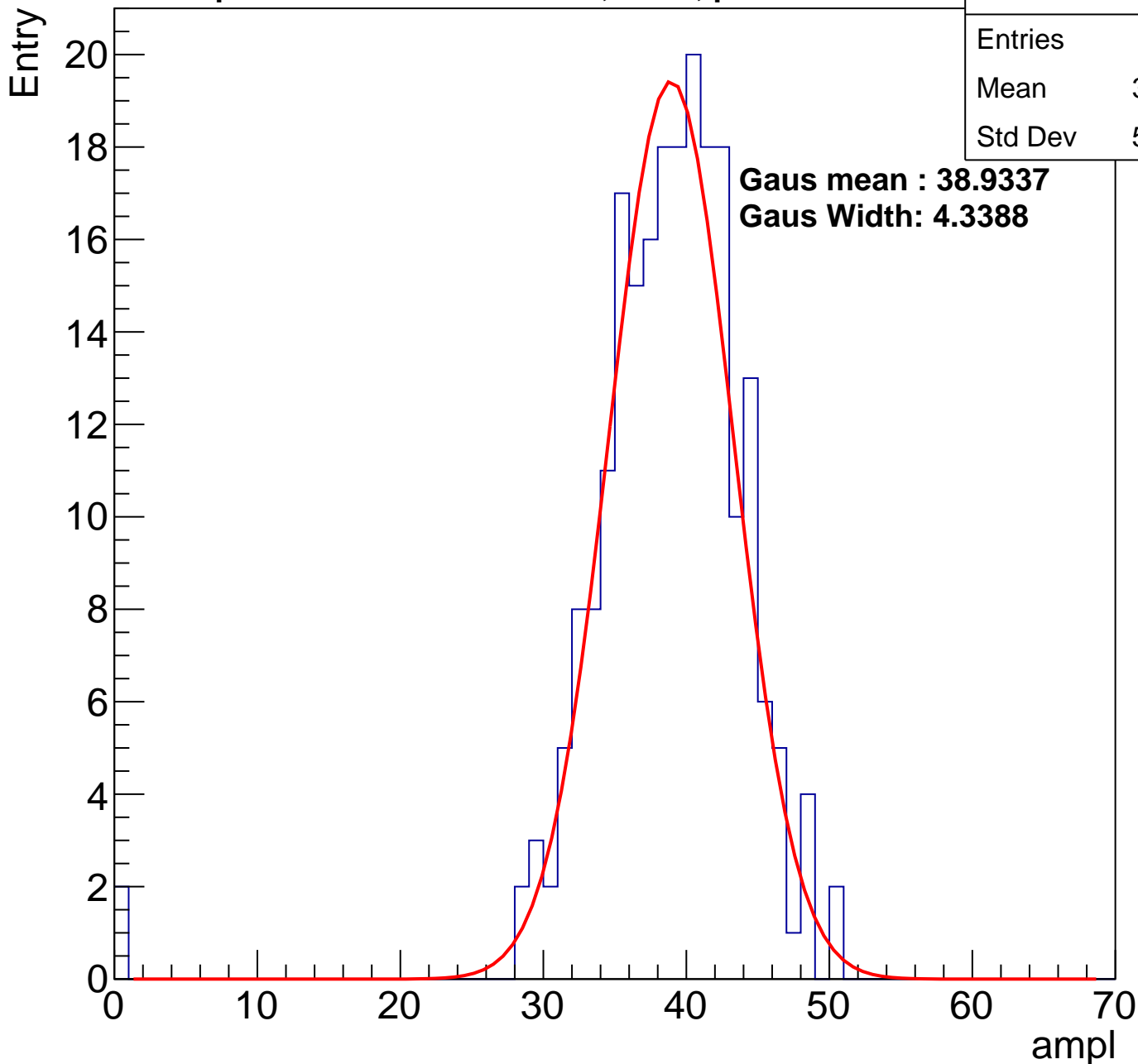


B0L101S, U14-ch86, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	222
Mean	38.29
Std Dev	5.706

Gaus mean : 38.9337
Gaus Width: 4.3388



B0L101S, U14-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

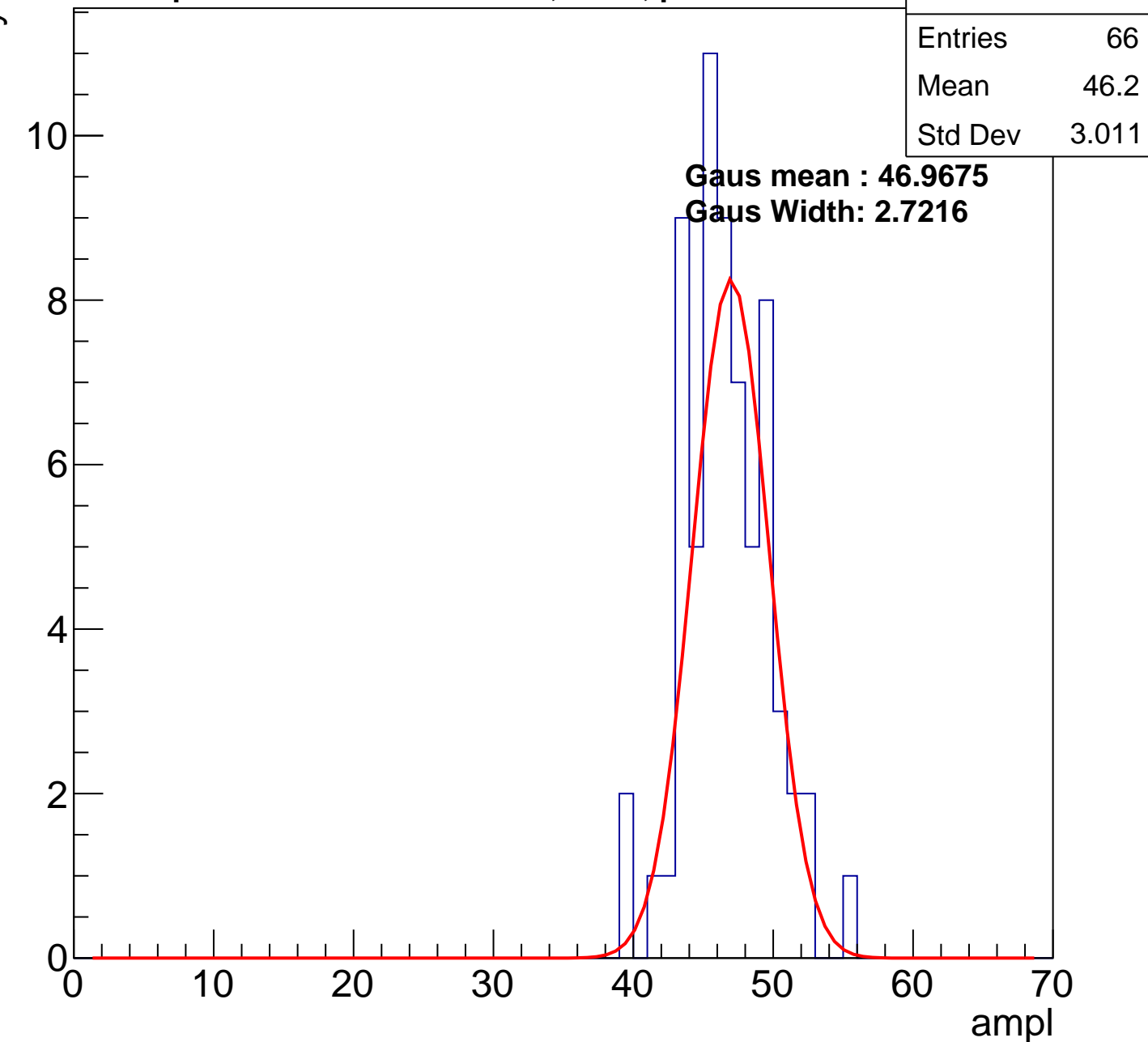
60

ampl

Entries	66
Mean	46.2
Std Dev	3.011

Gaus mean : 46.9675

Gaus Width: 2.7216



B0L101S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

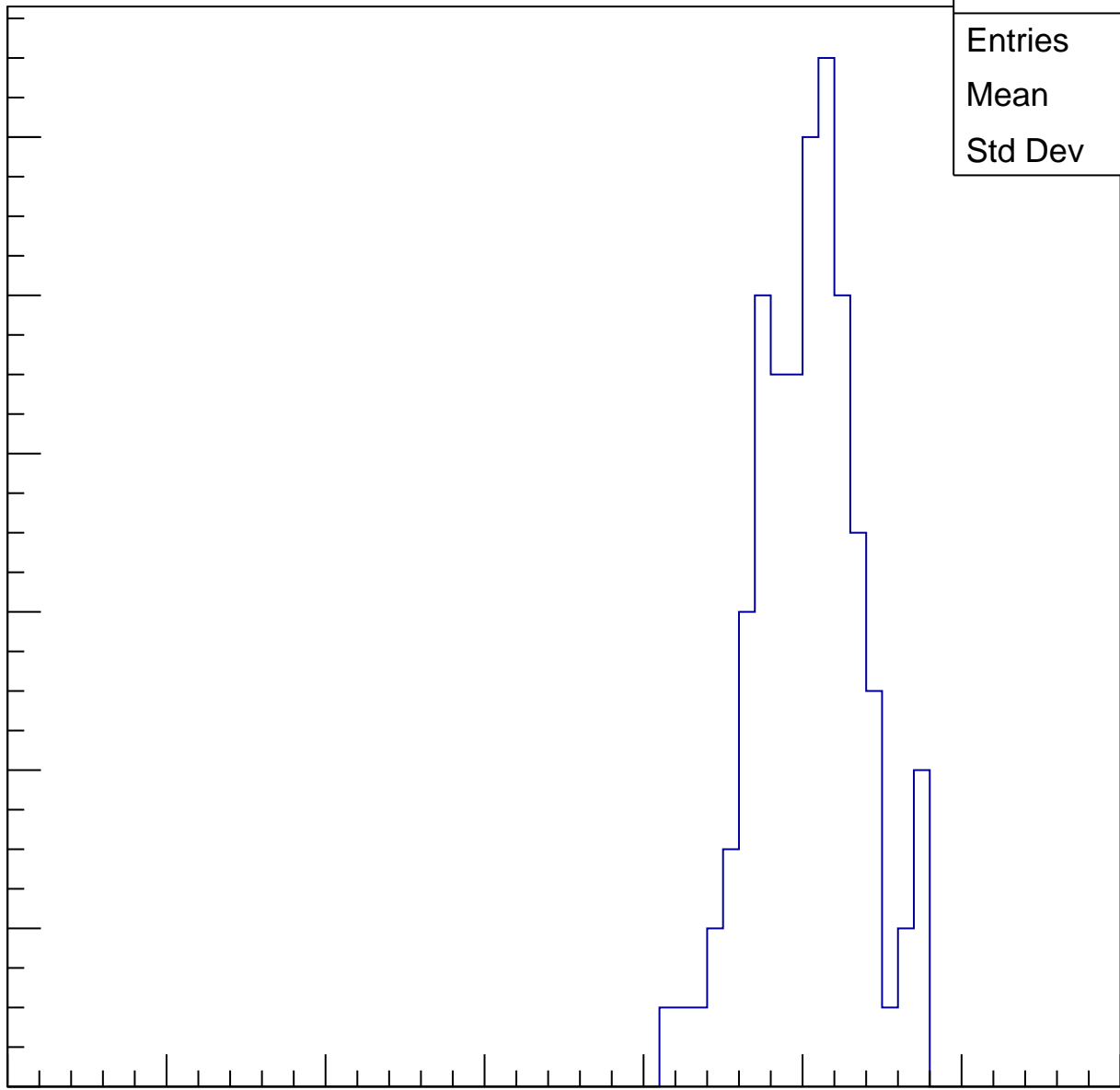
2

0

Entries	96
Mean	49.86
Std Dev	3.322

ampl

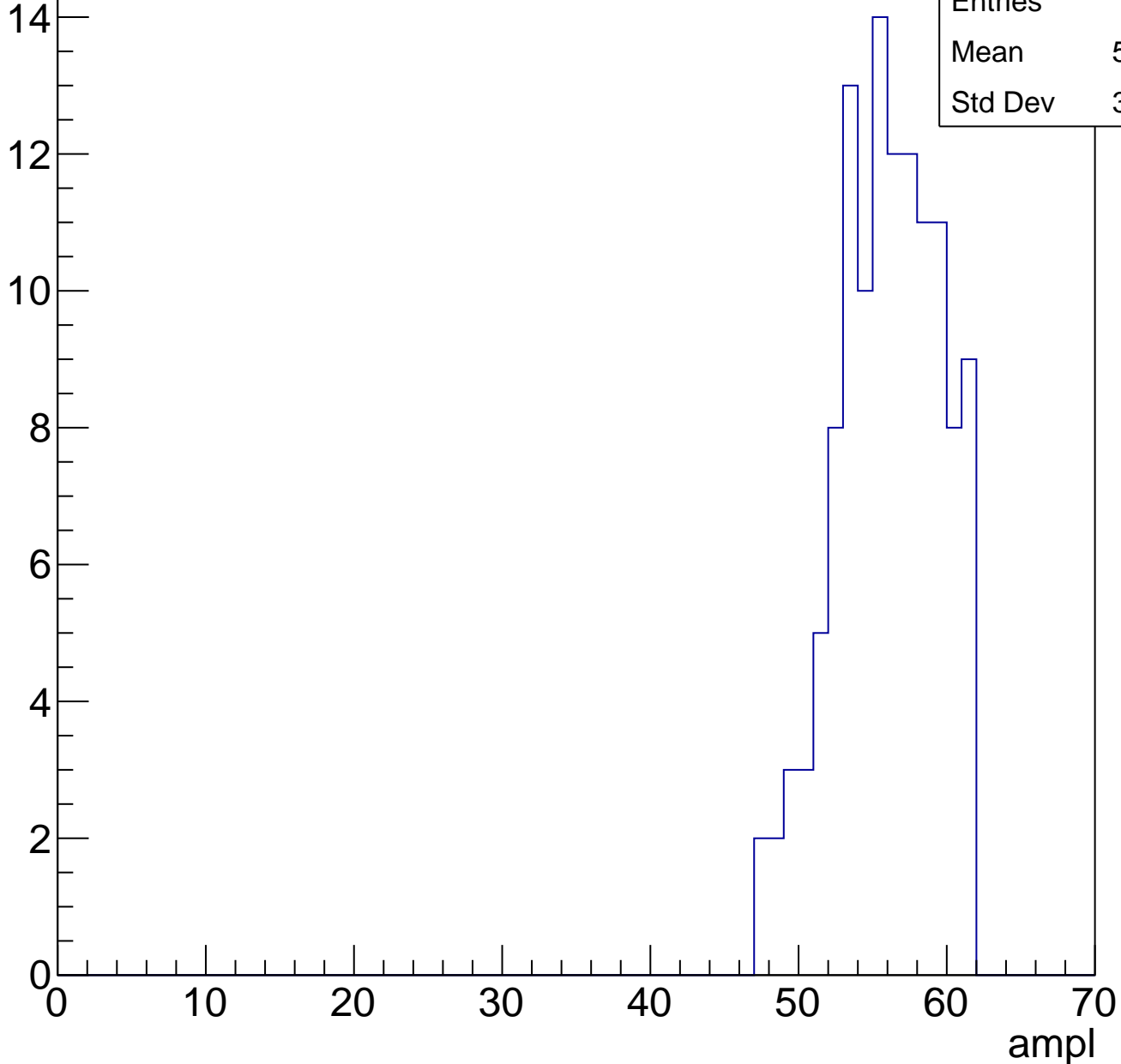
0 10 20 30 40 50 60 70



B0L101S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	123
Mean	55.52
Std Dev	3.432

B0L101S, U14-ch86, adc5

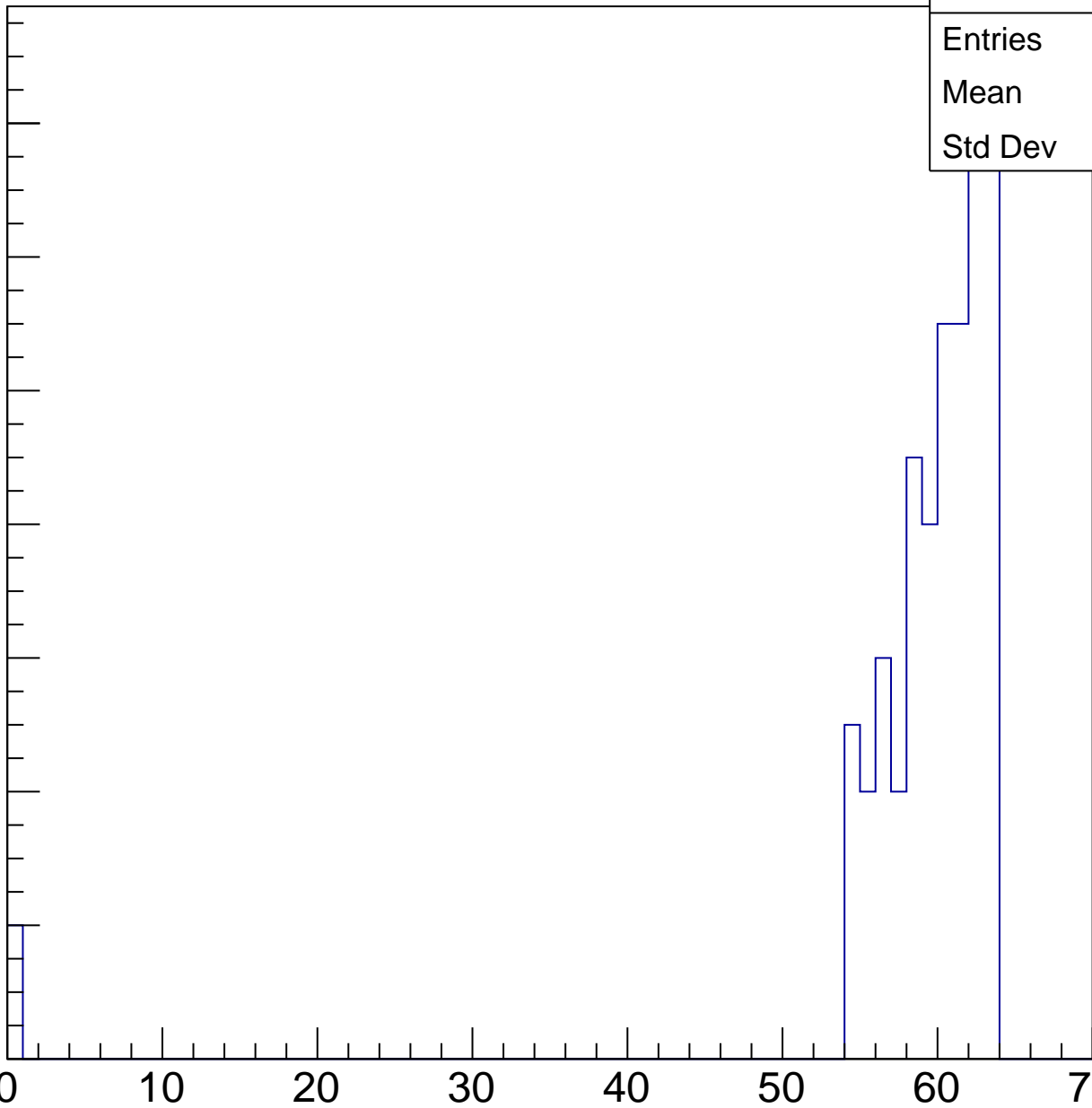
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.33
Std Dev	9.243

ampl

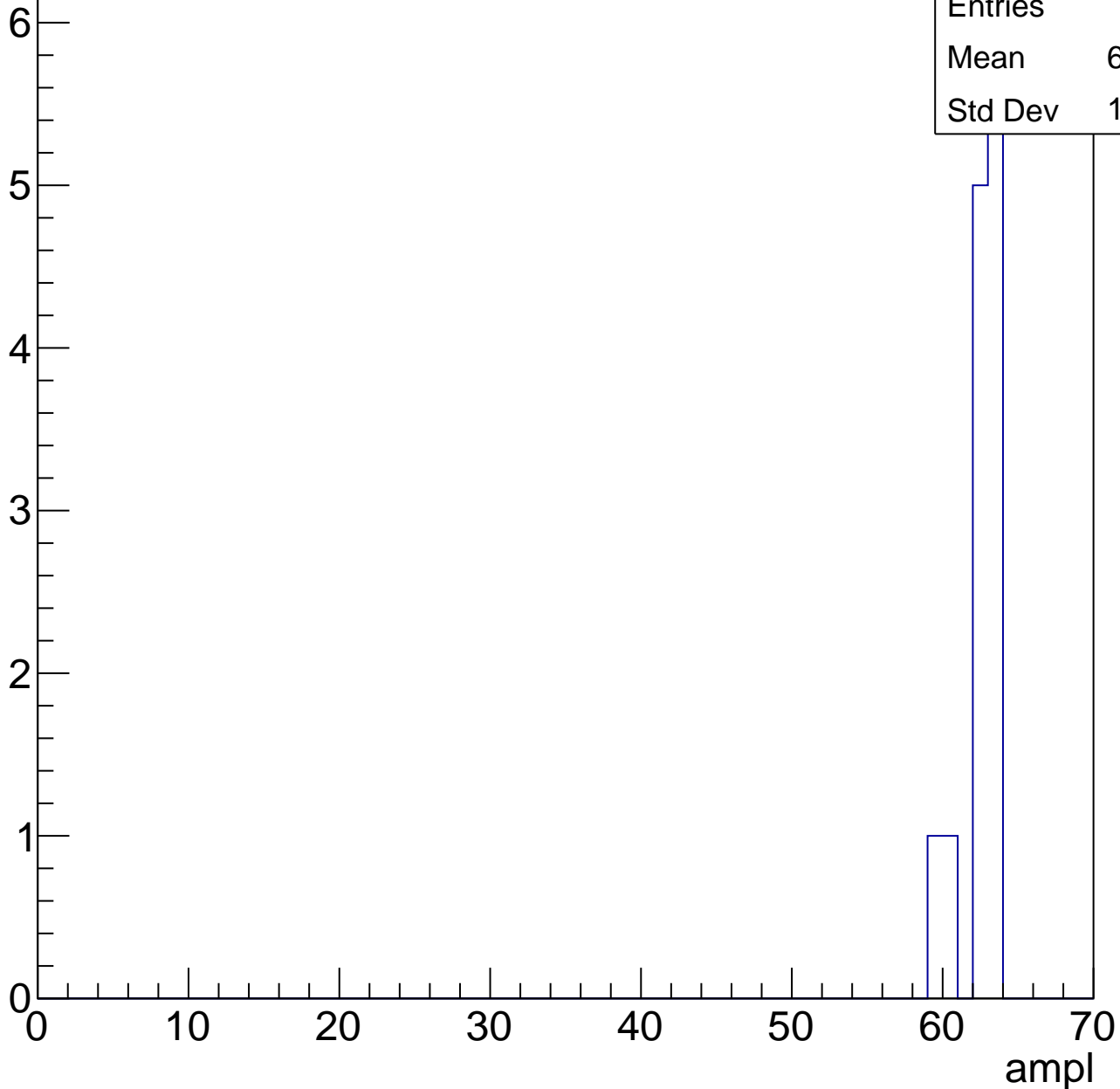


B0L101S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

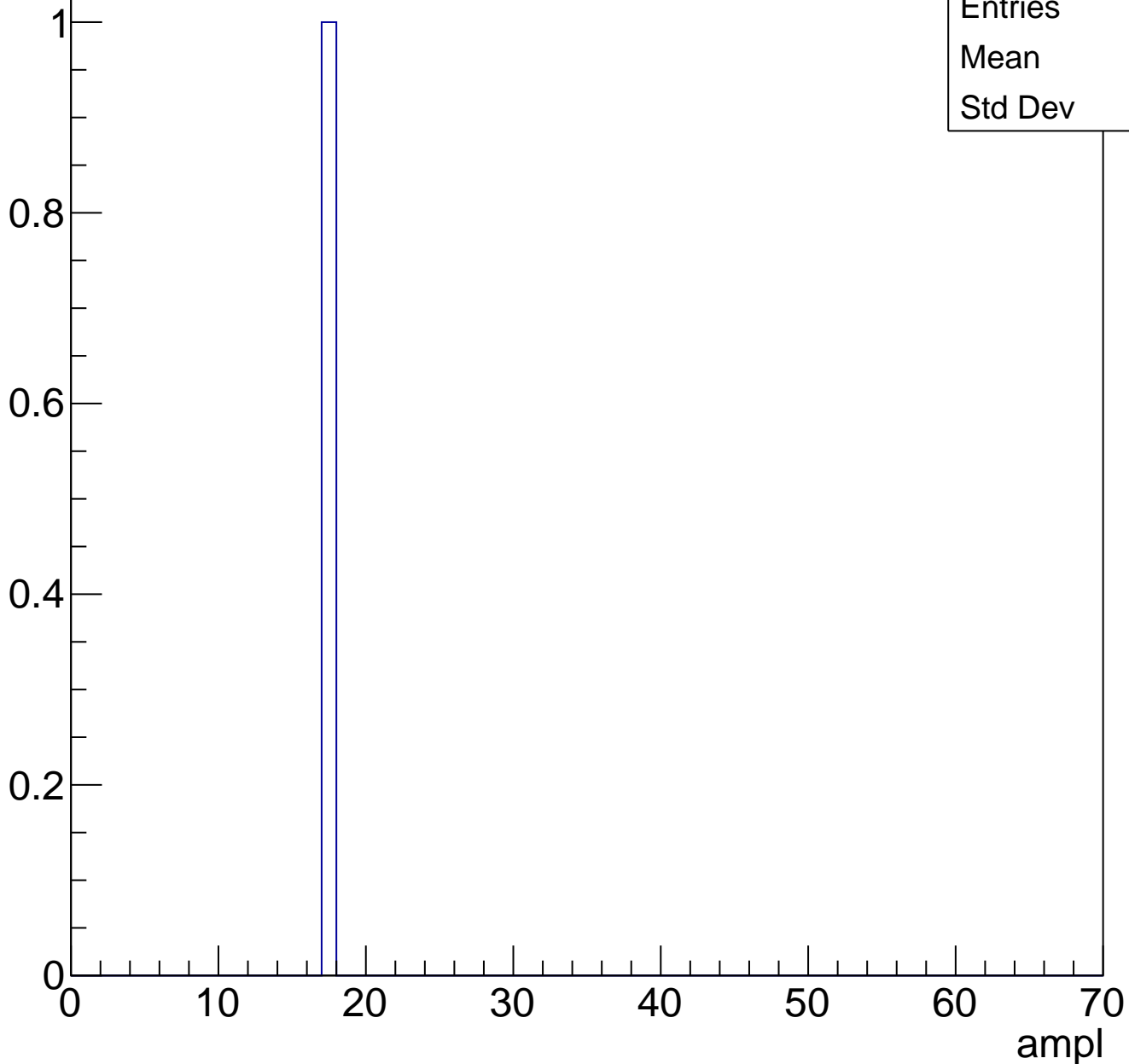
Entries	13
Mean	62.08
Std Dev	1.206



B0L101S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



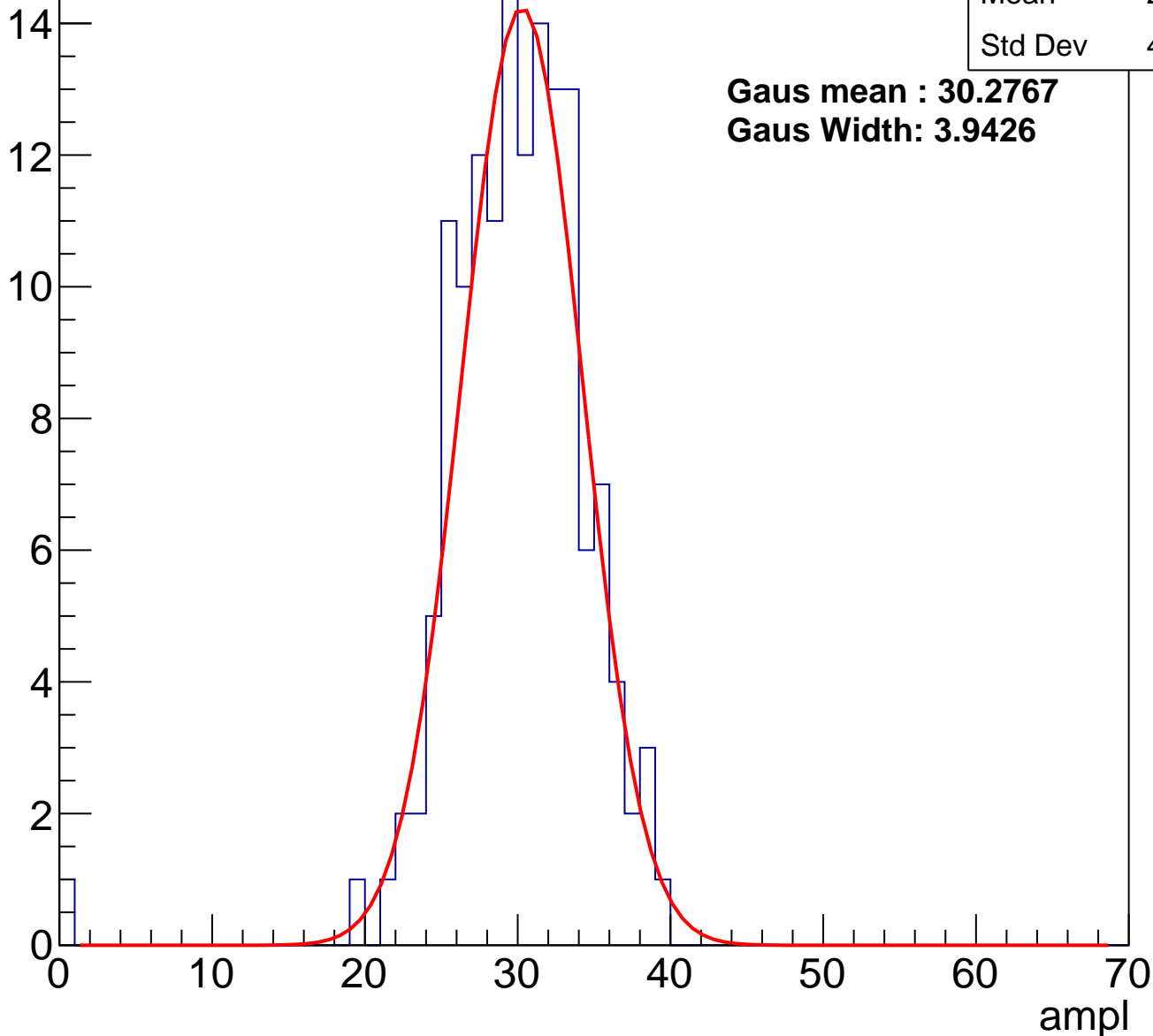
B0L101S, U14-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	29.53
Std Dev	4.568

Gaus mean : 30.2767
Gaus Width: 3.9426

Entry



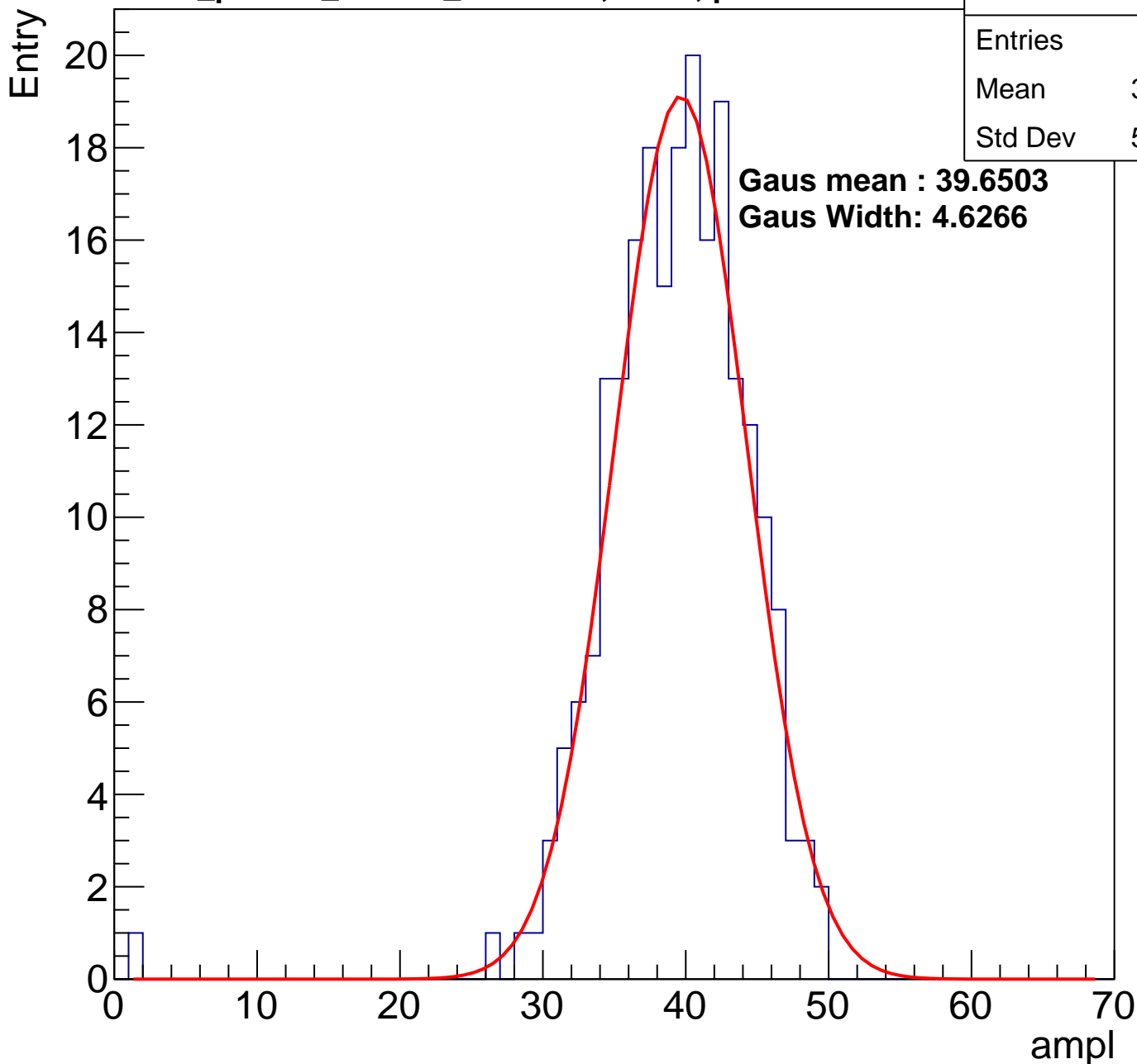
B0L101S, U14-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	224
Mean	38.86
Std Dev	5.112

Gaus mean : 39.6503

Gaus Width: 4.6266



B0L101S, U14-ch87, adc2

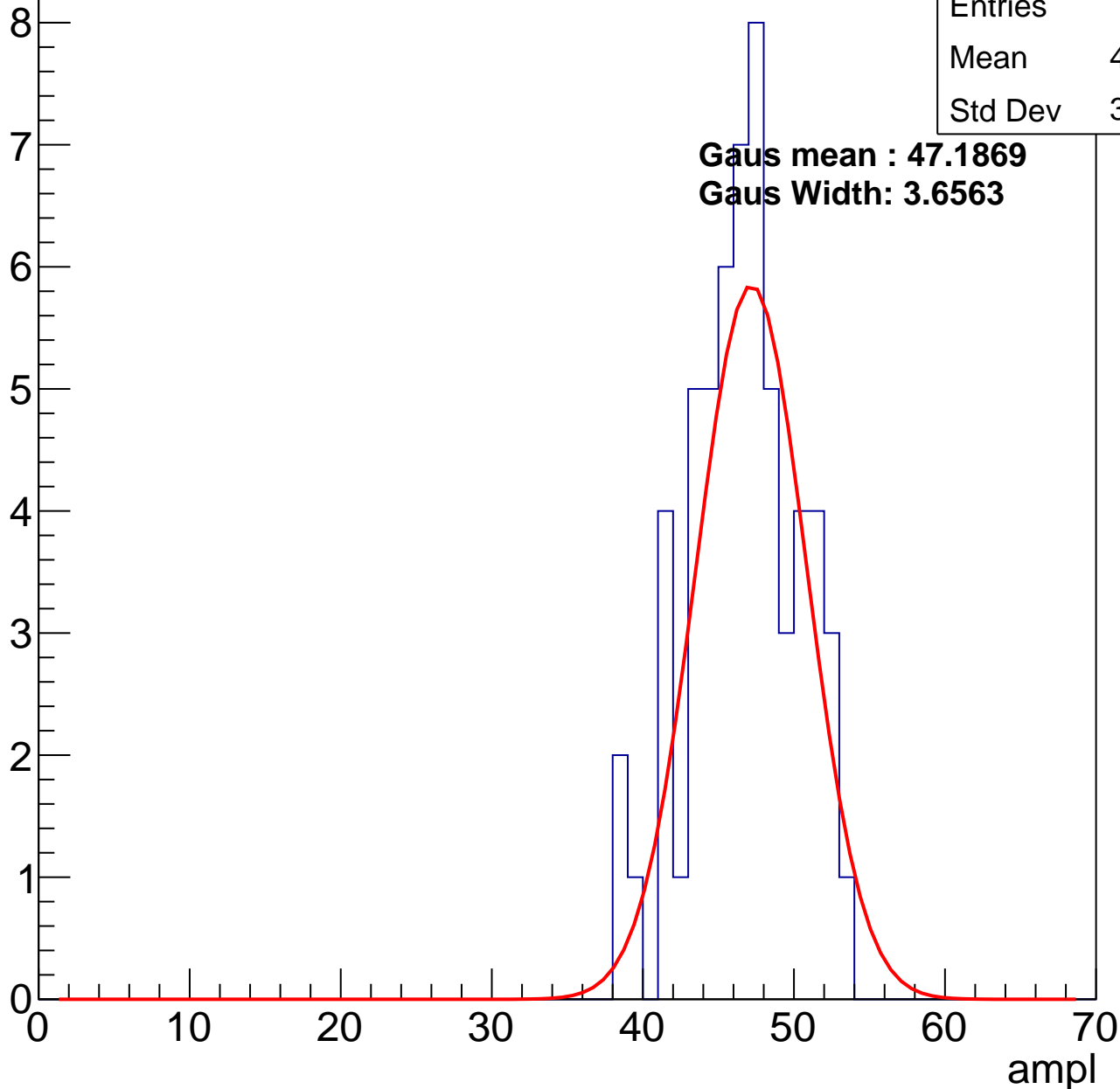
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	59
Mean	46.17
Std Dev	3.537

Gaus mean : 47.1869

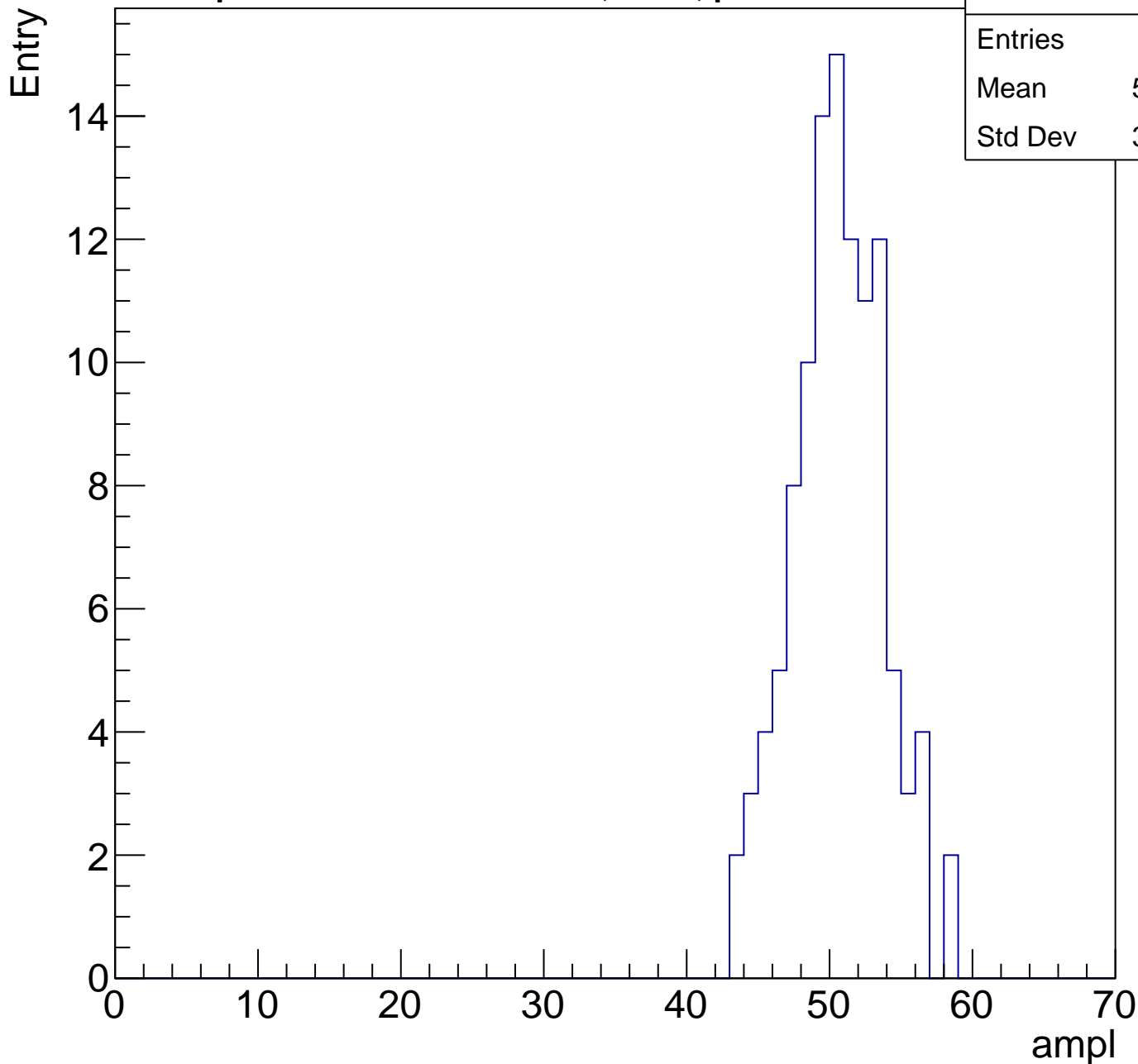
Gaus Width: 3.6563



B0L101S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	50.14
Std Dev	3.164



B0L101S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

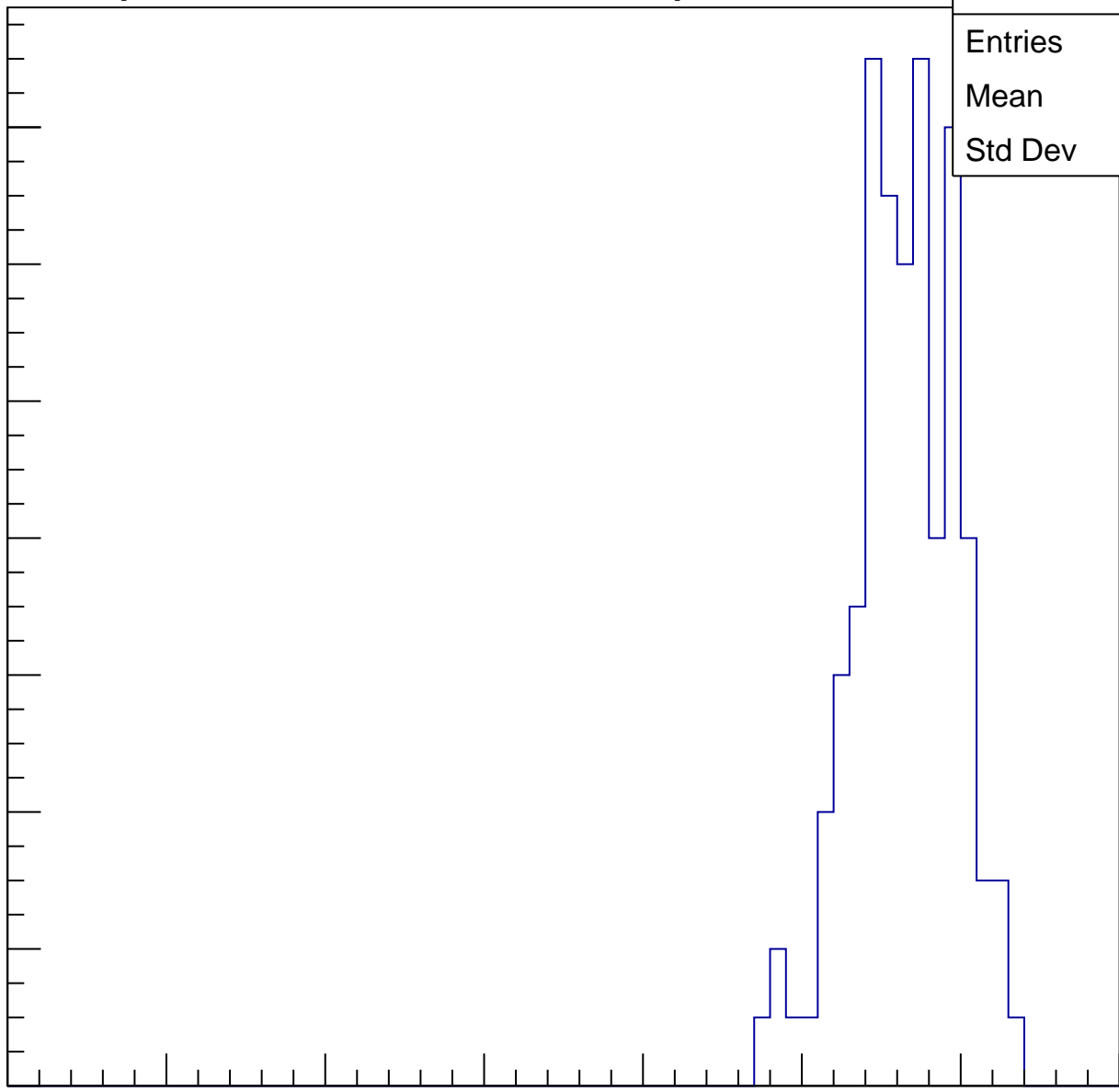
50

60

70

ampl

Entries	114
Mean	55.99
Std Dev	3.189



B0L101S, U14-ch87, adc5

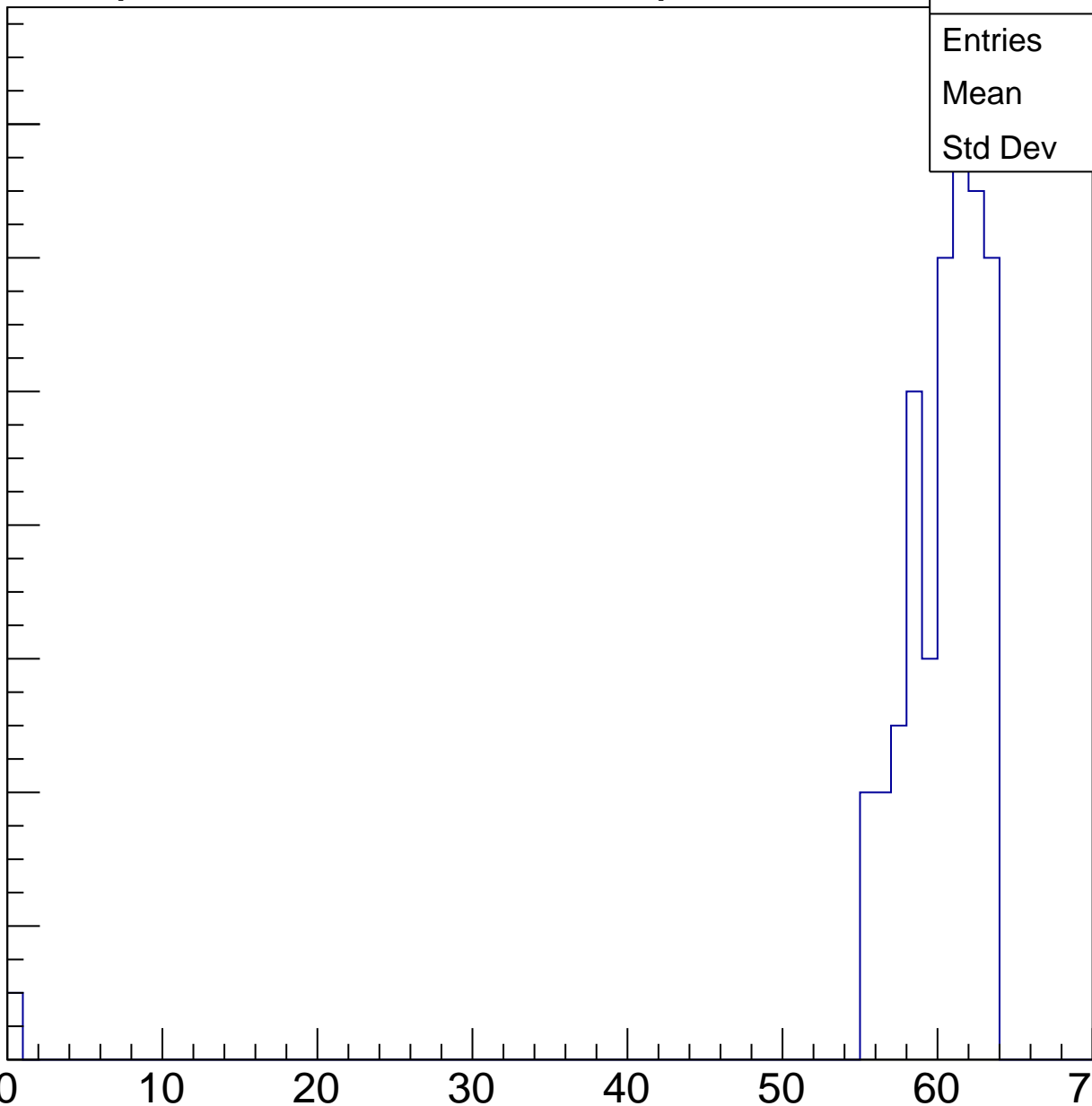
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	59.27
Std Dev	6.972

ampl

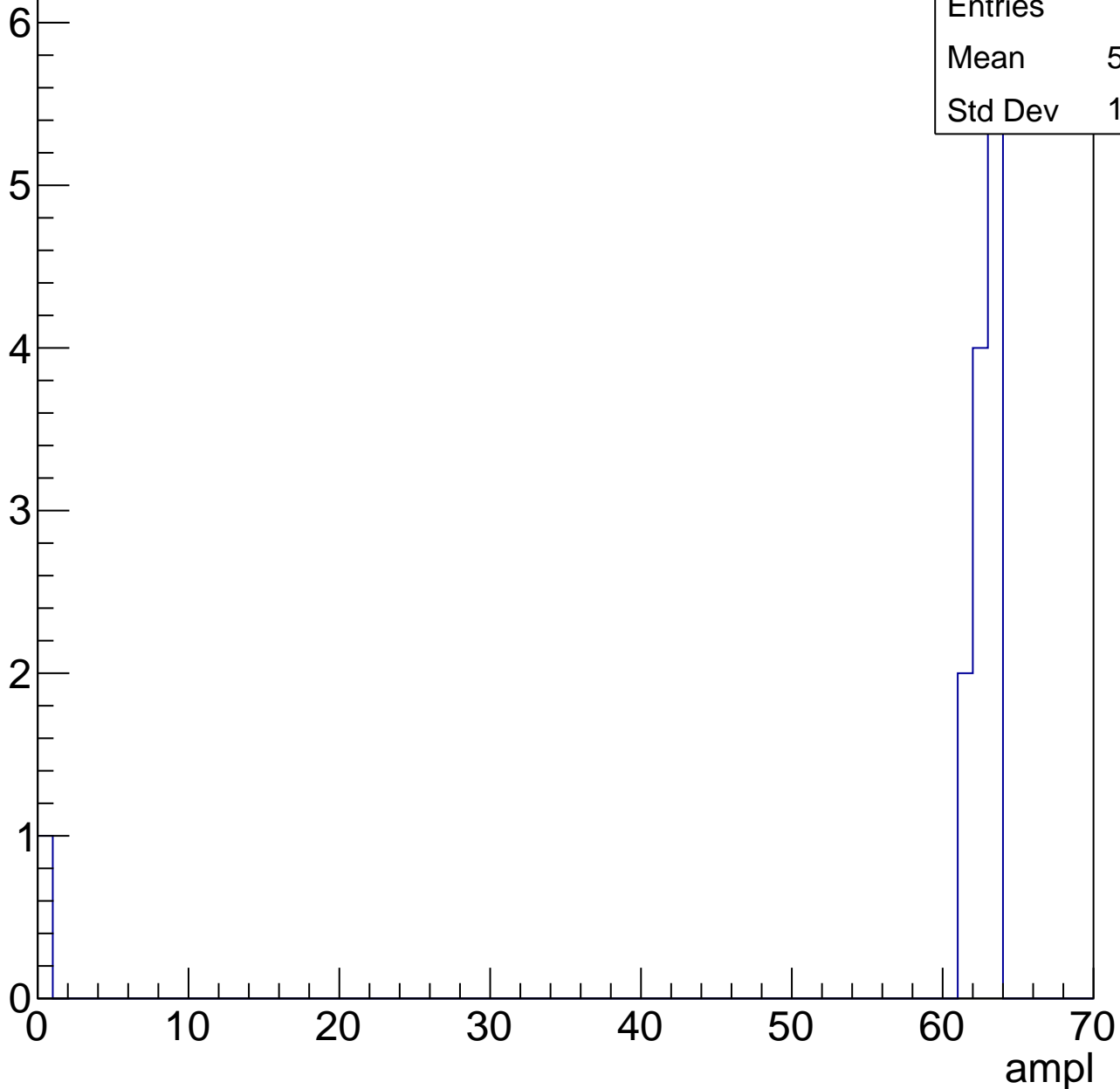


B0L101S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.54
Std Dev	16.63



B0L101S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch88, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	27.03
Std Dev	3.999

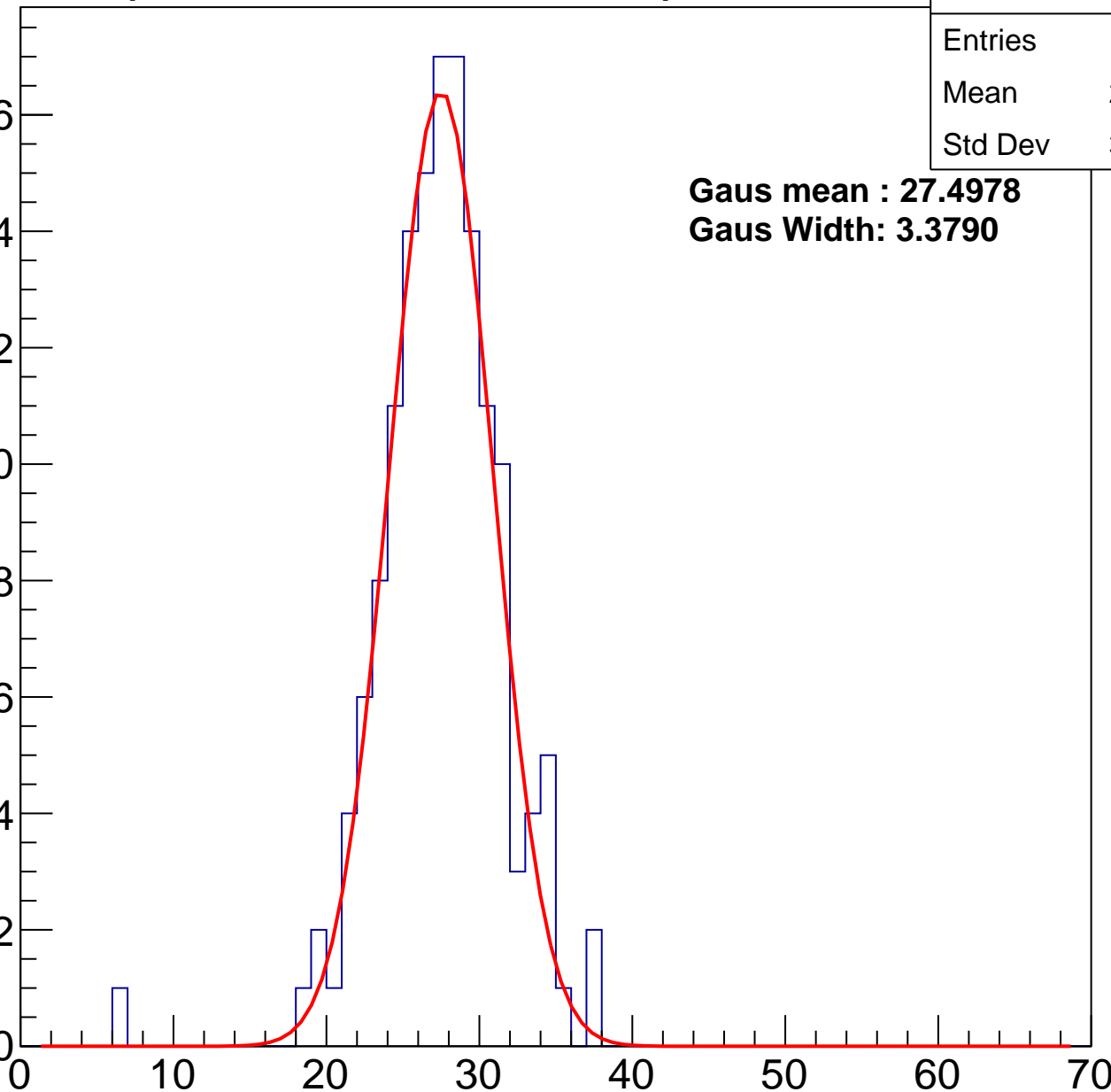
Gaus mean : 27.4978

Gaus Width: 3.3790

Entry

16
14
12
10
8
6
4
2
0

ampl



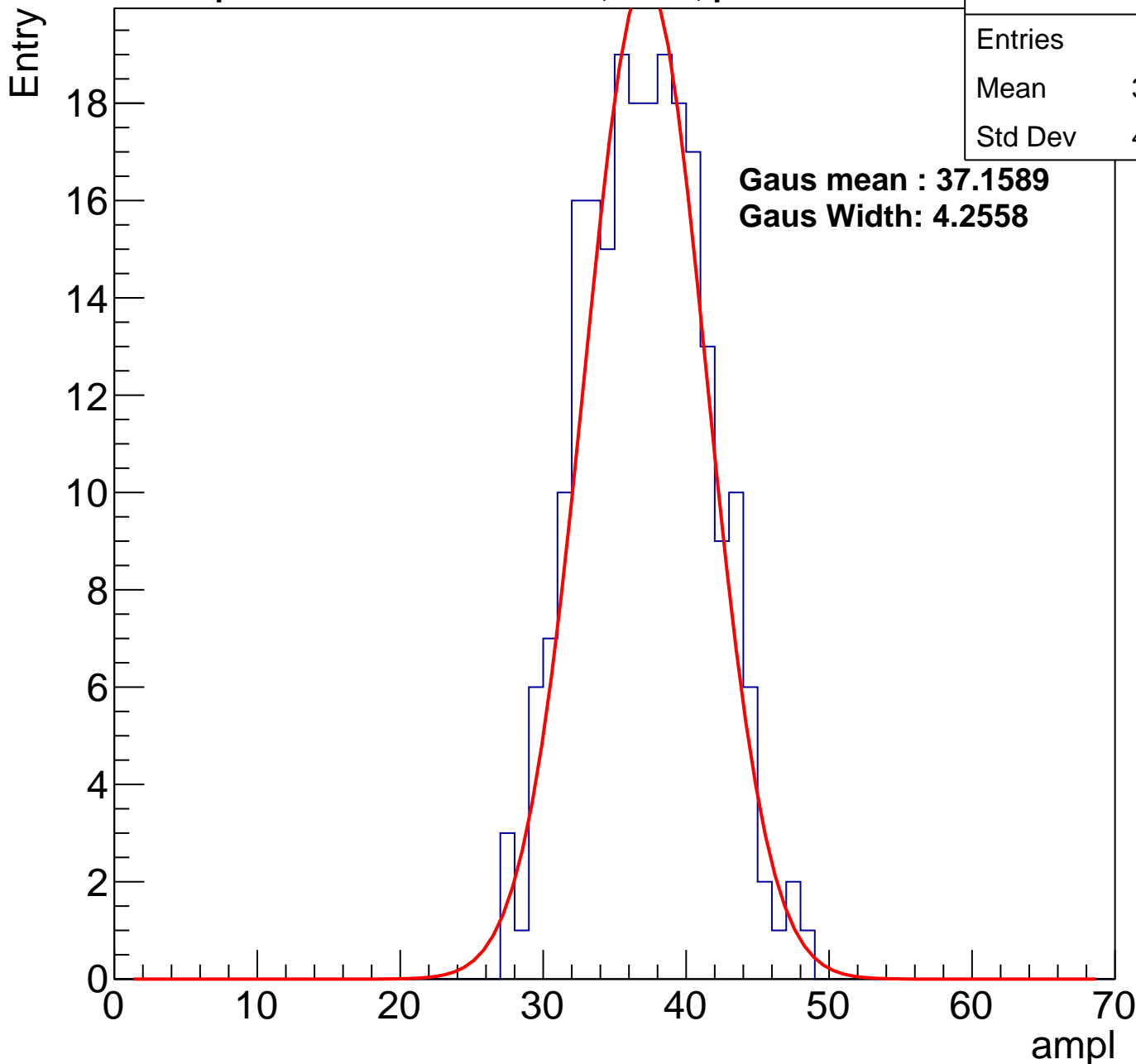
B0L101S, U14-ch88, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	227
Mean	36.65
Std Dev	4.294

Gaus mean : 37.1589

Gaus Width: 4.2558



B0L101S, U14-ch88, adc2

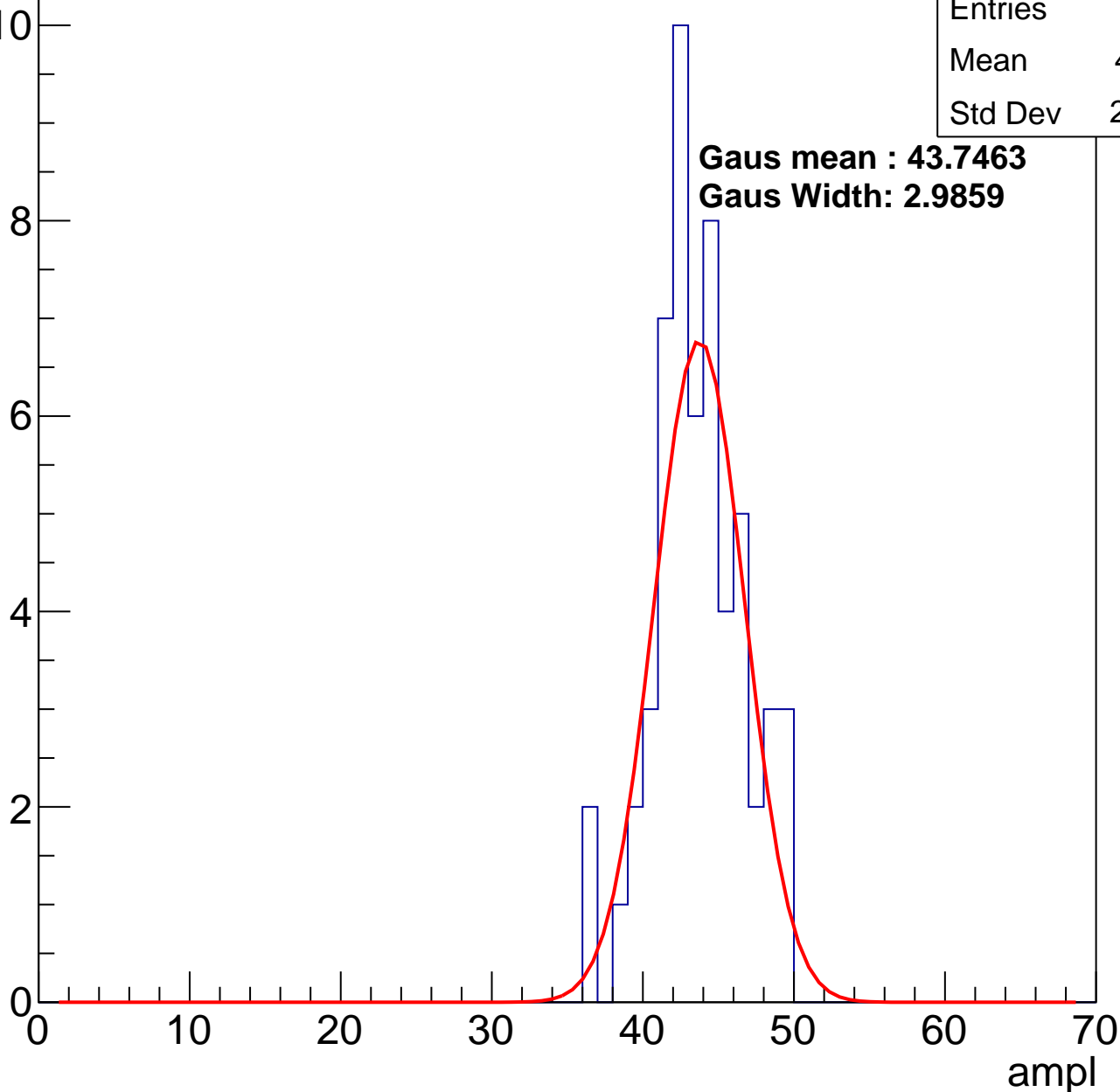
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	56
Mean	43.21
Std Dev	2.992

Gaus mean : 43.7463

Gaus Width: 2.9859



B0L101S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

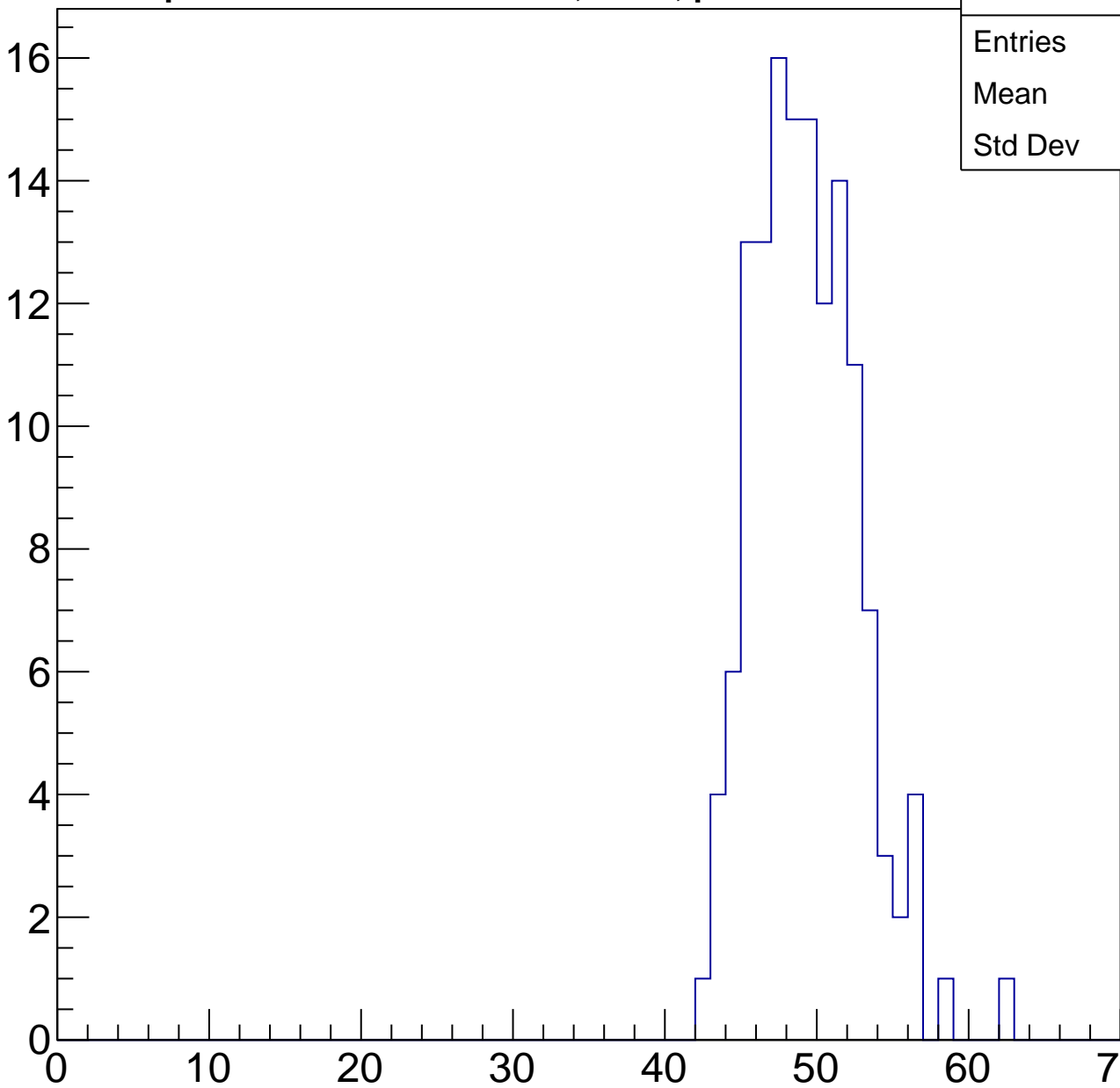
Entries	138
Mean	48.85
Std Dev	3.422

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

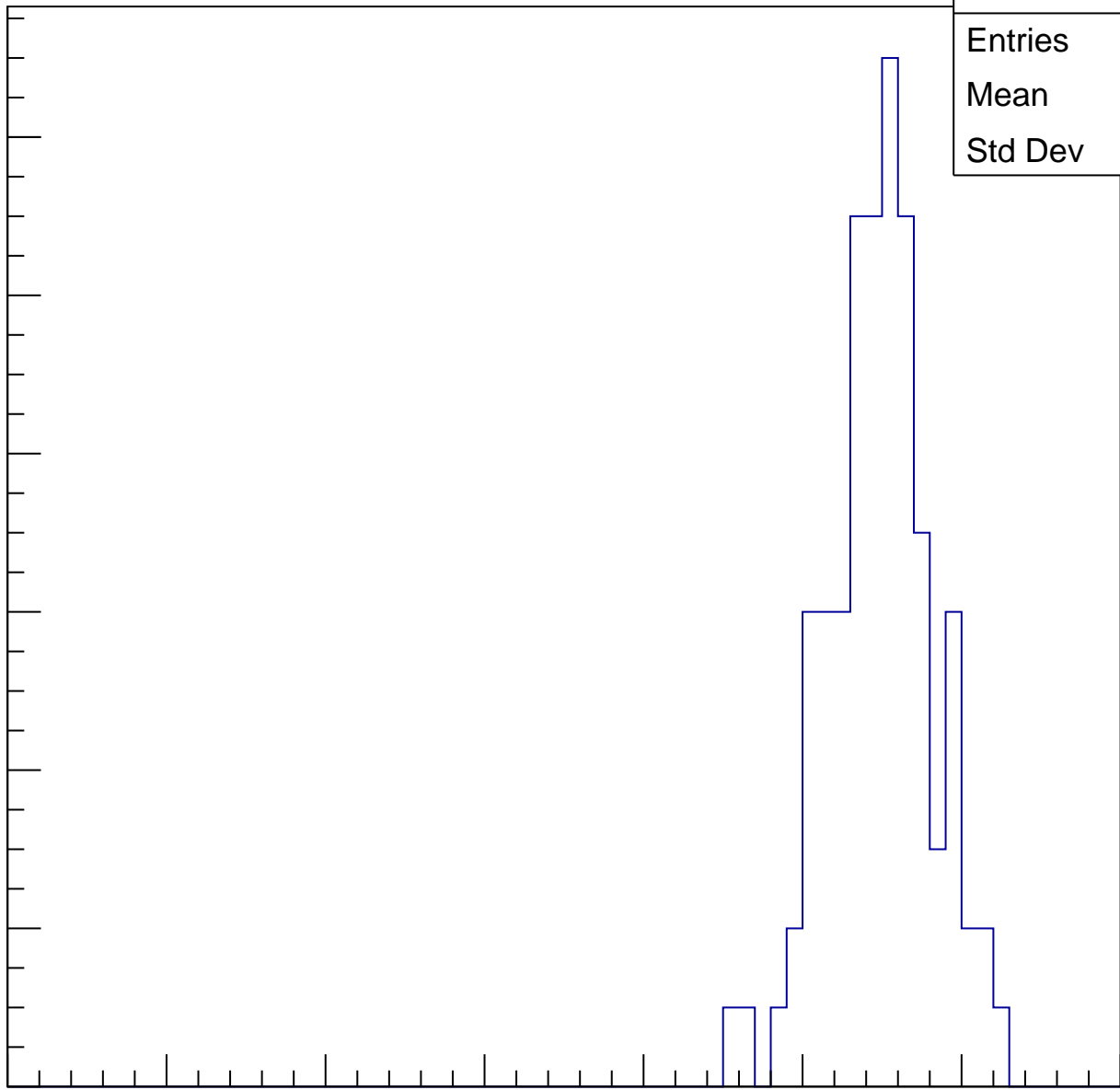
2

0

Entries	90
Mean	54.38
Std Dev	3.258

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch88, adc5

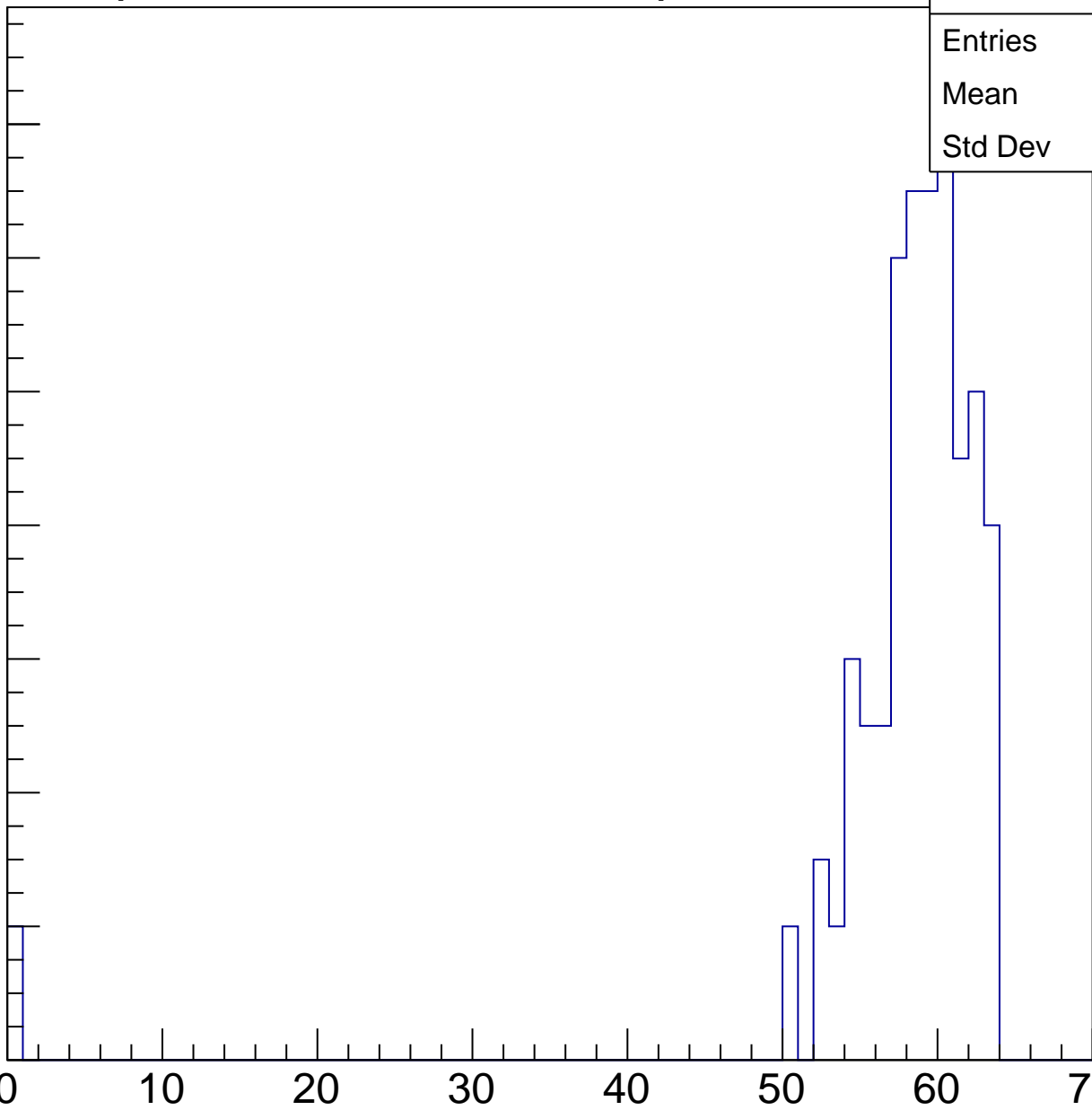
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	105
Mean	57.32
Std Dev	8.537

ampl



B0L101S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	34
Mean	61.29
Std Dev	1.672

Entry

10

8

6

4

2

0

0

10

20

30

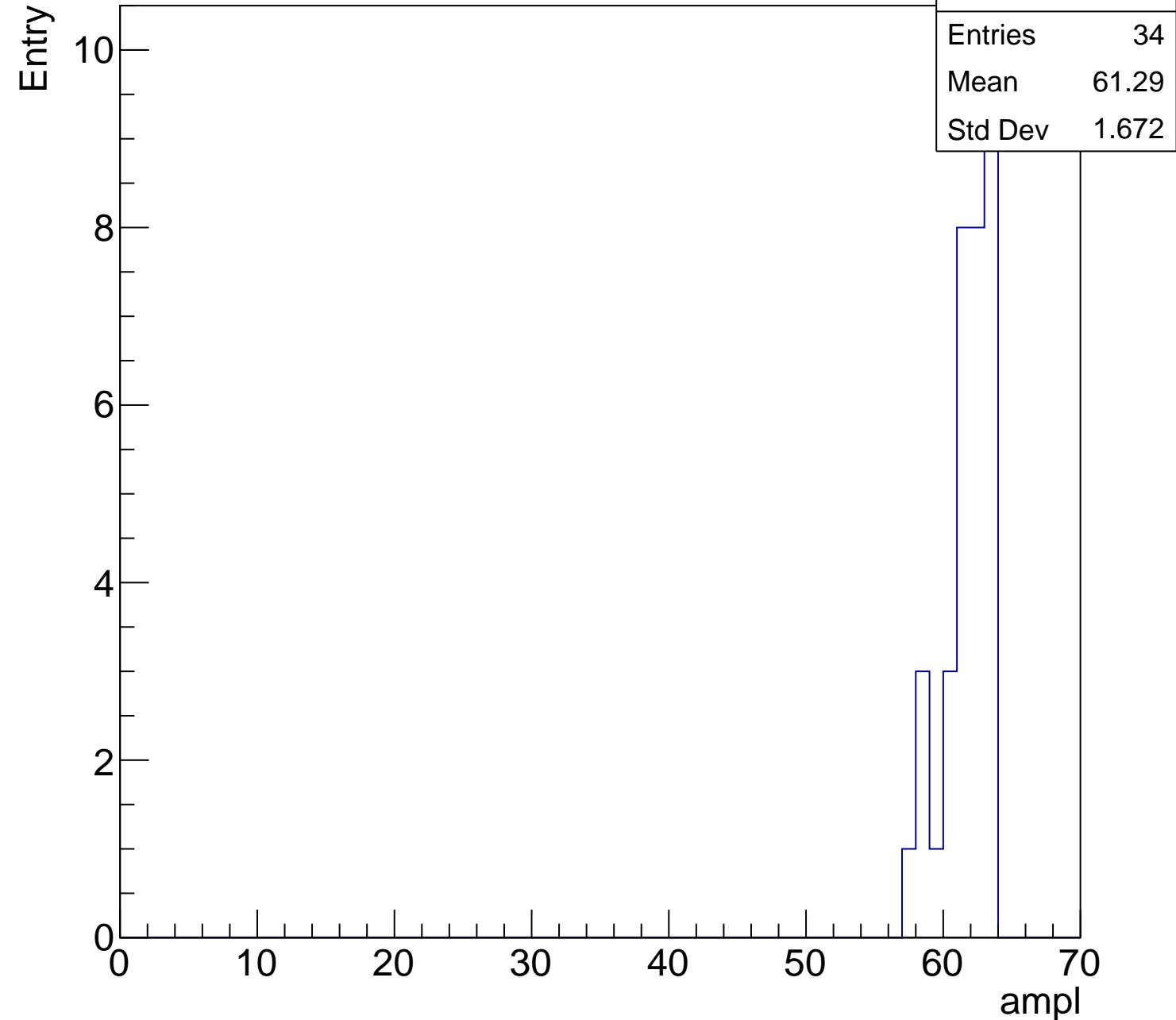
40

50

60

ampl

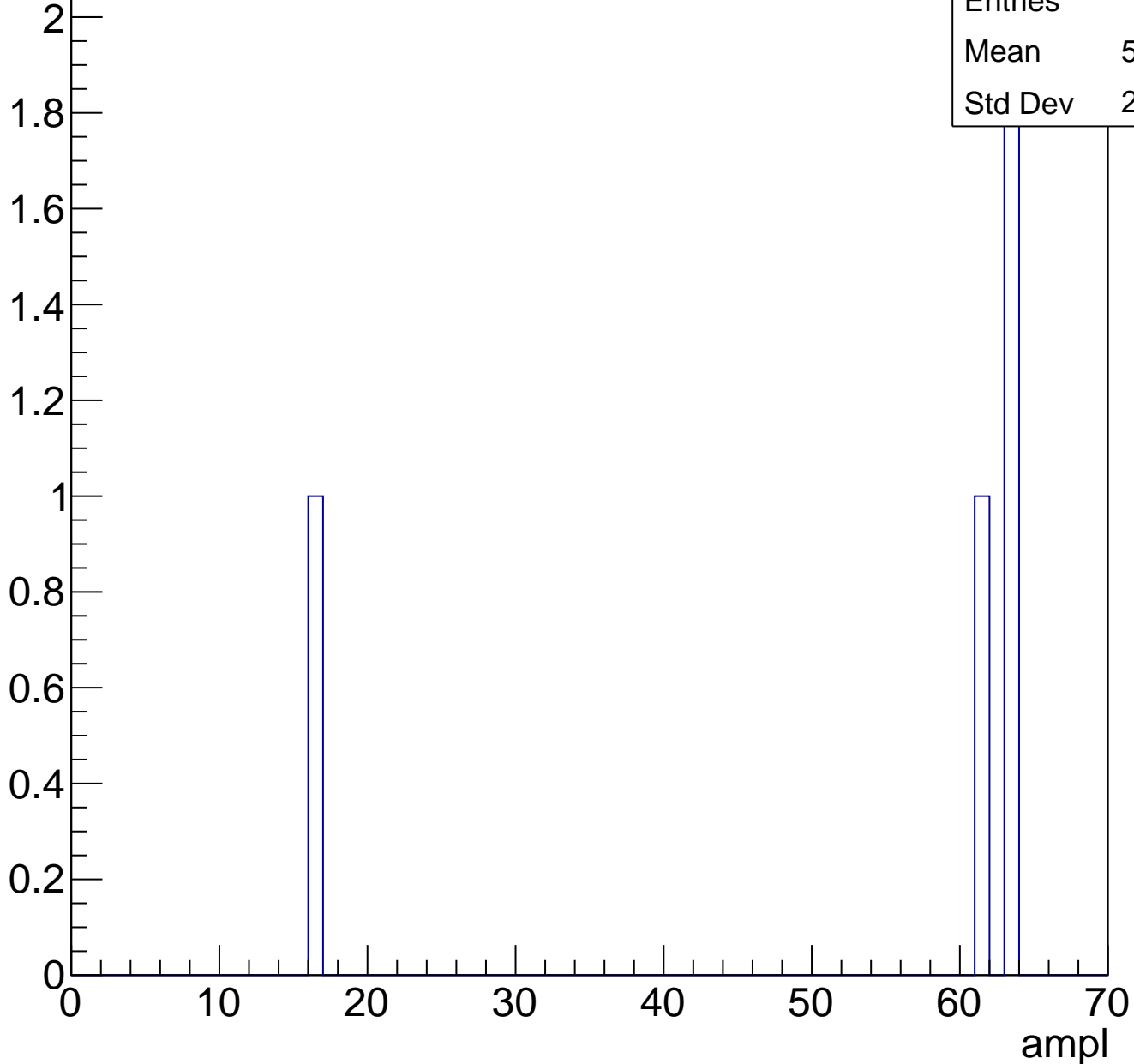
70



B0L101S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	50.75
Std Dev	20.08

B0L101S, U14-ch89, adc0

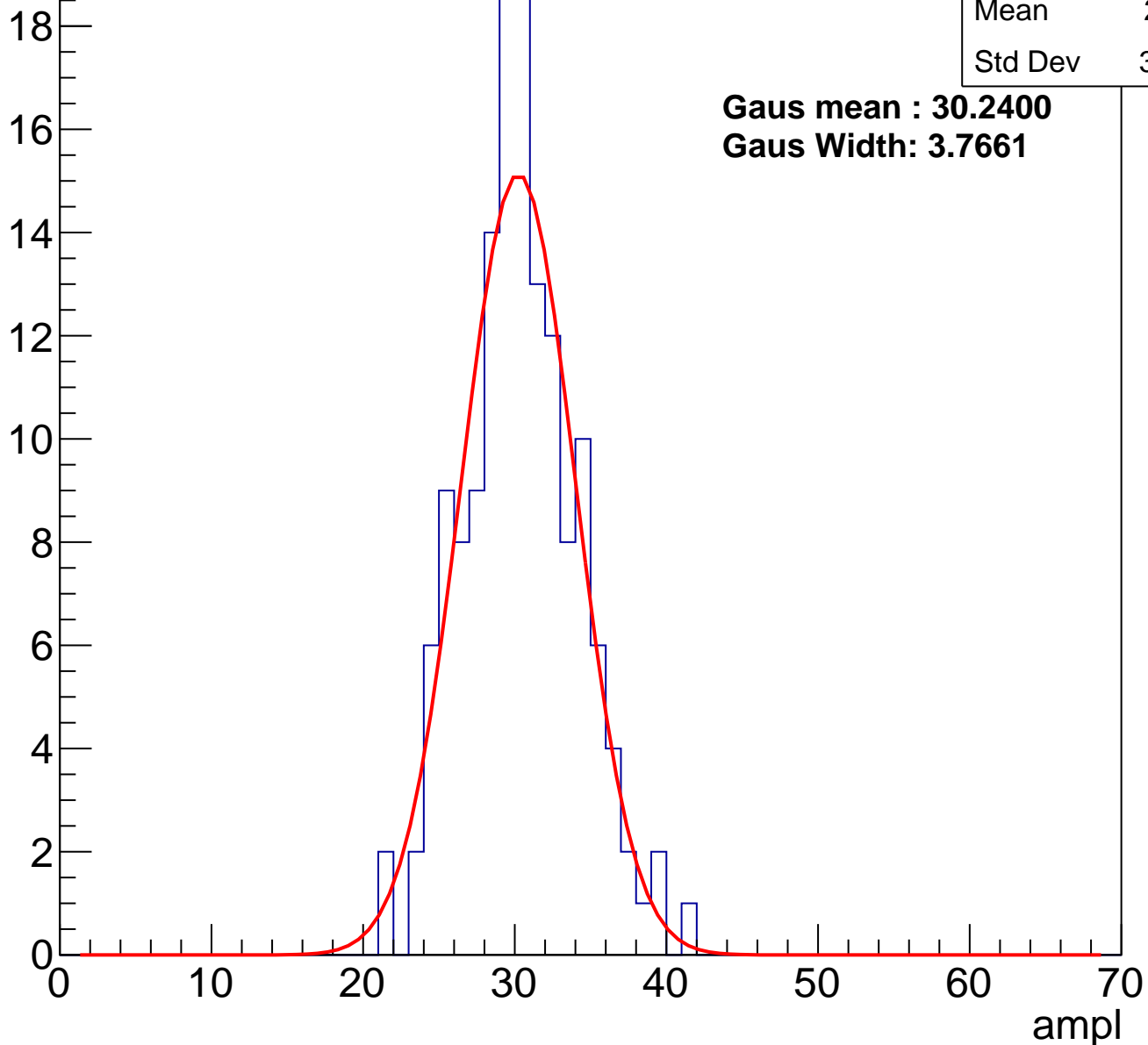
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.91
Std Dev	3.703

Gaus mean : 30.2400

Gaus Width: 3.7661

Entry



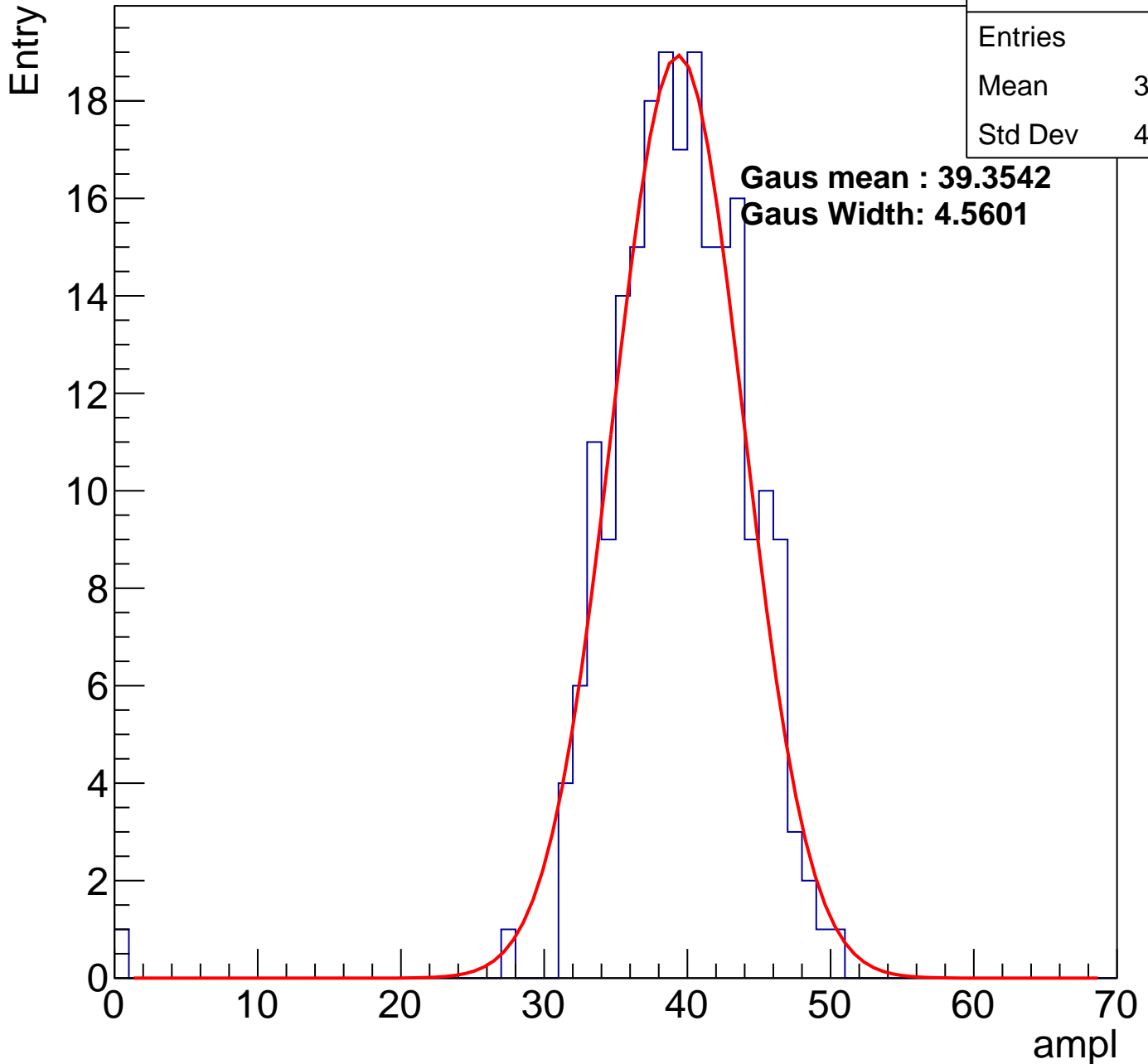
B0L101S, U14-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	215
Mean	38.99
Std Dev	4.992

Gaus mean : 39.3542

Gaus Width: 4.5601



B0L101S, U14-ch89, adc2

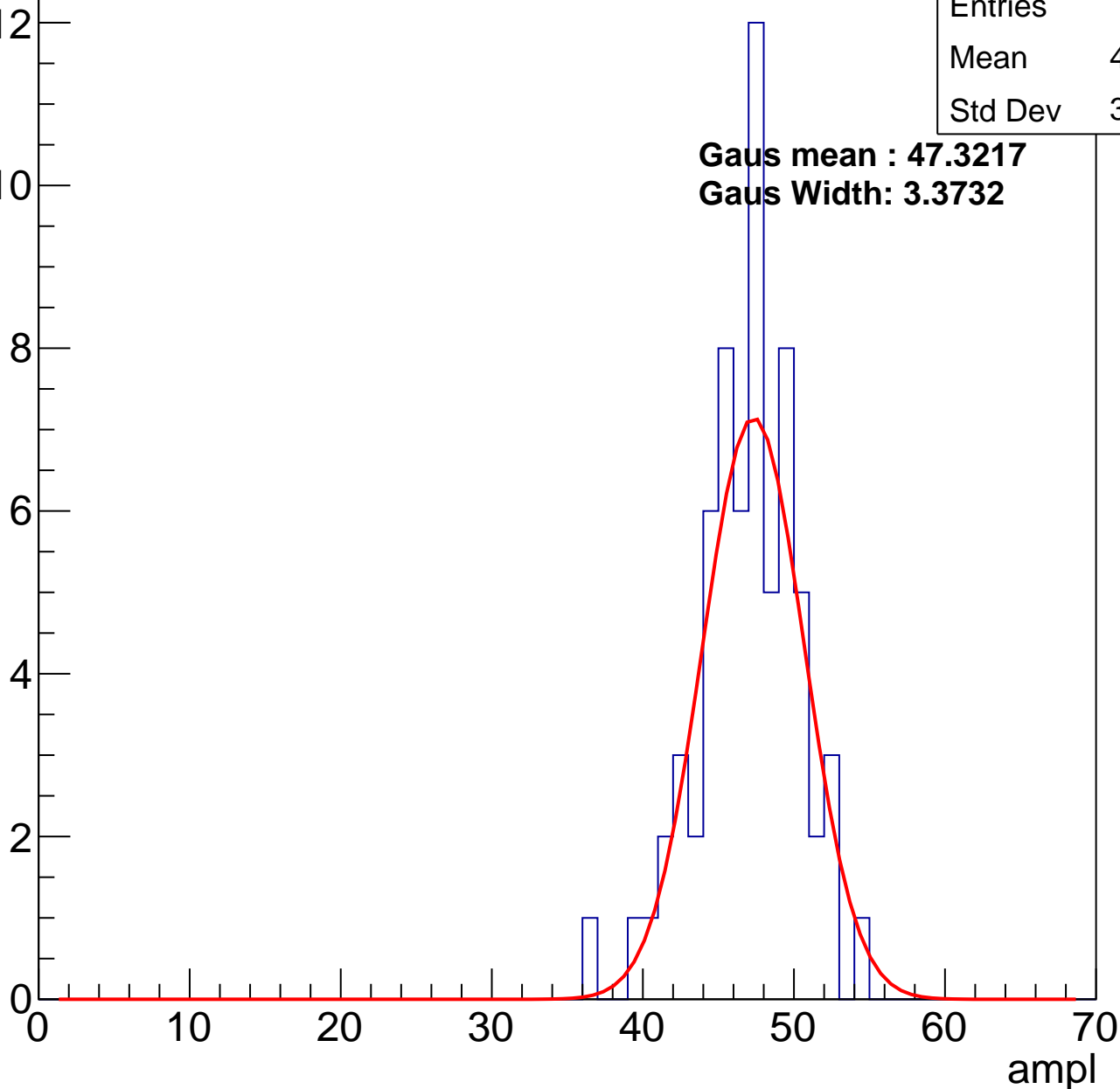
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	66
Mean	46.47
Std Dev	3.313

Gaus mean : 47.3217

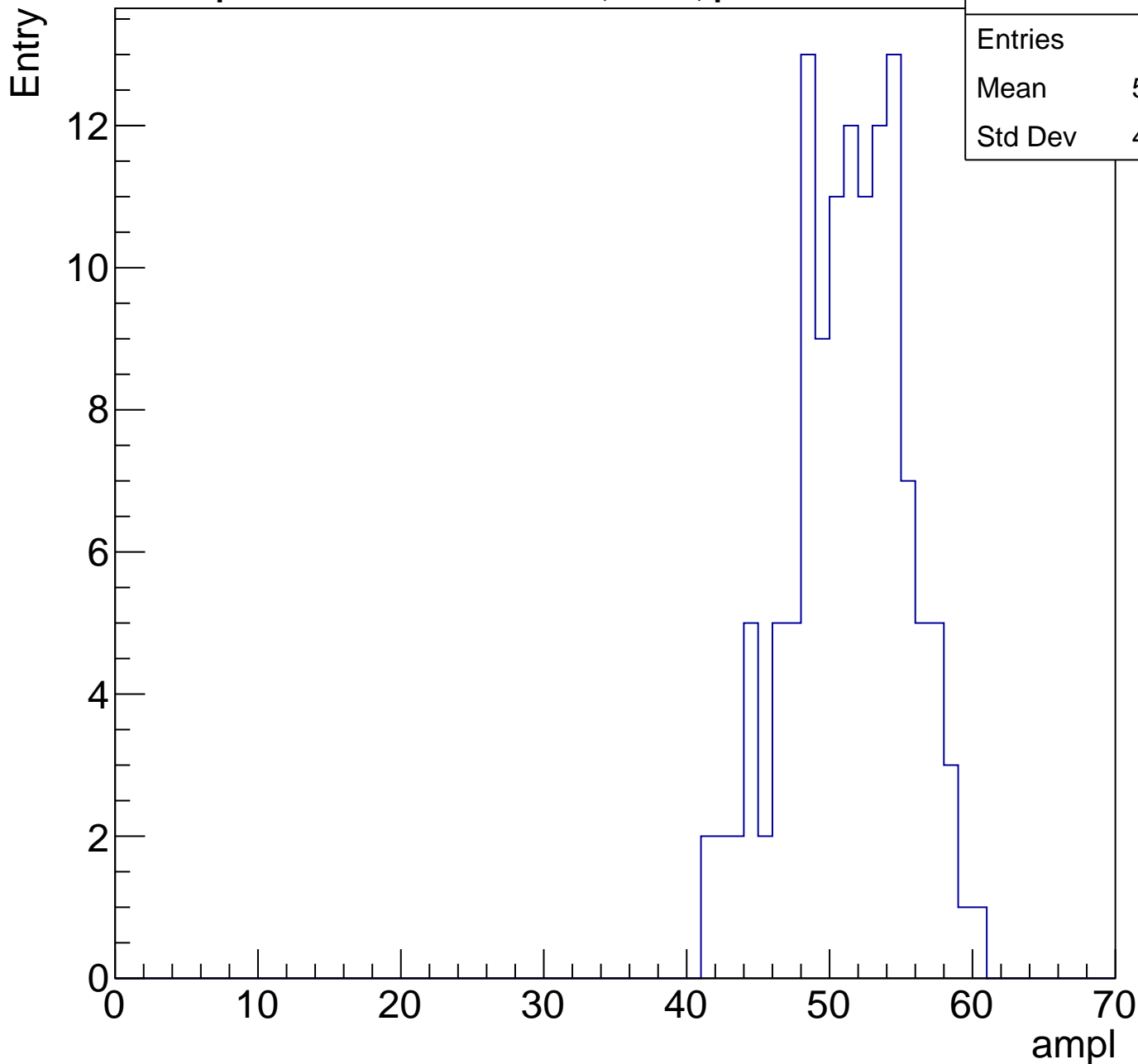
Gaus Width: 3.3732



B0L101S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	50.85
Std Dev	4.042



B0L101S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#1, port C1

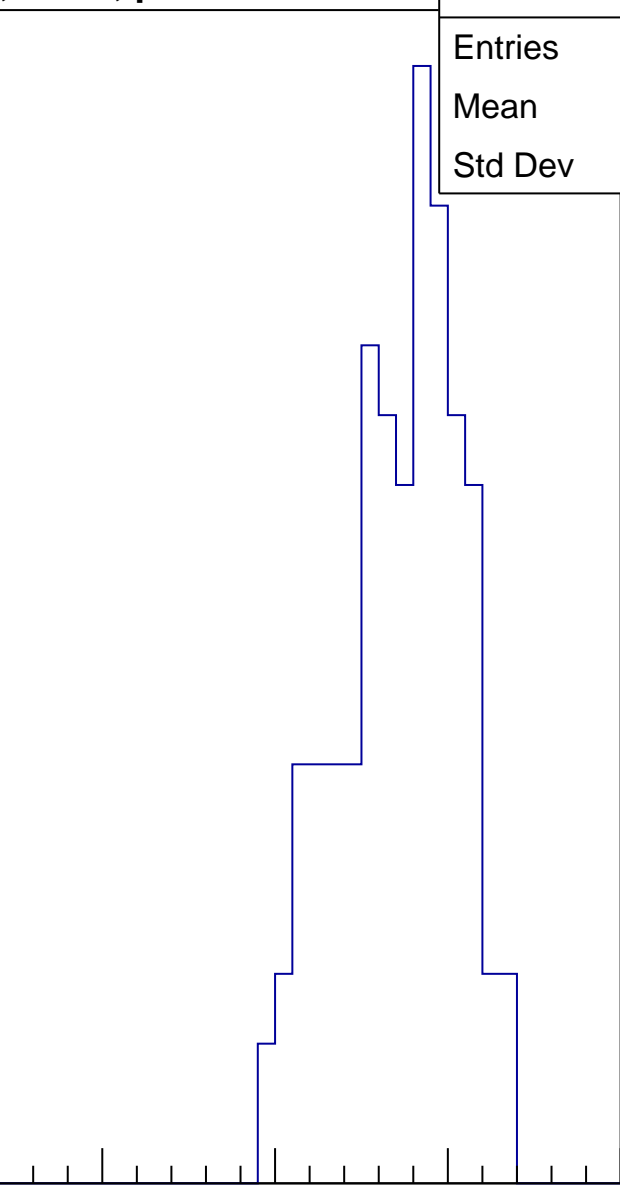
Entries	119
Mean	56.75
Std Dev	3.376

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch89, adc5

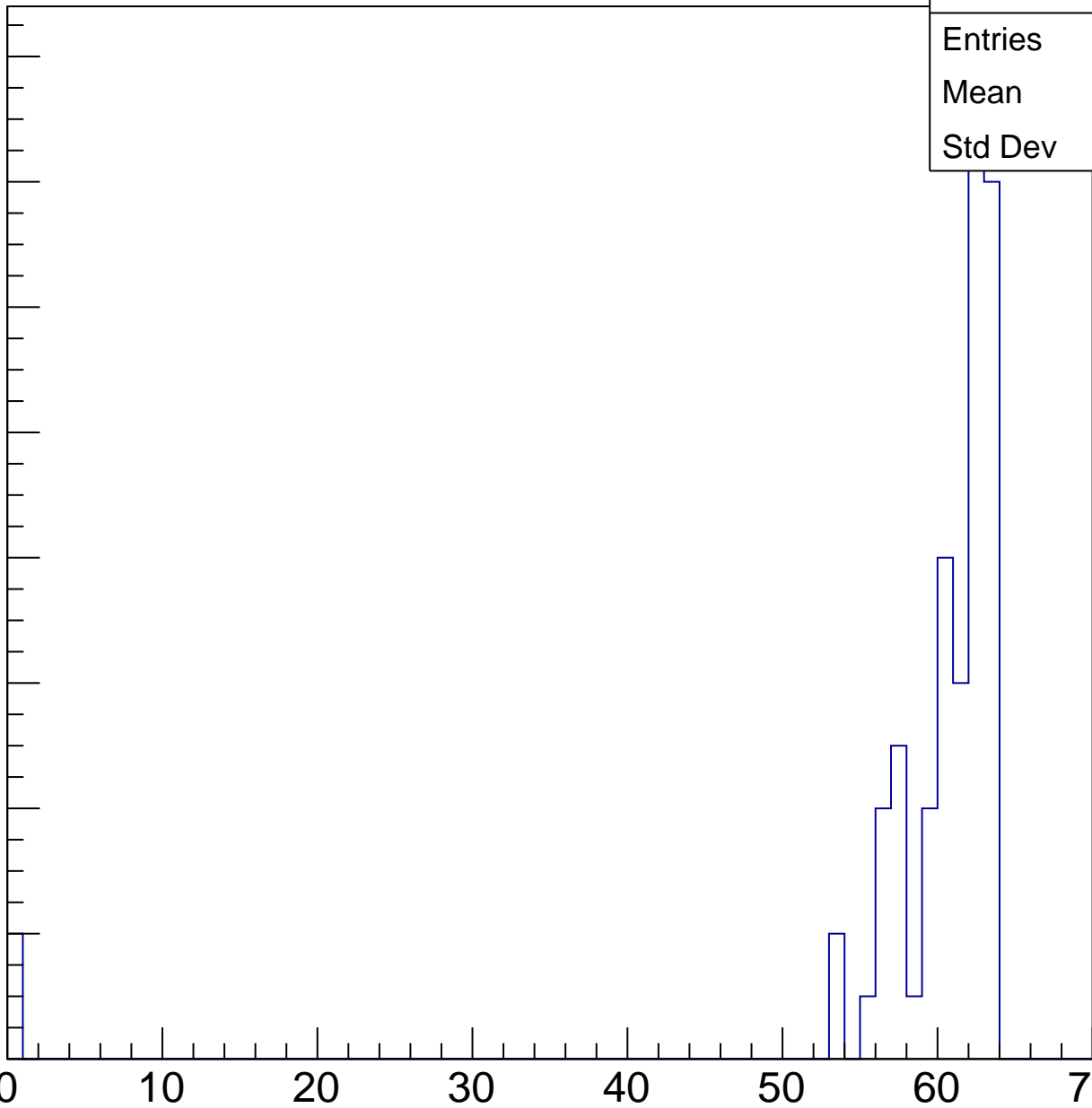
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	63
Mean	58.48
Std Dev	10.9

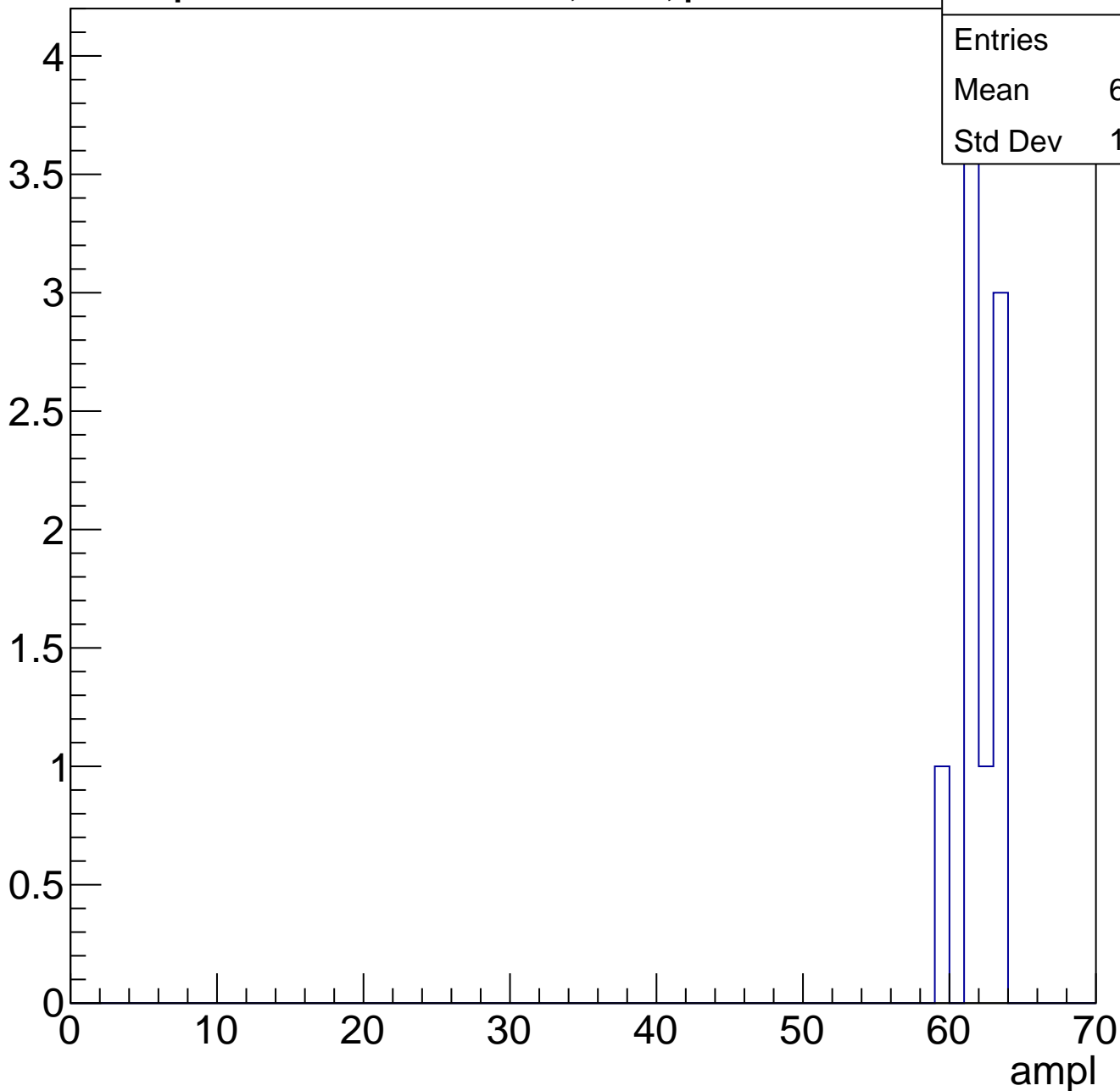
ampl



B0L101S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

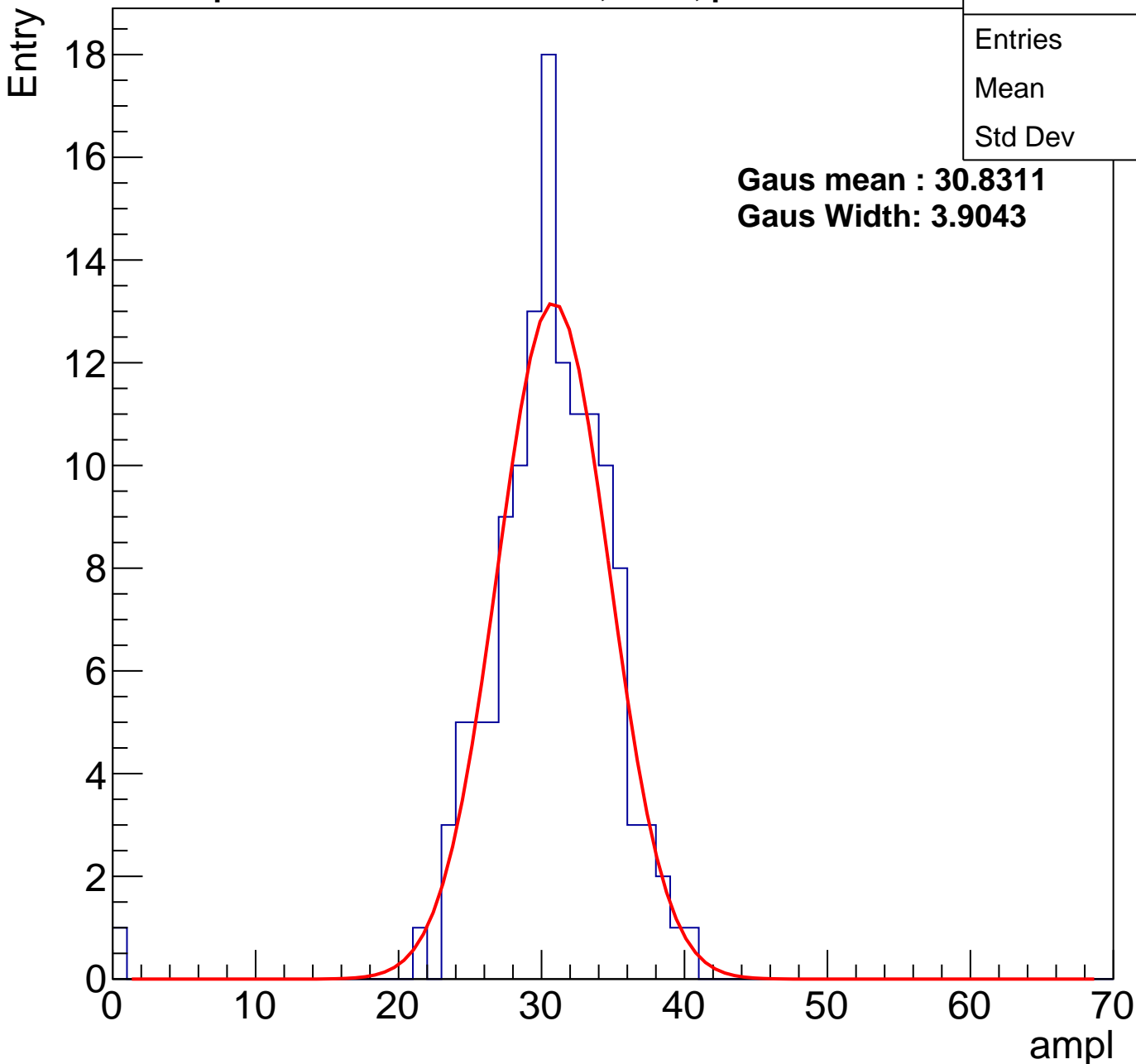
B0L101S, U14-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	30.2
Std Dev	4.54

Gaus mean : 30.8311

Gaus Width: 3.9043



B0L101S, U14-ch90, adc1

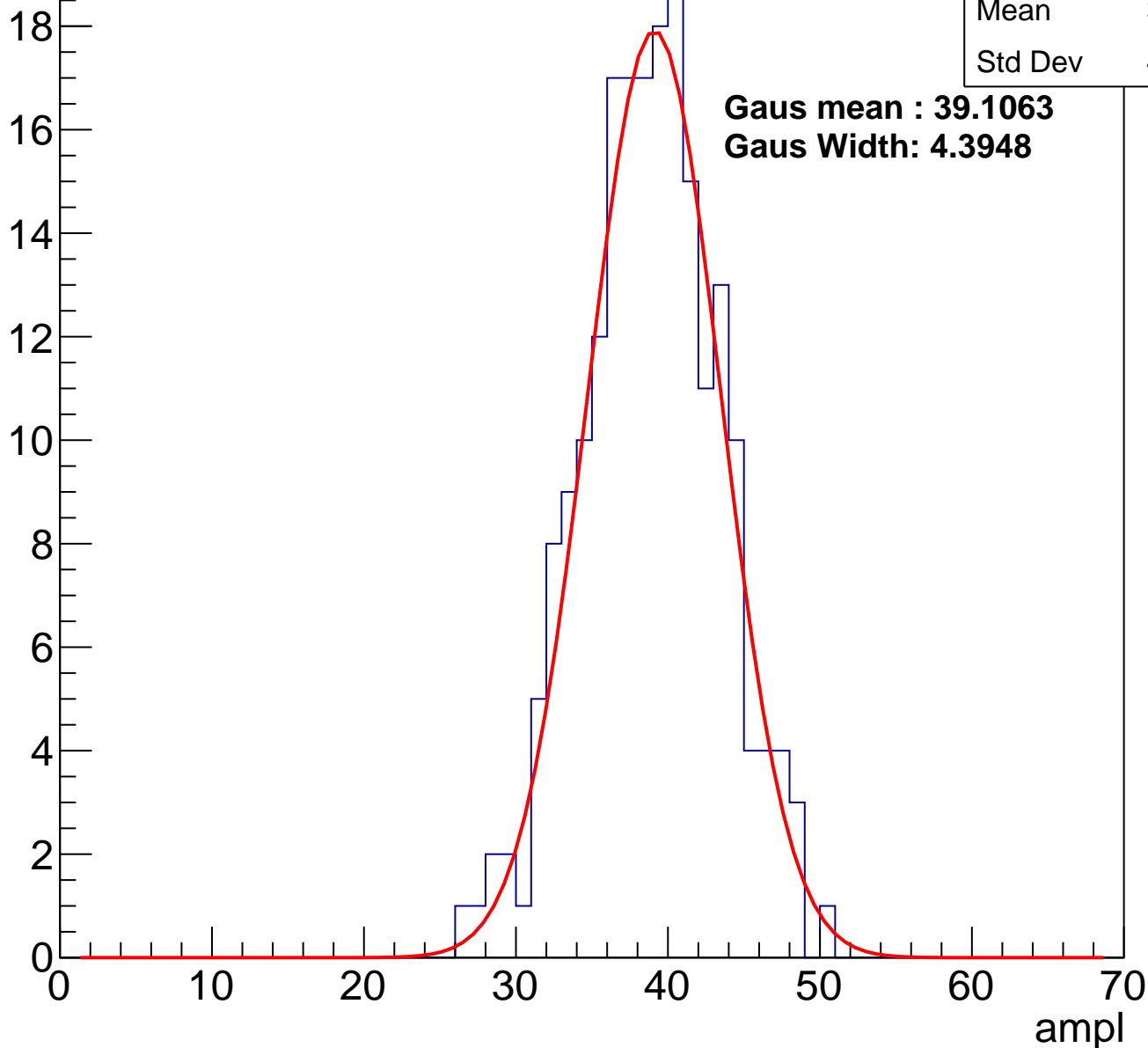
calib_packv5_042523_0143.root, FC#1, port C1

Entries	204
Mean	38.41
Std Dev	4.471

Gaus mean : 39.1063

Gaus Width: 4.3948

Entry



B0L101S, U14-ch90, adc2

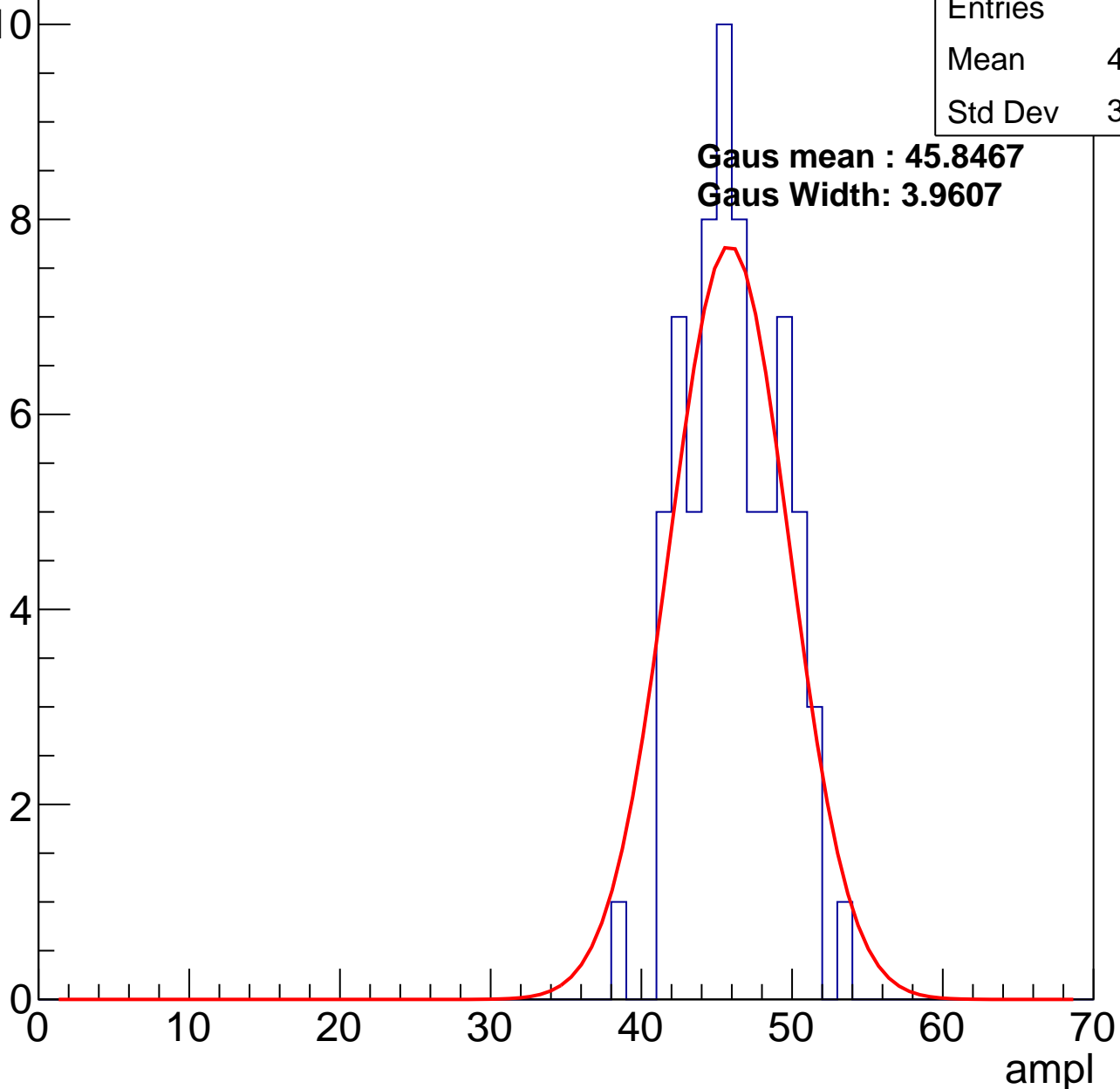
calib_packv5_042523_0143.root, FC#1, port C1

Entries	70
Mean	45.66
Std Dev	3.098

Gaus mean : 45.8467

Gaus Width: 3.9607

Entry

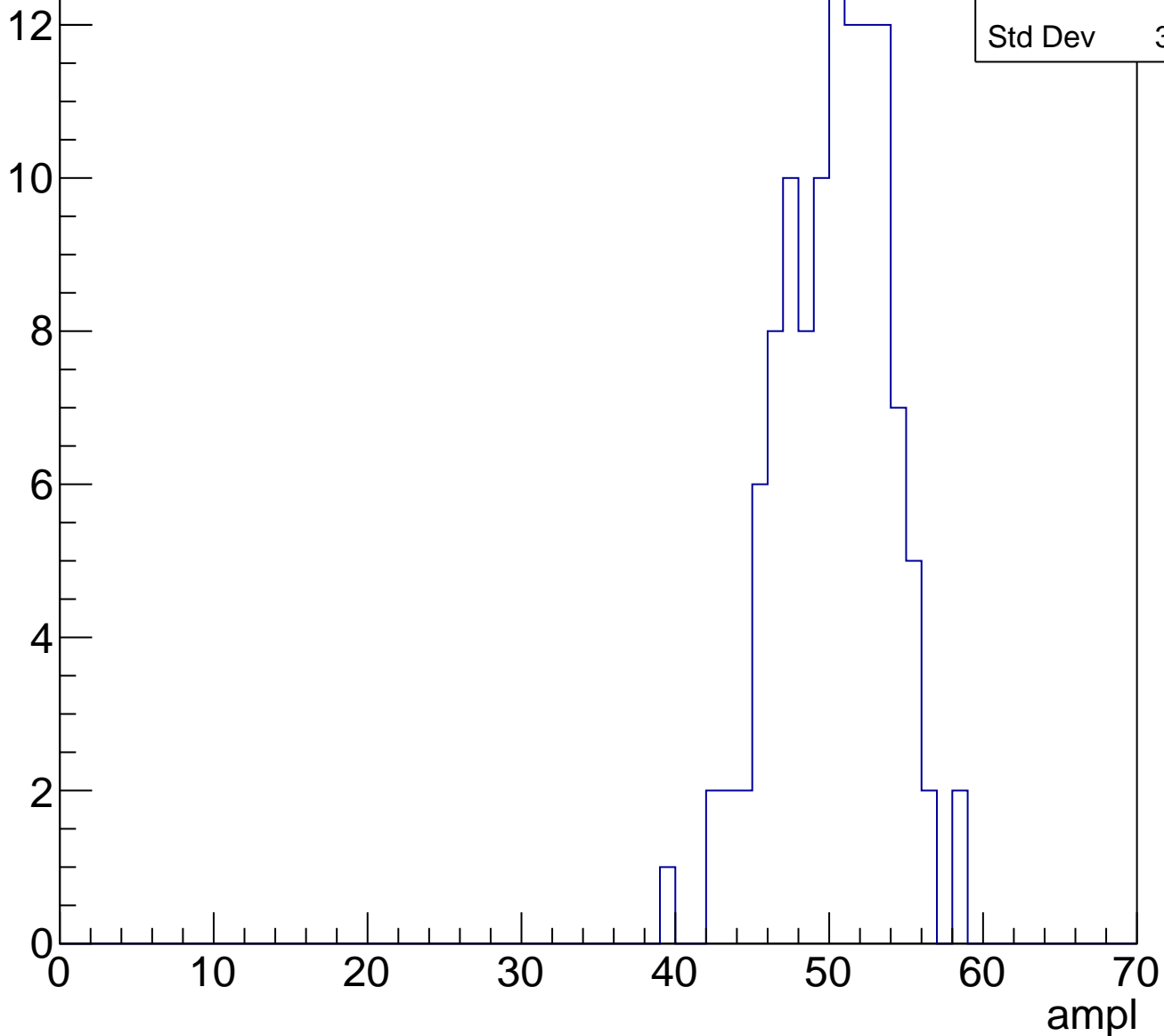


B0L101S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	49.84
Std Dev	3.543

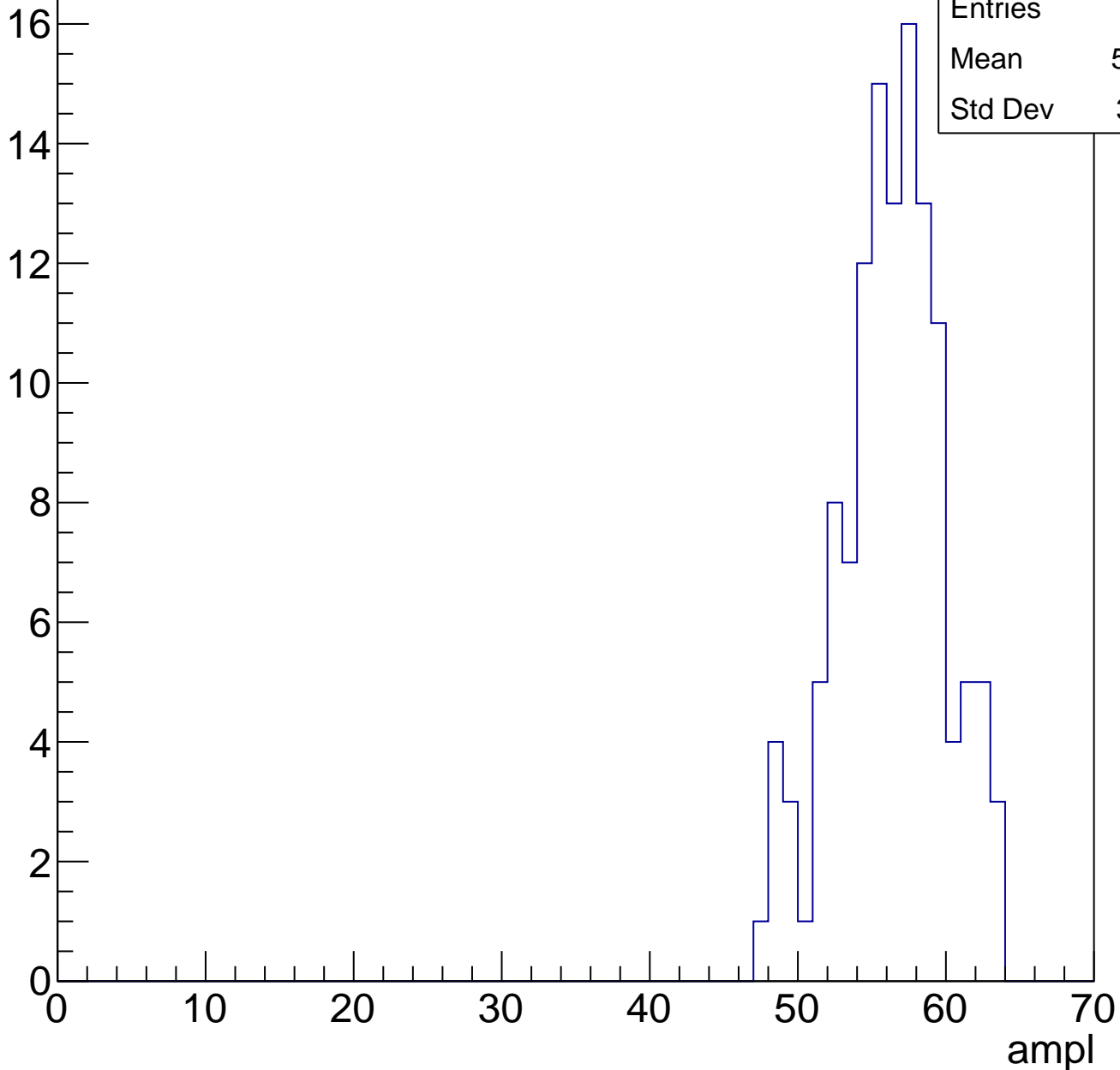
Entry



B0L101S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch90, adc5

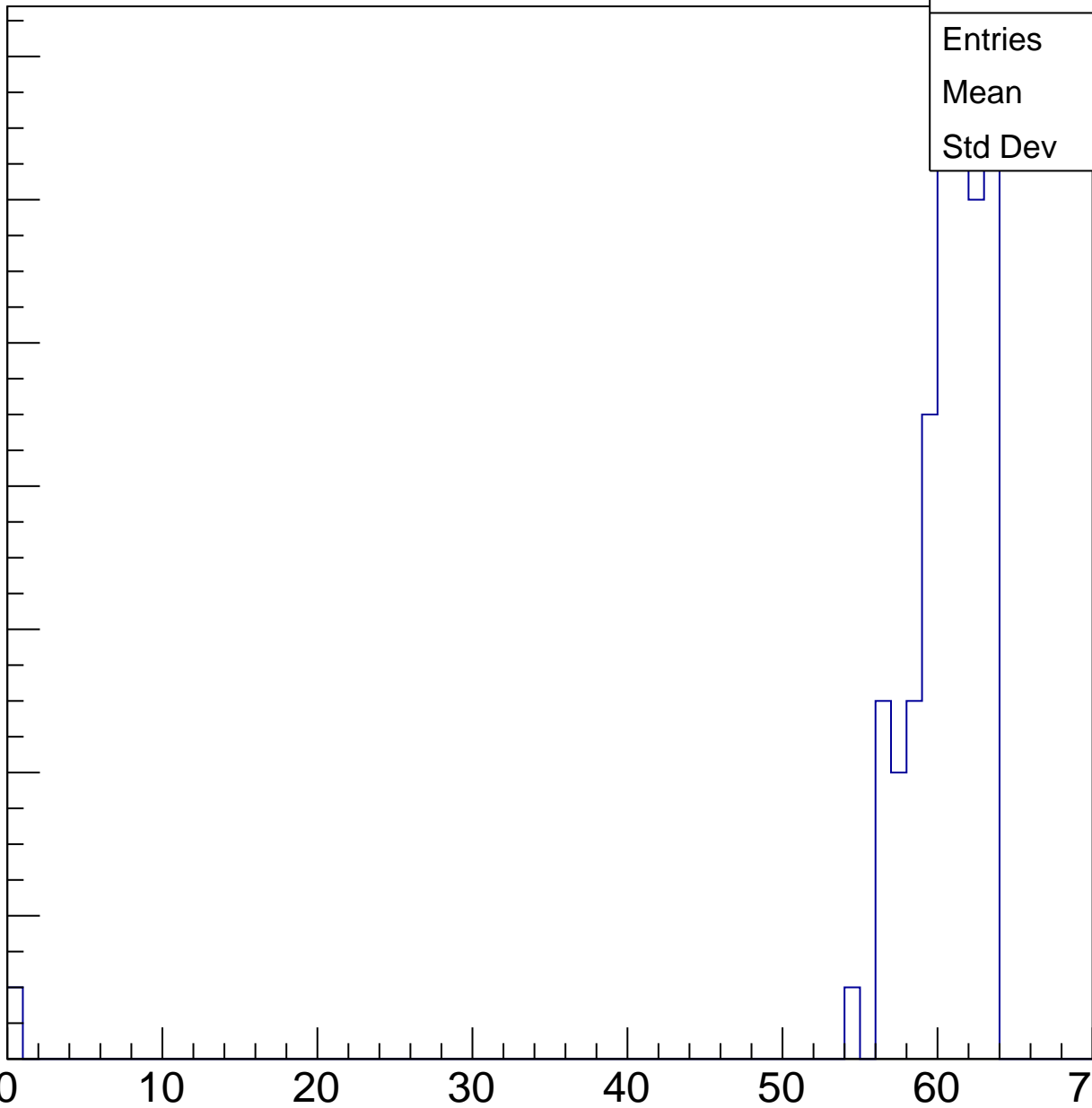
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.51
Std Dev	7.116

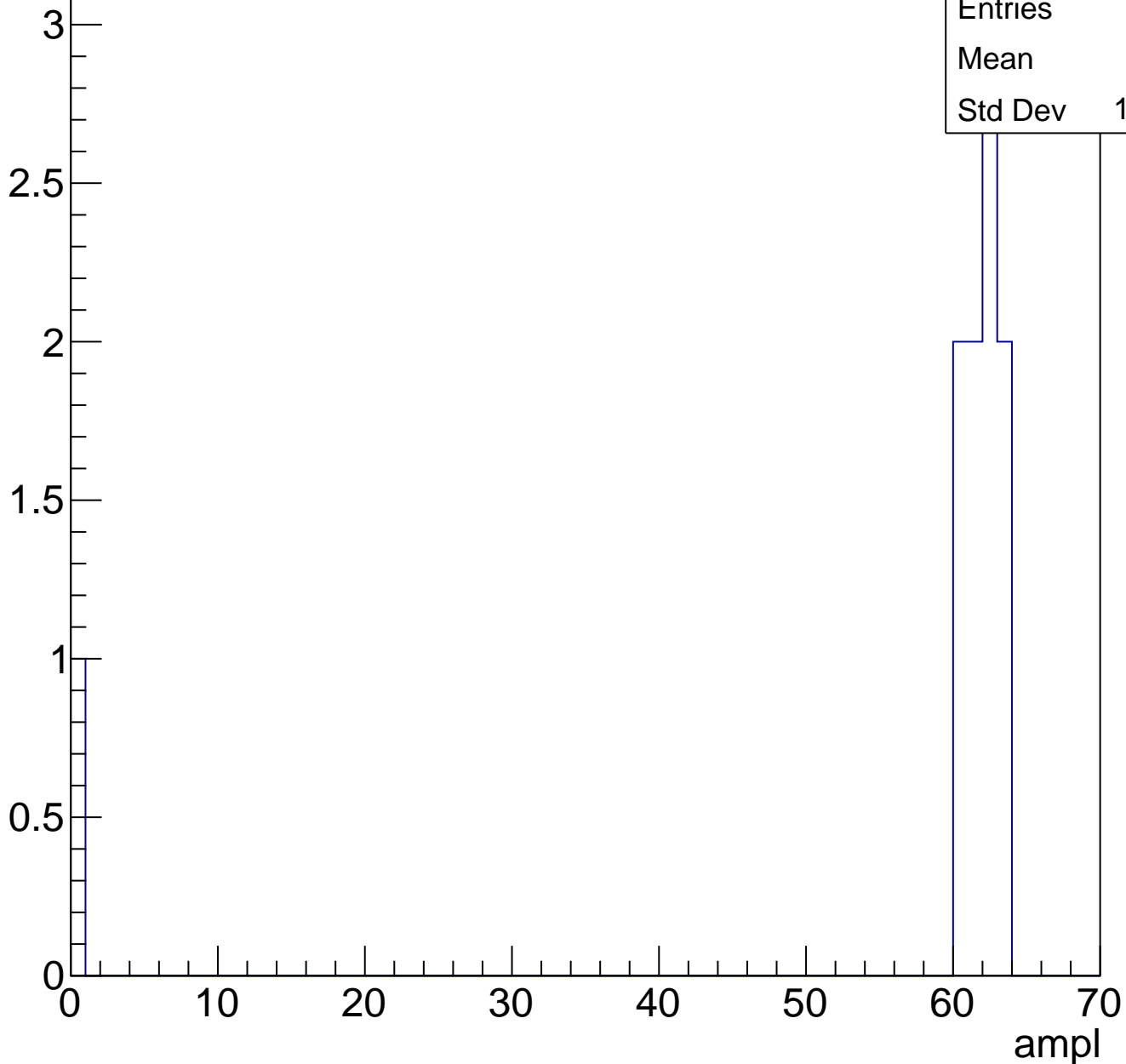
ampl



B0L101S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U14-ch91, adc0

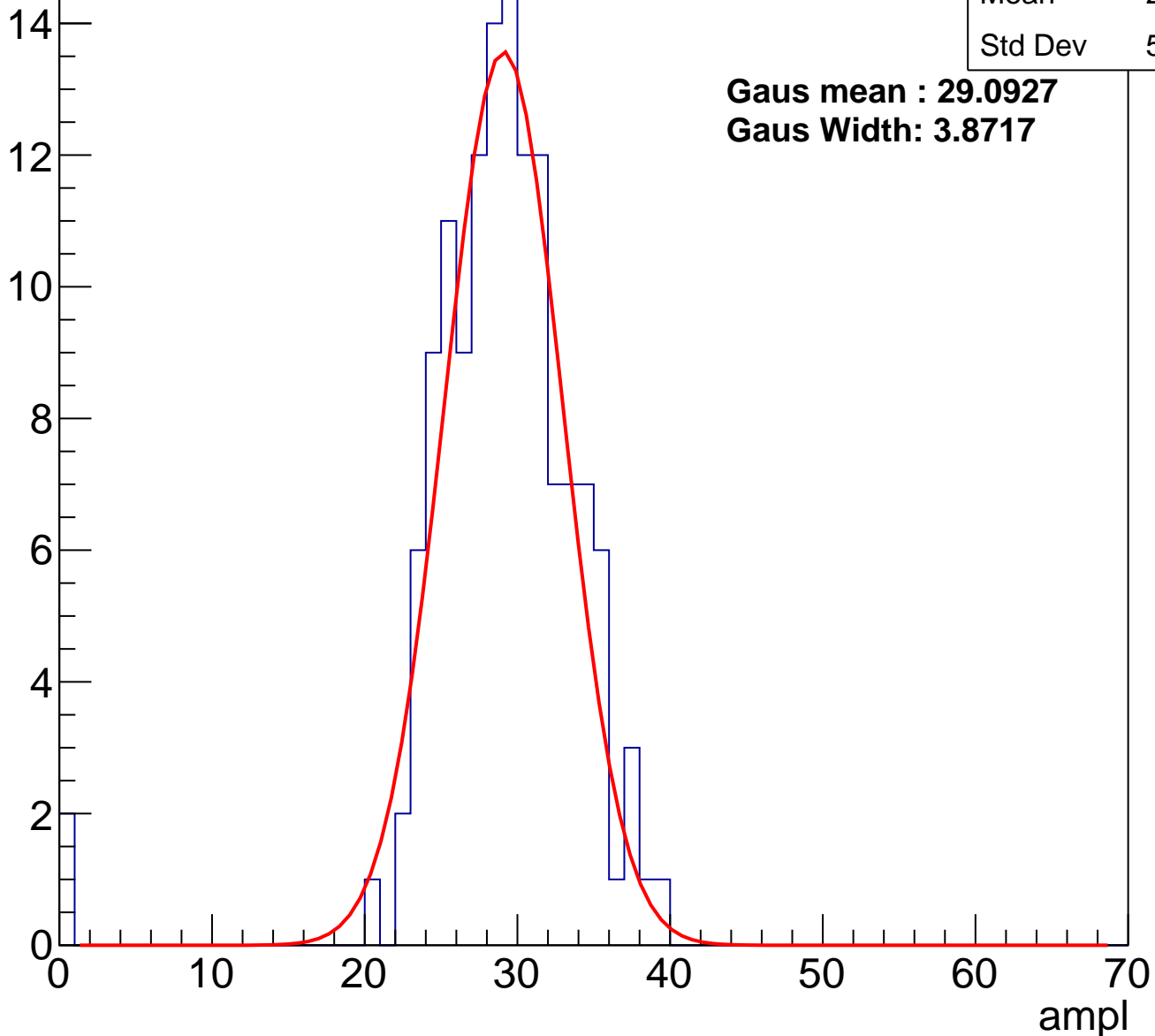
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	28.53
Std Dev	5.135

Gaus mean : 29.0927

Gaus Width: 3.8717

Entry



B0L101S, U14-ch91, adc1

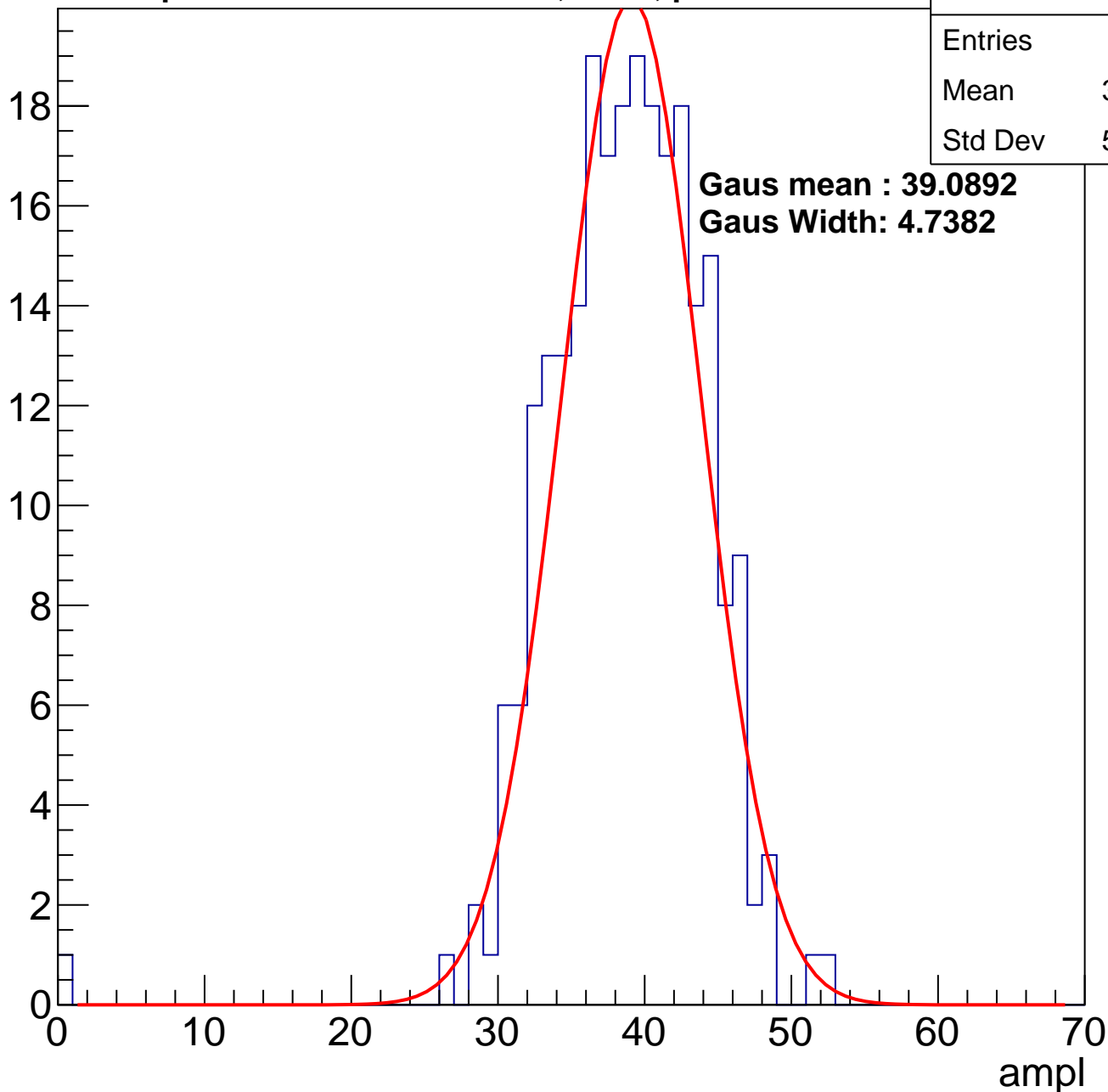
calib_packv5_042523_0143.root, FC#1, port C1

Entries	248
Mean	38.35
Std Dev	5.282

Gaus mean : 39.0892

Gaus Width: 4.7382

Entry



B0L101S, U14-ch91, adc2

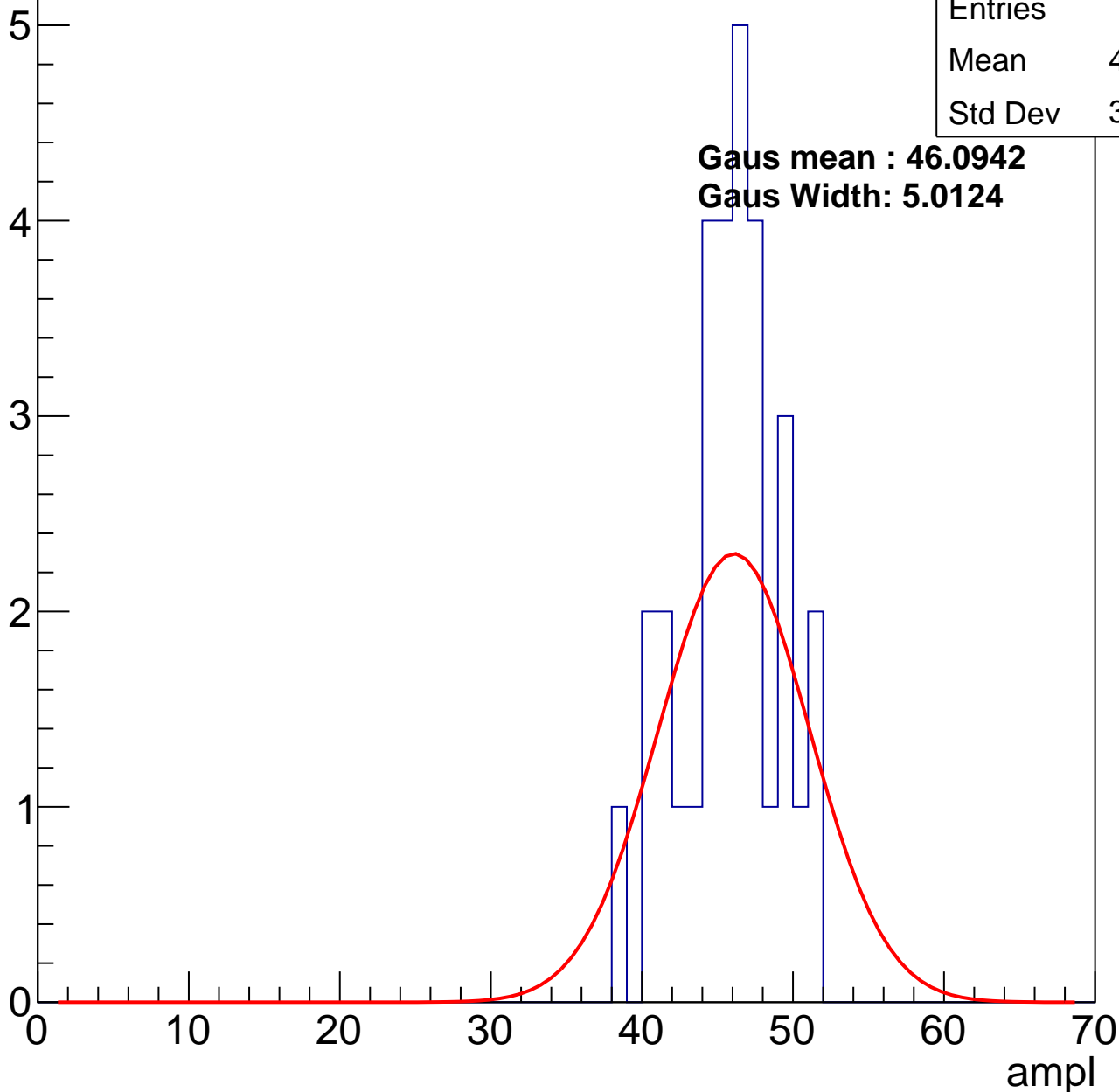
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	31
Mean	45.35
Std Dev	3.208

Gaus mean : 46.0942

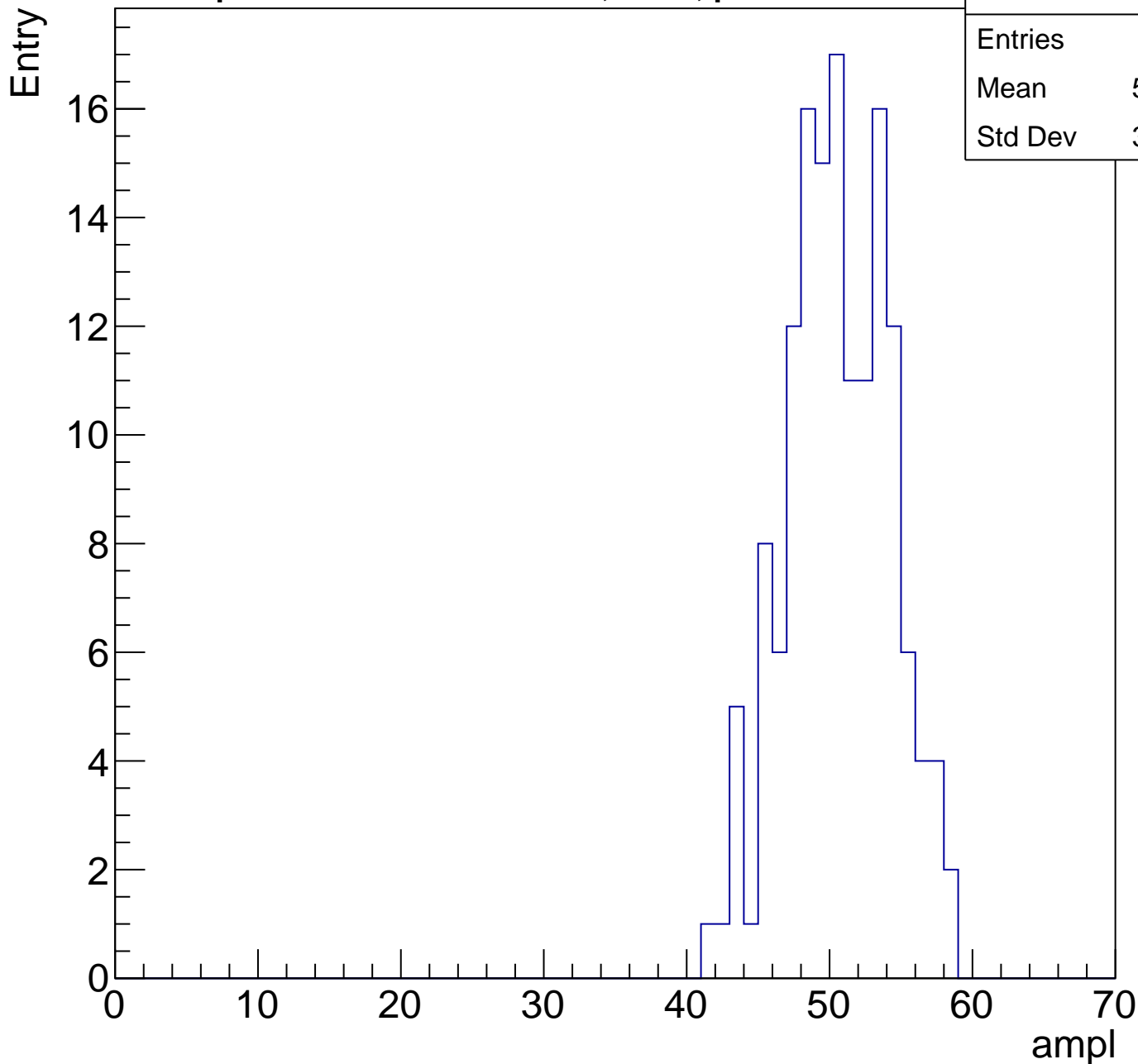
Gaus Width: 5.0124



B0L101S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	50.15
Std Dev	3.597

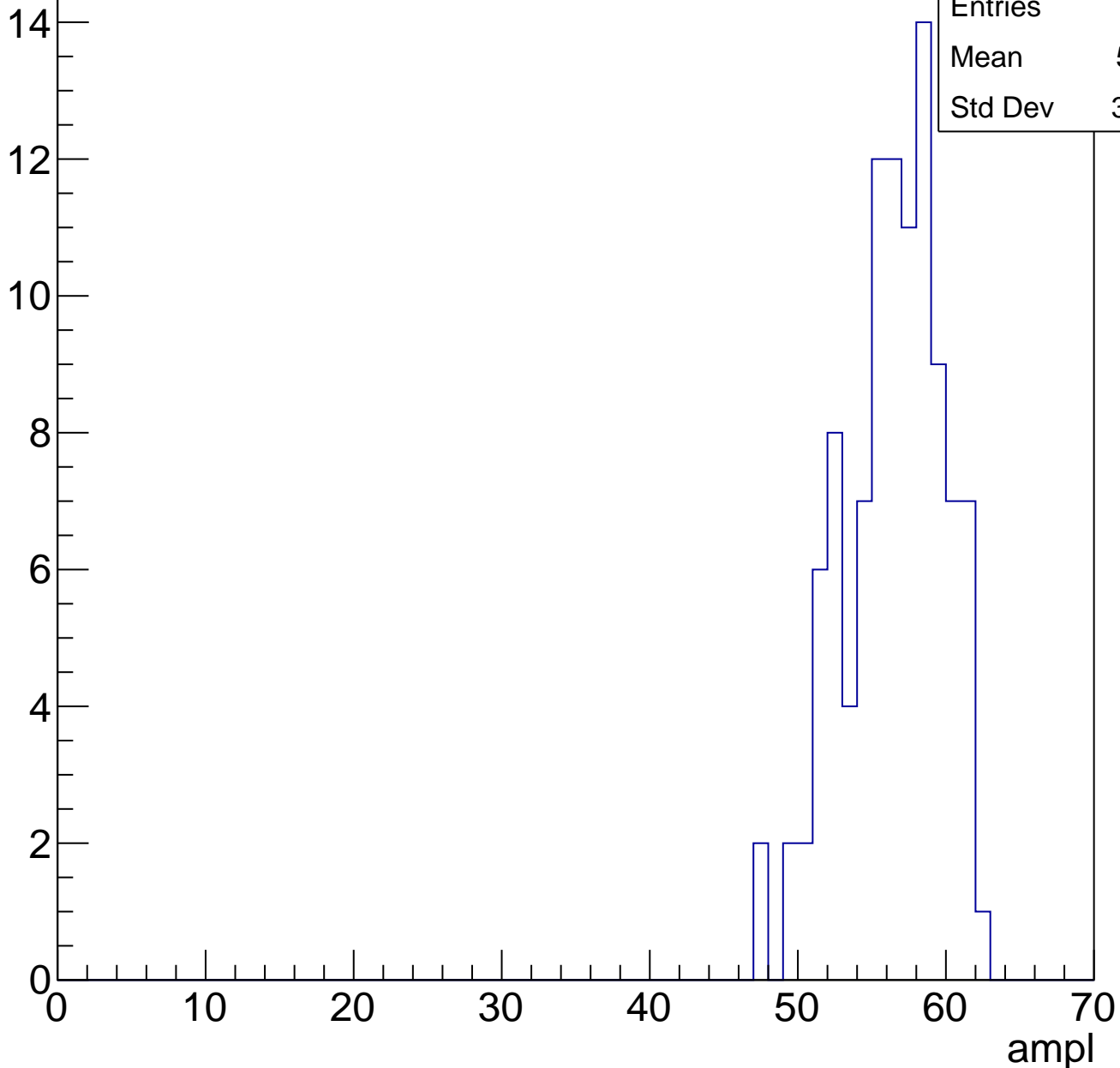


B0L101S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	55.91
Std Dev	3.334

Entry



B0L101S, U14-ch91, adc5

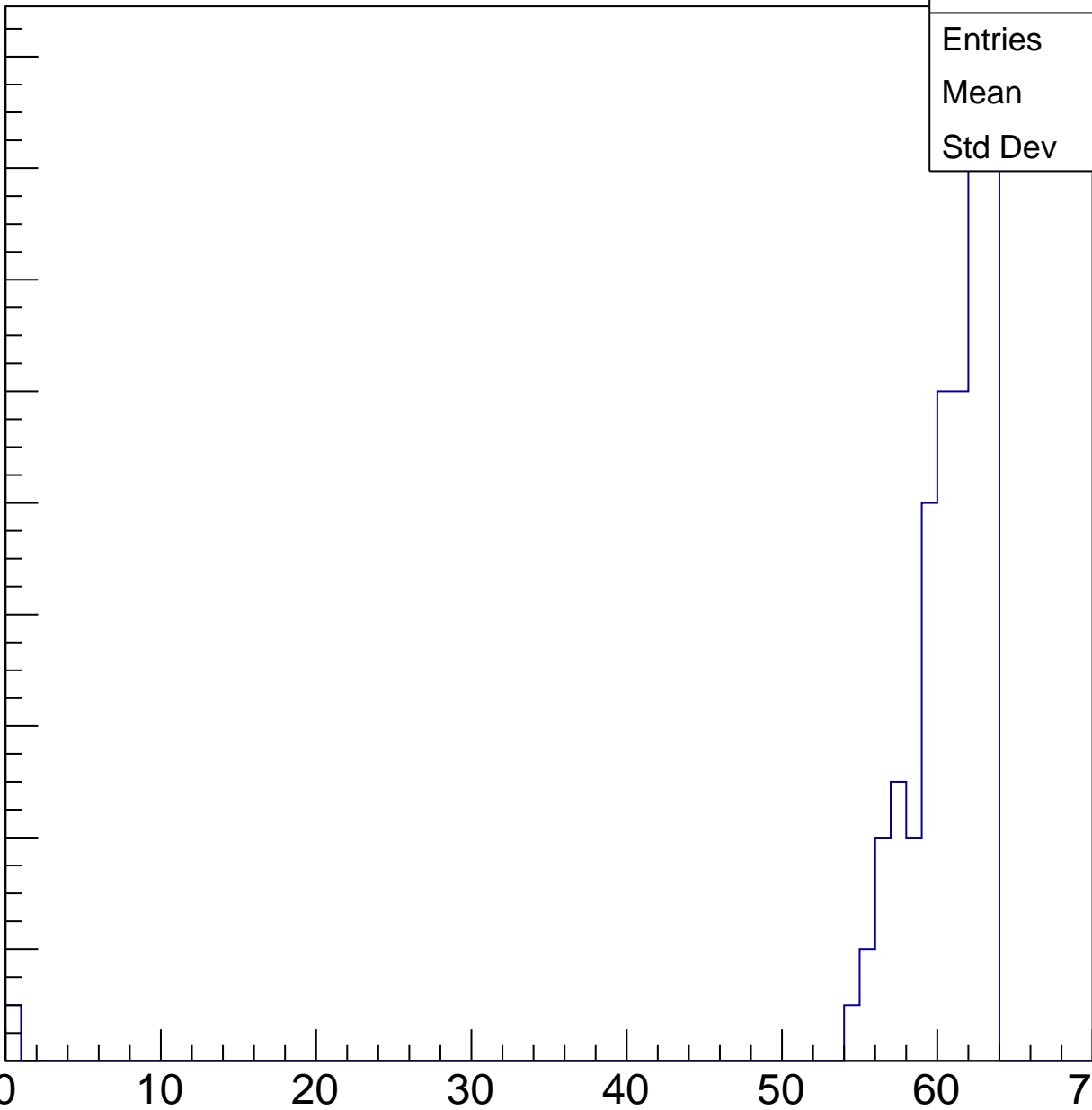
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.66
Std Dev	6.893

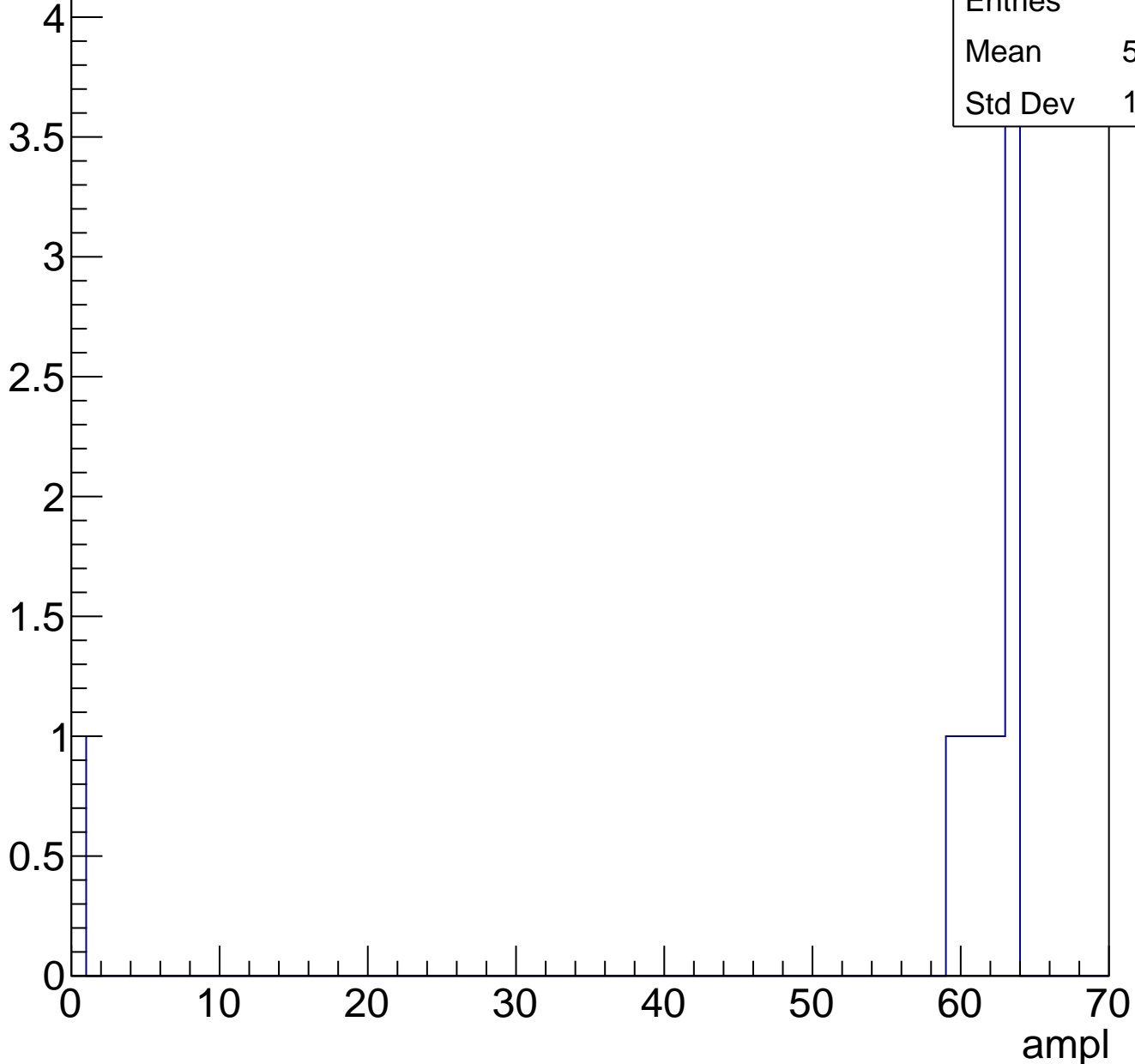
ampl



B0L101S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	9
Mean	54.89
Std Dev	19.46

B0L101S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	29.15
Std Dev	5.765

Gaus mean : 29.8014

Gaus Width: 3.9015

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

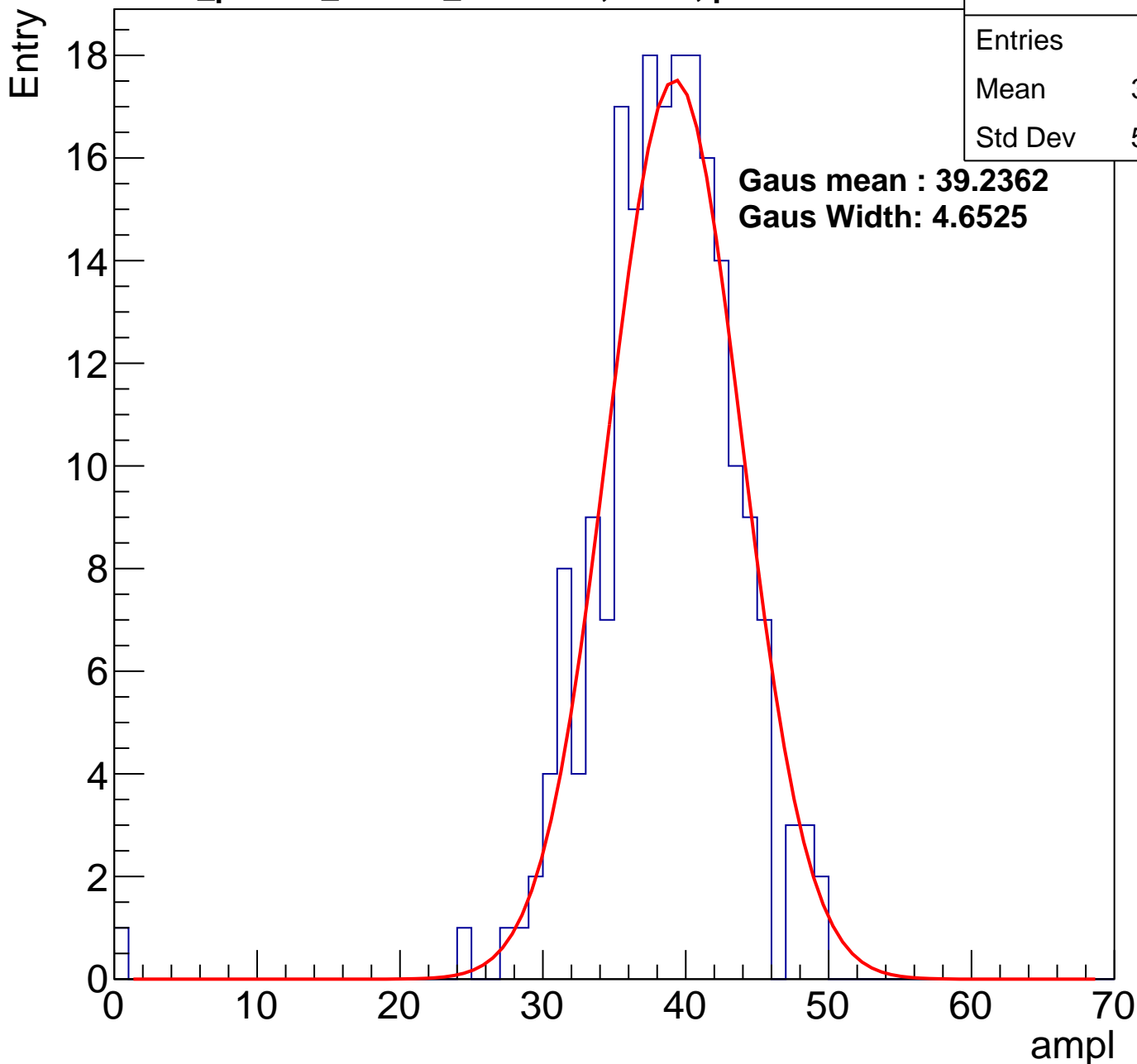
B0L101S, U14-ch92, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	205
Mean	38.07
Std Dev	5.199

Gaus mean : 39.2362

Gaus Width: 4.6525



B0L101S, U14-ch92, adc2

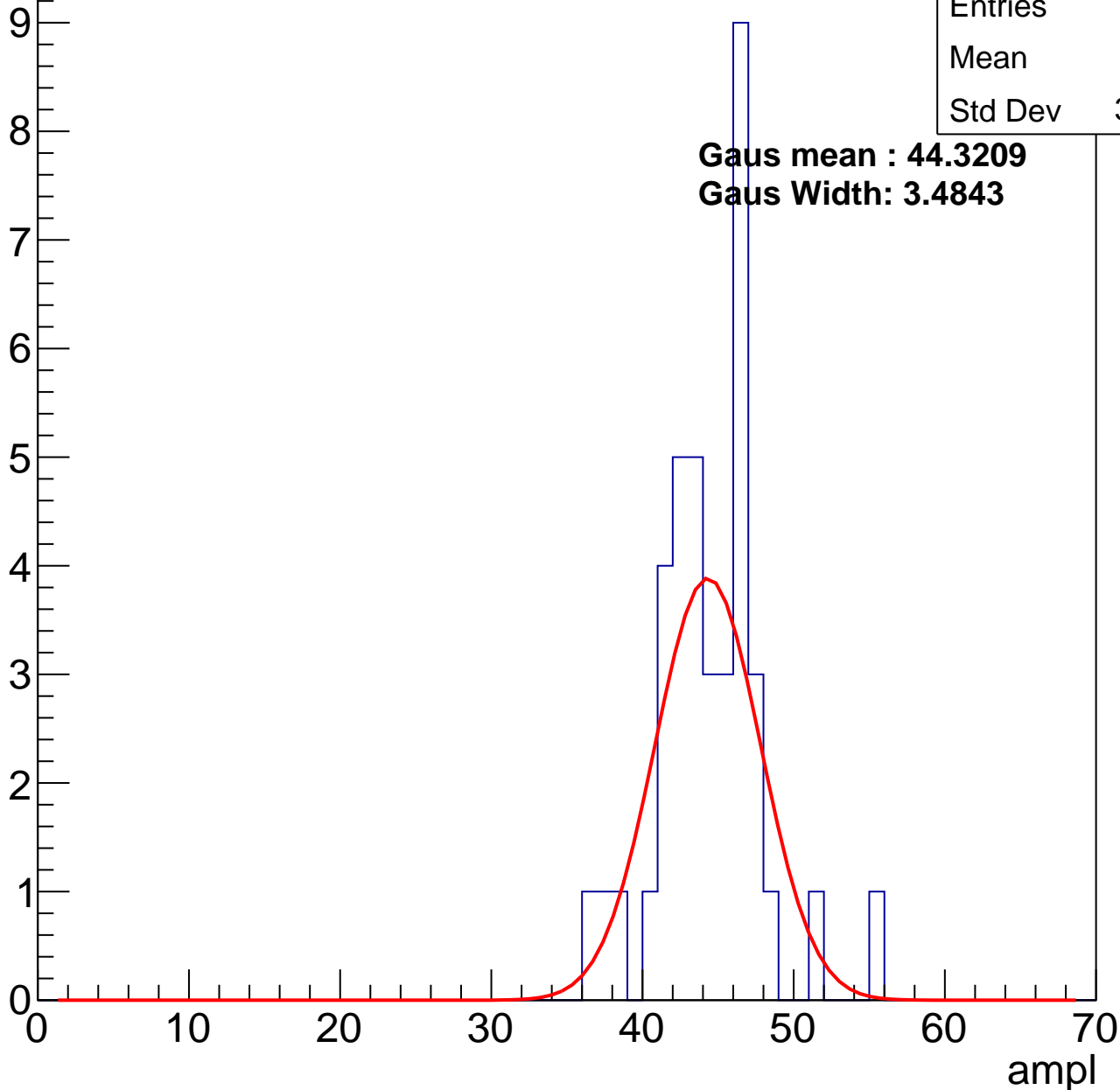
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	39
Mean	44
Std Dev	3.501

Gaus mean : 44.3209

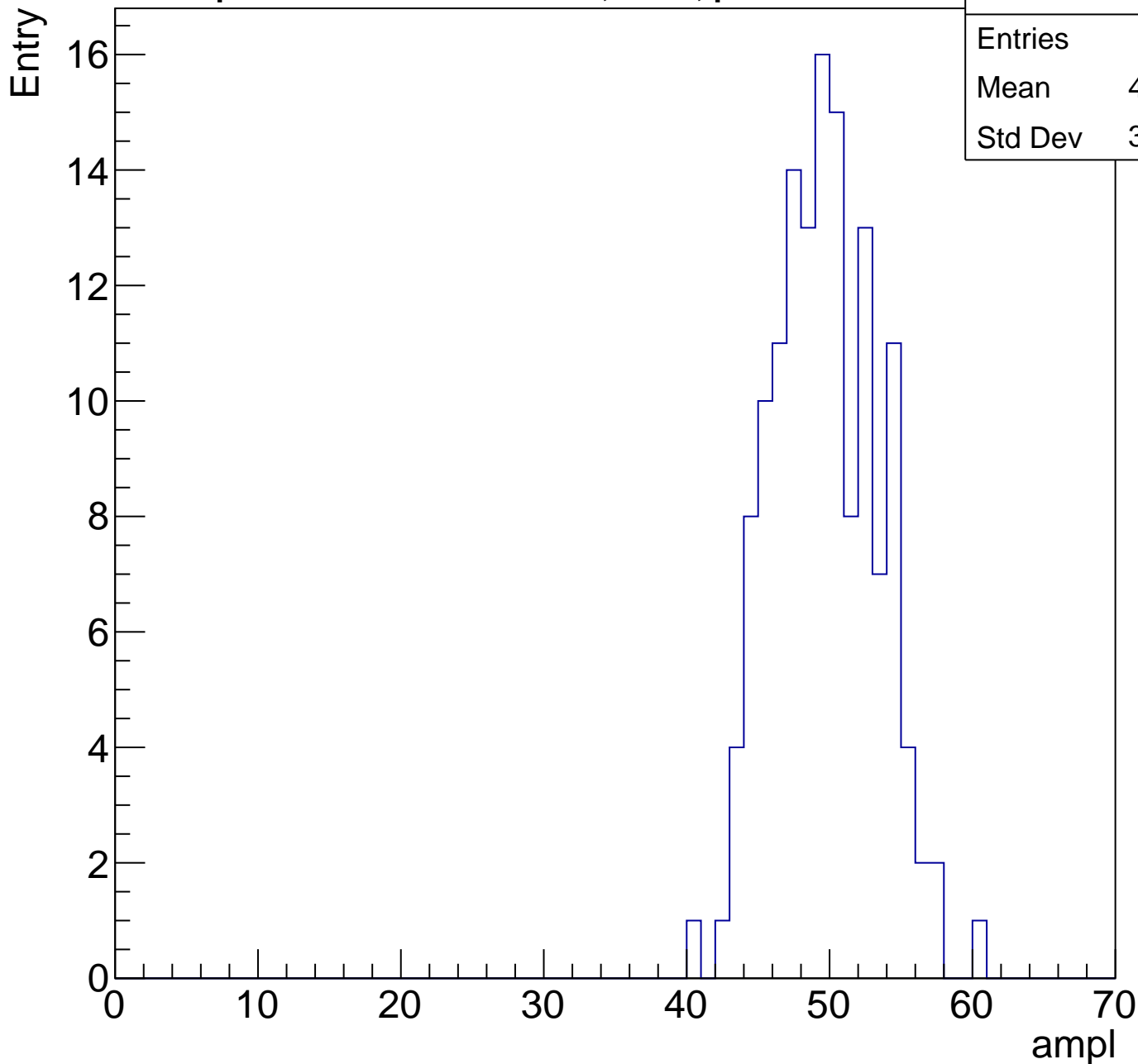
Gaus Width: 3.4843



B0L101S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

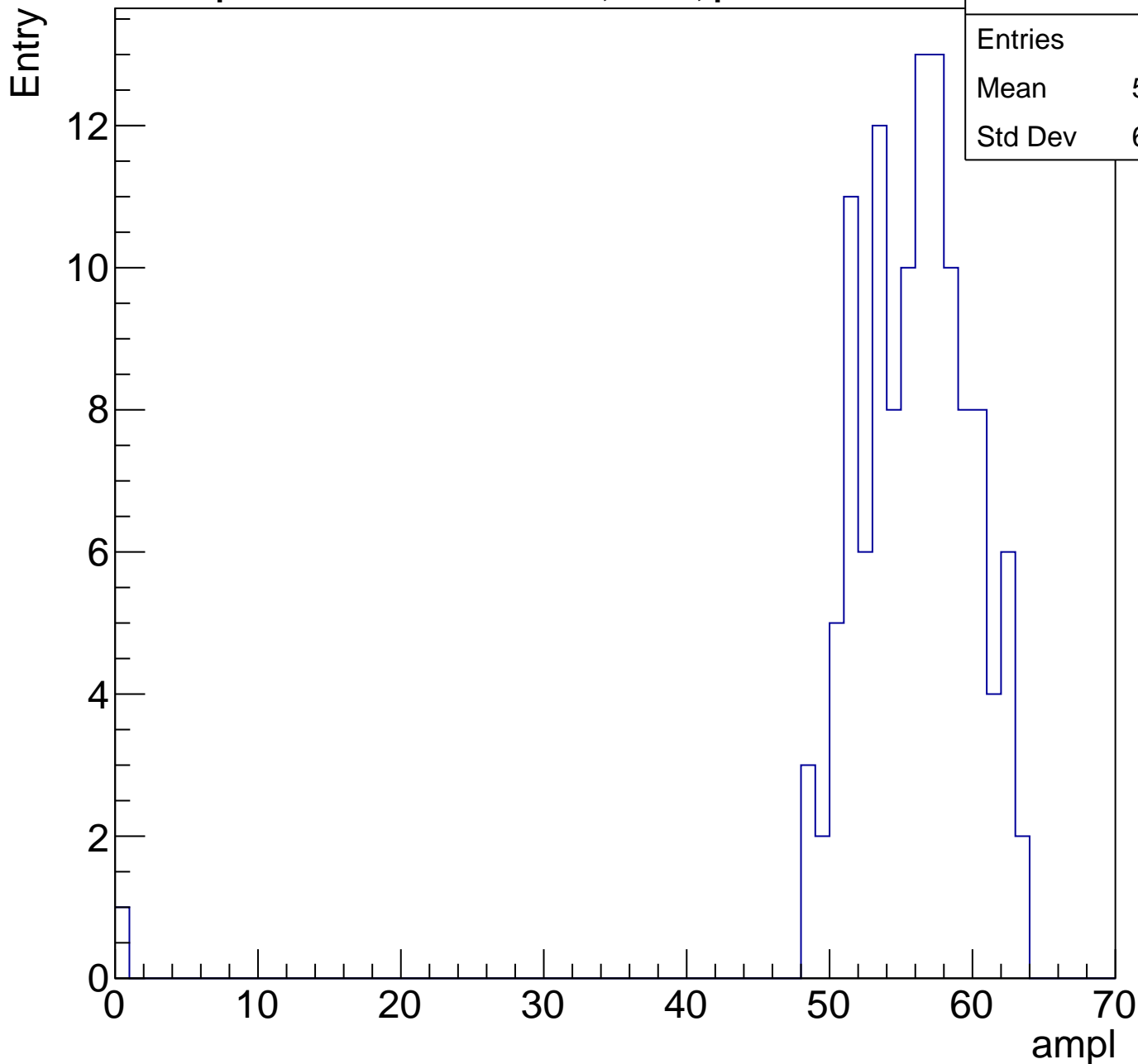
Entries	141
Mean	49.17
Std Dev	3.612



B0L101S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	55.13
Std Dev	6.208



B0L101S, U14-ch92, adc5

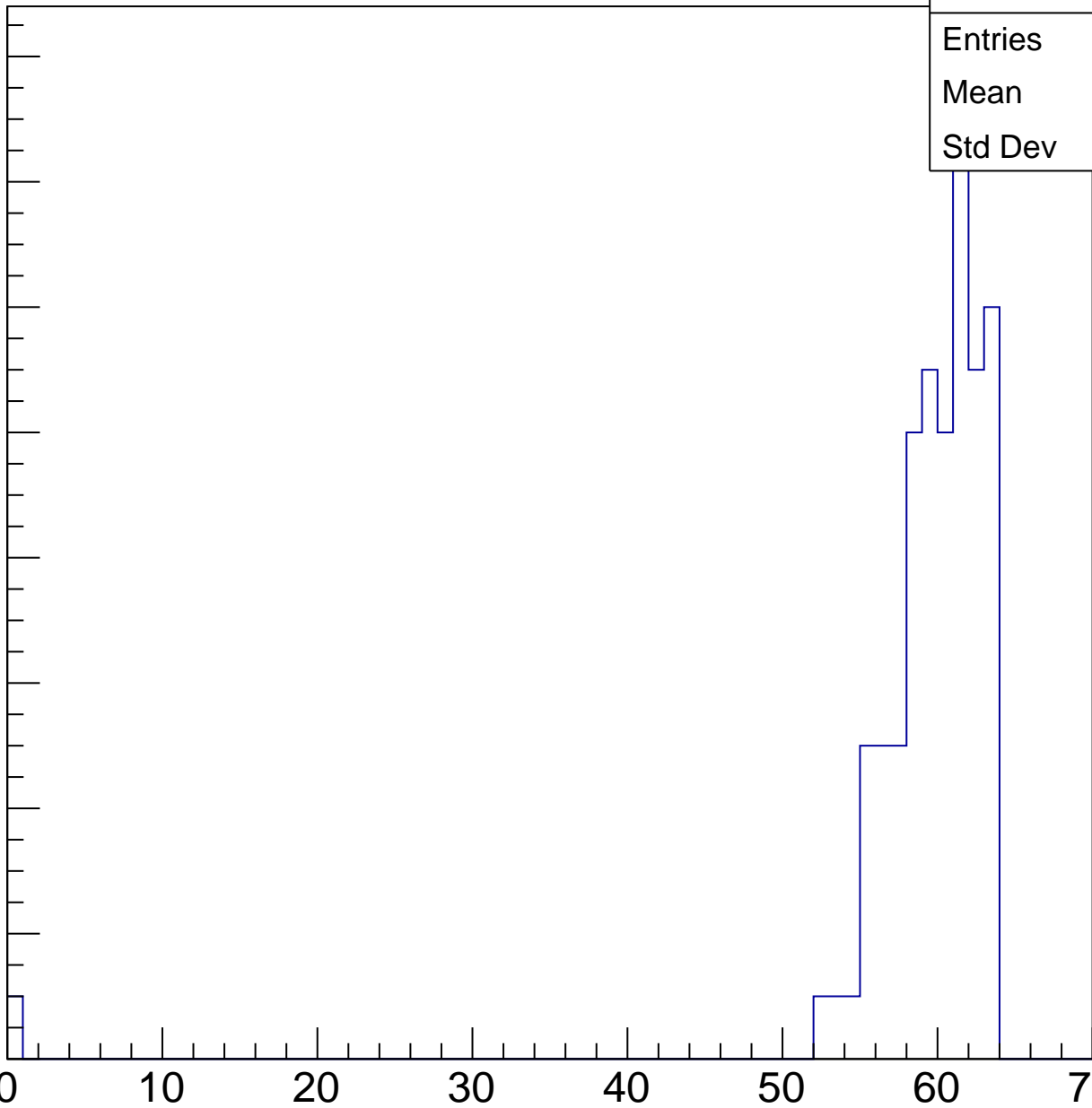
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.9
Std Dev	6.796

ampl

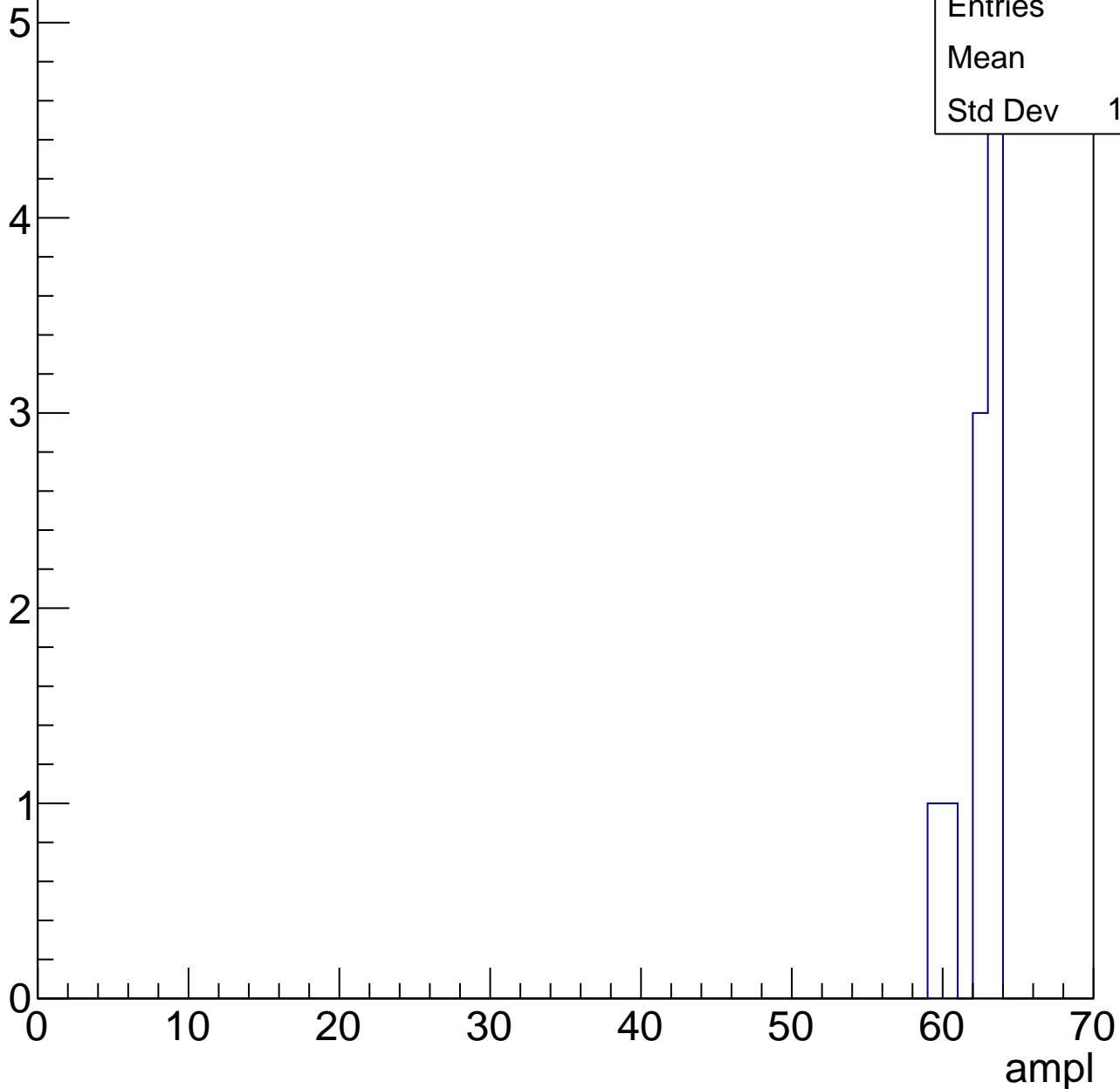


B0L101S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62
Std Dev	1.342



B0L101S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch93, adc0

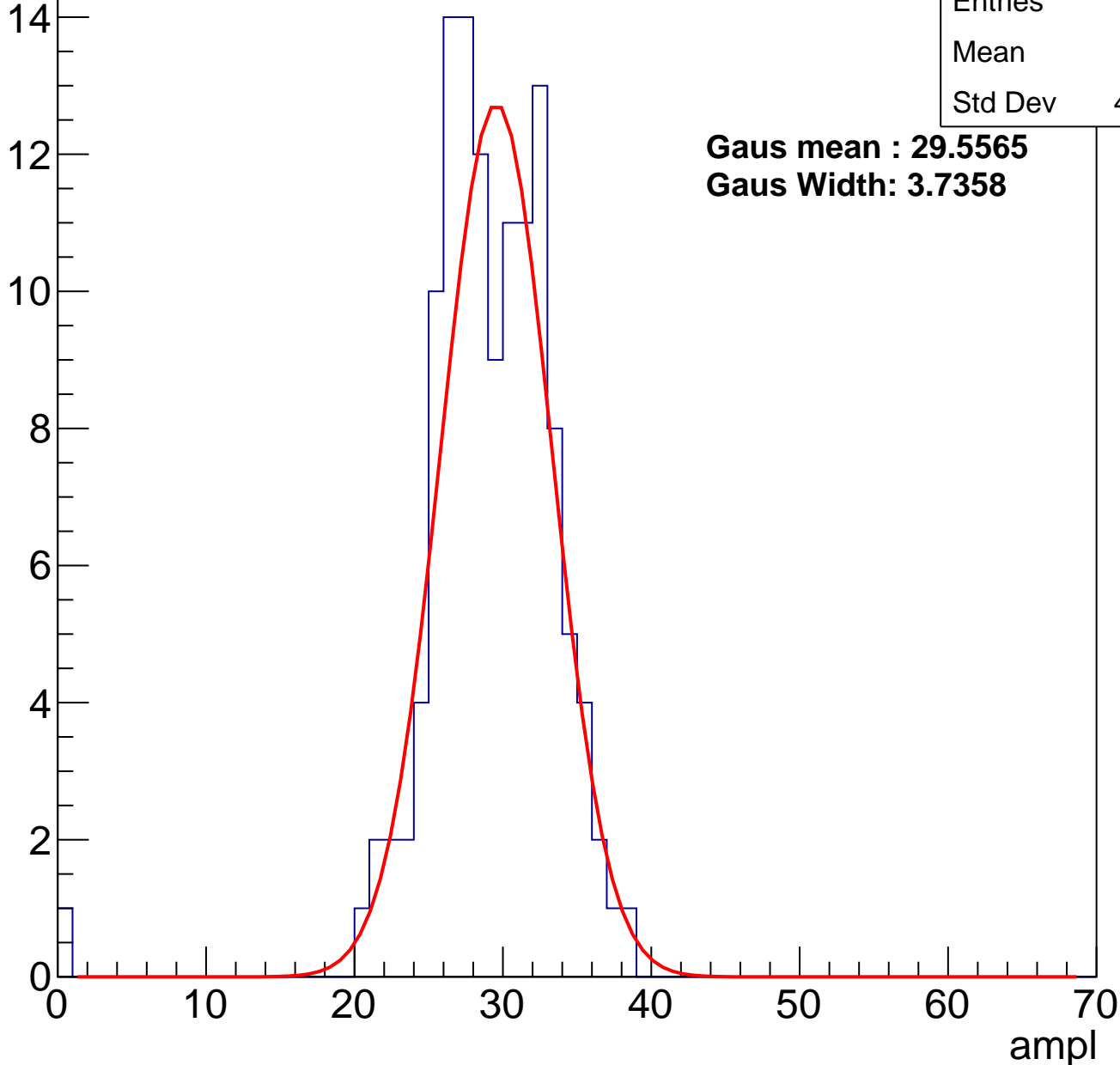
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	28.7
Std Dev	4.414

Gaus mean : 29.5565

Gaus Width: 3.7358

Entry



B0L101S, U14-ch93, adc1

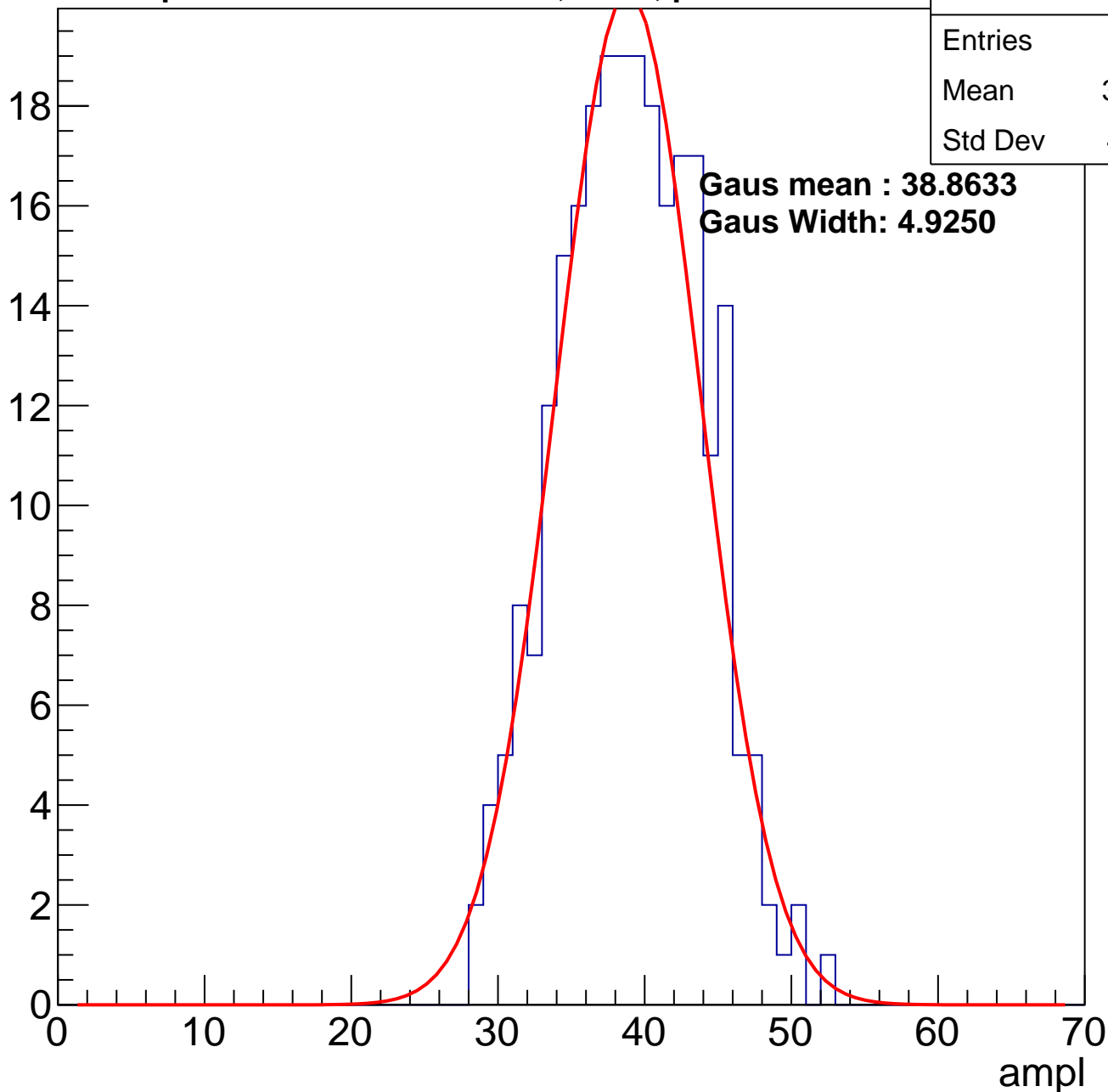
calib_packv5_042523_0143.root, FC#1, port C1

Entries	253
Mean	38.62
Std Dev	4.751

Gaus mean : 38.8633

Gaus Width: 4.9250

Entry



B0L101S, U14-ch93, adc2

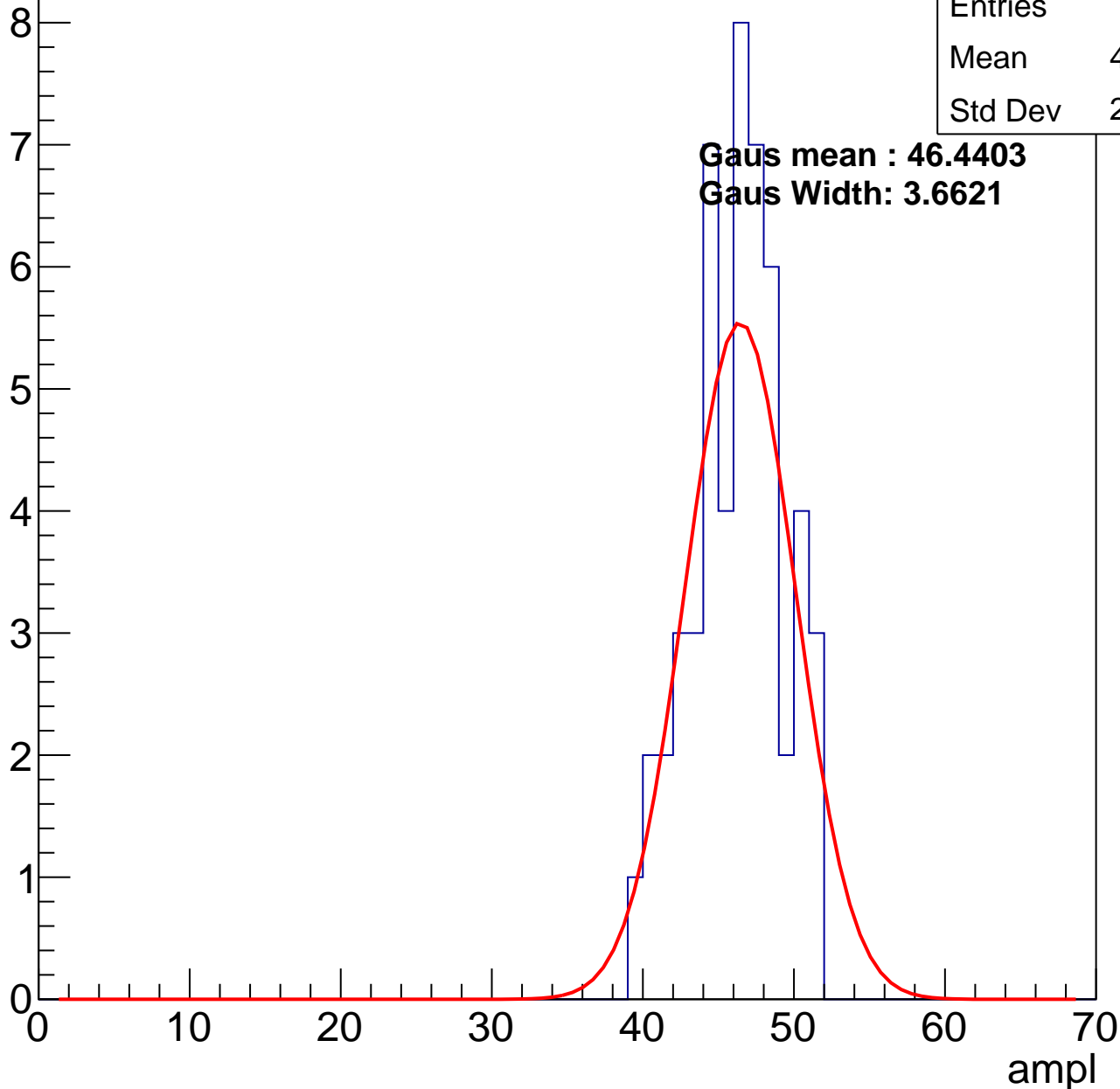
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	52
Mean	45.77
Std Dev	2.985

Gaus mean : 46.4403

Gaus Width: 3.6621

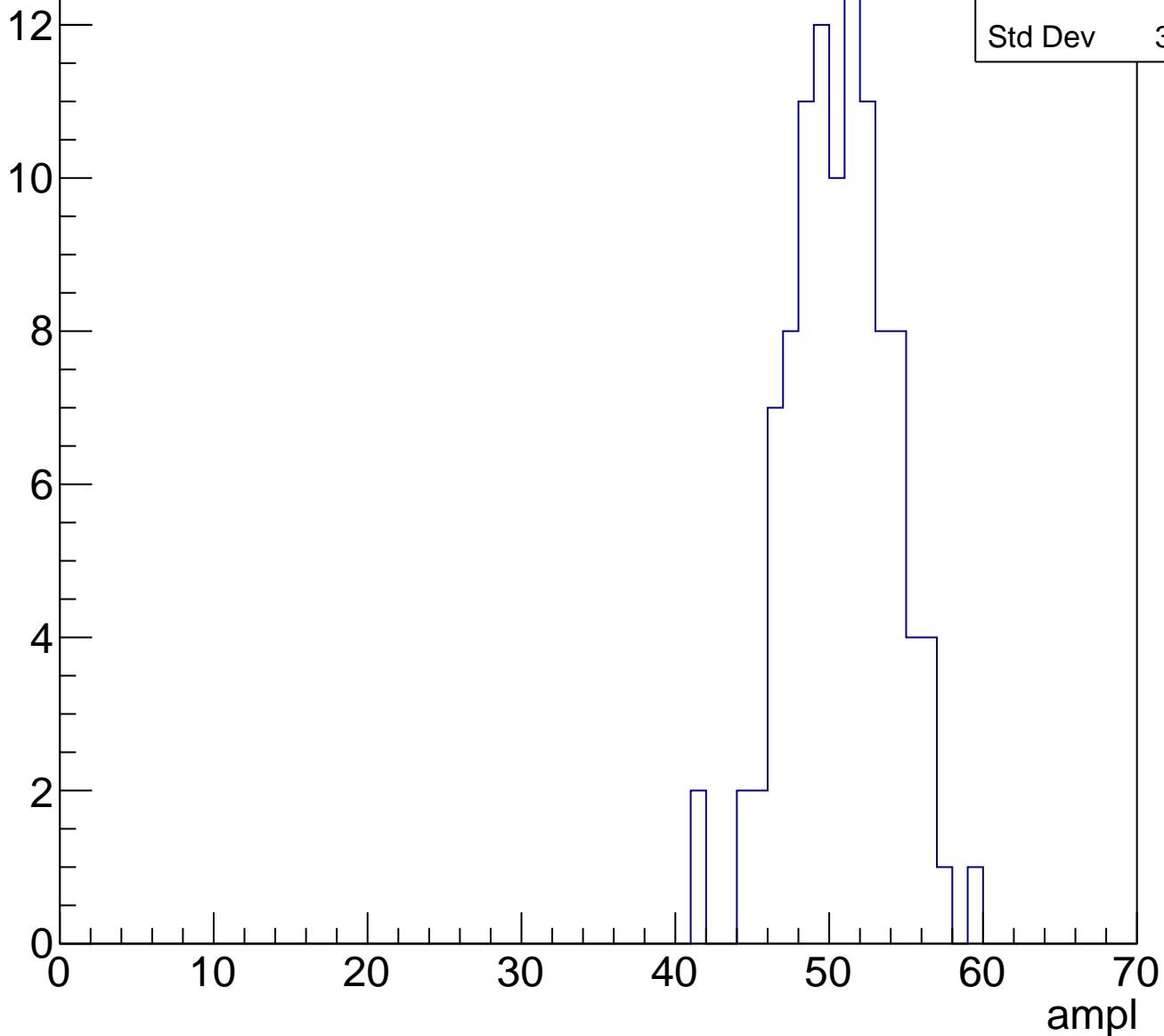


B0L101S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	50.24
Std Dev	3.324

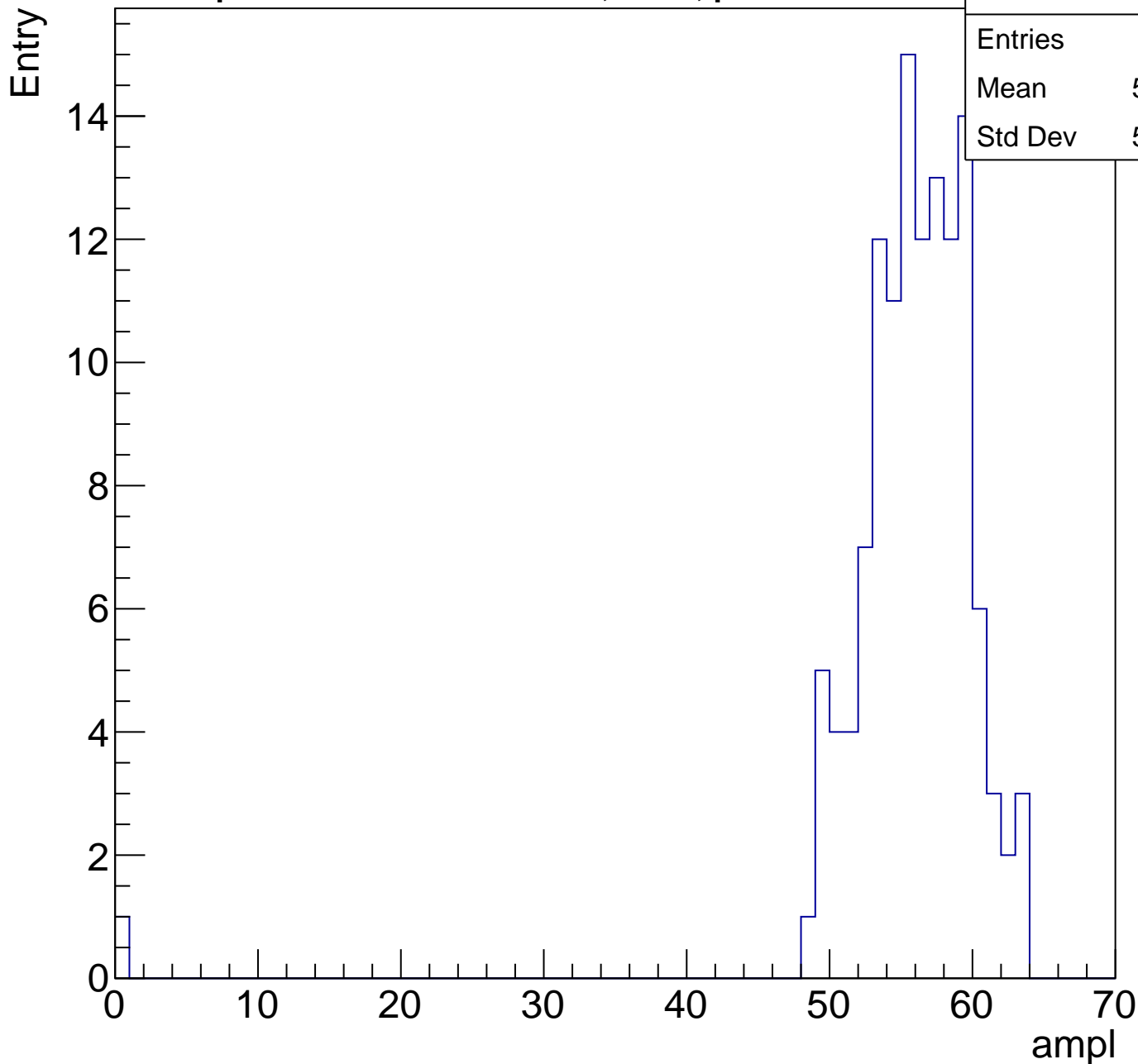
Entry



B0L101S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.26
Std Dev	5.984



B0L101S, U14-ch93, adc5

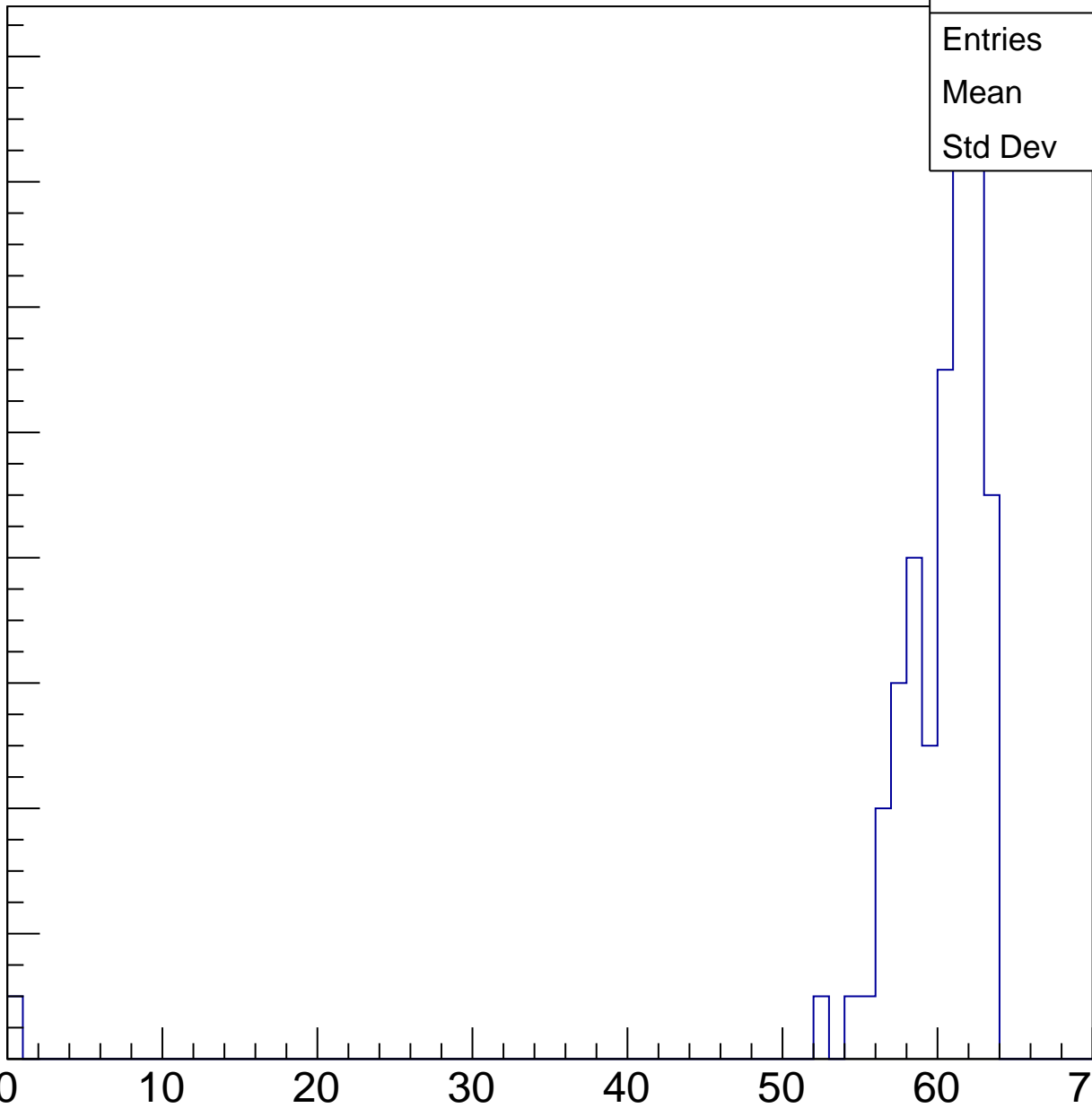
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.23
Std Dev	7.156

ampl



B0L101S, U14-ch93, adc6

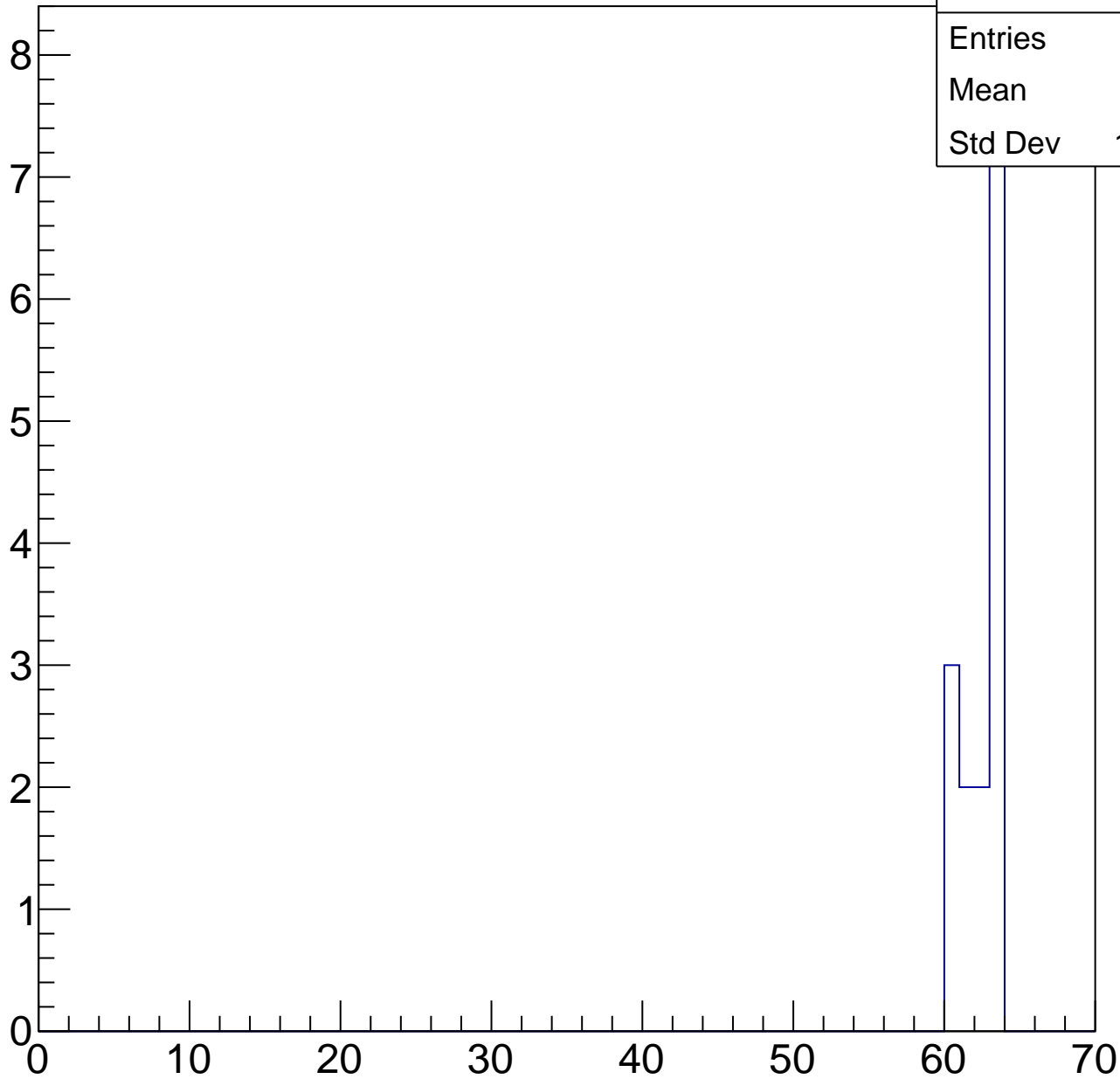
calib_packv5_042523_0143.root, FC#1, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	15
Mean	62
Std Dev	1.211

ampl



B0L101S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

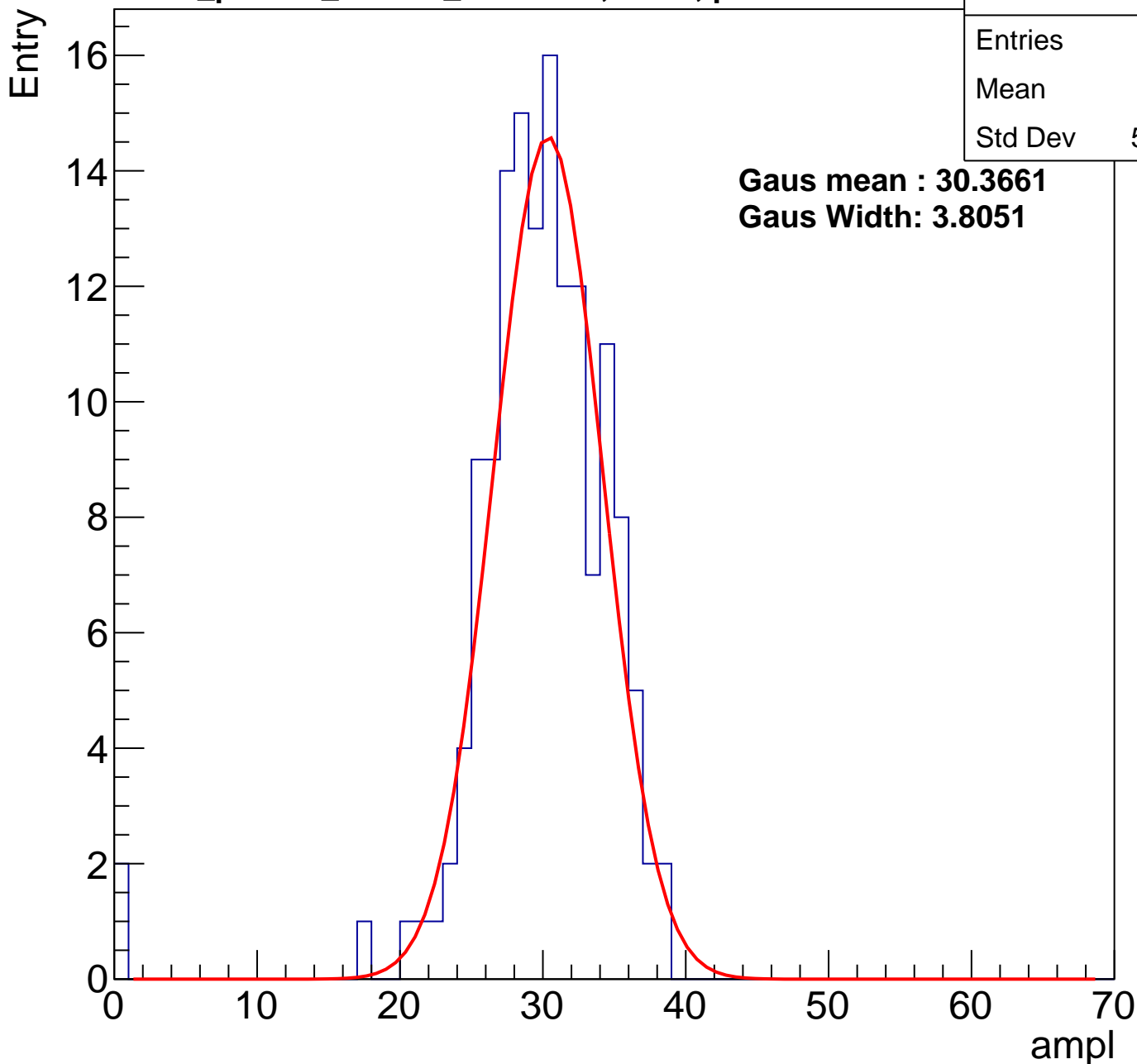
B0L101S, U14-ch94, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.3
Std Dev	5.122

Gaus mean : 30.3661

Gaus Width: 3.8051



B0L101S, U14-ch94, adc1

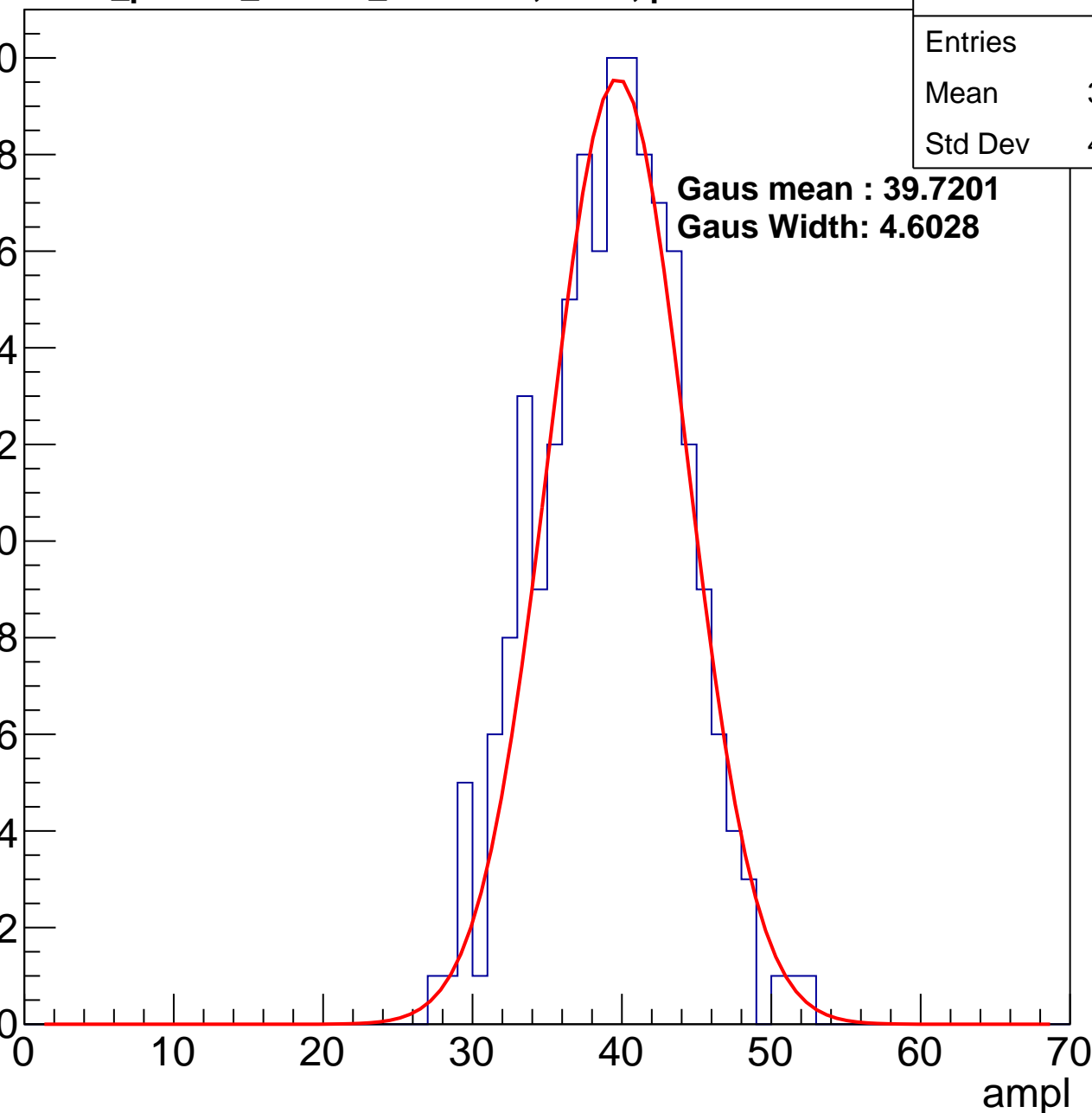
calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	233
Mean	38.86
Std Dev	4.668

Gaus mean : 39.7201
Gaus Width: 4.6028



B0L101S, U14-ch94, adc2

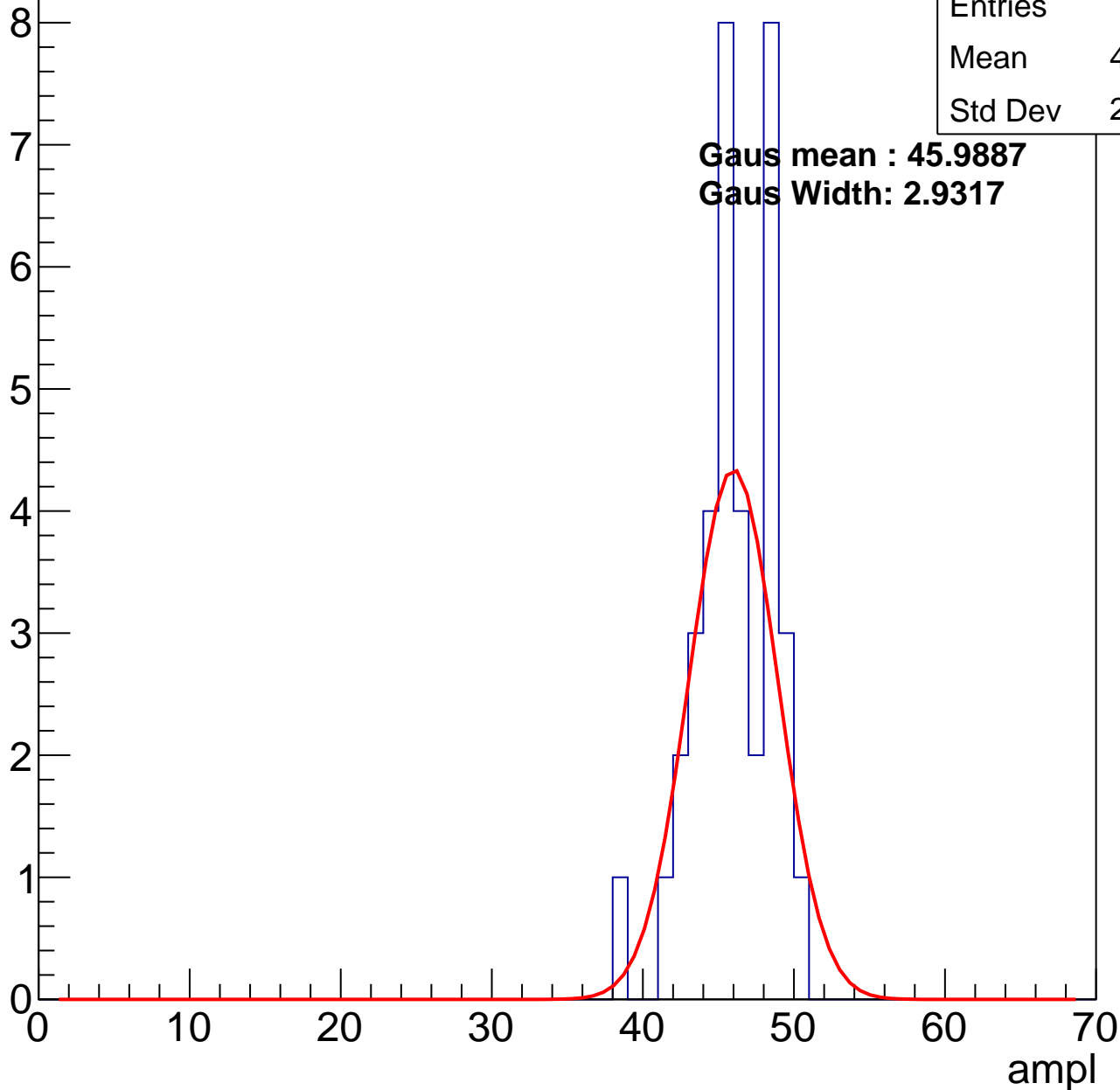
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	37
Mean	45.59
Std Dev	2.562

Gaus mean : 45.9887

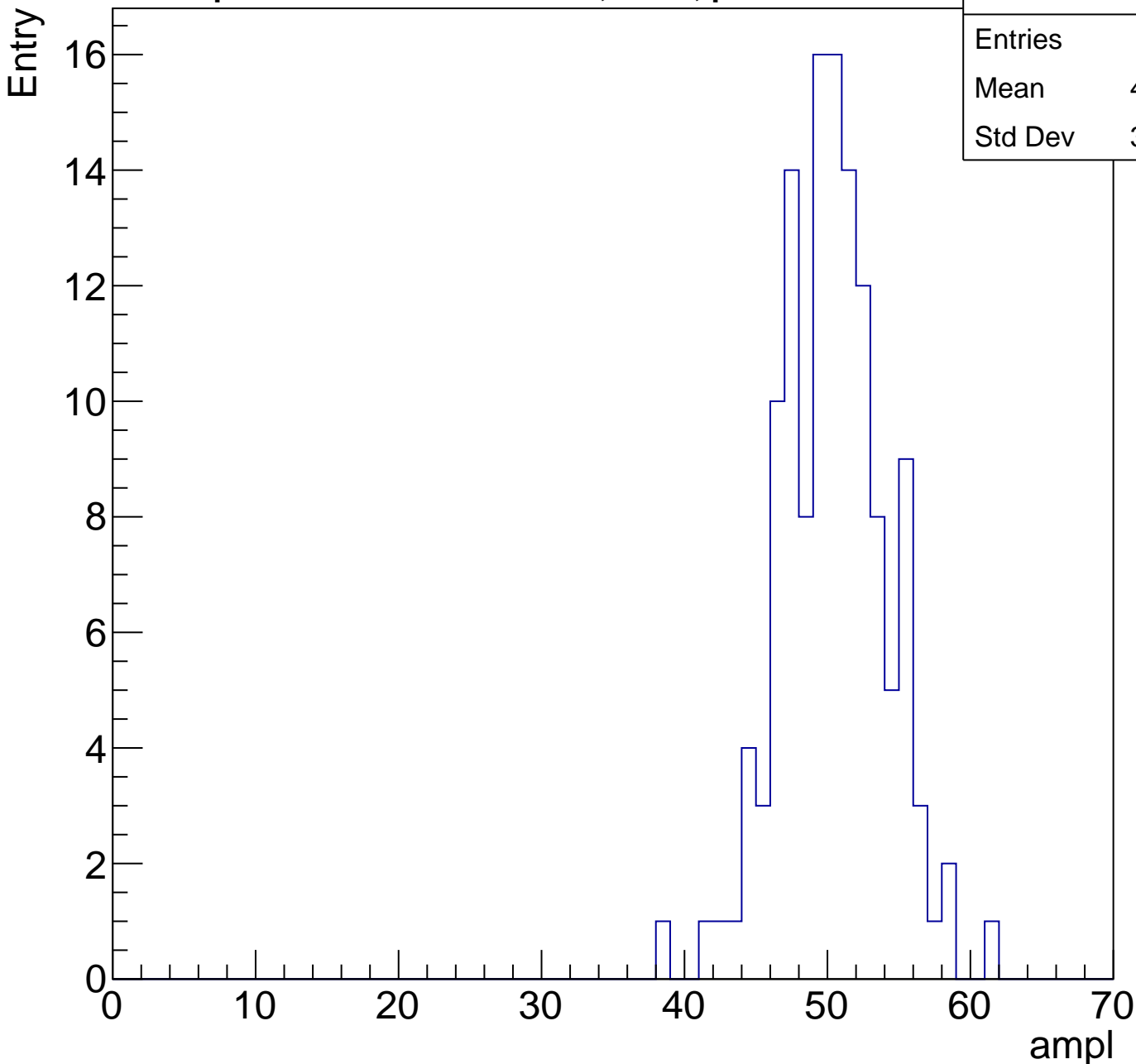
Gaus Width: 2.9317



B0L101S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

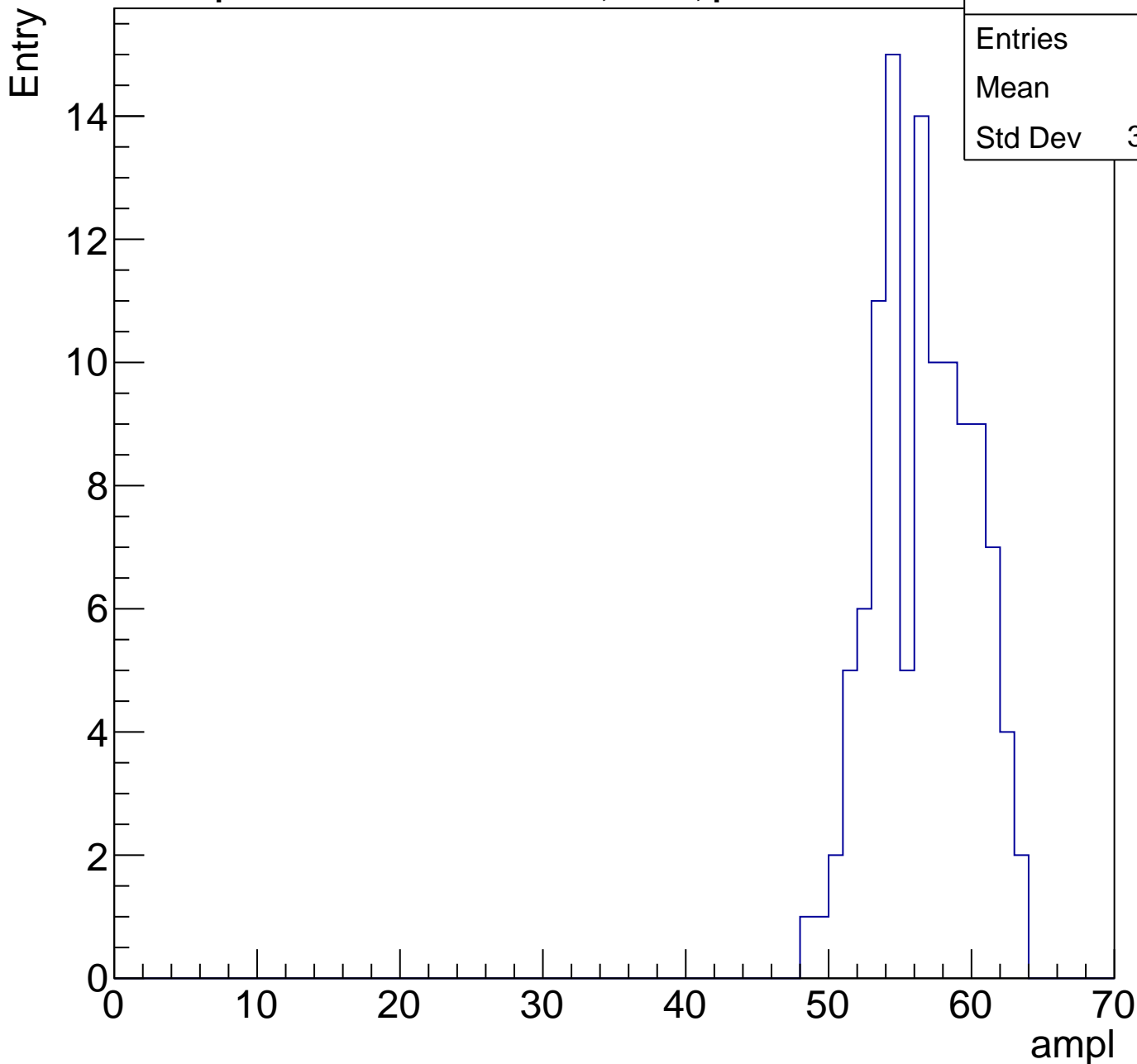
Entries	130
Mean	49.92
Std Dev	3.653



B0L101S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	56.2
Std Dev	3.366



B0L101S, U14-ch94, adc5

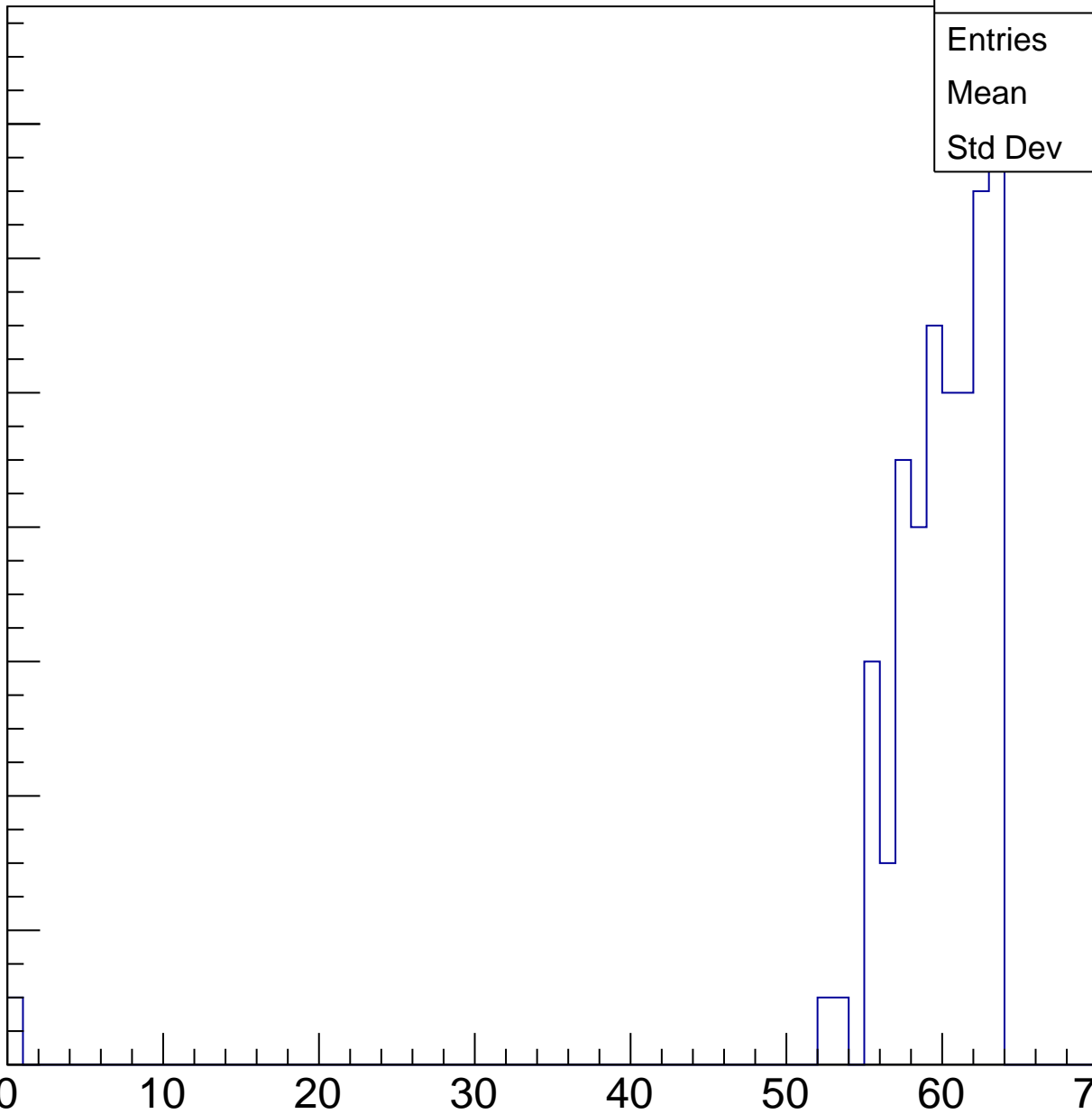
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	88
Mean	58.98
Std Dev	6.859

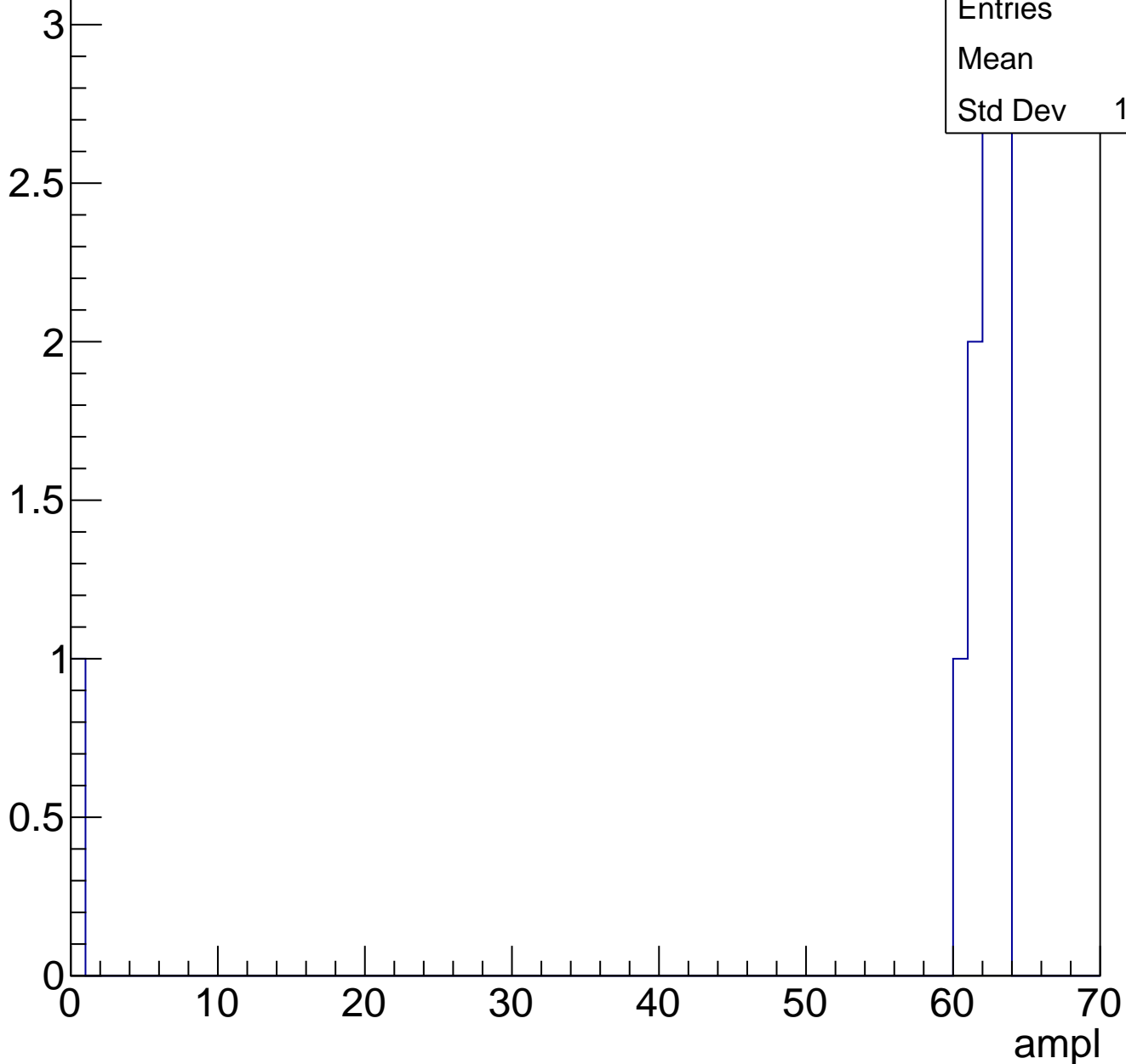
ampl



B0L101S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



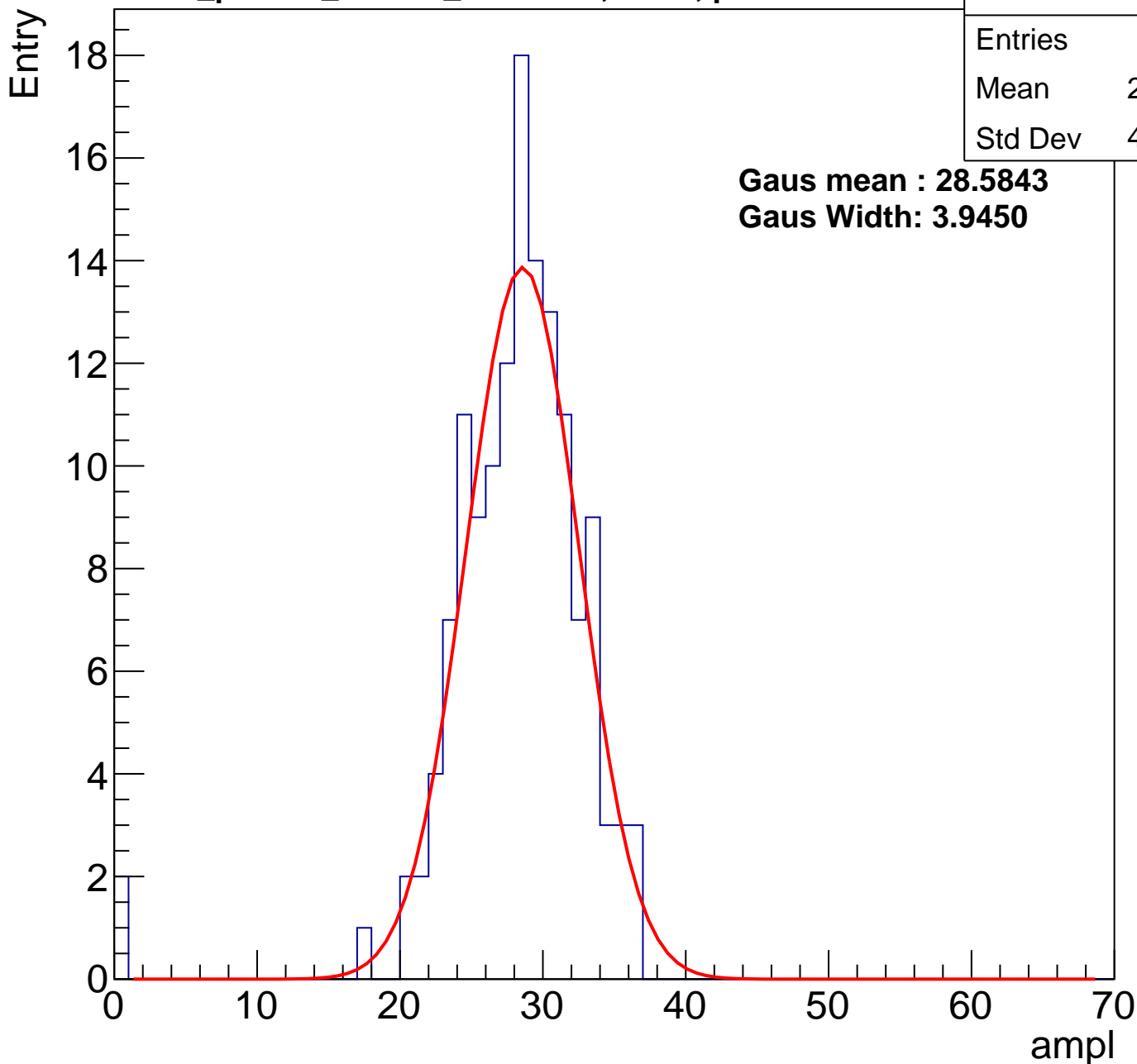
B0L101S, U14-ch95, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	27.65
Std Dev	4.964

Gaus mean : 28.5843

Gaus Width: 3.9450



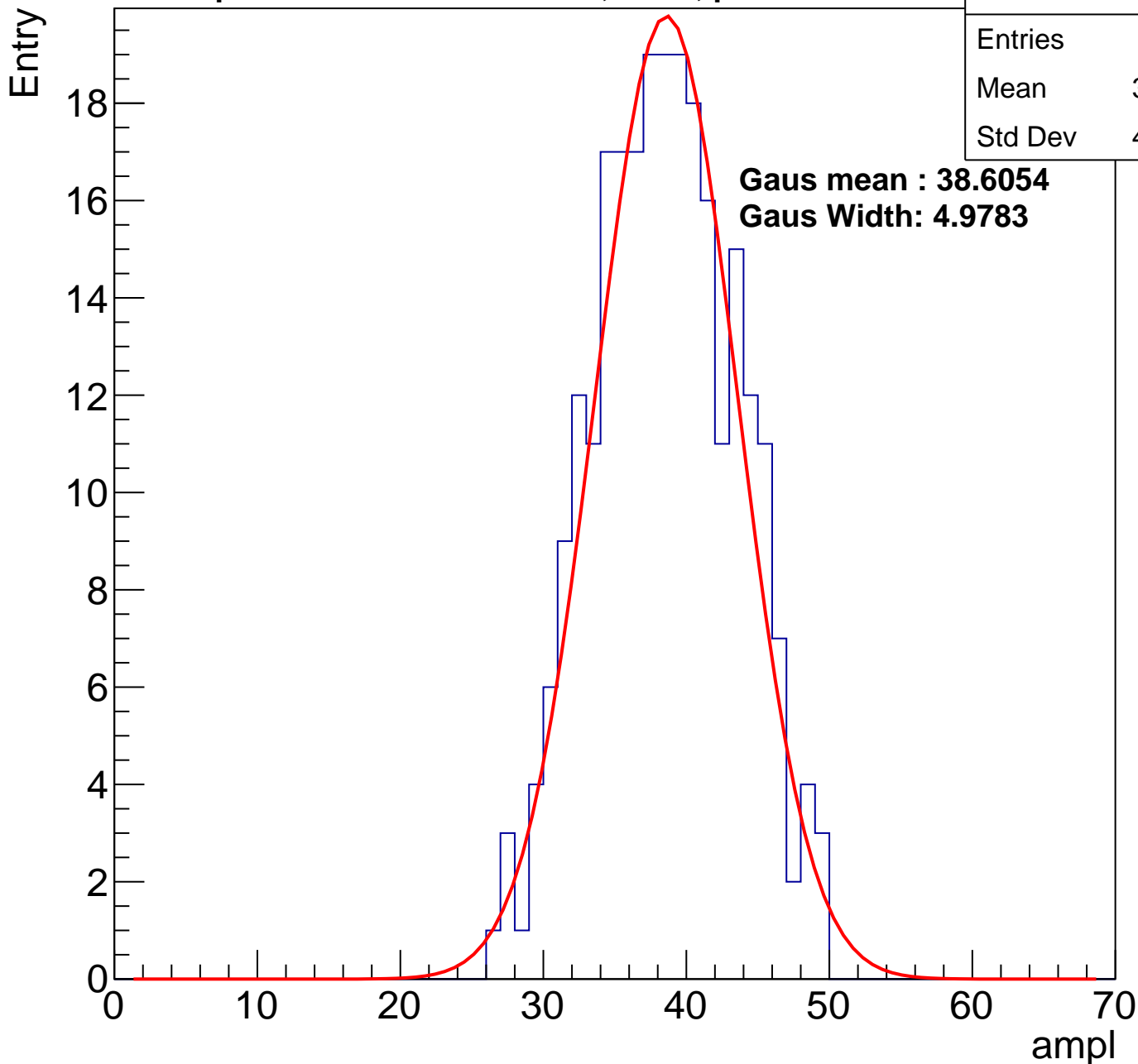
B0L101S, U14-ch95, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	254
Mean	38.07
Std Dev	4.902

Gaus mean : 38.6054

Gaus Width: 4.9783



B0L101S, U14-ch95, adc2

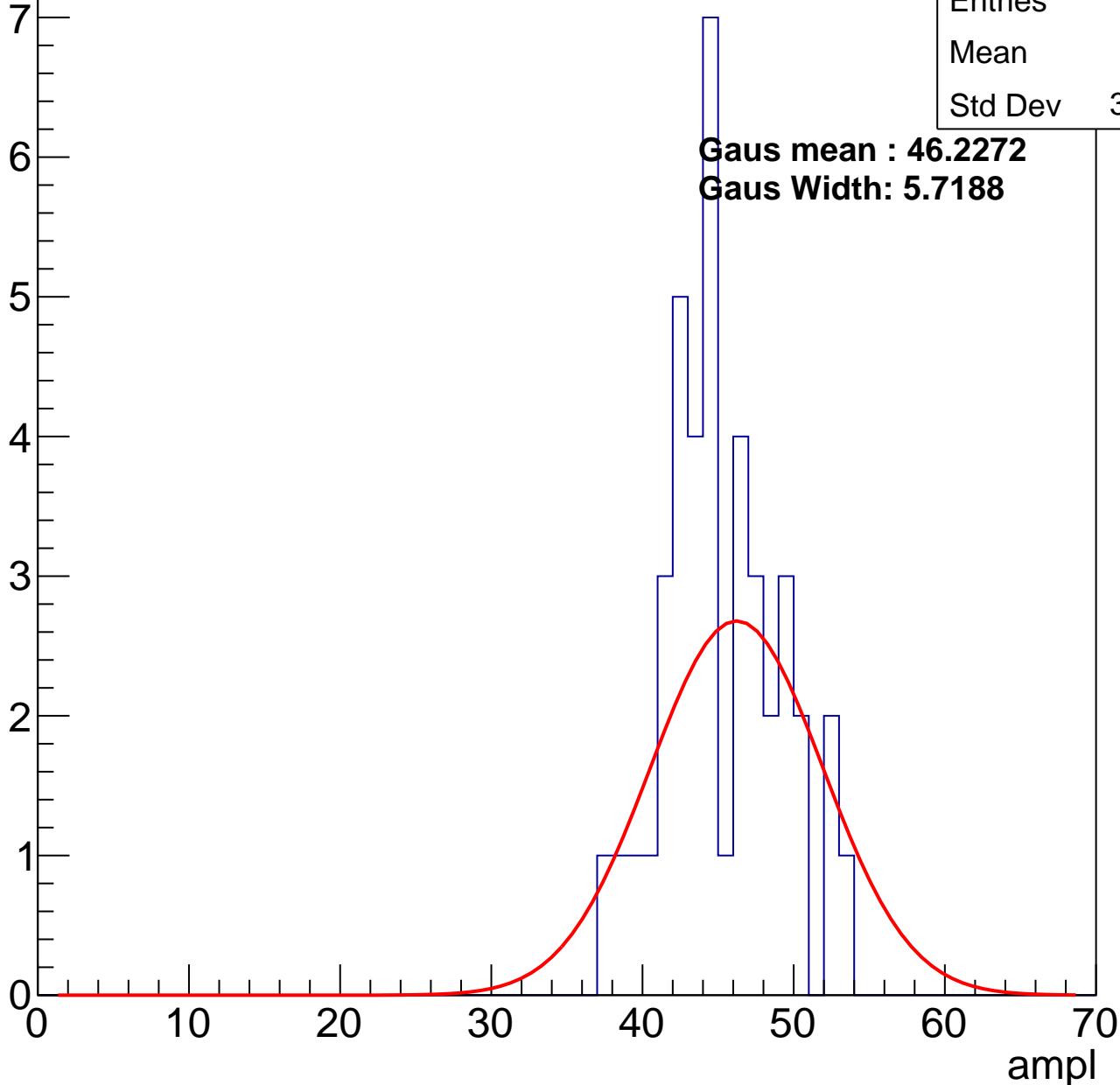
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	41
Mean	44.8
Std Dev	3.769

Gaus mean : 46.2272

Gaus Width: 5.7188



B0L101S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

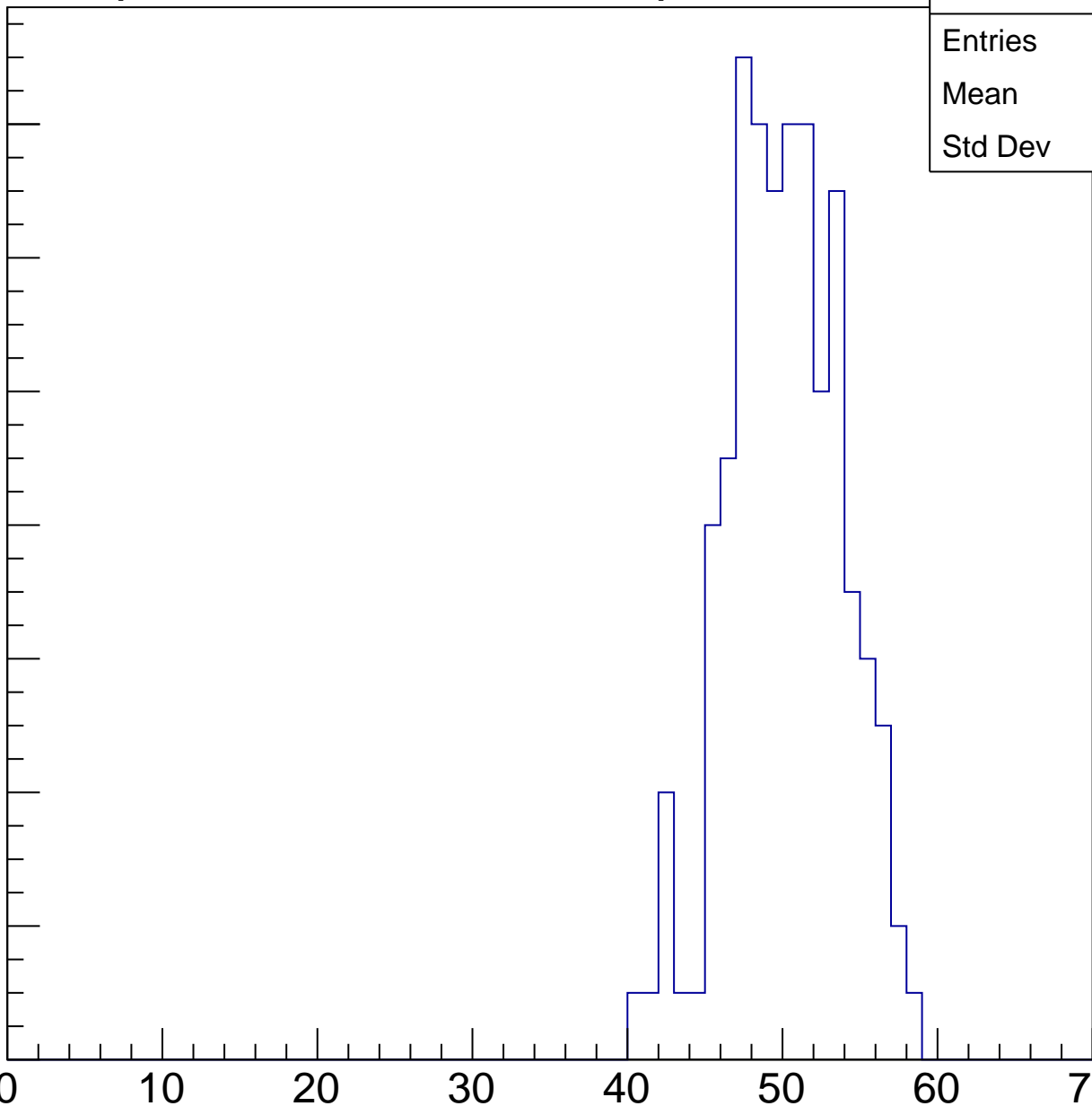
Entries	139
Mean	49.69
Std Dev	3.63

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

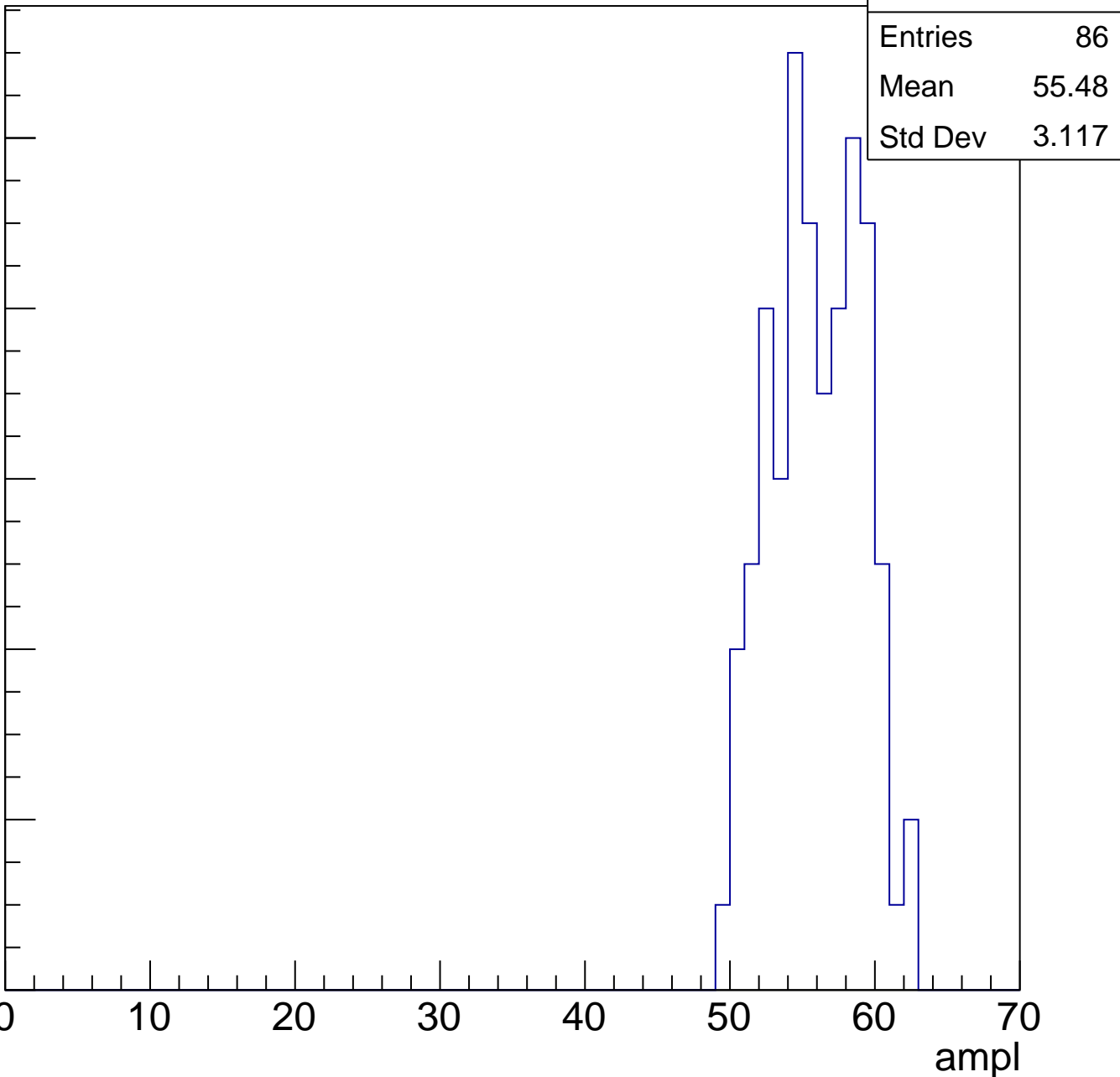
30

40

50

ampl

Entries	86
Mean	55.48
Std Dev	3.117



B0L101S, U14-ch95, adc5

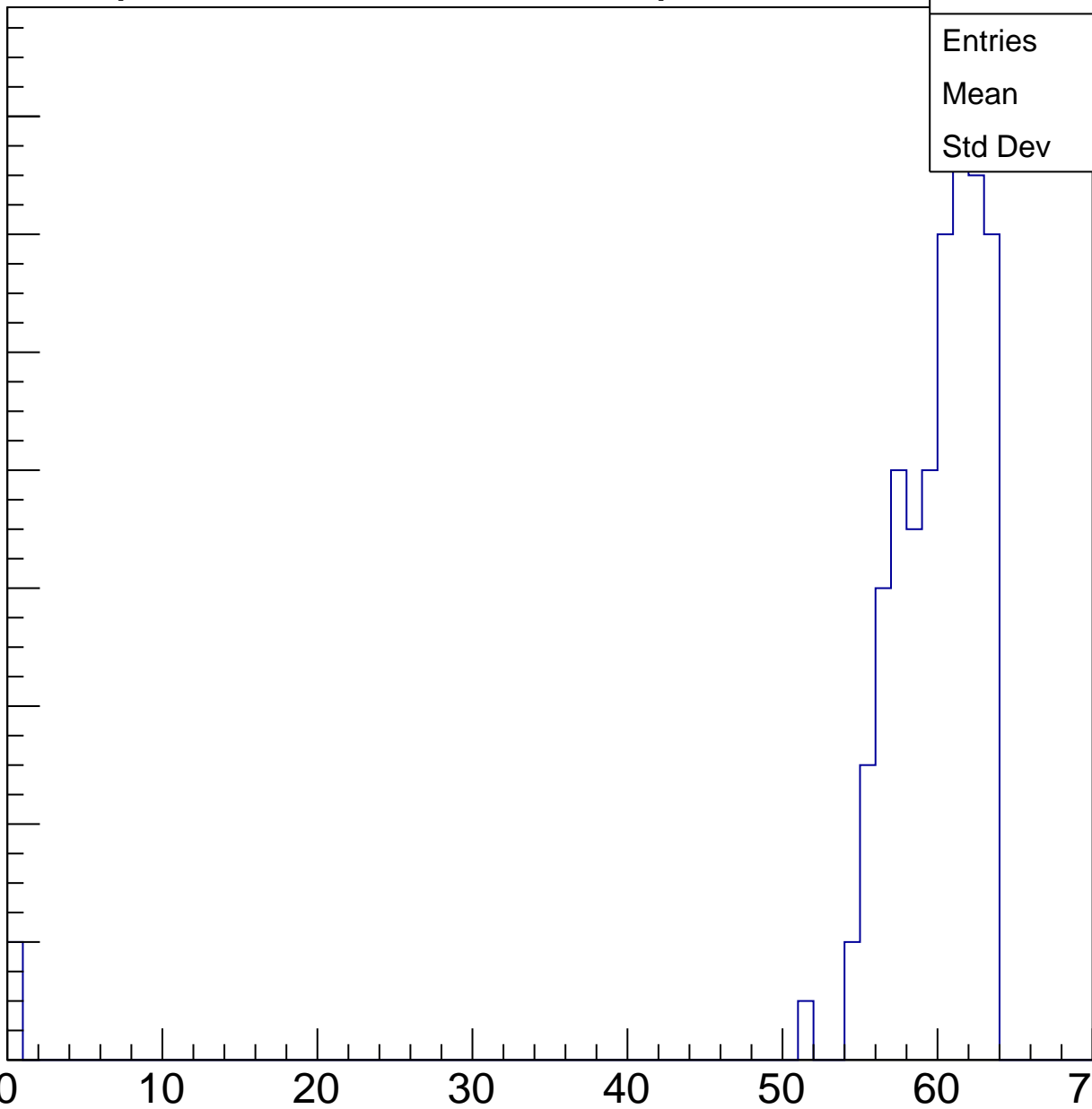
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.44
Std Dev	8.474

ampl

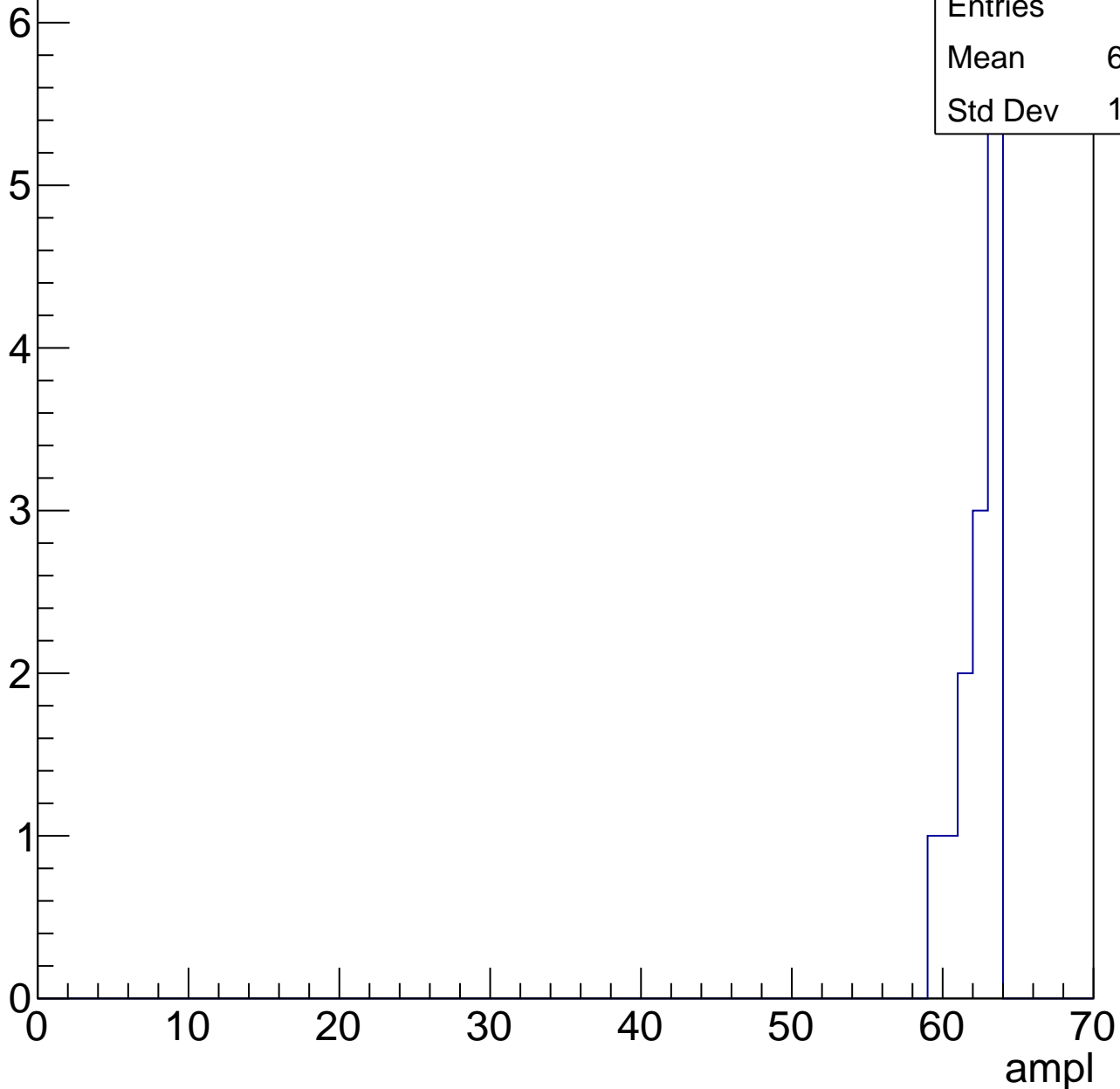


B0L101S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	61.92
Std Dev	1.269



B0L101S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch96, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	29.5
Std Dev	3.88

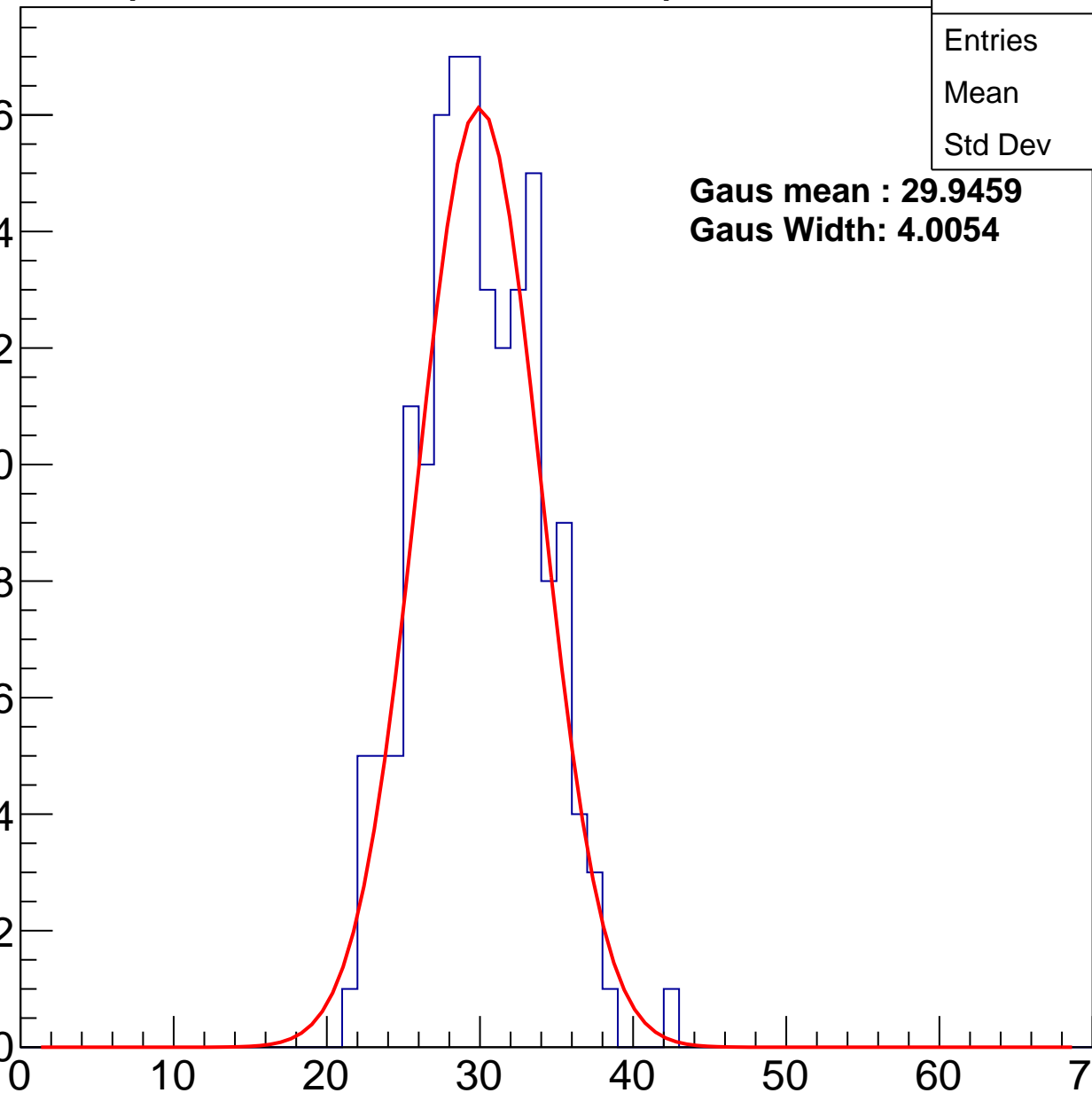
Gaus mean : 29.9459

Gaus Width: 4.0054

Entry

16
14
12
10
8
6
4
2
0

ampl



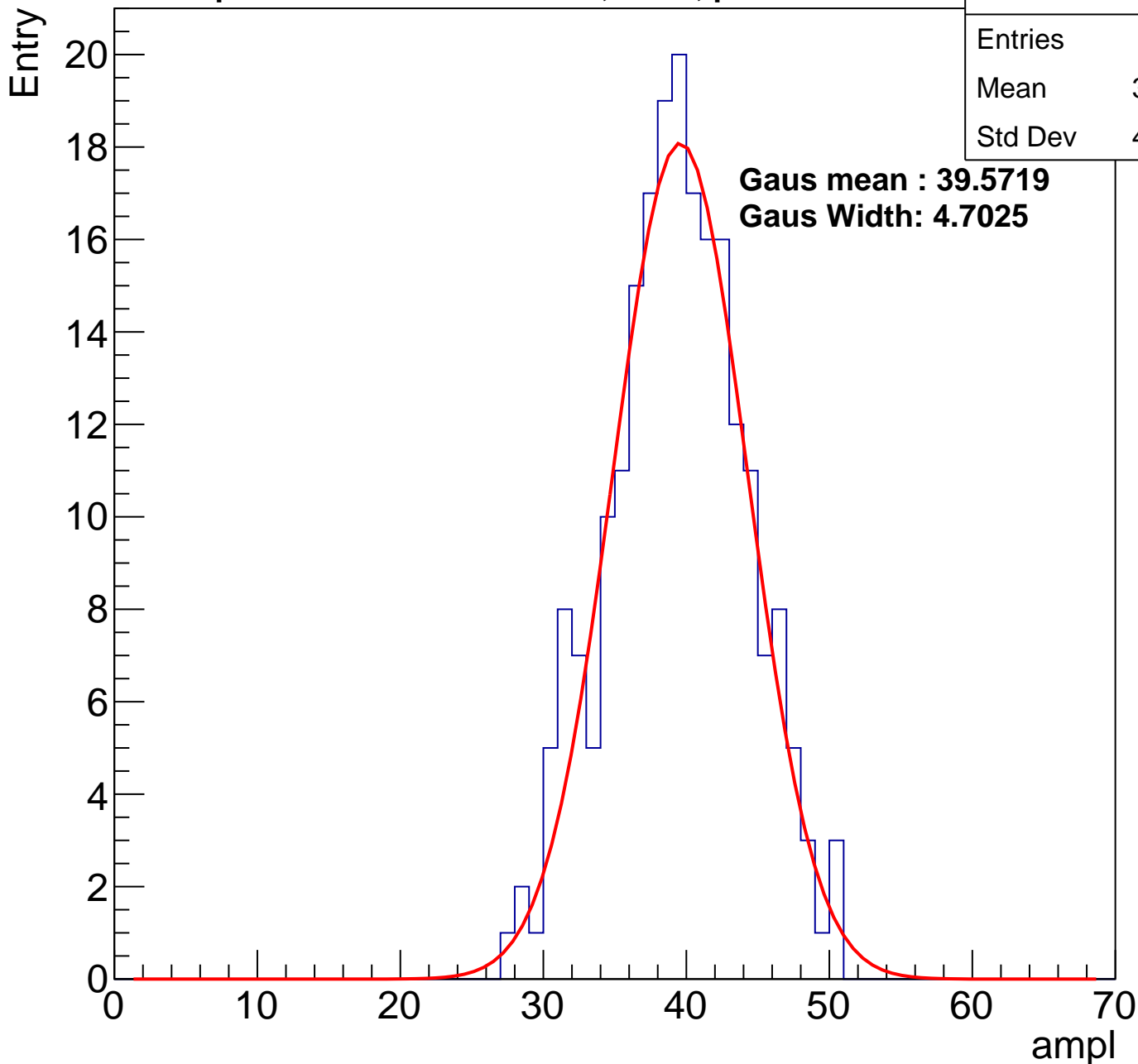
B0L101S, U14-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	220
Mean	38.93
Std Dev	4.736

Gaus mean : 39.5719

Gaus Width: 4.7025



B0L101S, U14-ch96, adc2

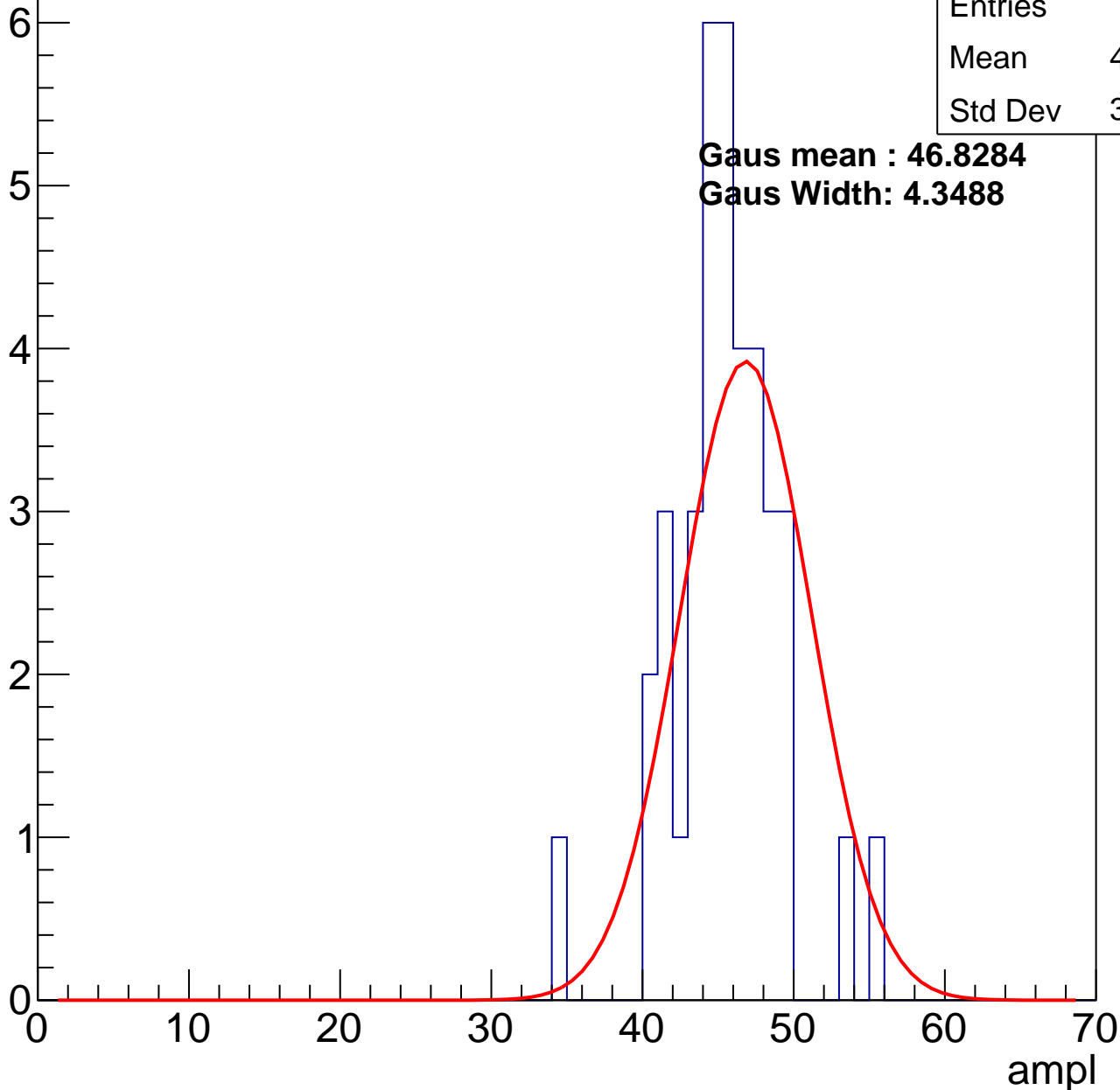
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	38
Mean	45.08
Std Dev	3.645

Gaus mean : 46.8284

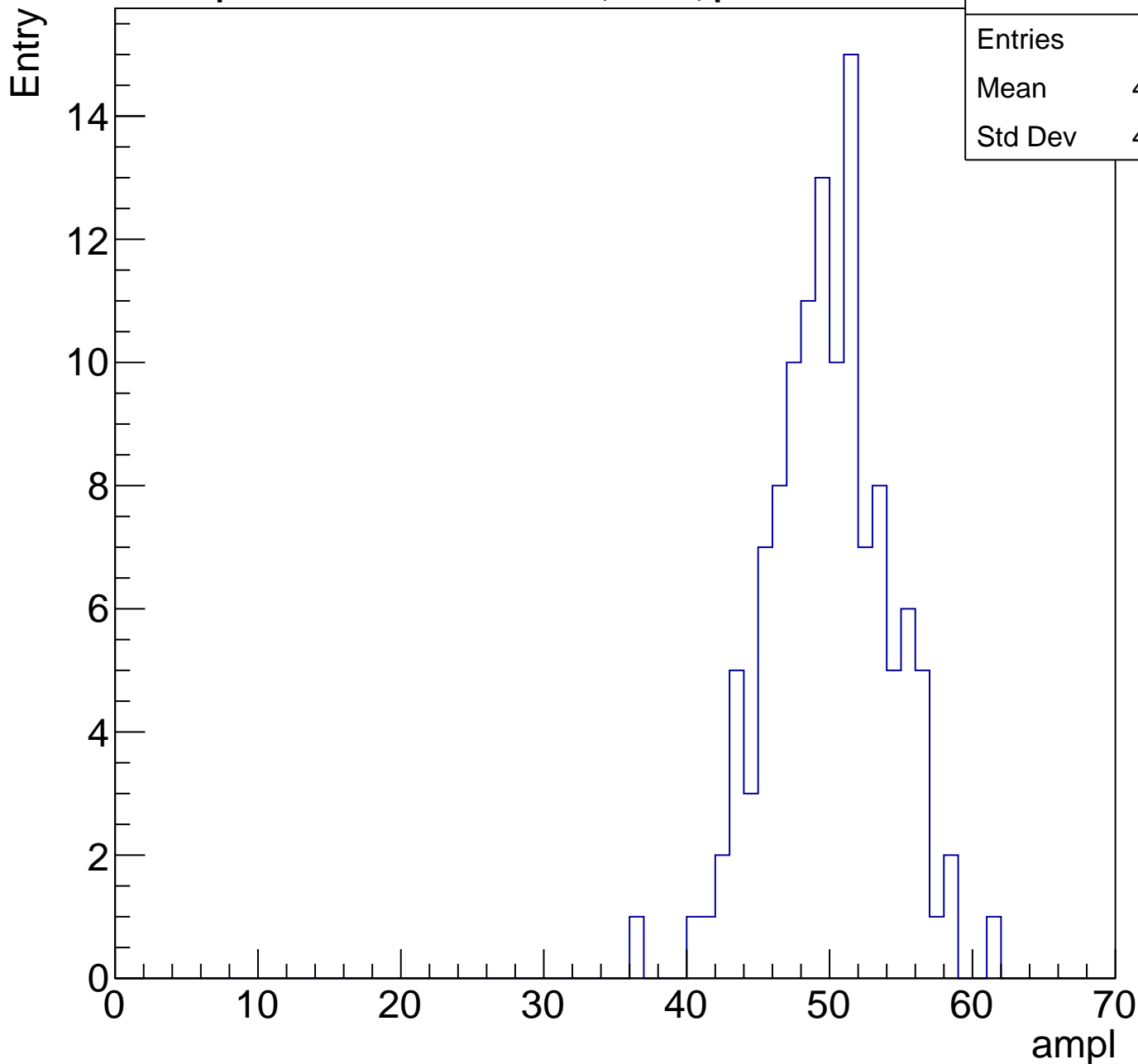
Gaus Width: 4.3488



B0L101S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	49.45
Std Dev	4.143



B0L101S, U14-ch96, adc4

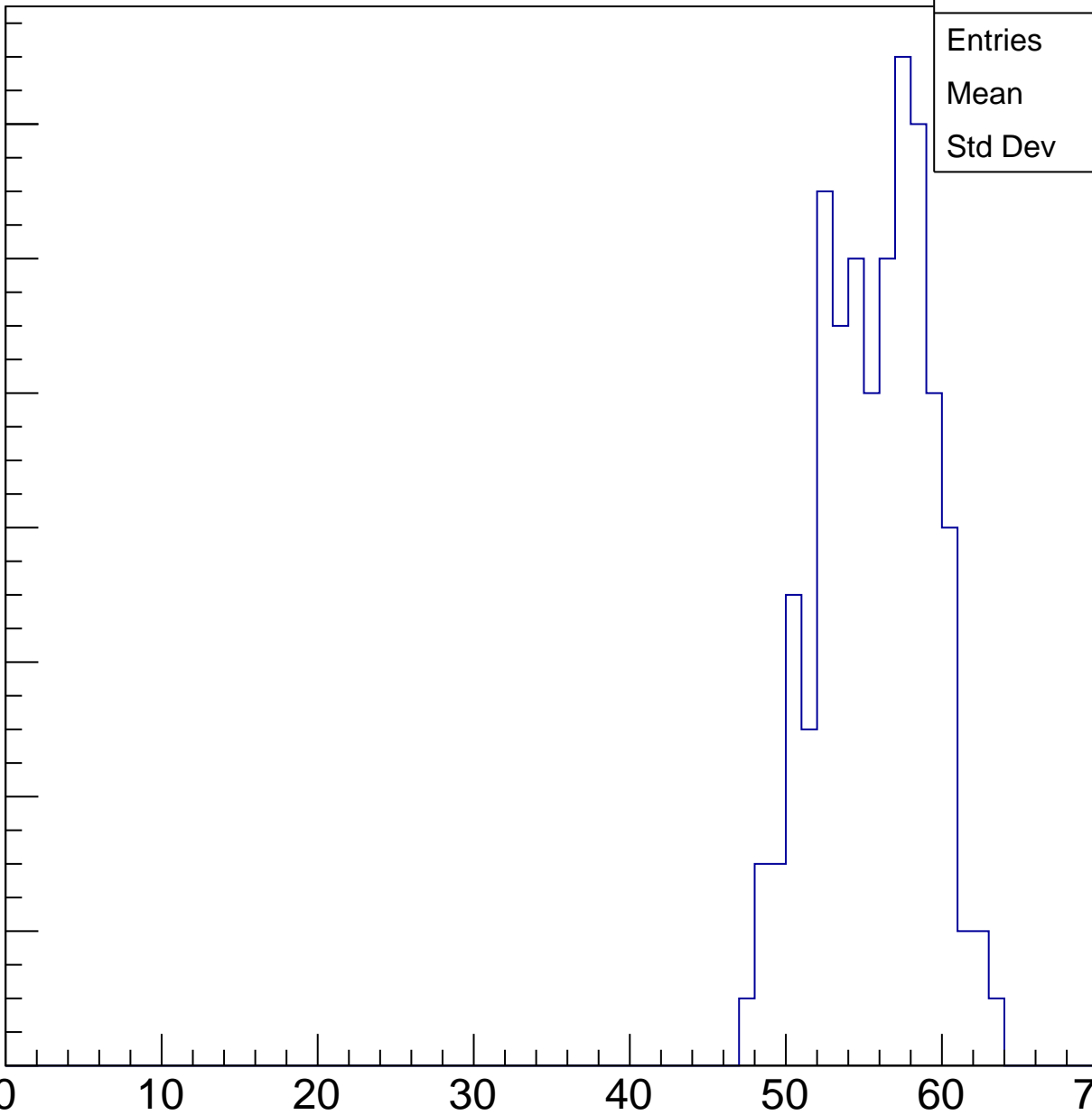
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	55.18
Std Dev	3.449

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

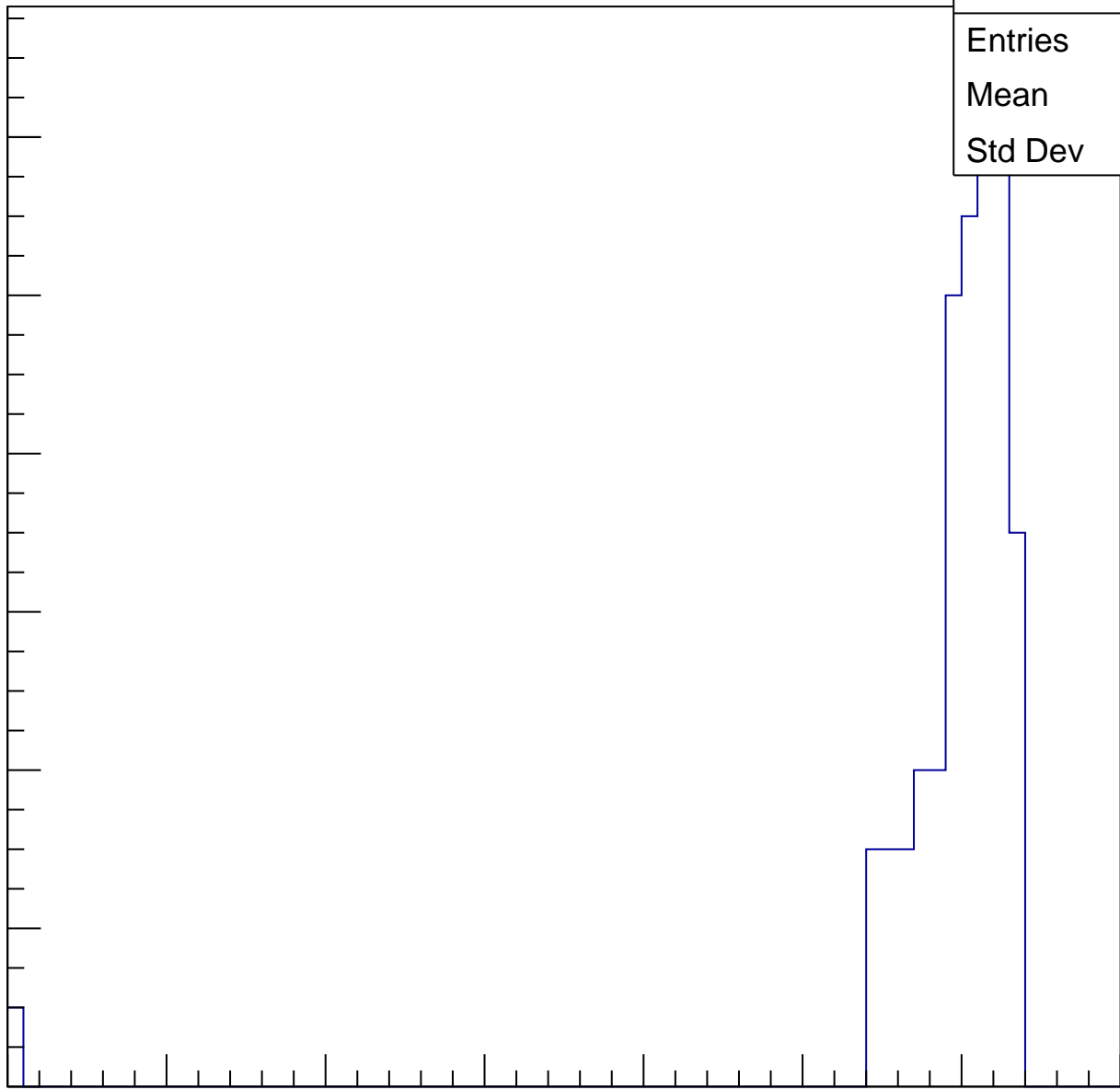
2

0

Entries	71
Mean	58.92
Std Dev	7.443

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	23
Mean	59.52
Std Dev	12.72

Entry

10

8

6

4

2

0

0

10

20

30

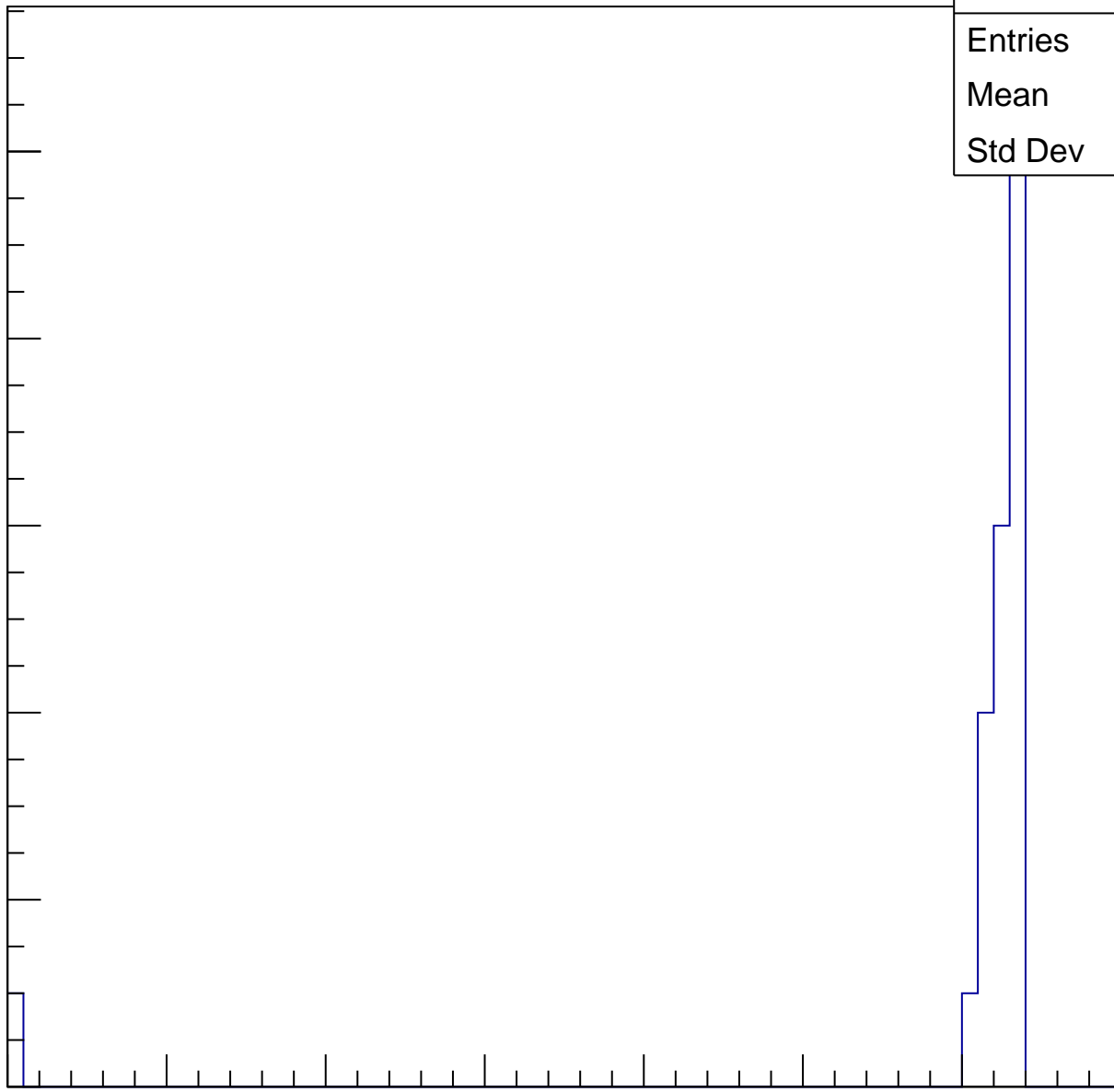
40

50

60

70

ampl



B0L101S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch97, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	28.92
Std Dev	4.304

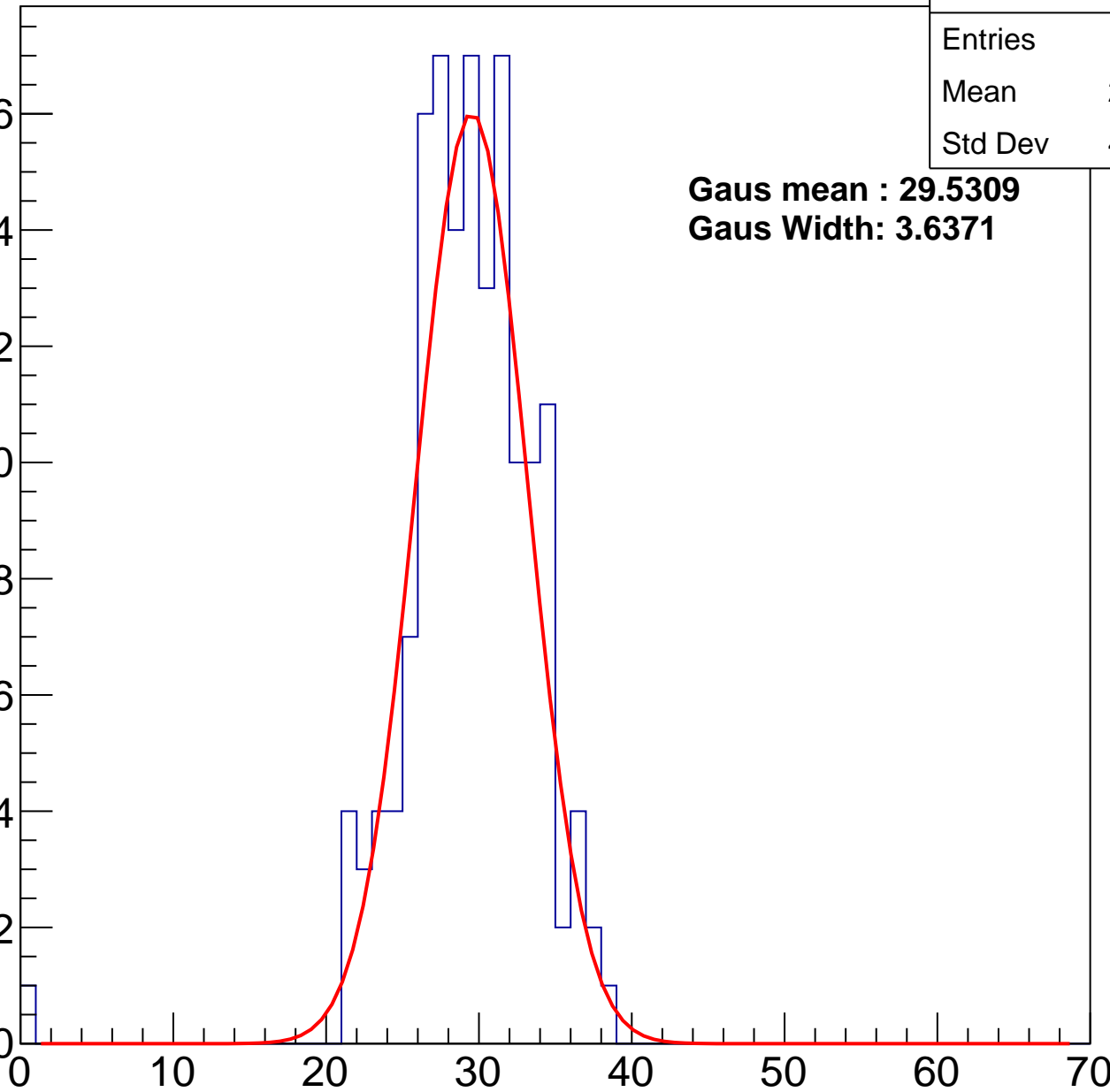
Gaus mean : 29.5309

Gaus Width: 3.6371

Entry

16
14
12
10
8
6
4
2
0

ampl



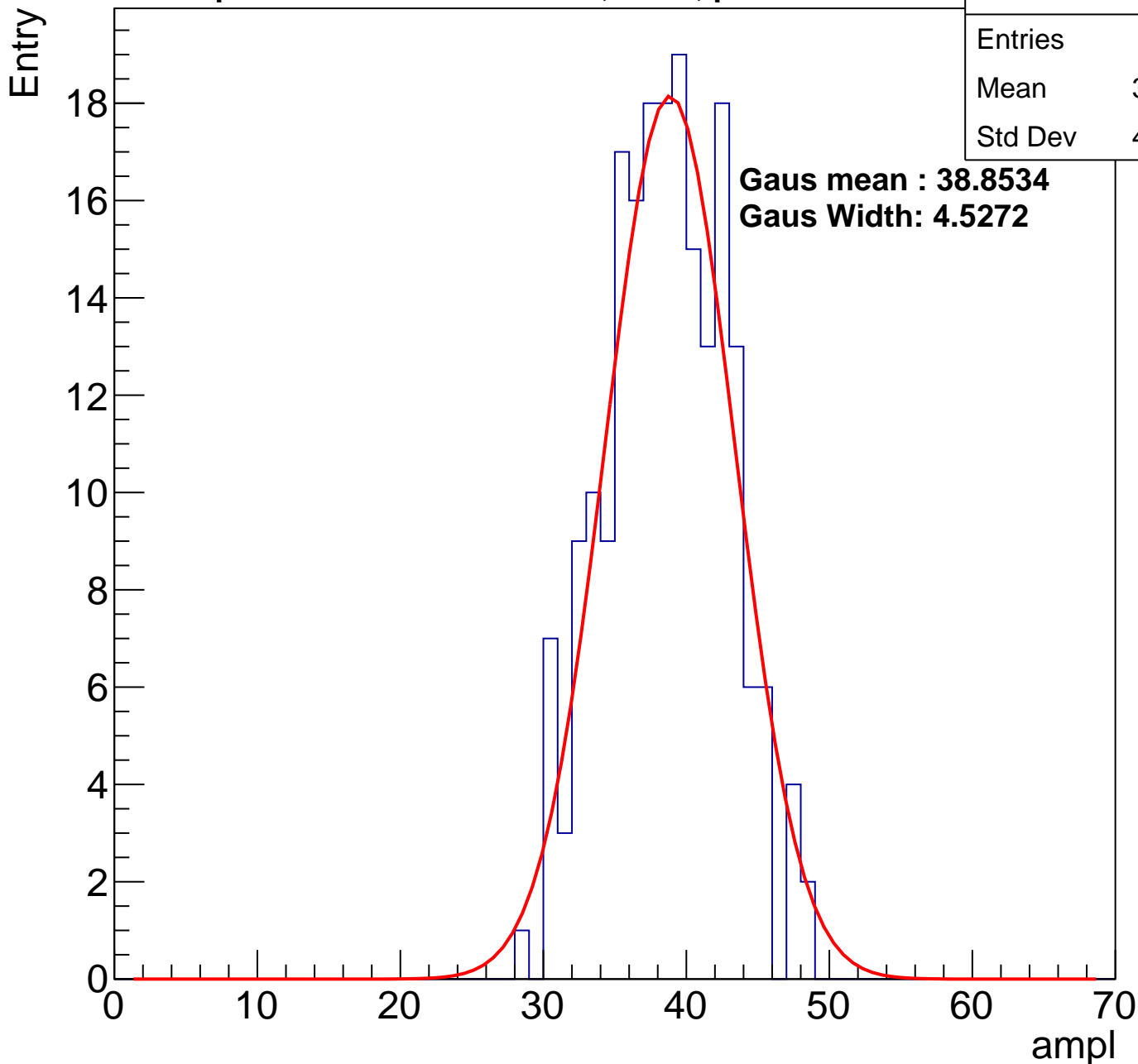
B0L101S, U14-ch97, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	204
Mean	38.15
Std Dev	4.134

Gaus mean : 38.8534

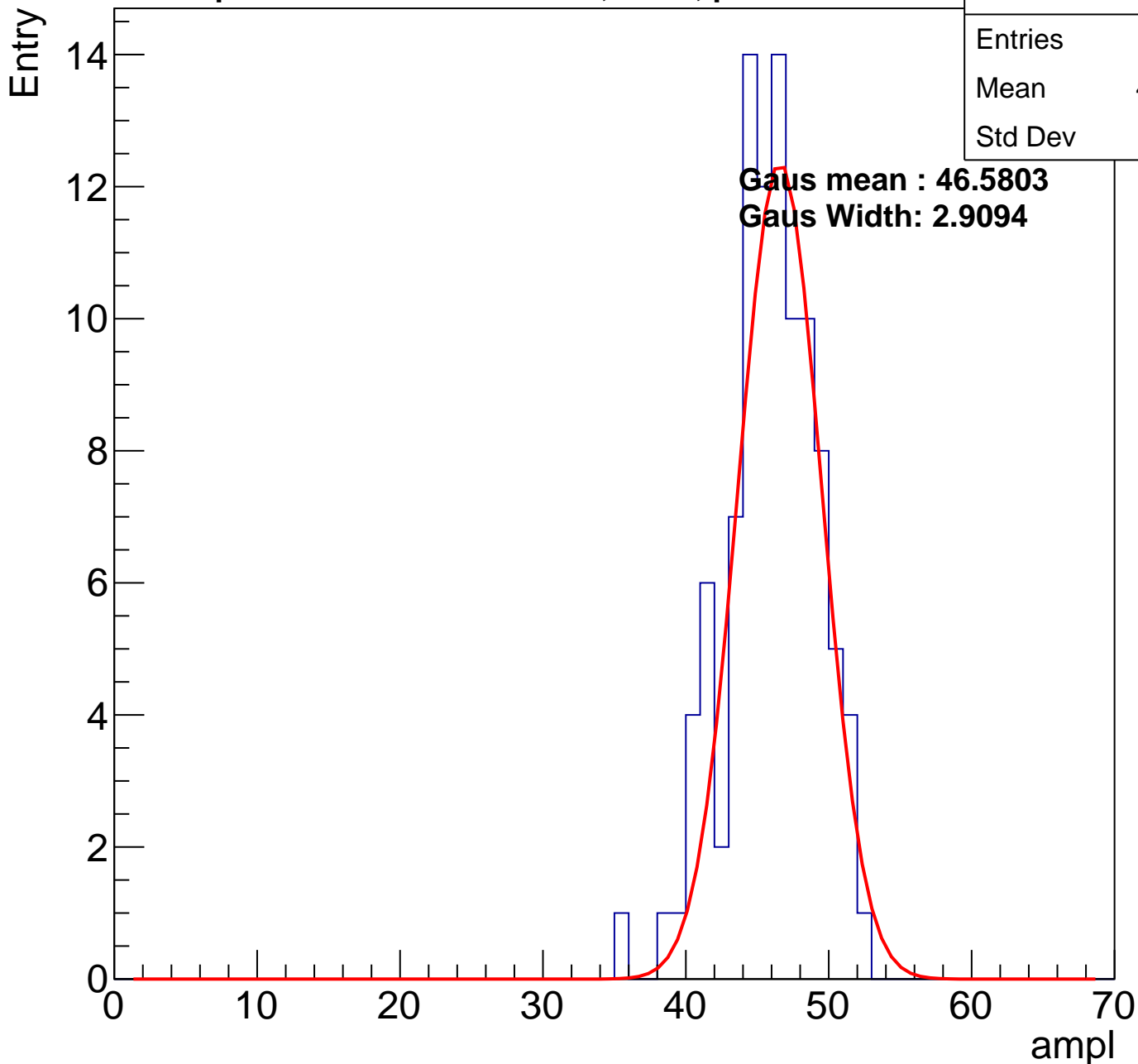
Gaus Width: 4.5272



B0L101S, U14-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

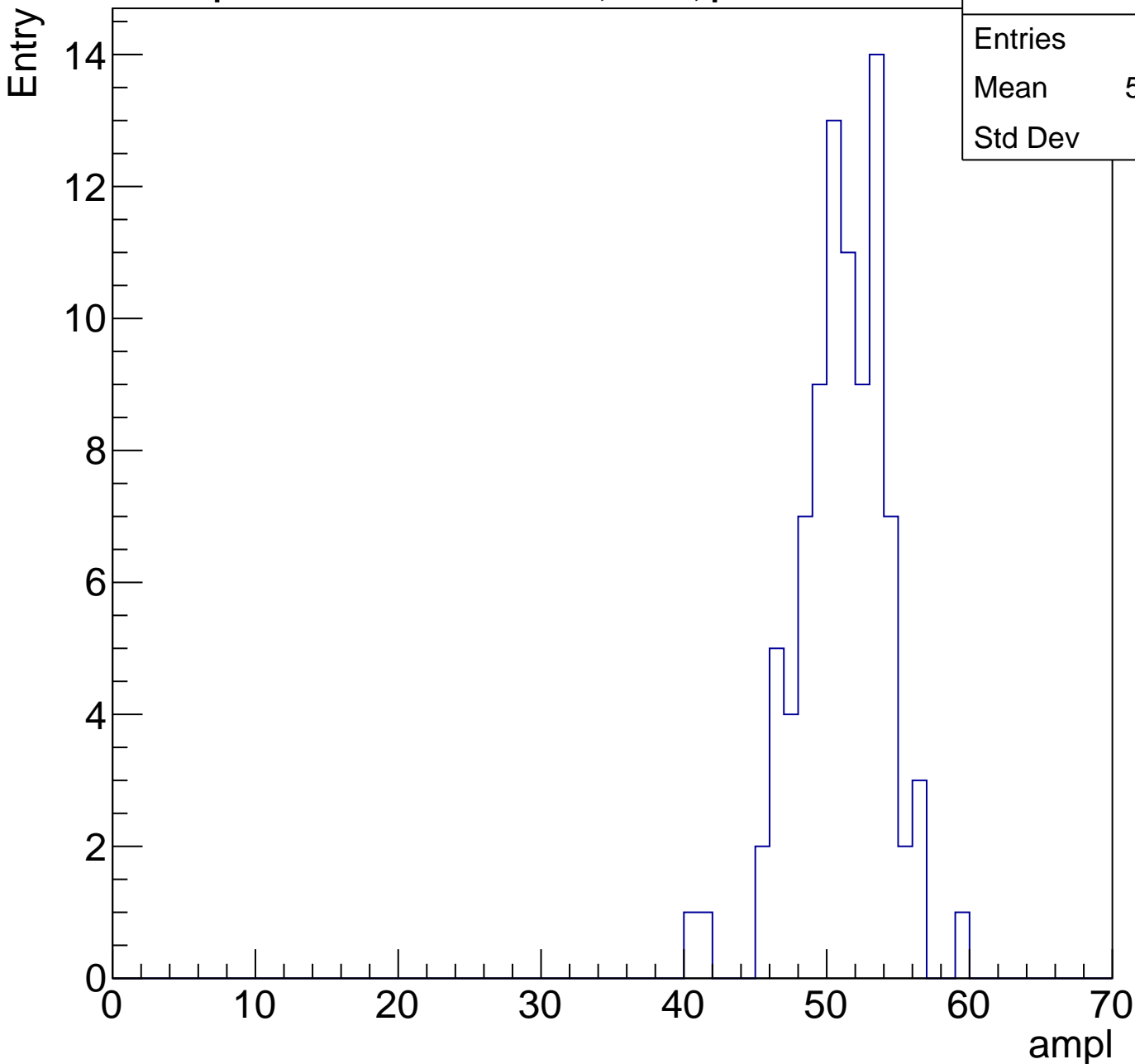
Entries	100
Mean	45.51
Std Dev	3.17



B0L101S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

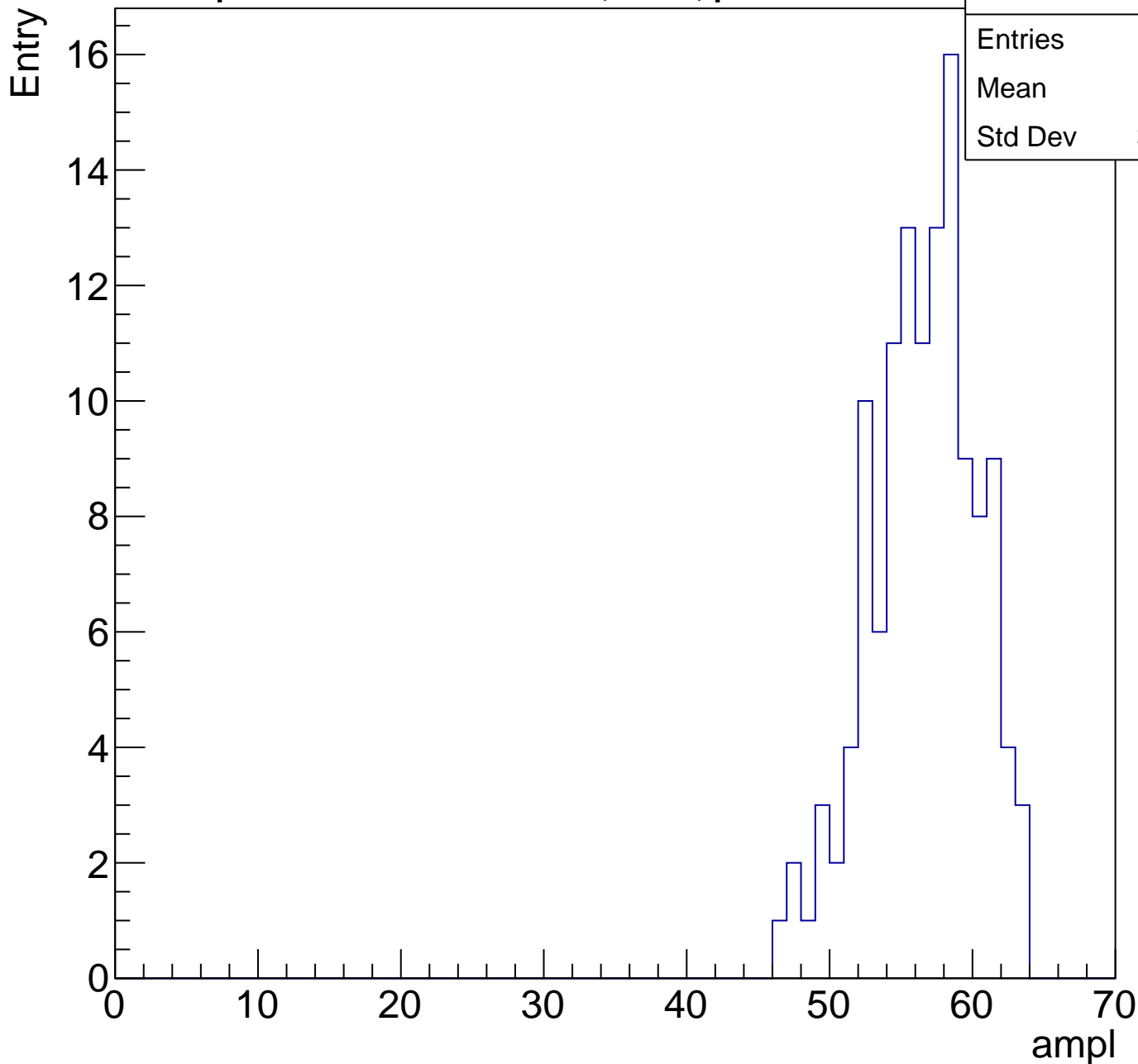
Entries	89
Mean	50.58
Std Dev	3.14



B0L101S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	56.1
Std Dev	3.691



B0L101S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

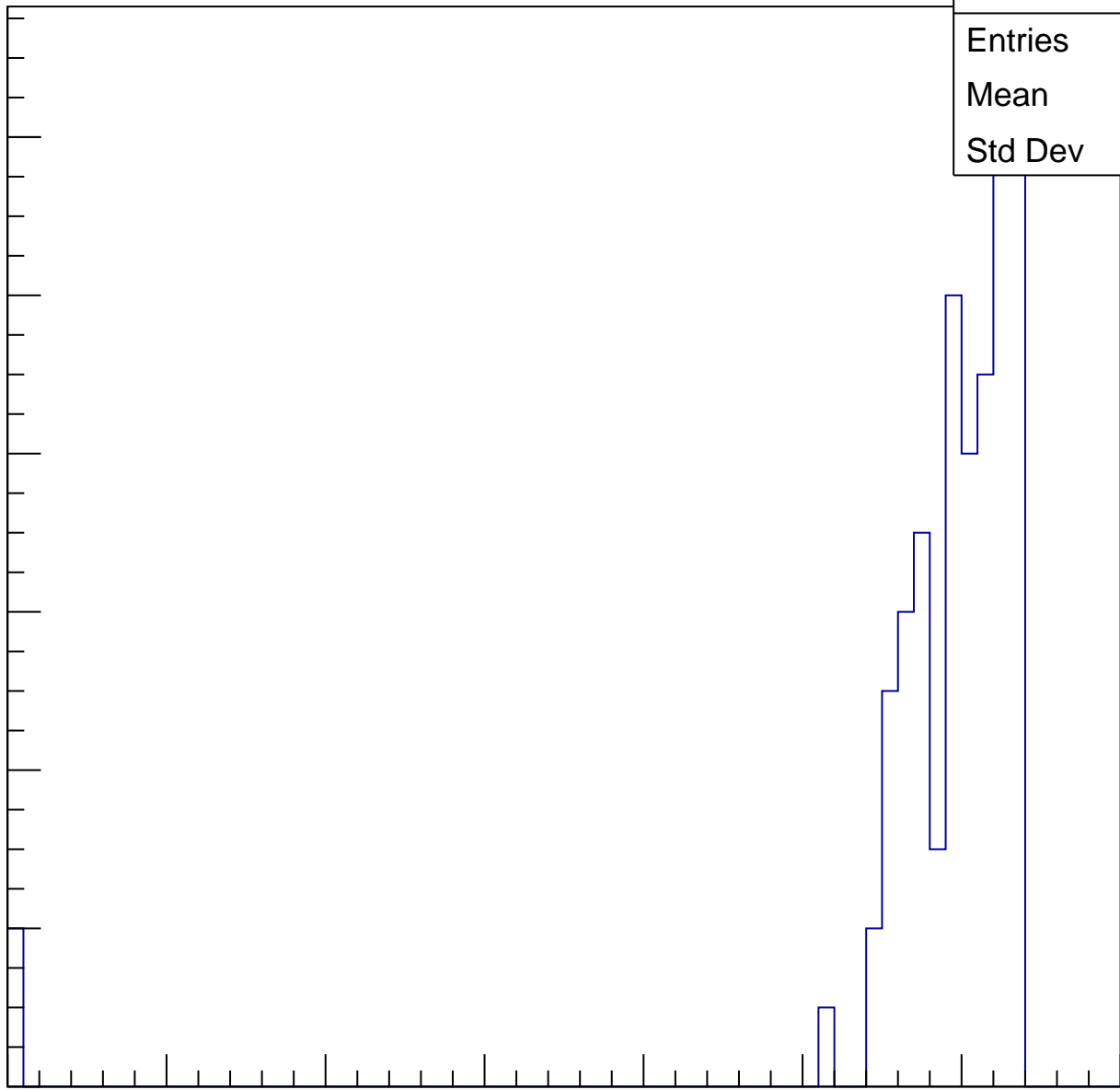
2

0

Entries	78
Mean	58.01
Std Dev	9.822

ampl

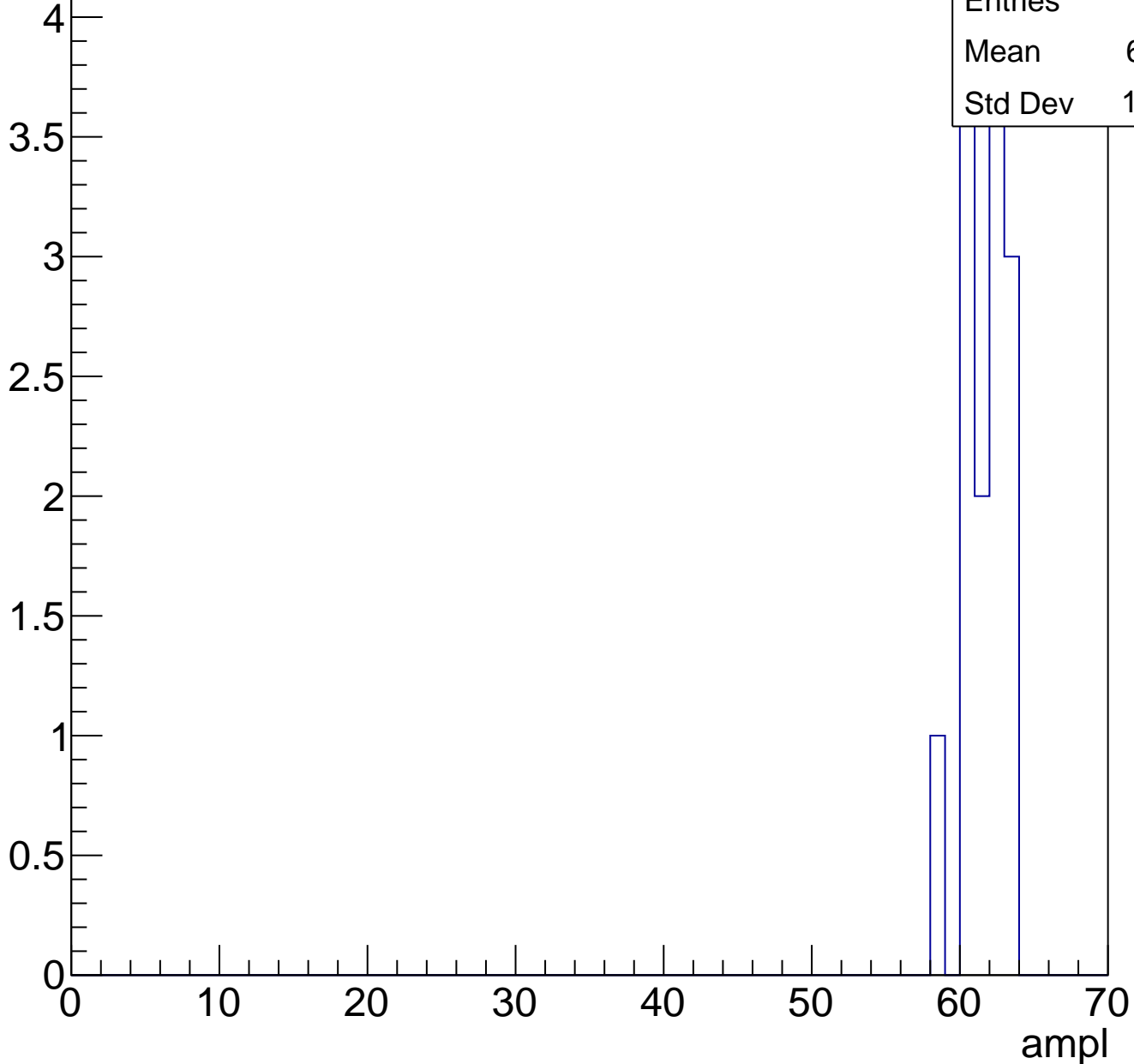
0 10 20 30 40 50 60 70



B0L101S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch97, adc7

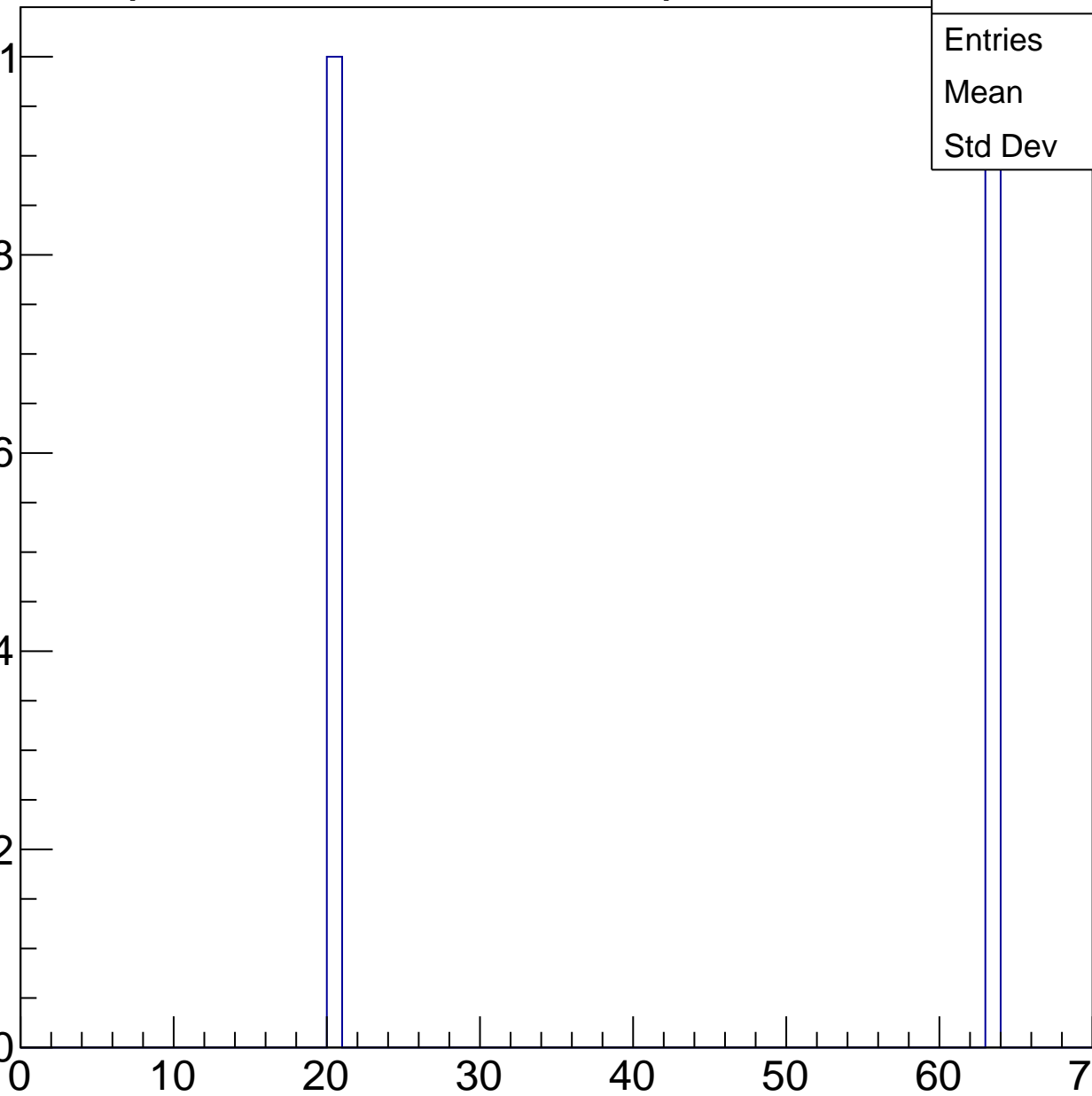
calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	21.5

ampl



B0L101S, U14-ch98, adc0

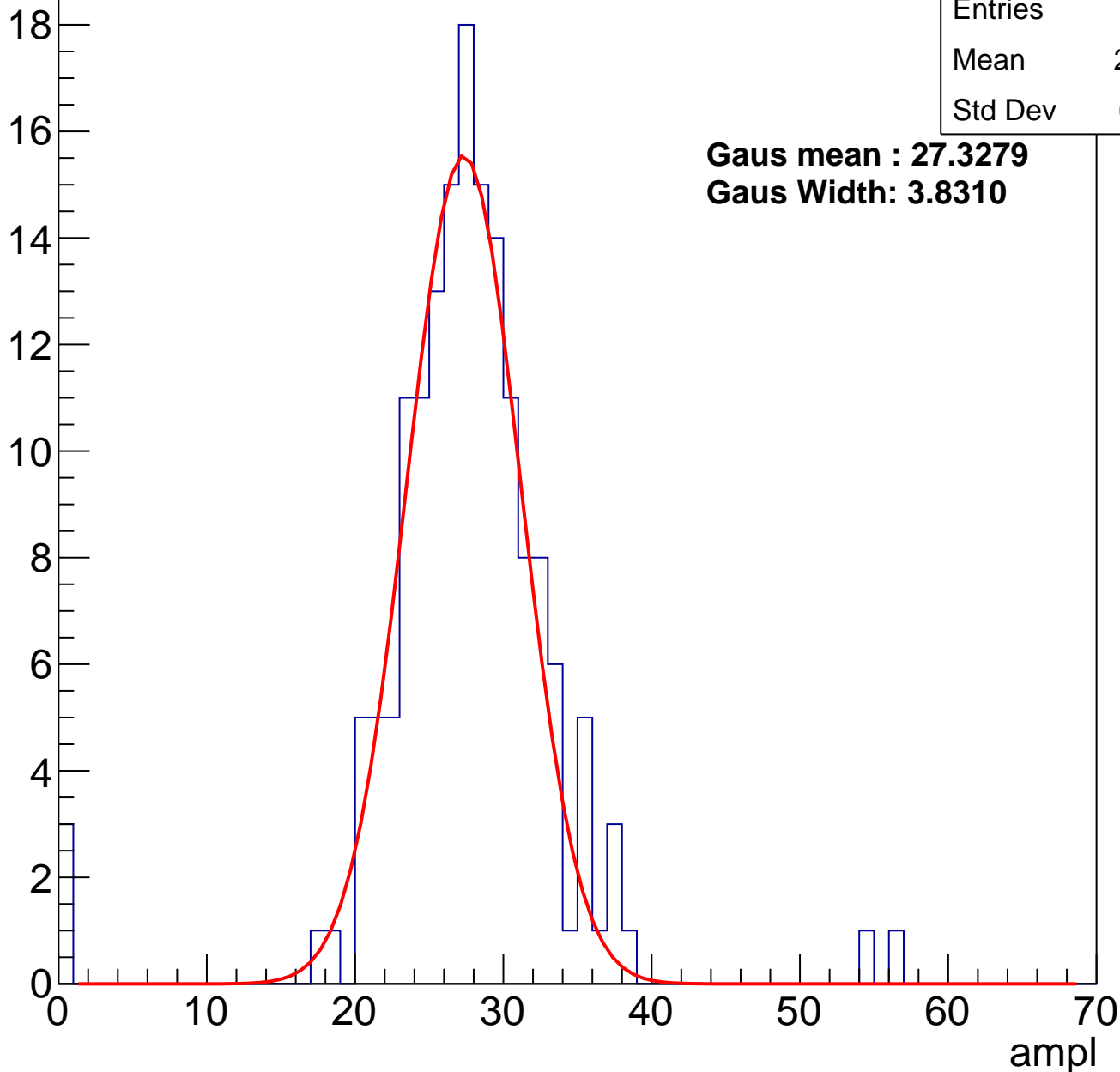
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	27.18
Std Dev	6.271

Gaus mean : 27.3279

Gaus Width: 3.8310

Entry



B0L101S, U14-ch98, adc1

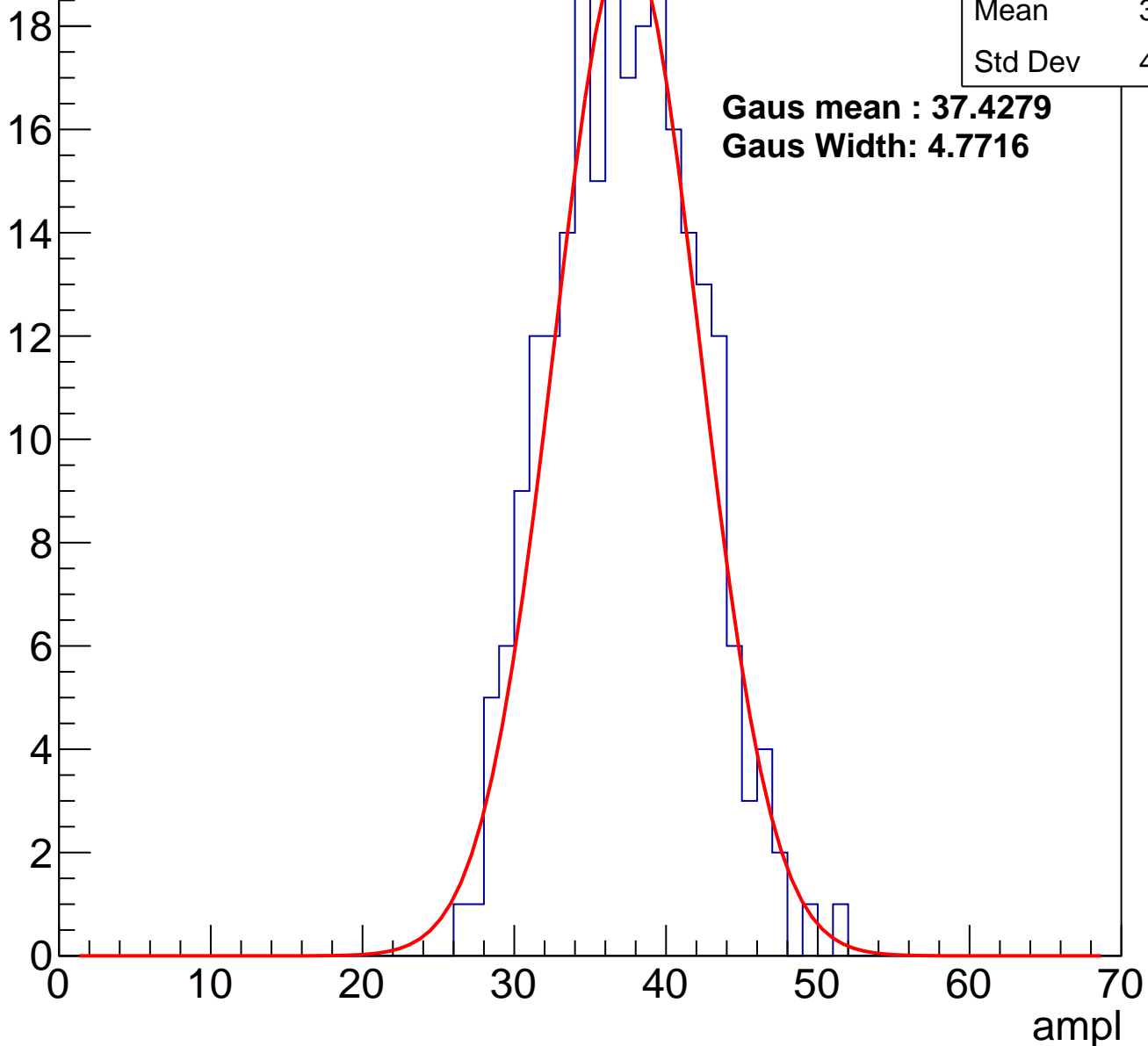
calib_packv5_042523_0143.root, FC#1, port C1

Entries	239
Mean	36.89
Std Dev	4.675

Gaus mean : 37.4279

Gaus Width: 4.7716

Entry



B0L101S, U14-ch98, adc2

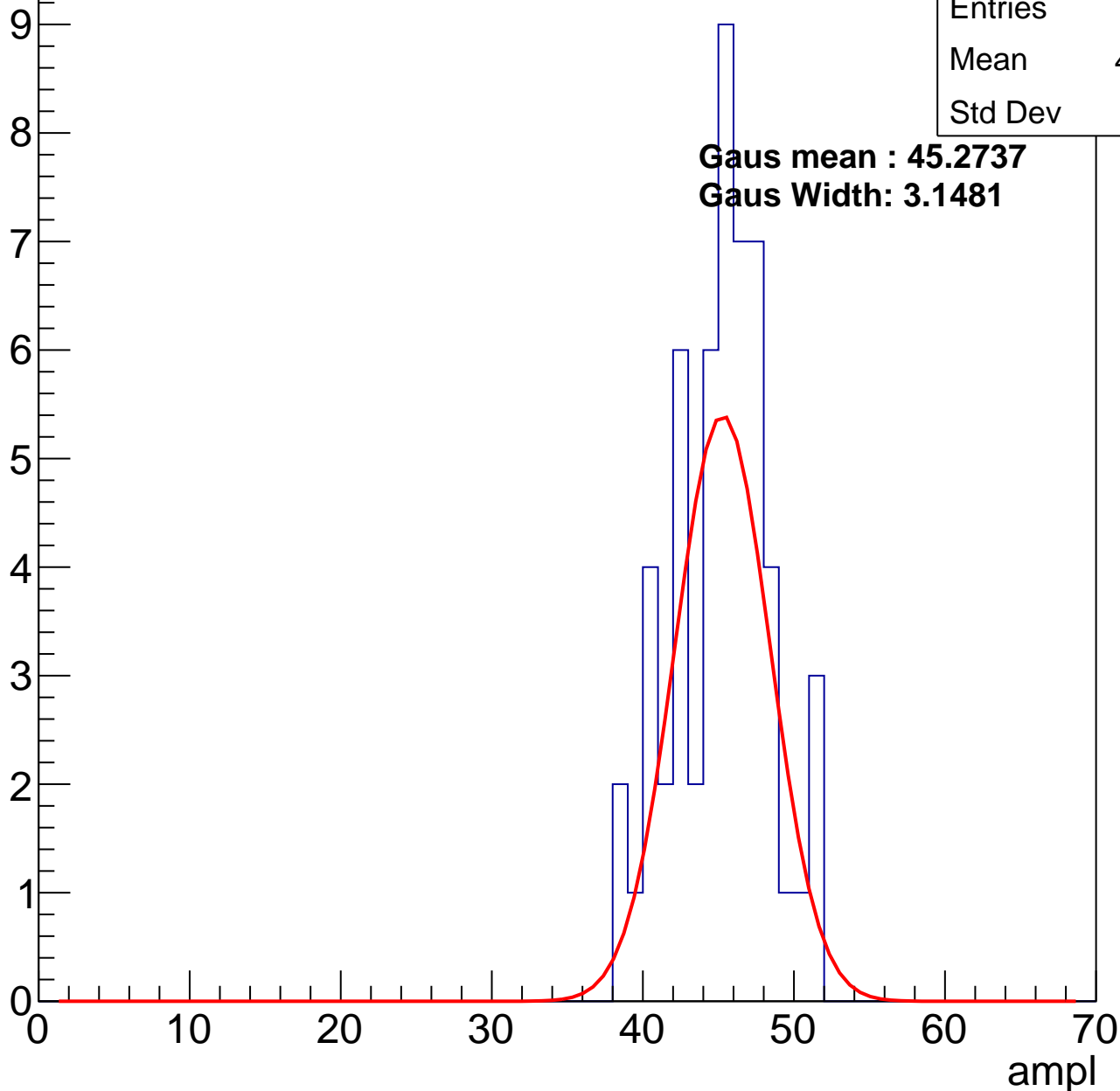
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	55
Mean	44.71
Std Dev	3.16

Gaus mean : 45.2737

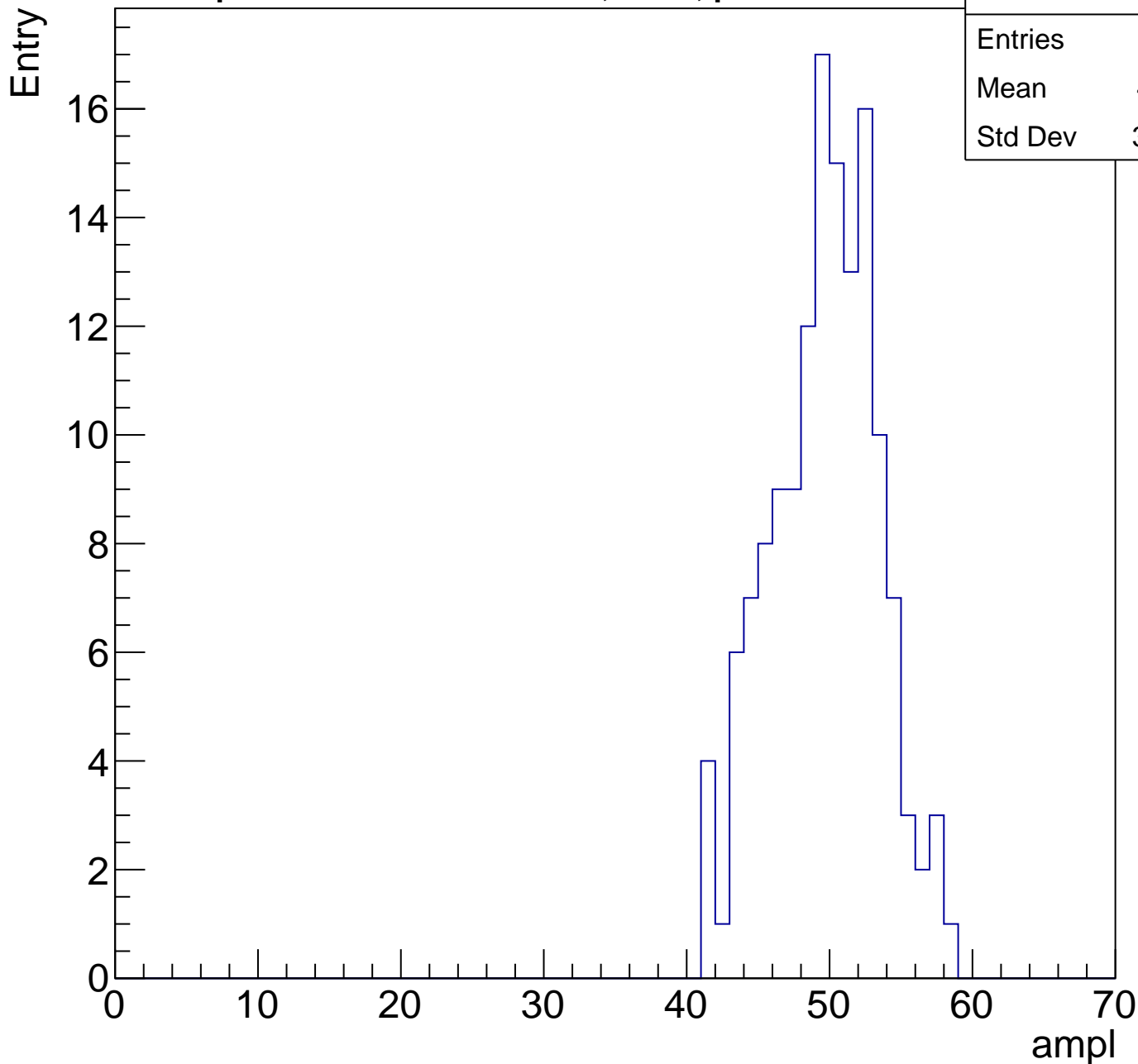
Gaus Width: 3.1481



B0L101S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

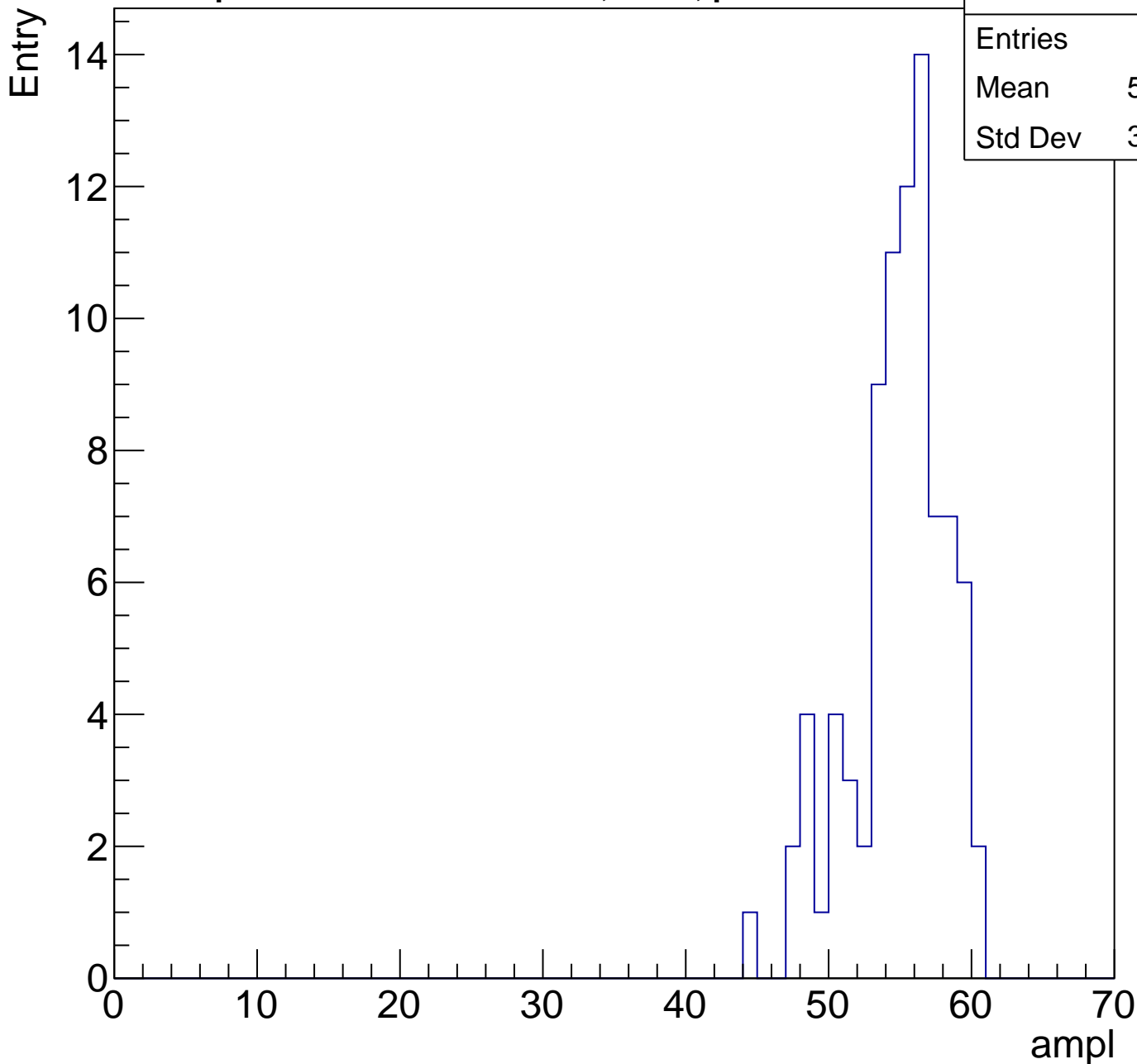
Entries	143
Mean	49.21
Std Dev	3.713



B0L101S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	85
Mean	54.47
Std Dev	3.292



B0L101S, U14-ch98, adc5

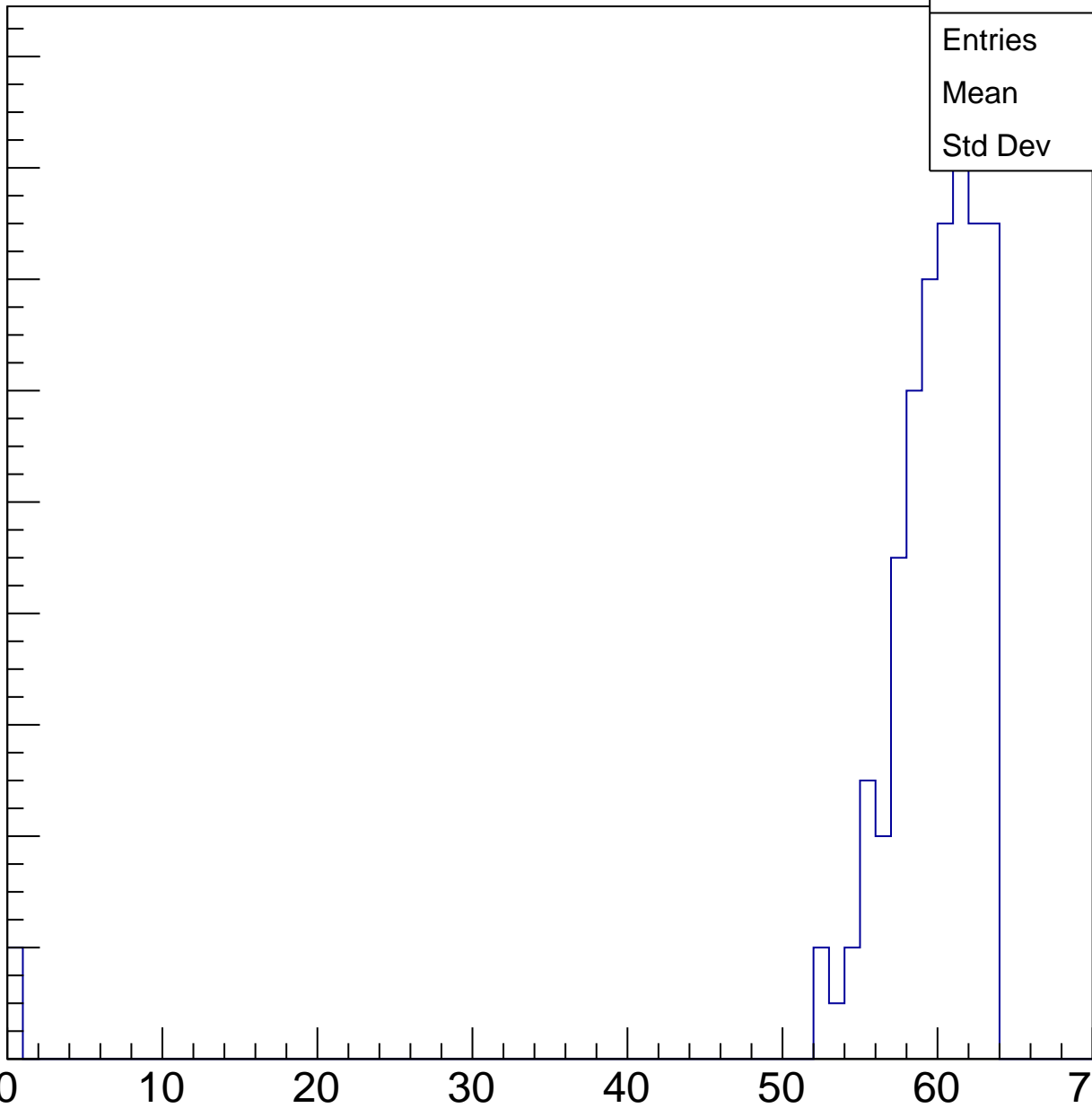
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	114
Mean	58.53
Std Dev	8.243

ampl

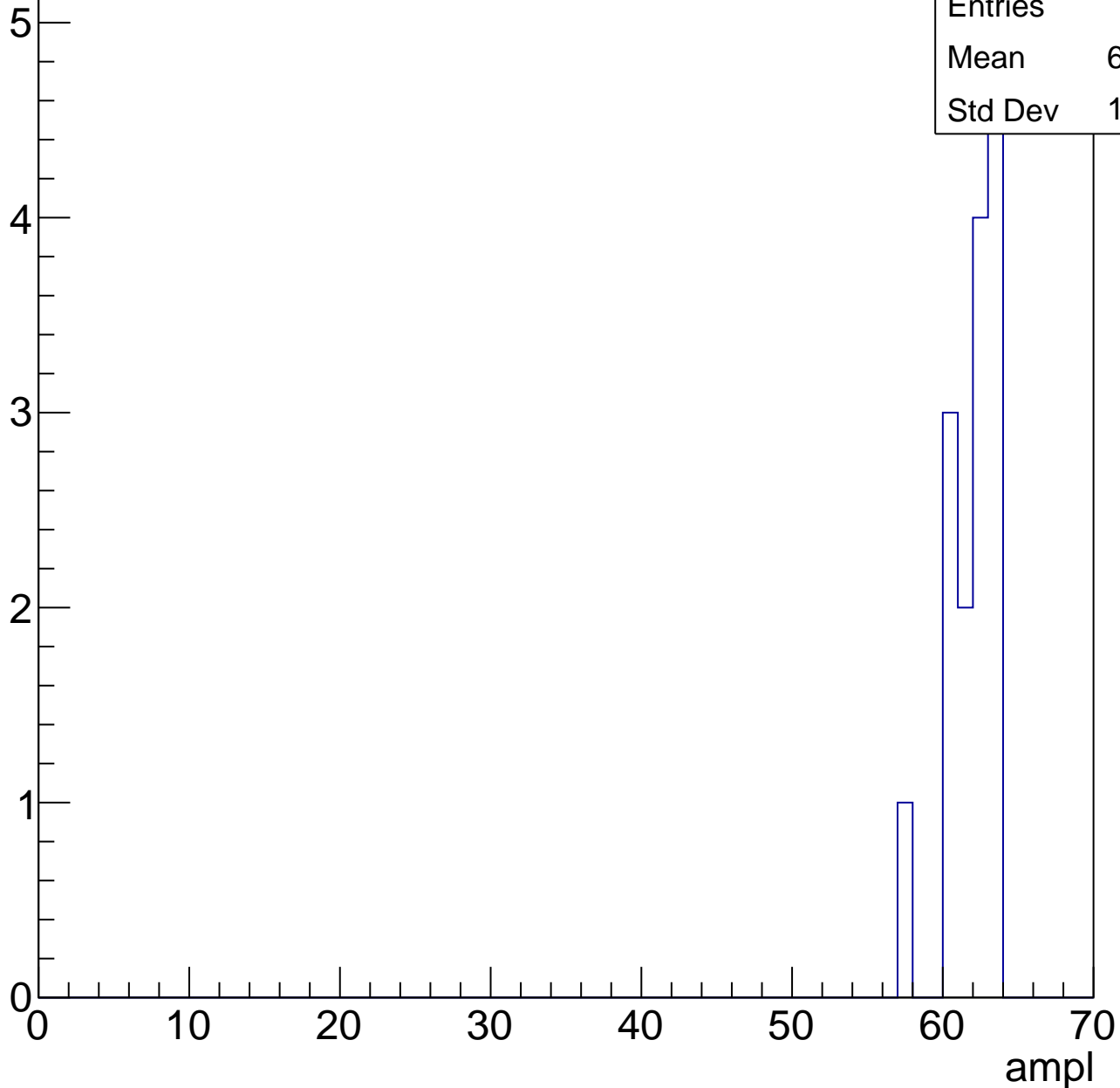


B0L101S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.47
Std Dev	1.628



B0L101S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch99, adc0

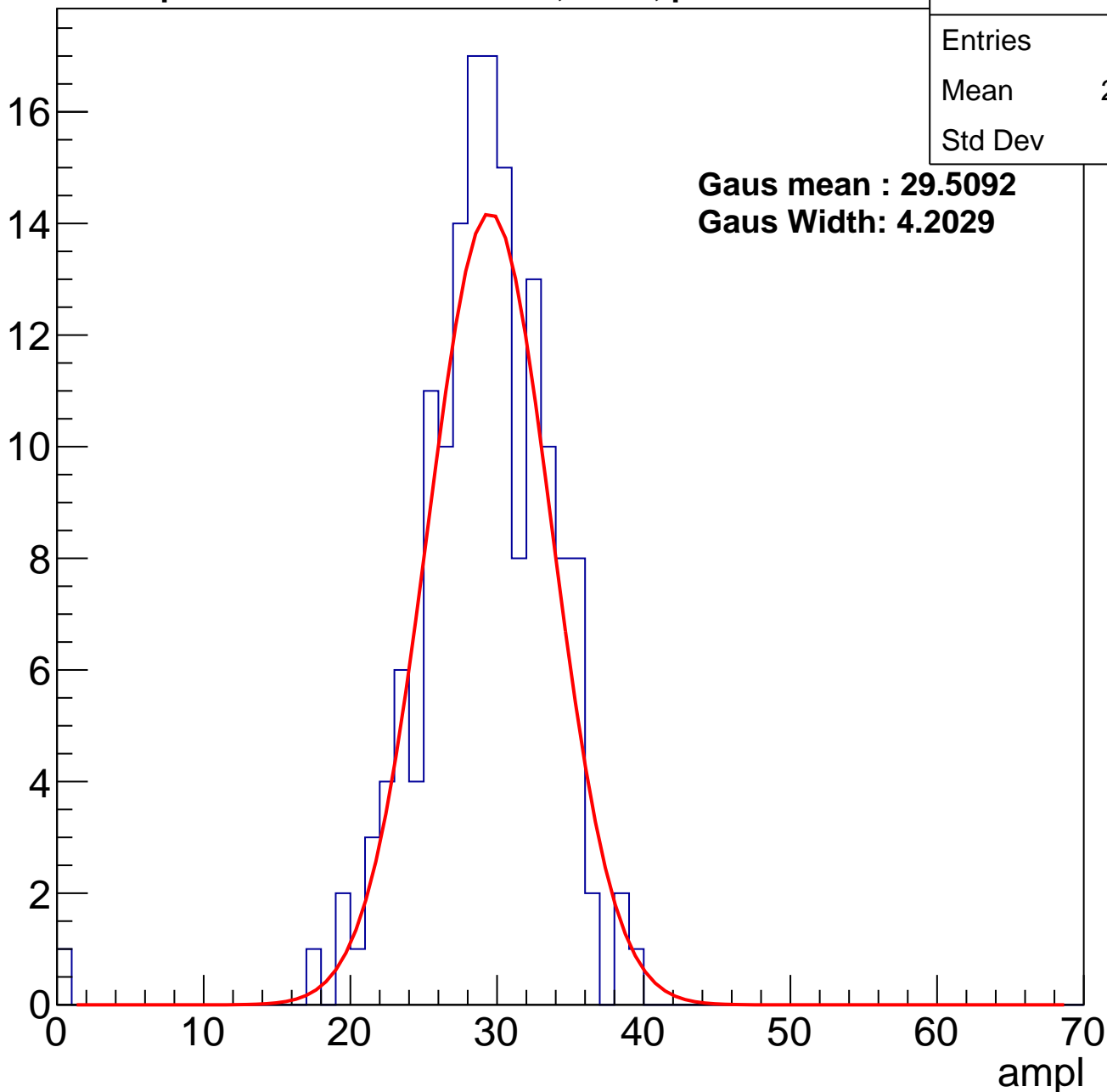
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.64
Std Dev	4.68

Gaus mean : 29.5092

Gaus Width: 4.2029

Entry



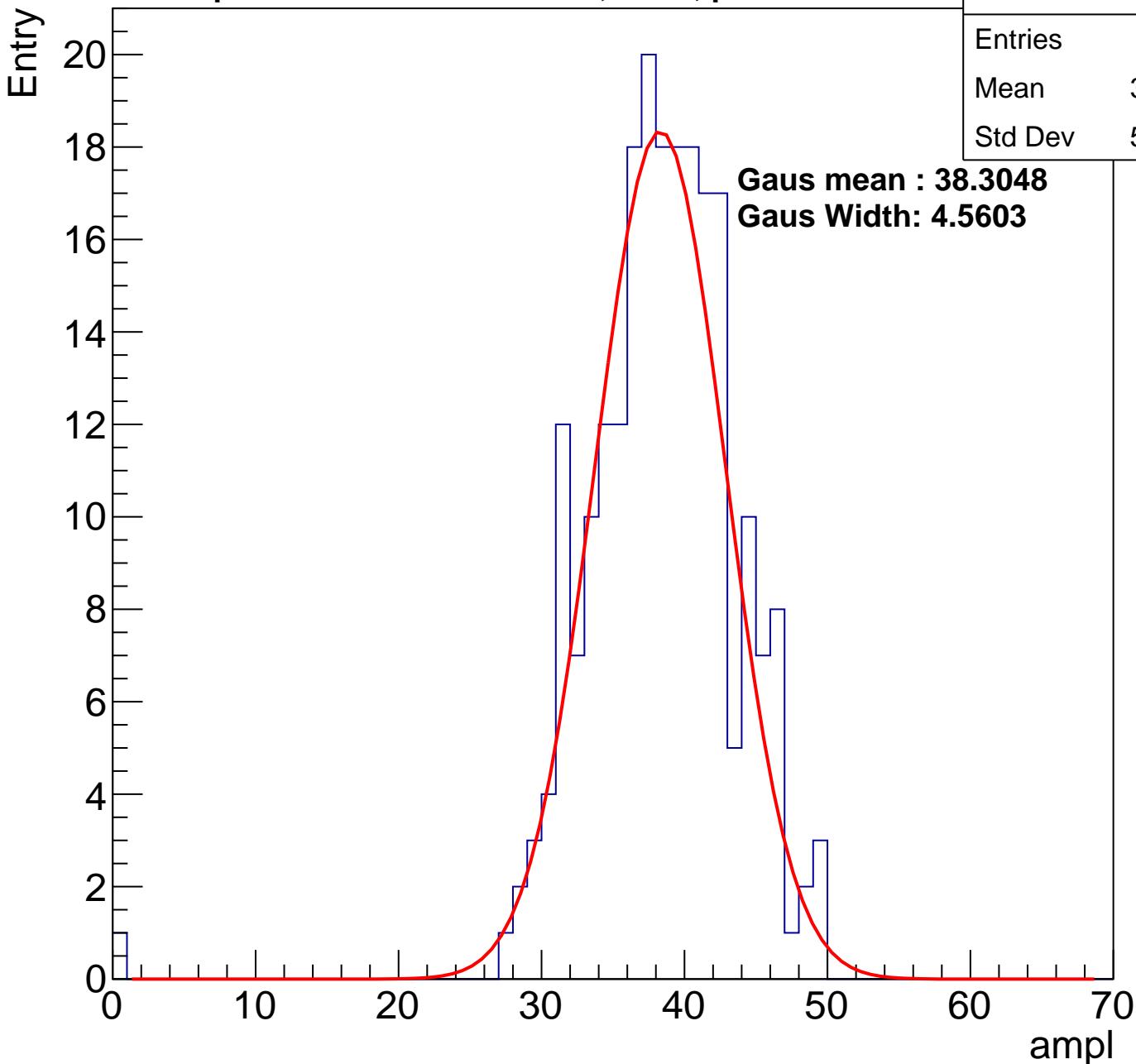
B0L101S, U14-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	226
Mean	37.95
Std Dev	5.256

Gaus mean : 38.3048

Gaus Width: 4.5603



B0L101S, U14-ch99, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	75
Mean	46.19
Std Dev	3.244

Gaus mean : 46.7393

Gaus Width: 3.4402

Entry

12

10

8

6

4

2

0

0

10

20

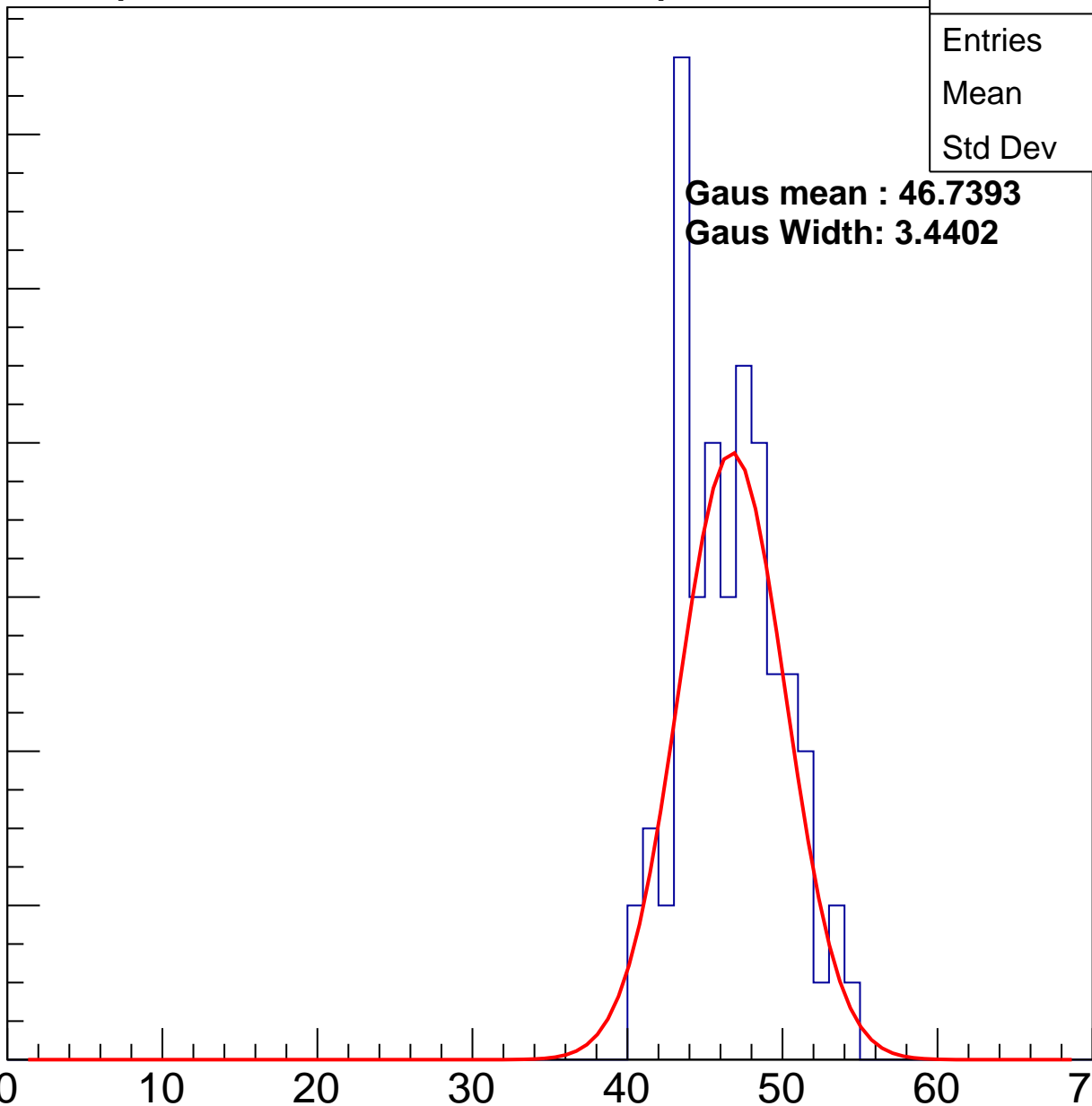
30

40

50

60

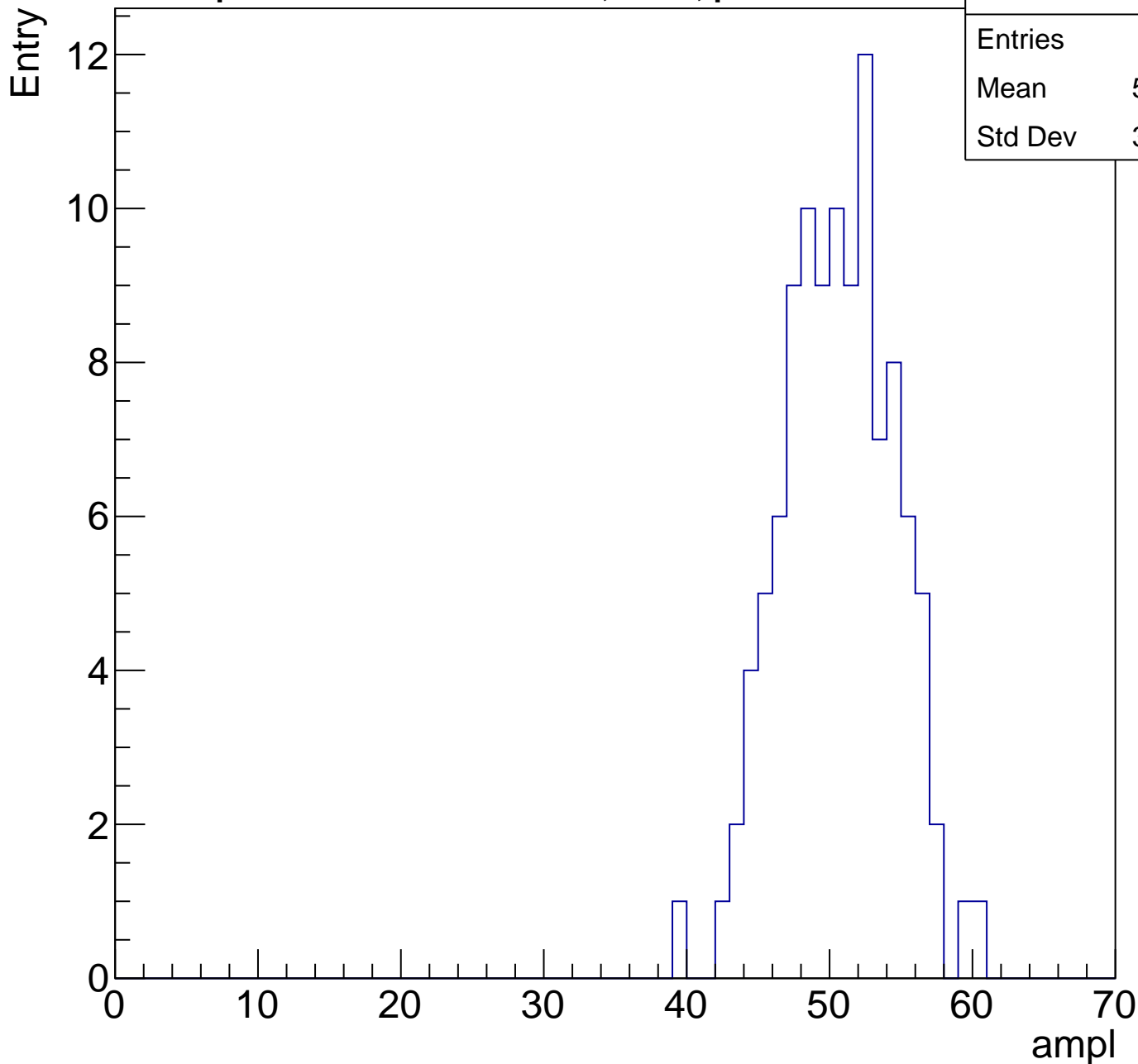
ampl



B0L101S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

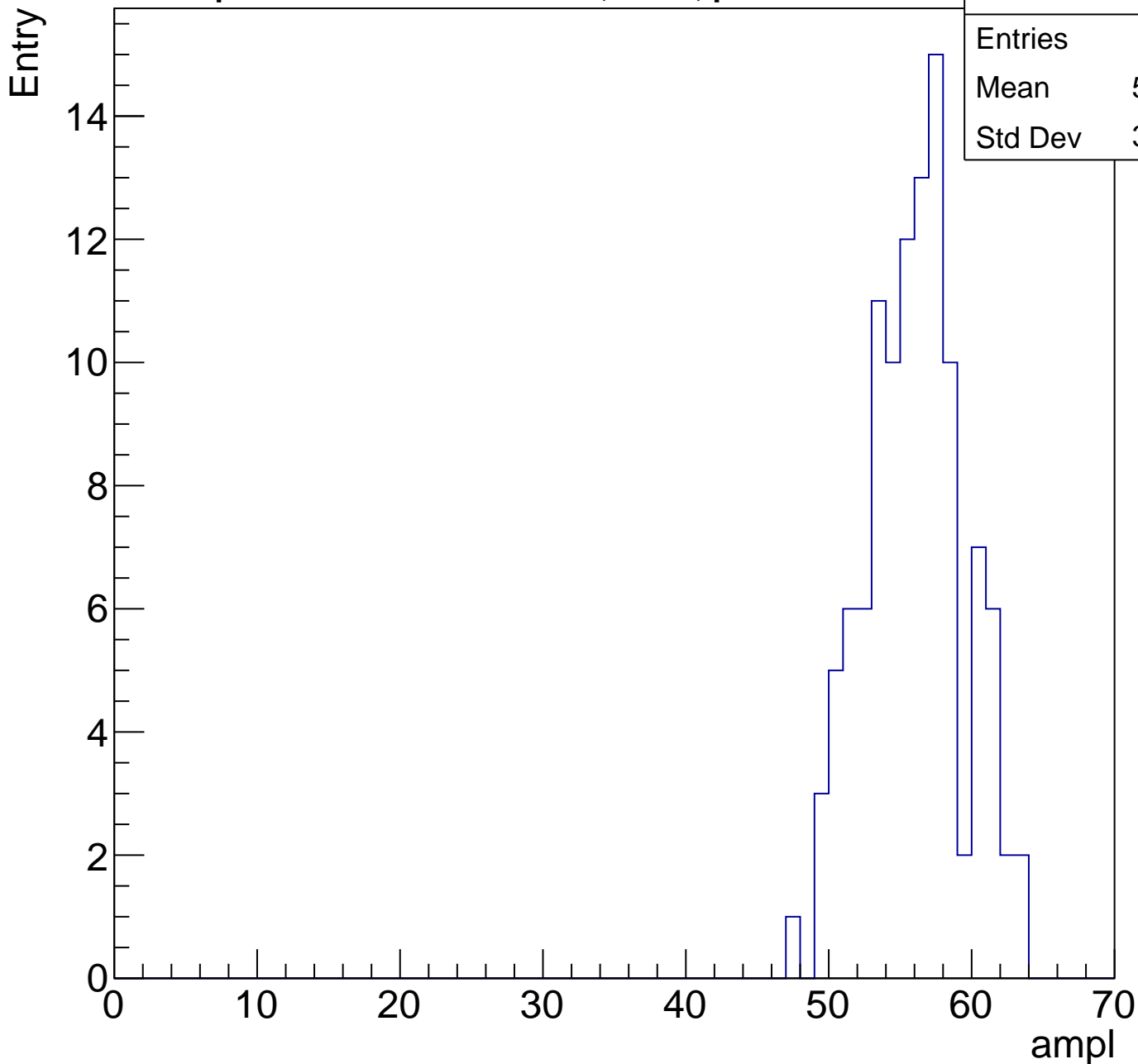
Entries	108
Mean	50.16
Std Dev	3.888



B0L101S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

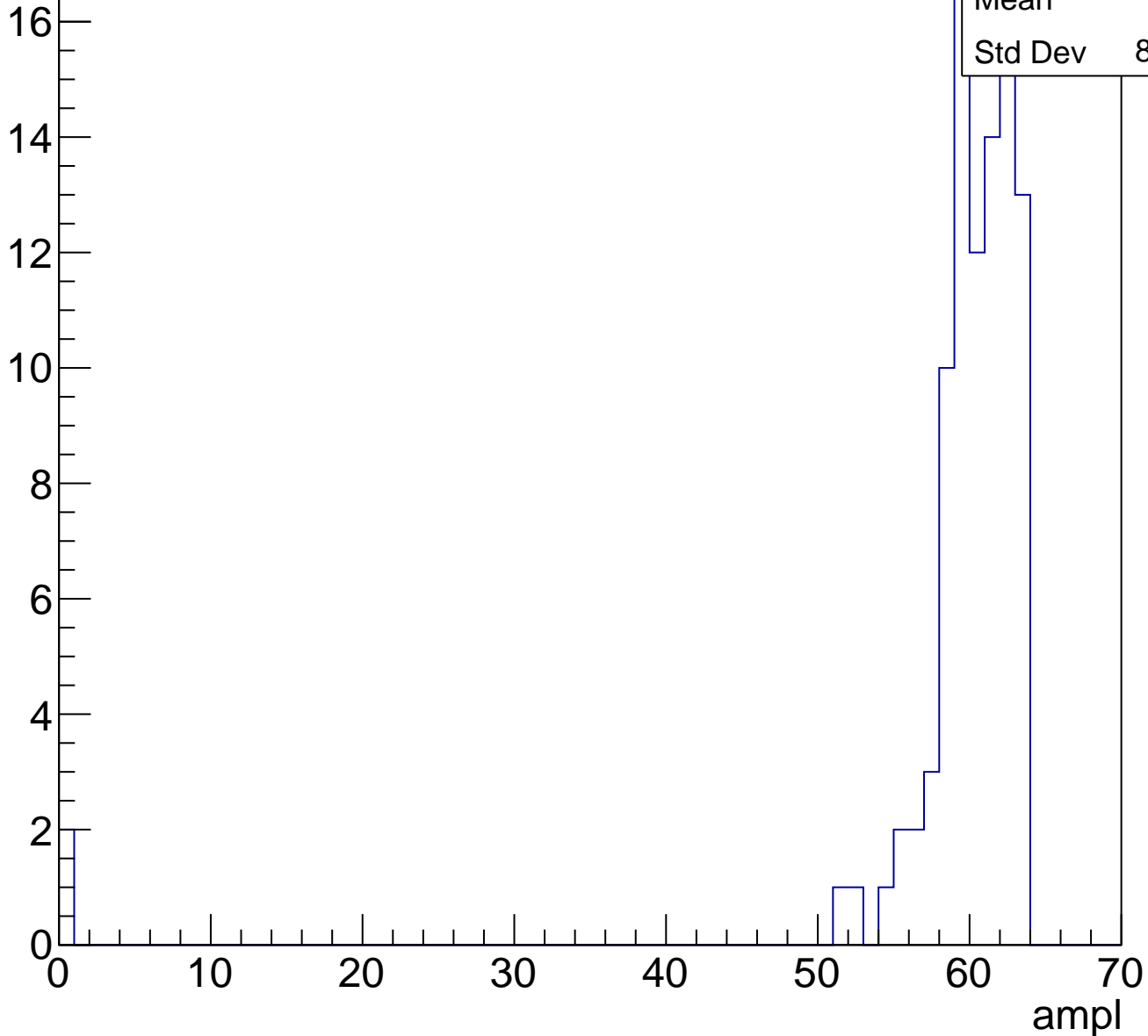
Entries	111
Mean	55.51
Std Dev	3.401



B0L101S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#1, port C1

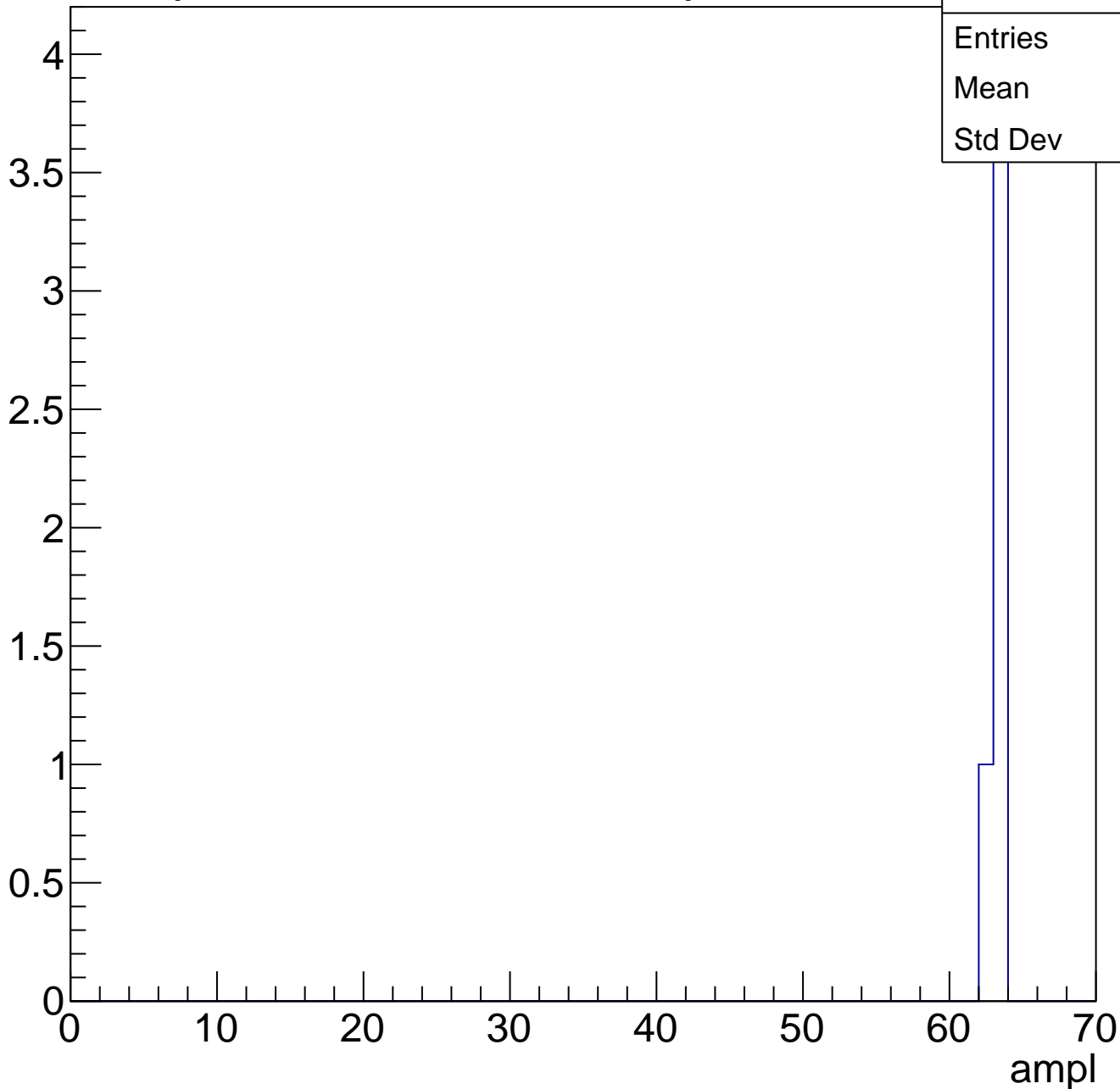
Entry



B0L101S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B0L101S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	29.5
Std Dev	3.958

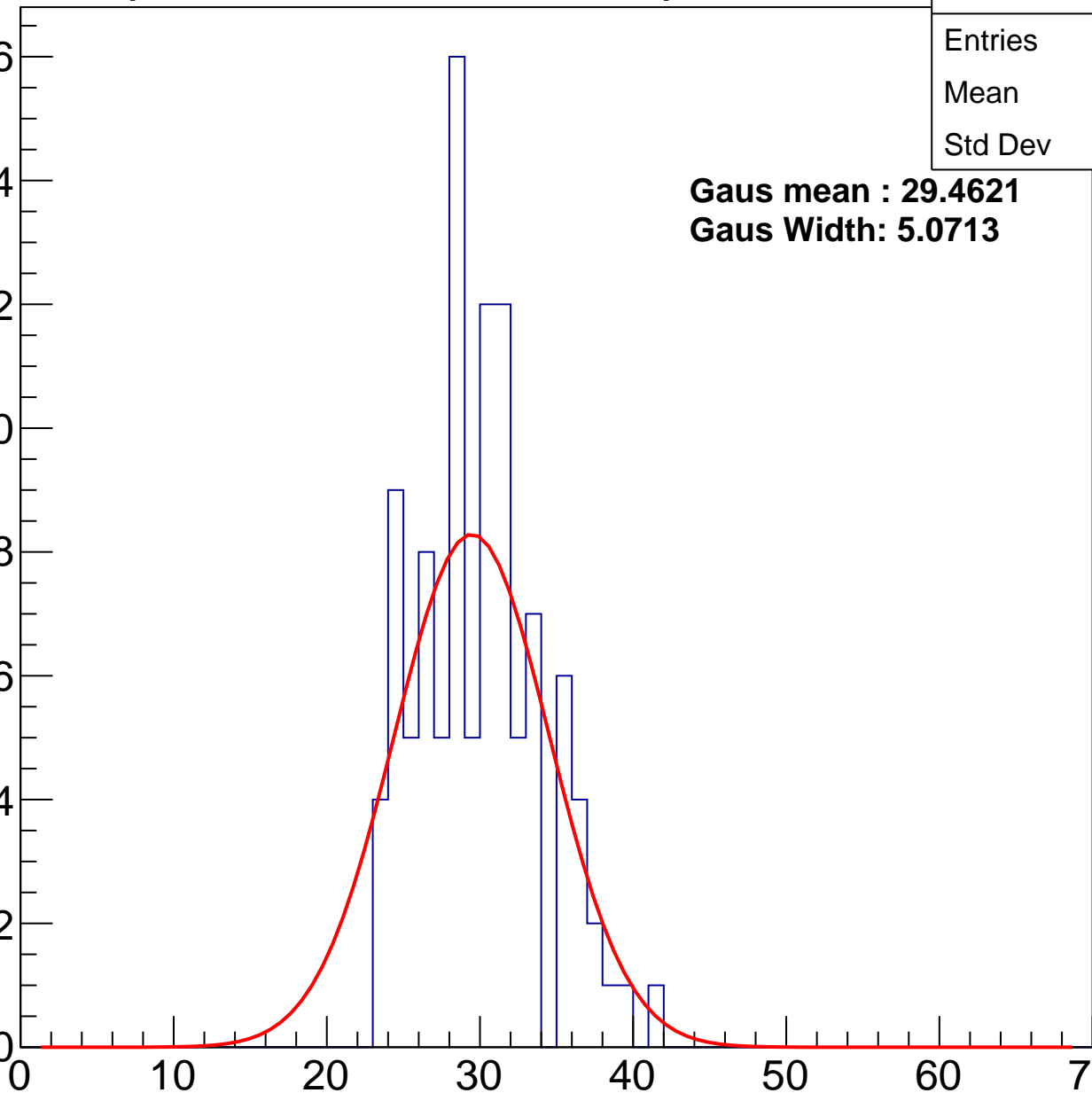
Gaus mean : 29.4621

Gaus Width: 5.0713

Entry

16
14
12
10
8
6
4
2
0

ampl



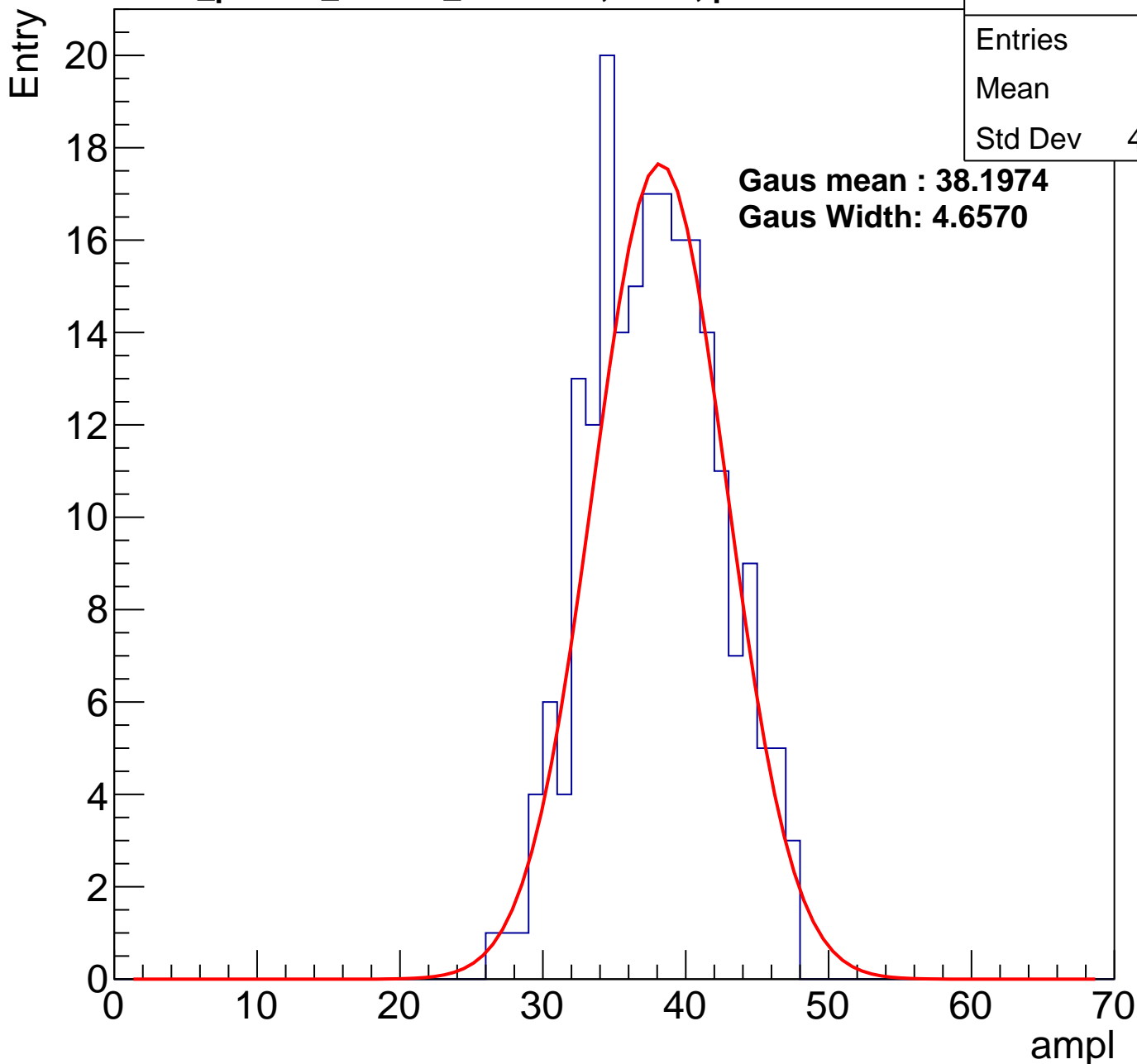
B0L101S, U14-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	211
Mean	37.4
Std Dev	4.447

Gaus mean : 38.1974

Gaus Width: 4.6570



B0L101S, U14-ch100, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	81
Mean	44.72
Std Dev	3.662

Gaus mean : 45.0540

Gaus Width: 3.8496

10

8

6

4

2

0

0

10

20

30

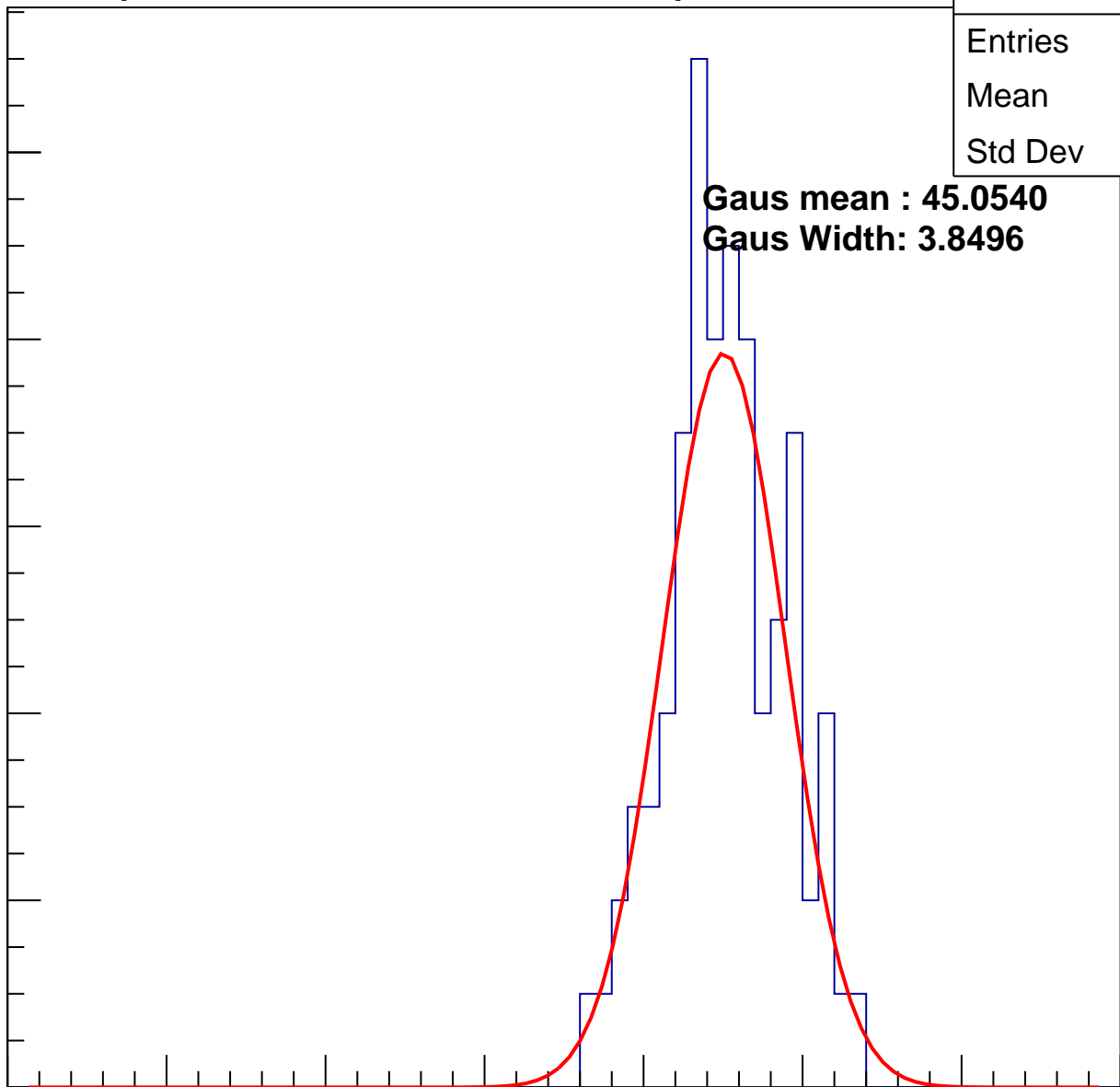
40

50

60

70

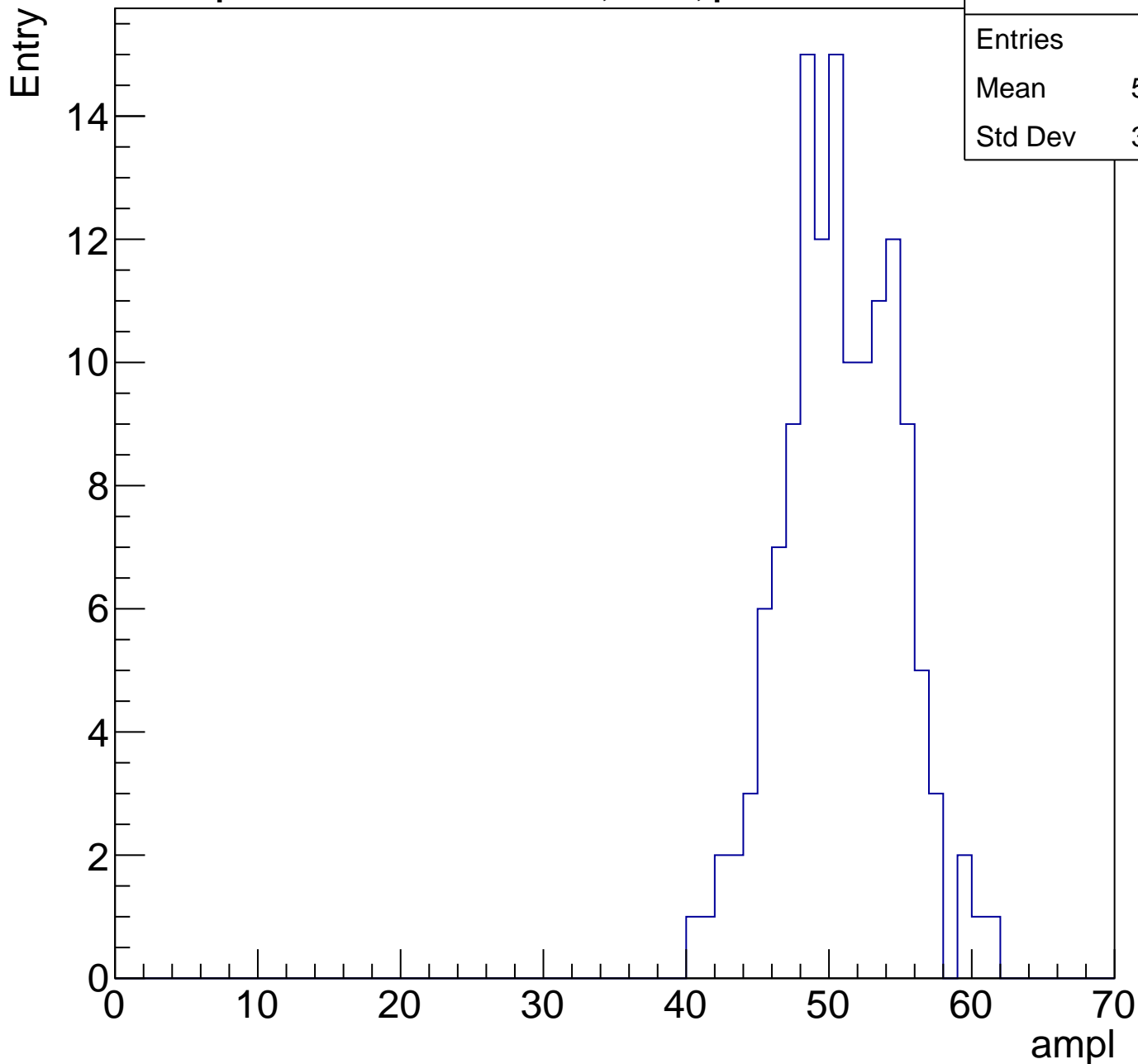
ampl



B0L101S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	50.38
Std Dev	3.995



B0L101S, U14-ch100, adc4

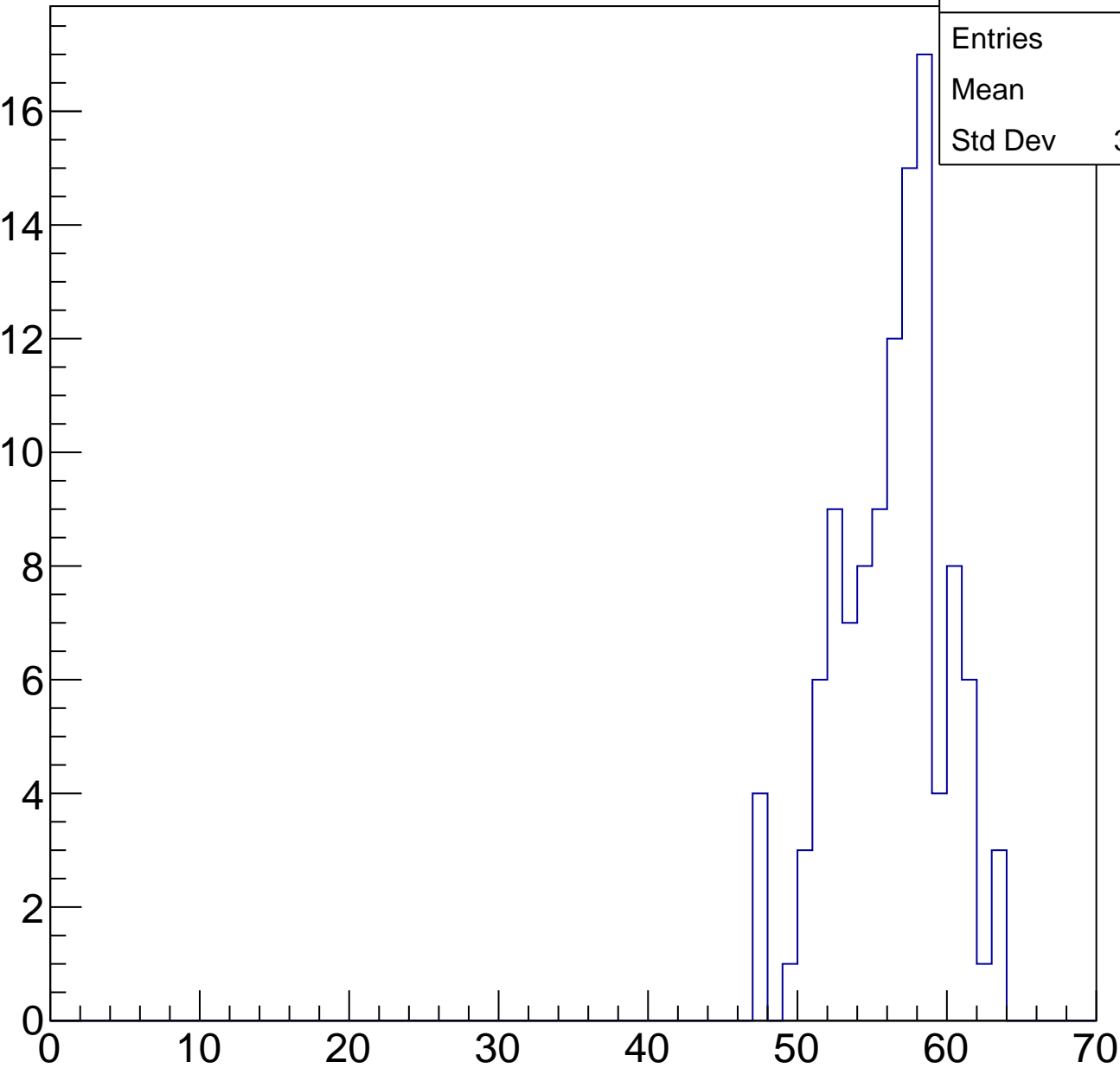
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	55.8
Std Dev	3.587

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	65
Mean	58.34
Std Dev	10.62

2

4

6

8

10

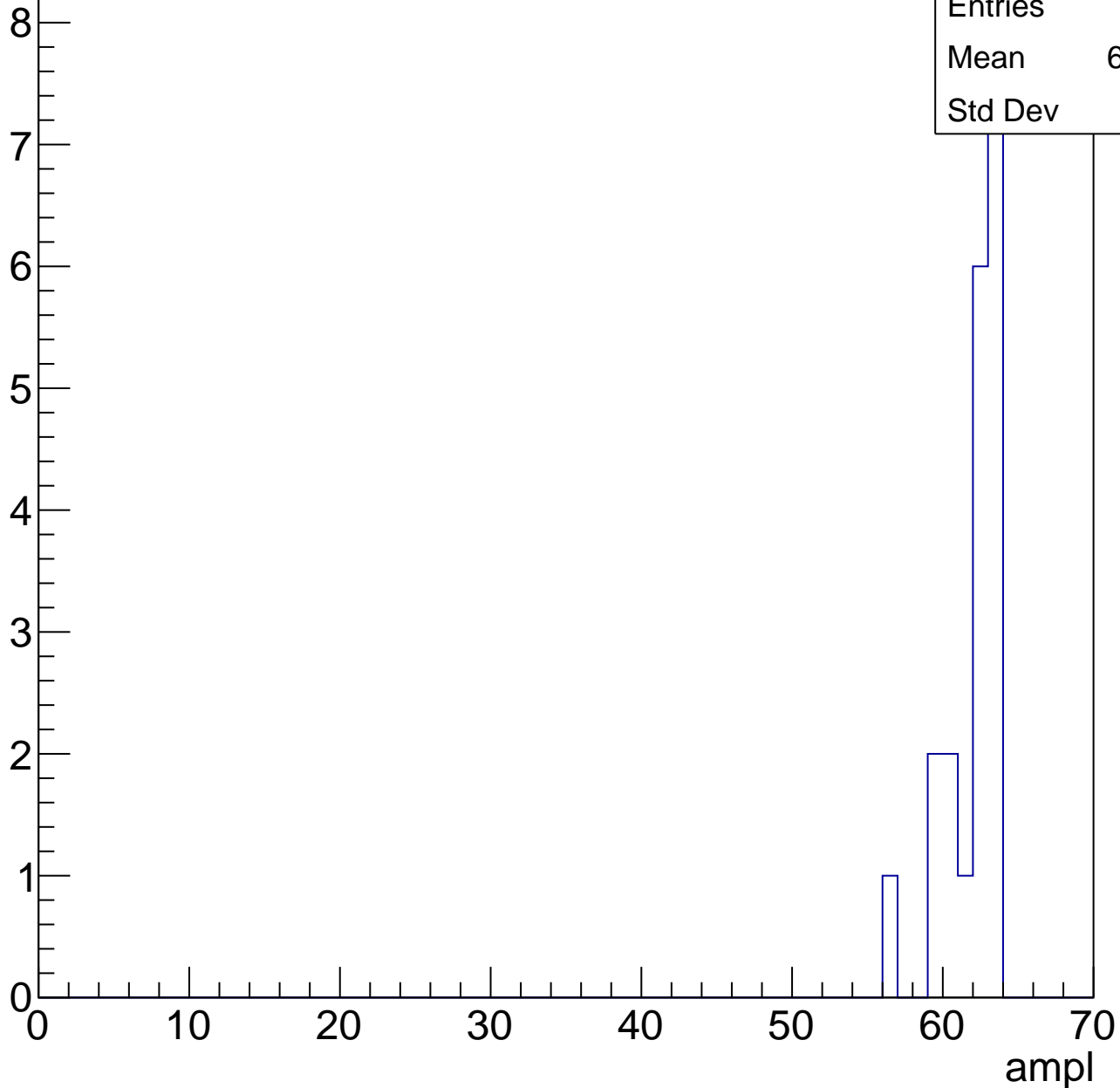
12

B0L101S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

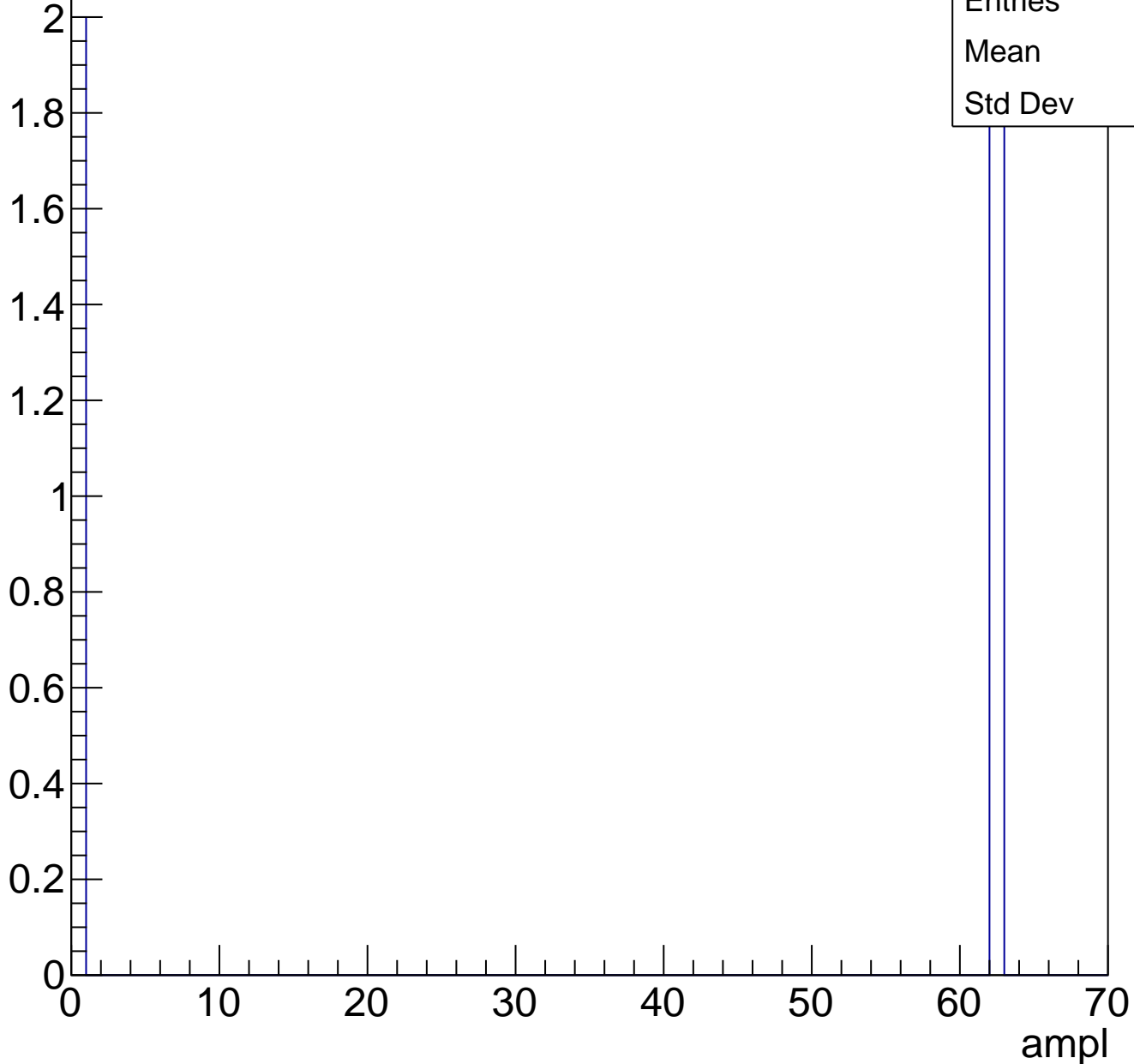
Entries	20
Mean	61.55
Std Dev	1.83



B0L101S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch101, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	30.88
Std Dev	3.743

Gaus mean : 31.2012

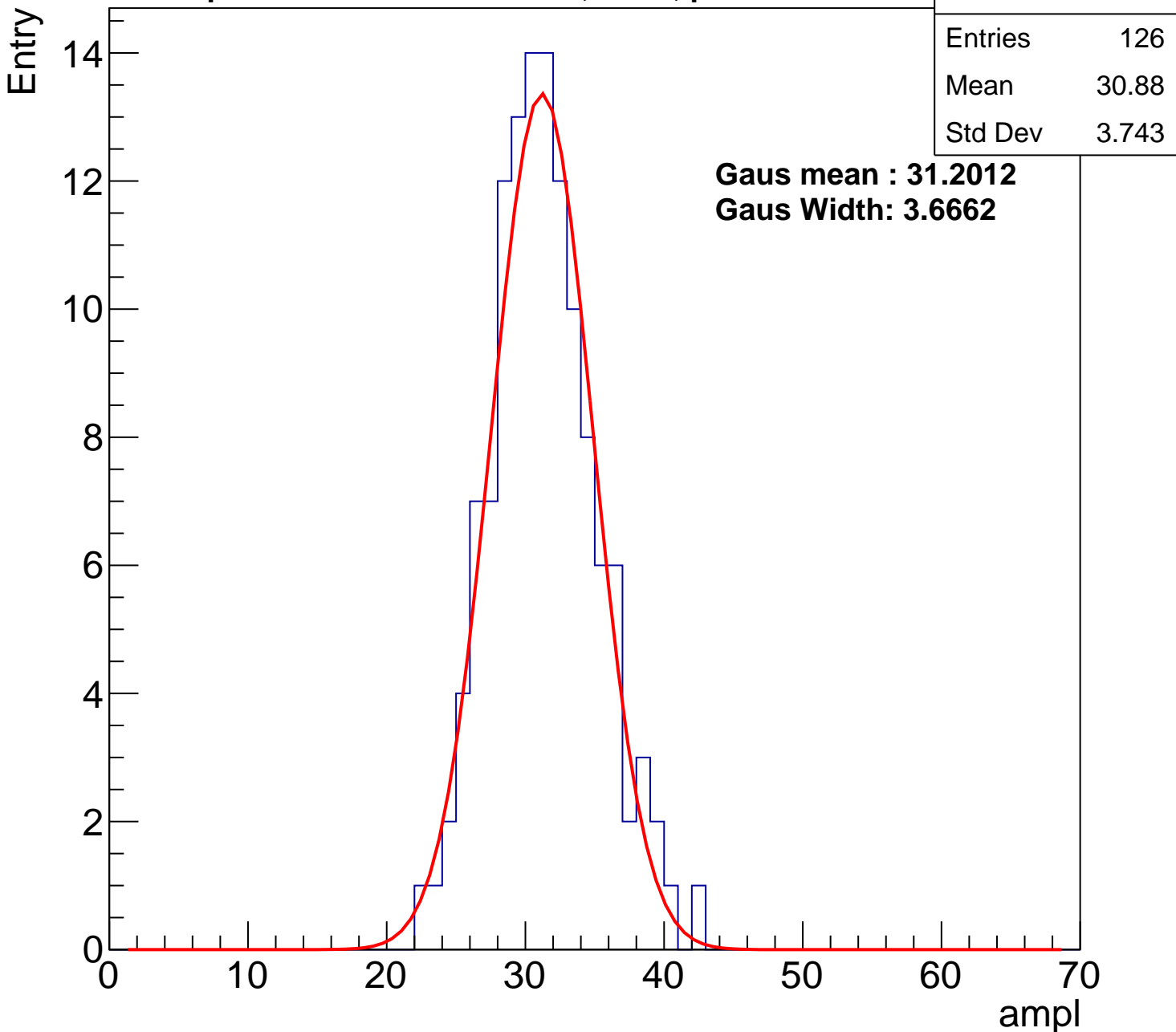
Gaus Width: 3.6662

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch101, adc1

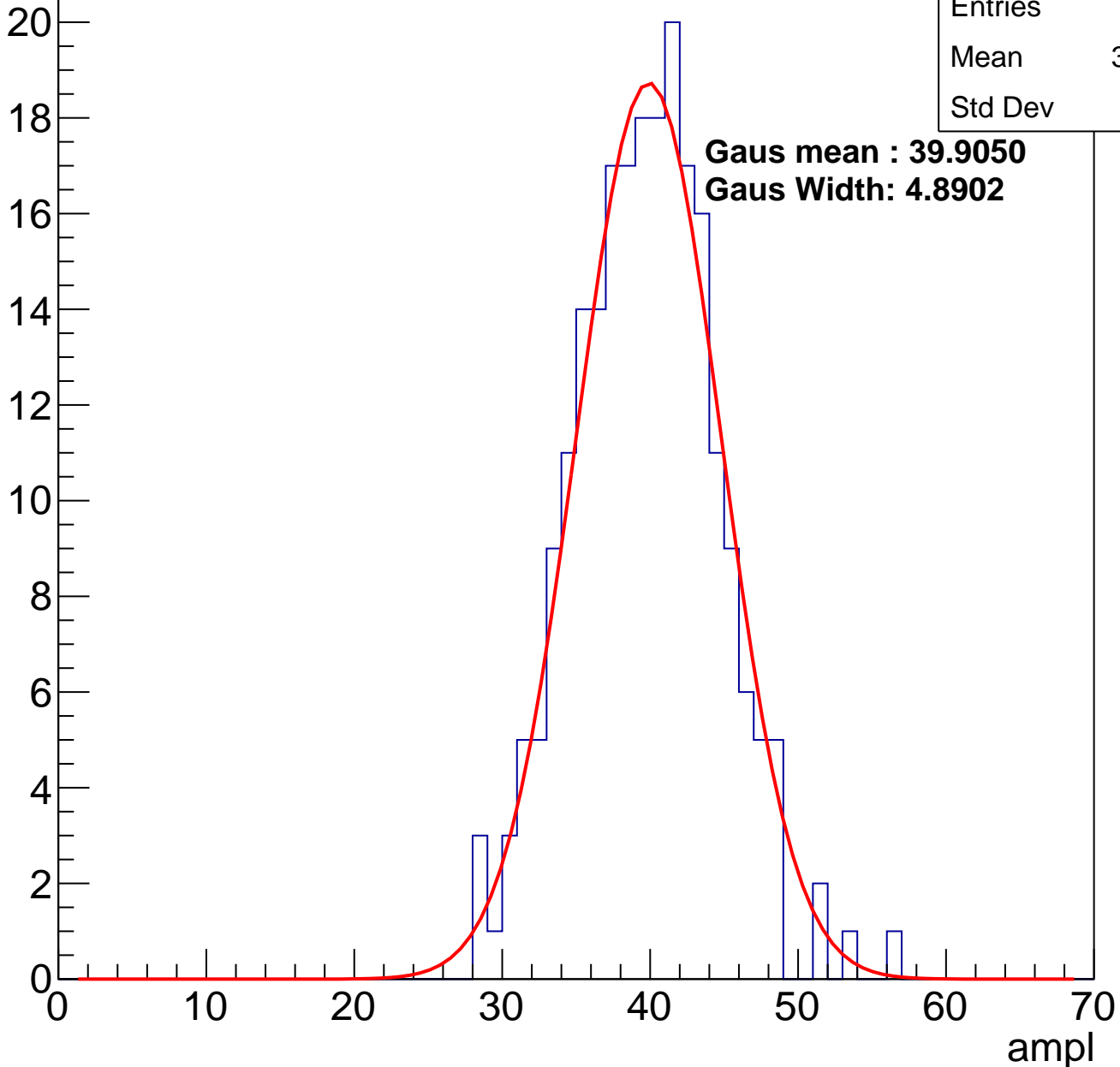
calib_packv5_042523_0143.root, FC#1, port C1

Entries	228
Mean	39.27
Std Dev	4.77

Gaus mean : 39.9050

Gaus Width: 4.8902

Entry



B0L101S, U14-ch101, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	50
Mean	46.7
Std Dev	2.632

Gaus mean : 46.9799

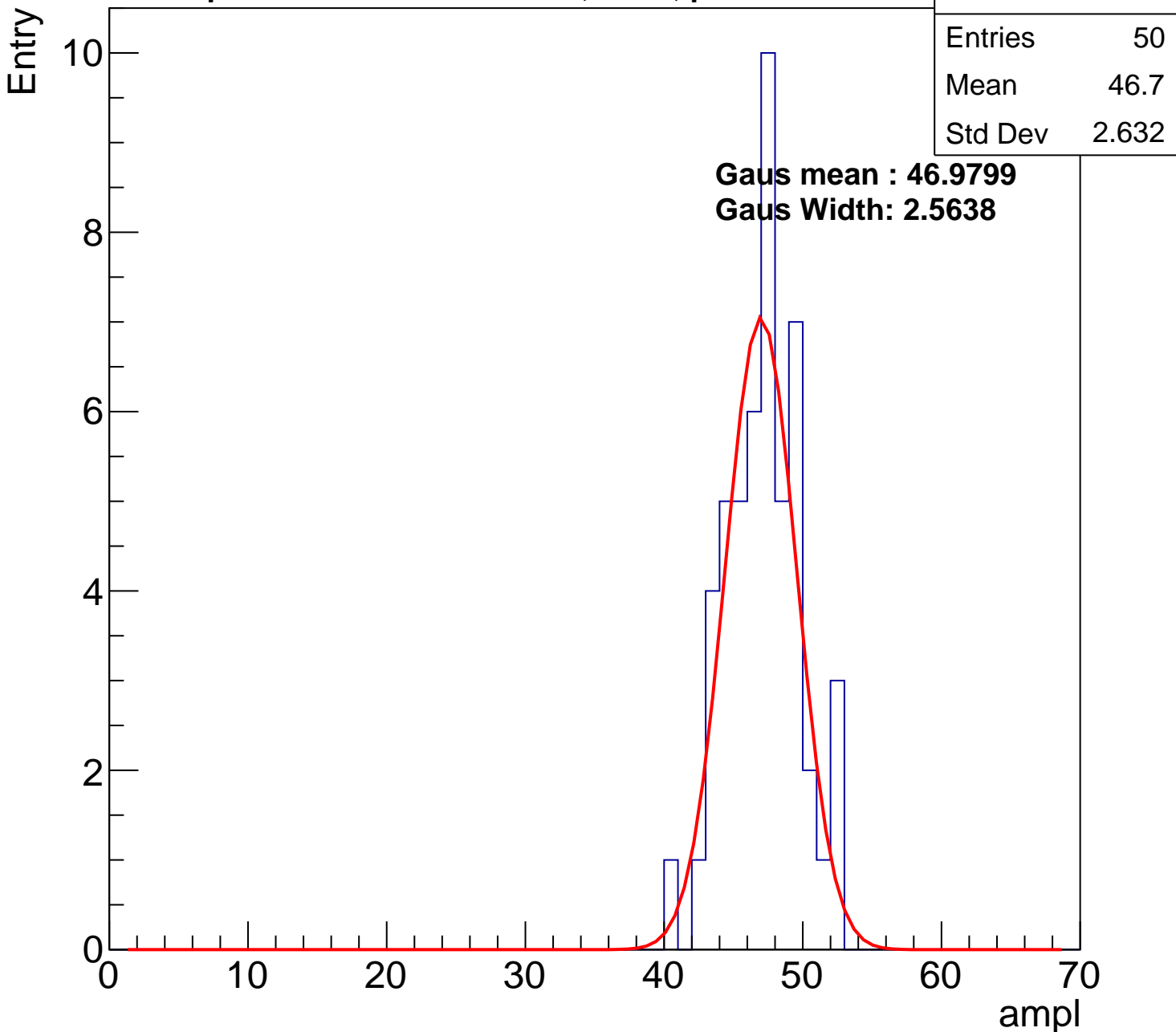
Gaus Width: 2.5638

Entry

10
8
6
4
2
0

ampl

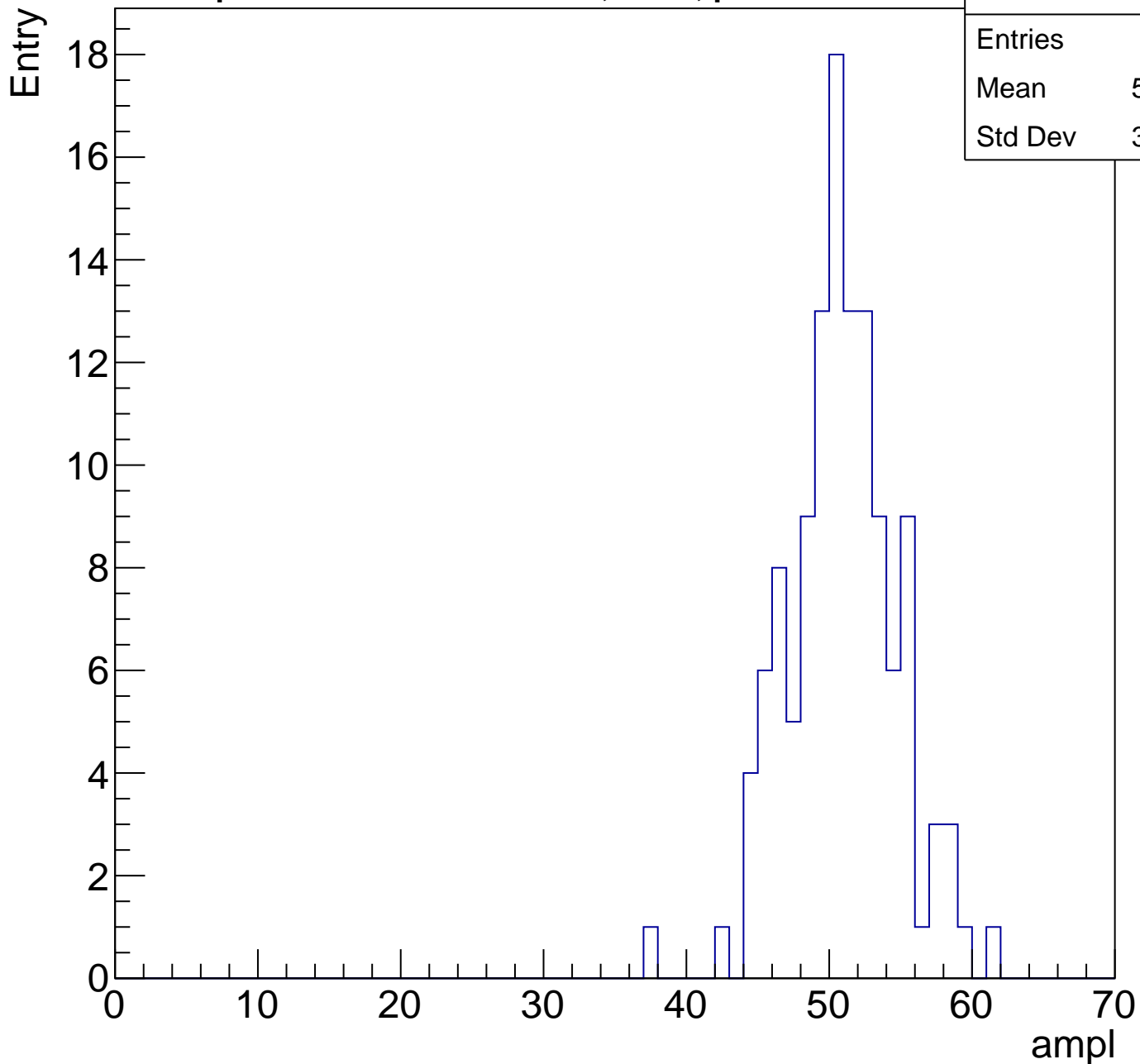
0 10 20 30 40 50 60 70



B0L101S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	50.43
Std Dev	3.778



B0L101S, U14-ch101, adc4

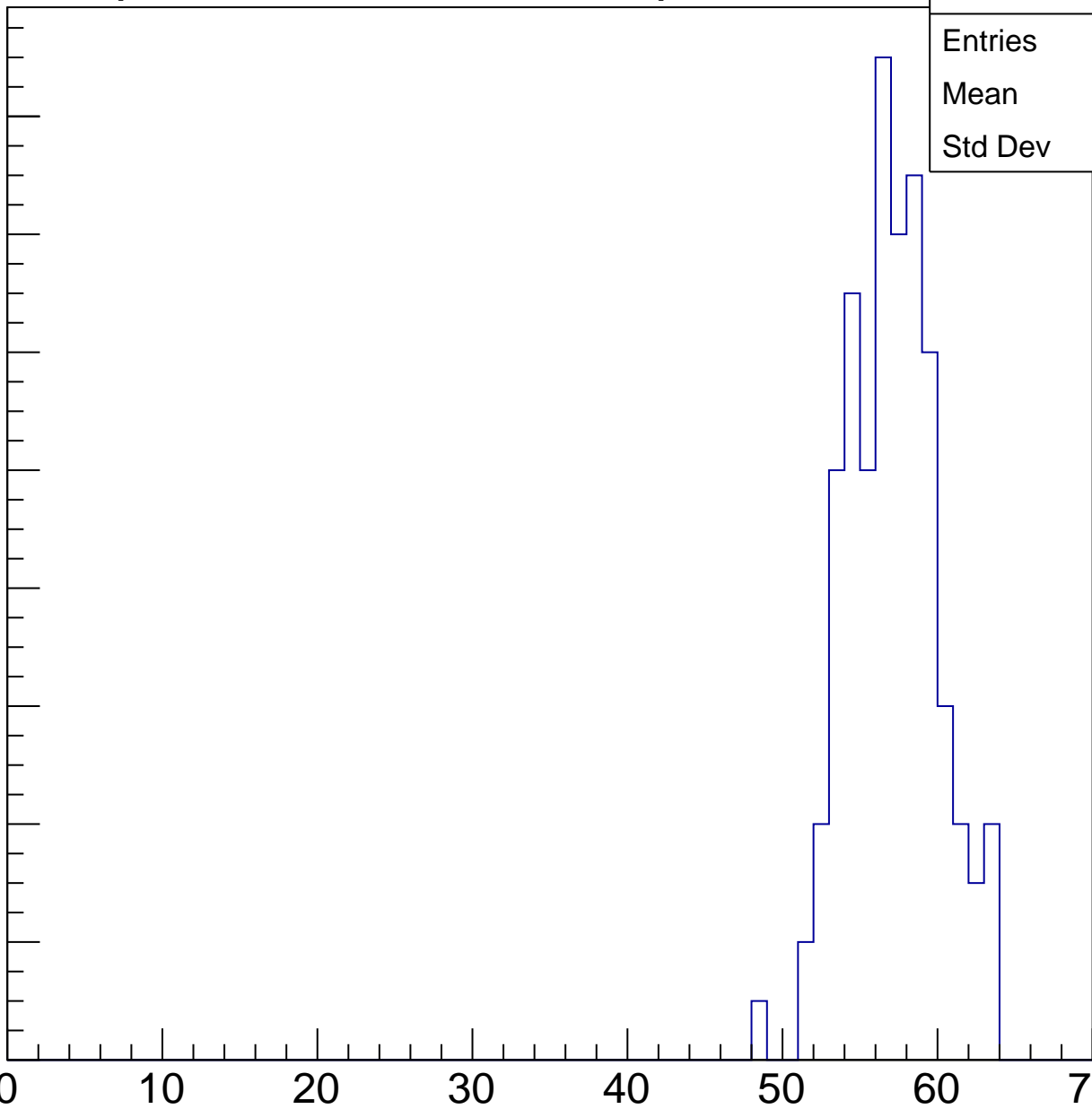
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	56.61
Std Dev	2.903

ampl



B0L101S, U14-ch101, adc5

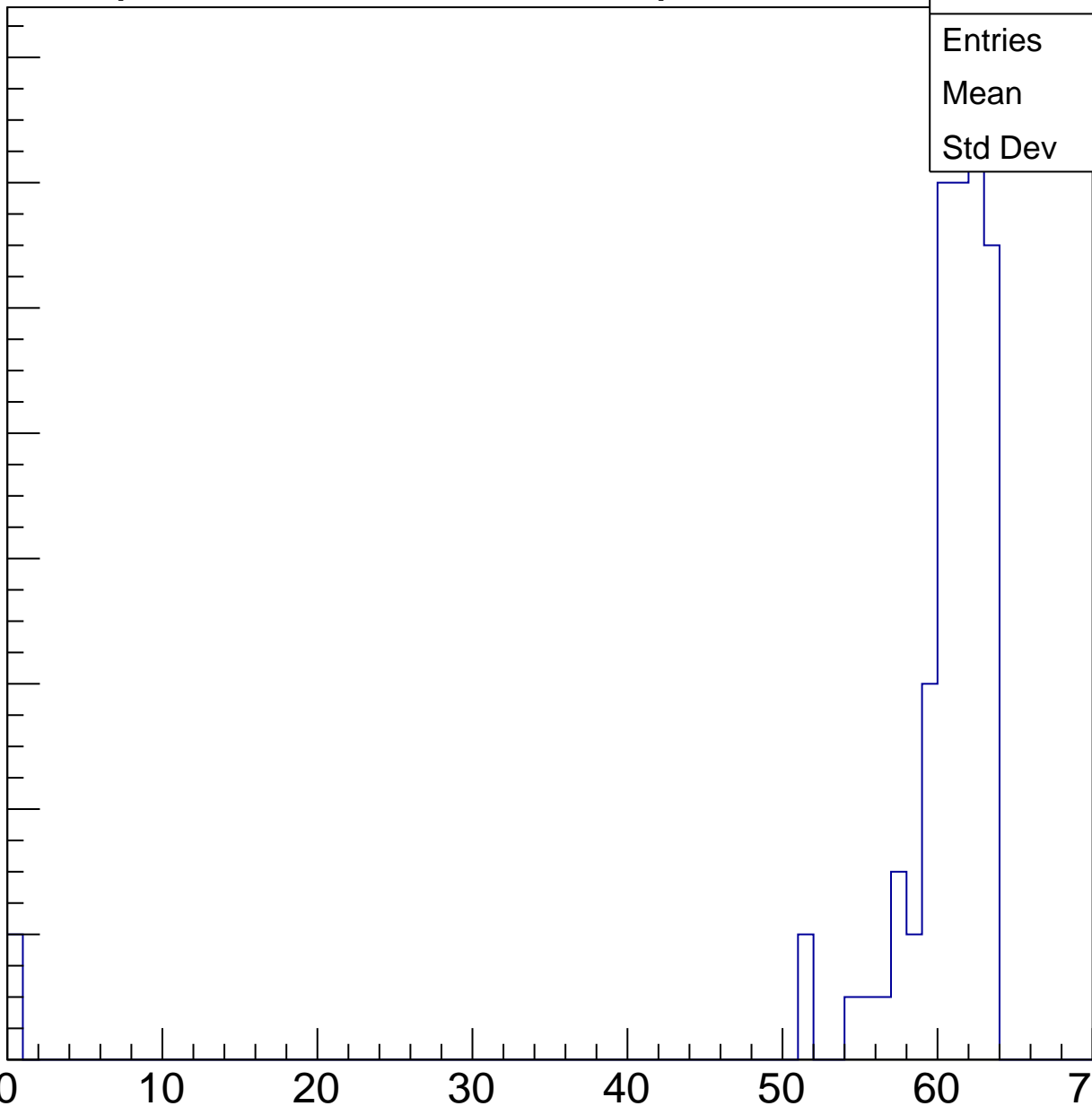
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	75
Mean	58.84
Std Dev	10.05

ampl



B0L101S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

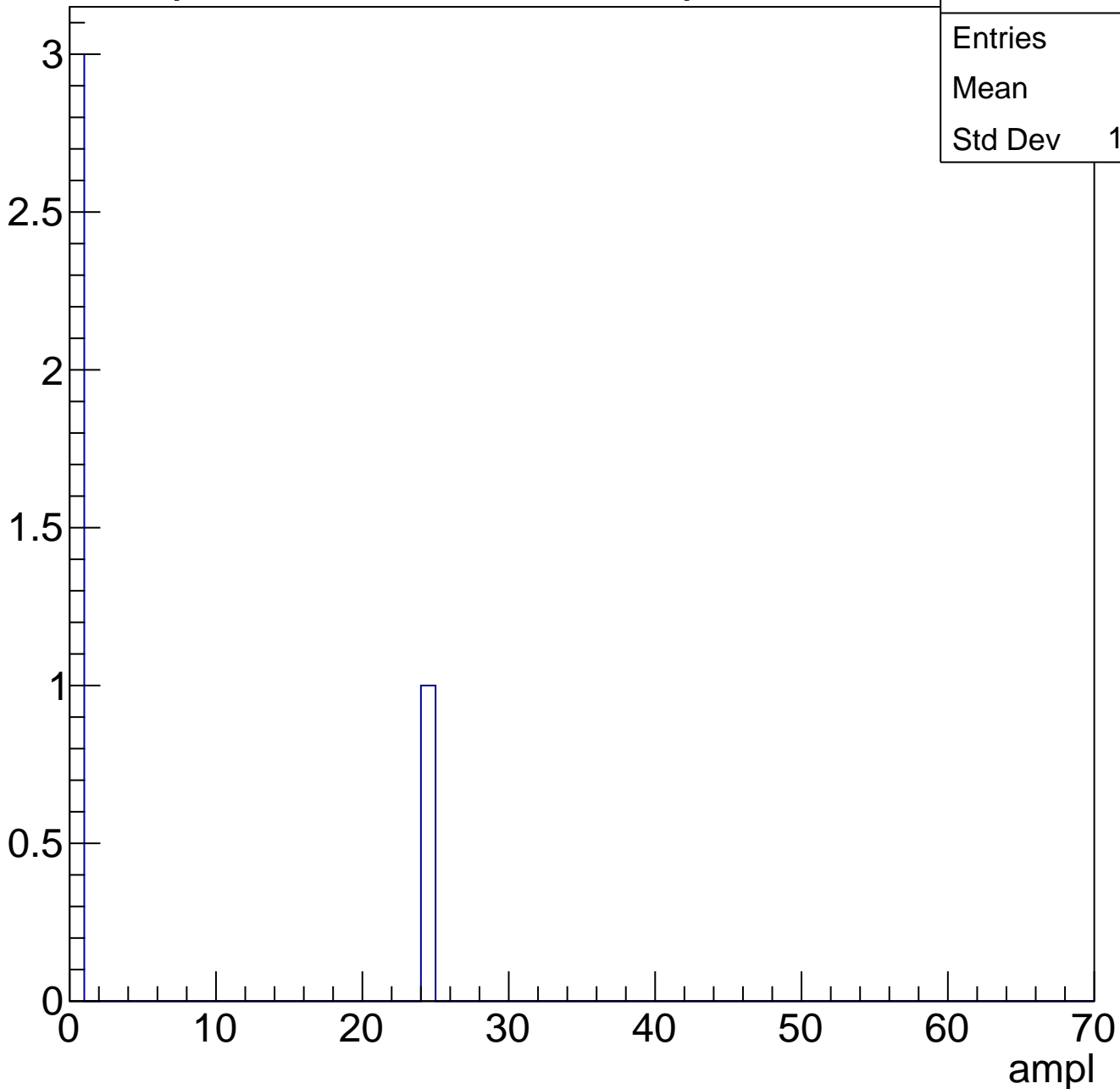
Entry



B0L101S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch102, adc0

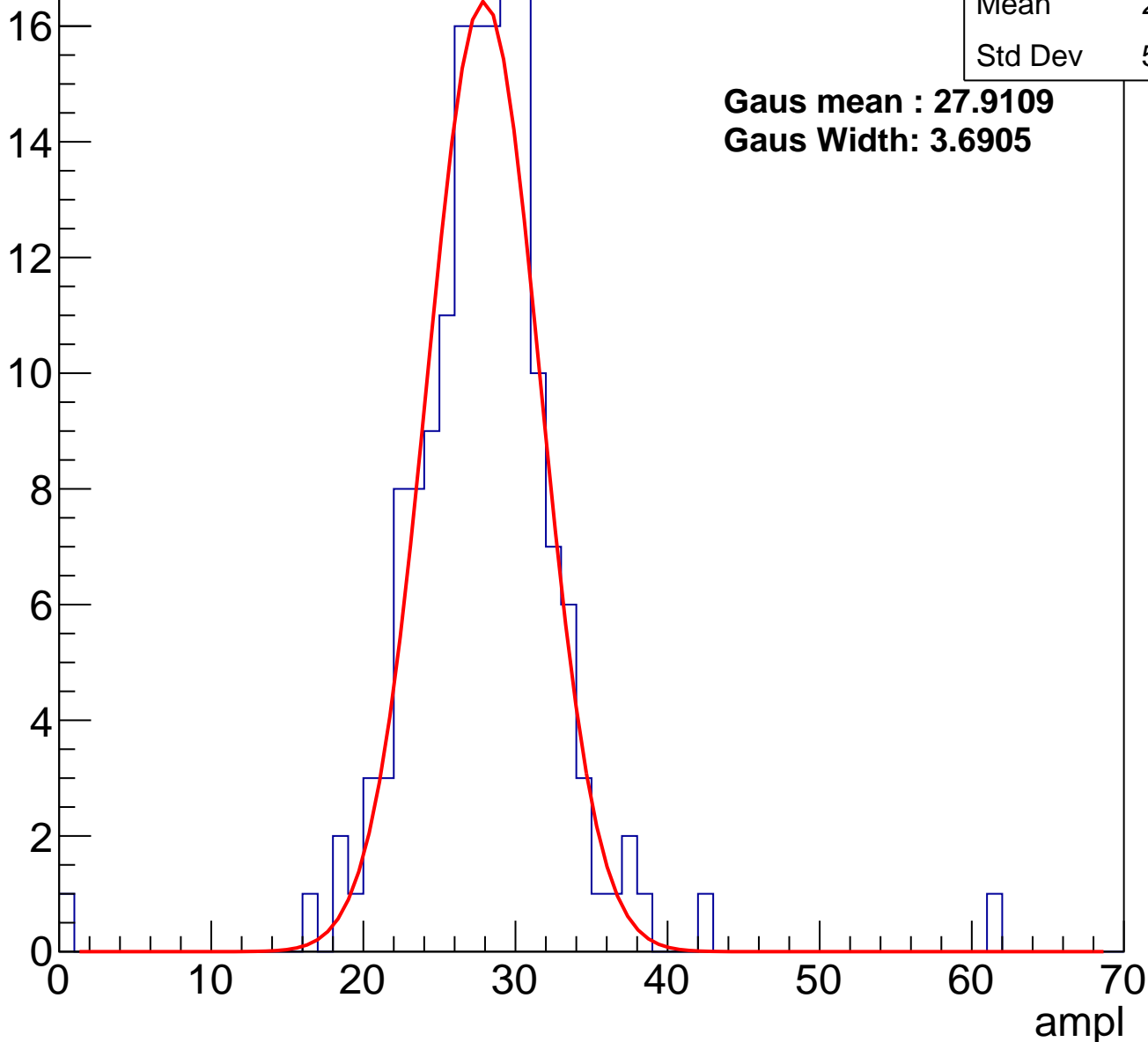
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	27.56
Std Dev	5.289

Gaus mean : 27.9109

Gaus Width: 3.6905

Entry



B0L101S, U14-ch102, adc1

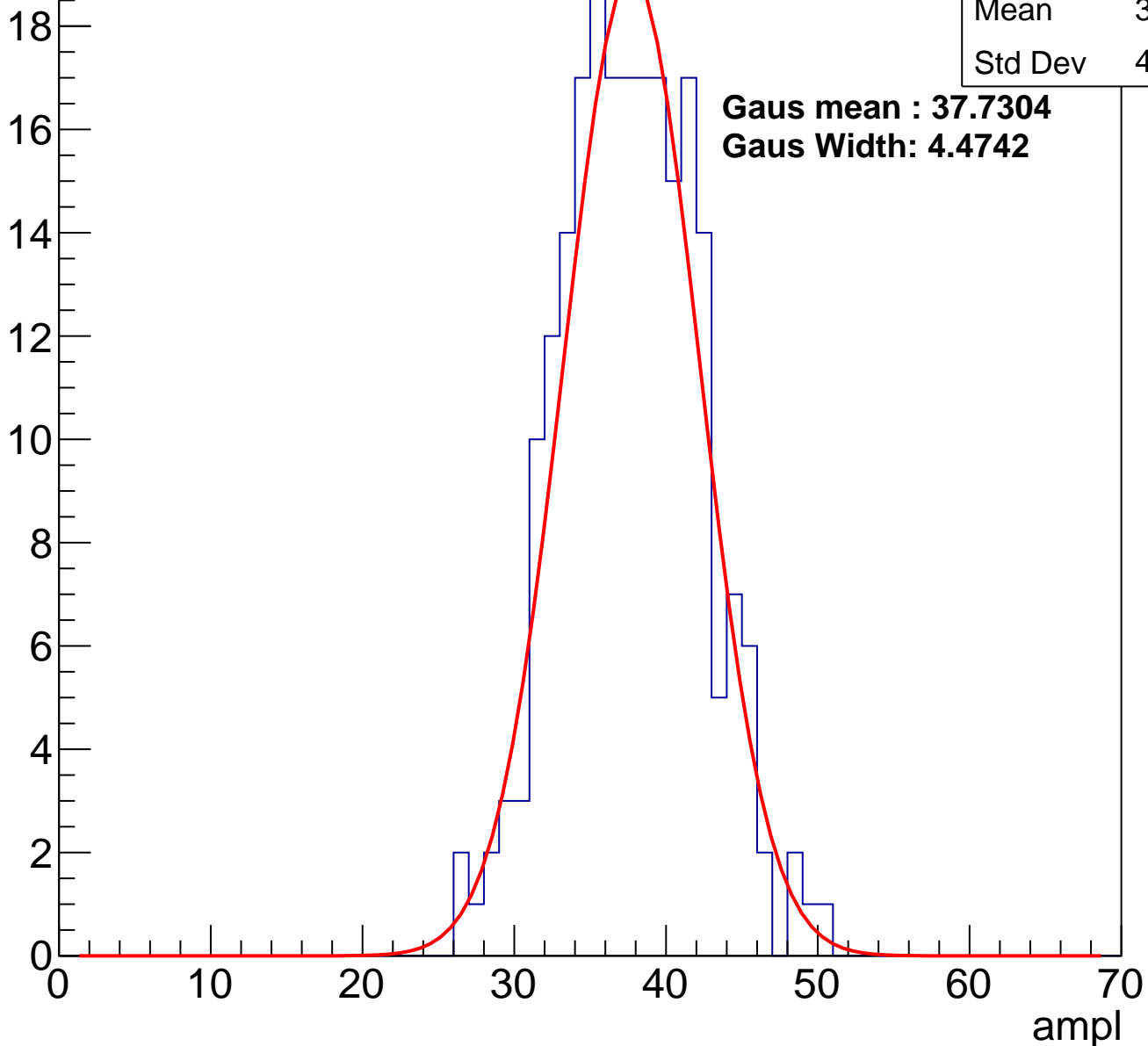
calib_packv5_042523_0143.root, FC#1, port C1

Entries	221
Mean	37.22
Std Dev	4.476

Gaus mean : 37.7304

Gaus Width: 4.4742

Entry



B0L101S, U14-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	44
Mean	42.91
Std Dev	3.866

Gaus mean : 43.3312

Gaus Width: 4.8541

8

10

8

6

4

2

0

2

4

6

8

10

12

14

16

18

20

22

24

26

28

30

32

34

36

38

40

42

44

46

48

50

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54

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58

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574

576

578

580

582

584

586

588

590

592

594

596

598

600

602

604

606

608

610

612

614

616

618

B0L101S, U14-ch102, adc3

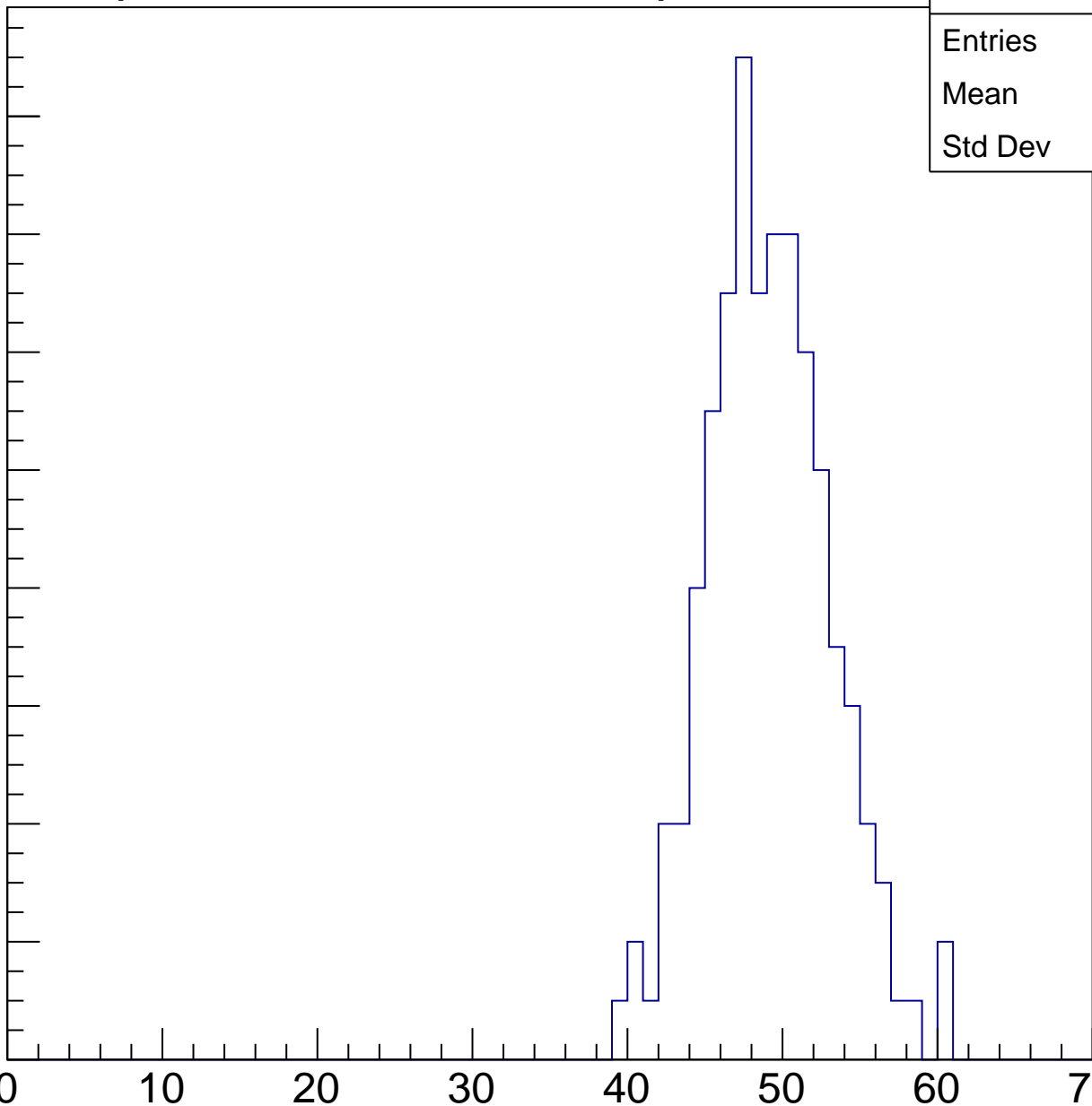
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	148
Mean	48.68
Std Dev	3.956

ampl



B0L101S, U14-ch102, adc4

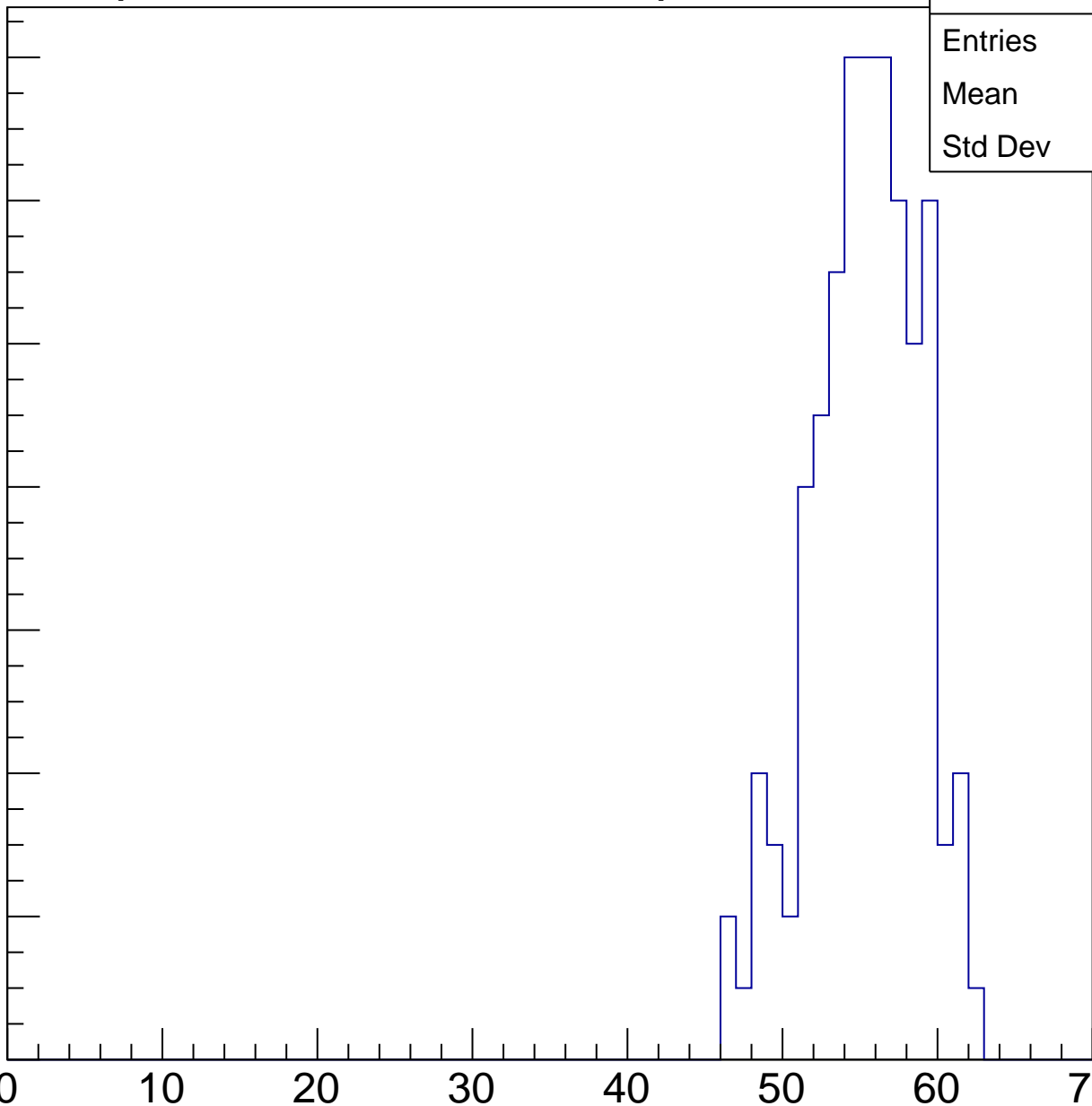
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	124
Mean	54.88
Std Dev	3.428

ampl



B0L101S, U14-ch102, adc5

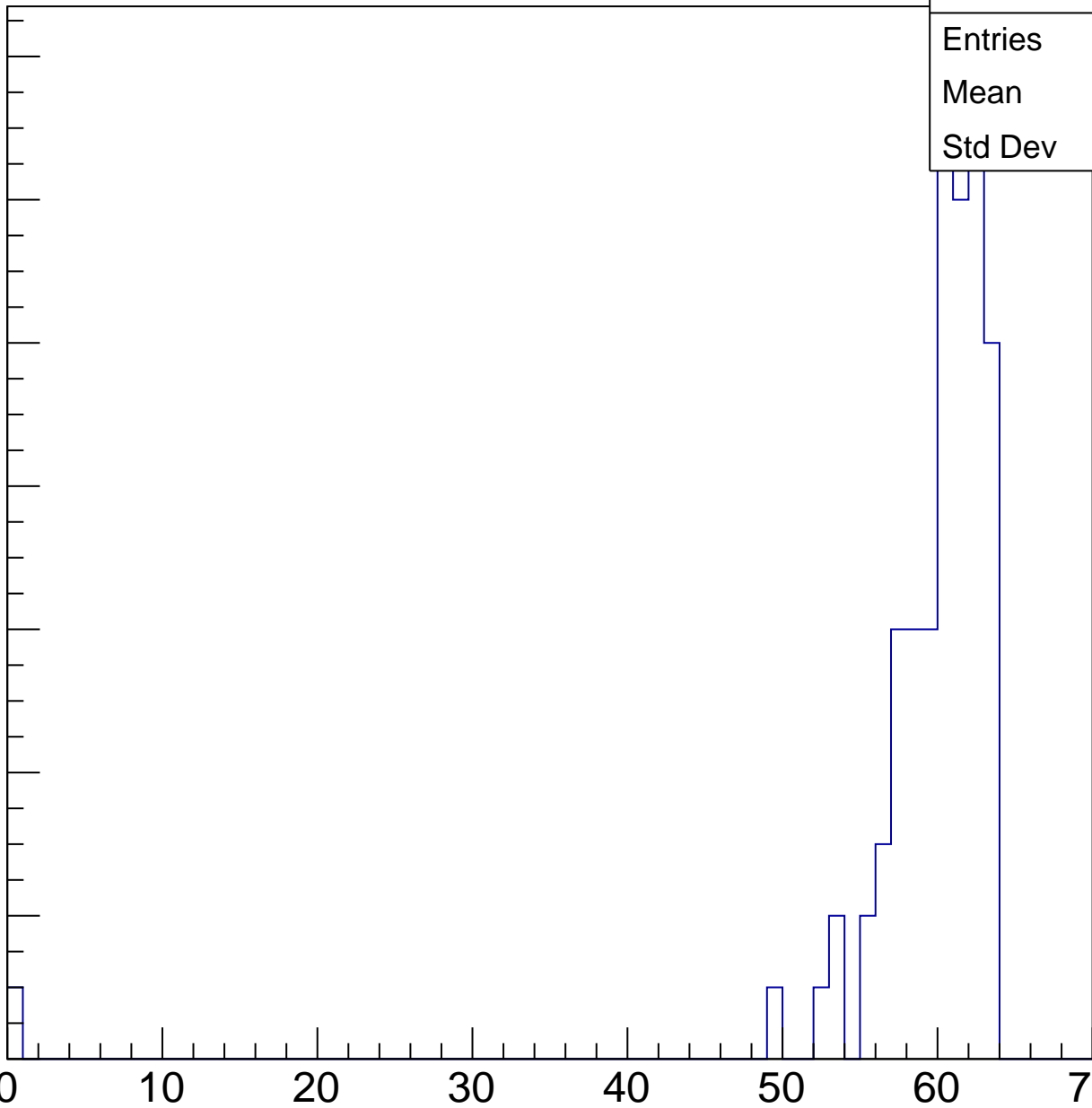
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.95
Std Dev	7.331

ampl

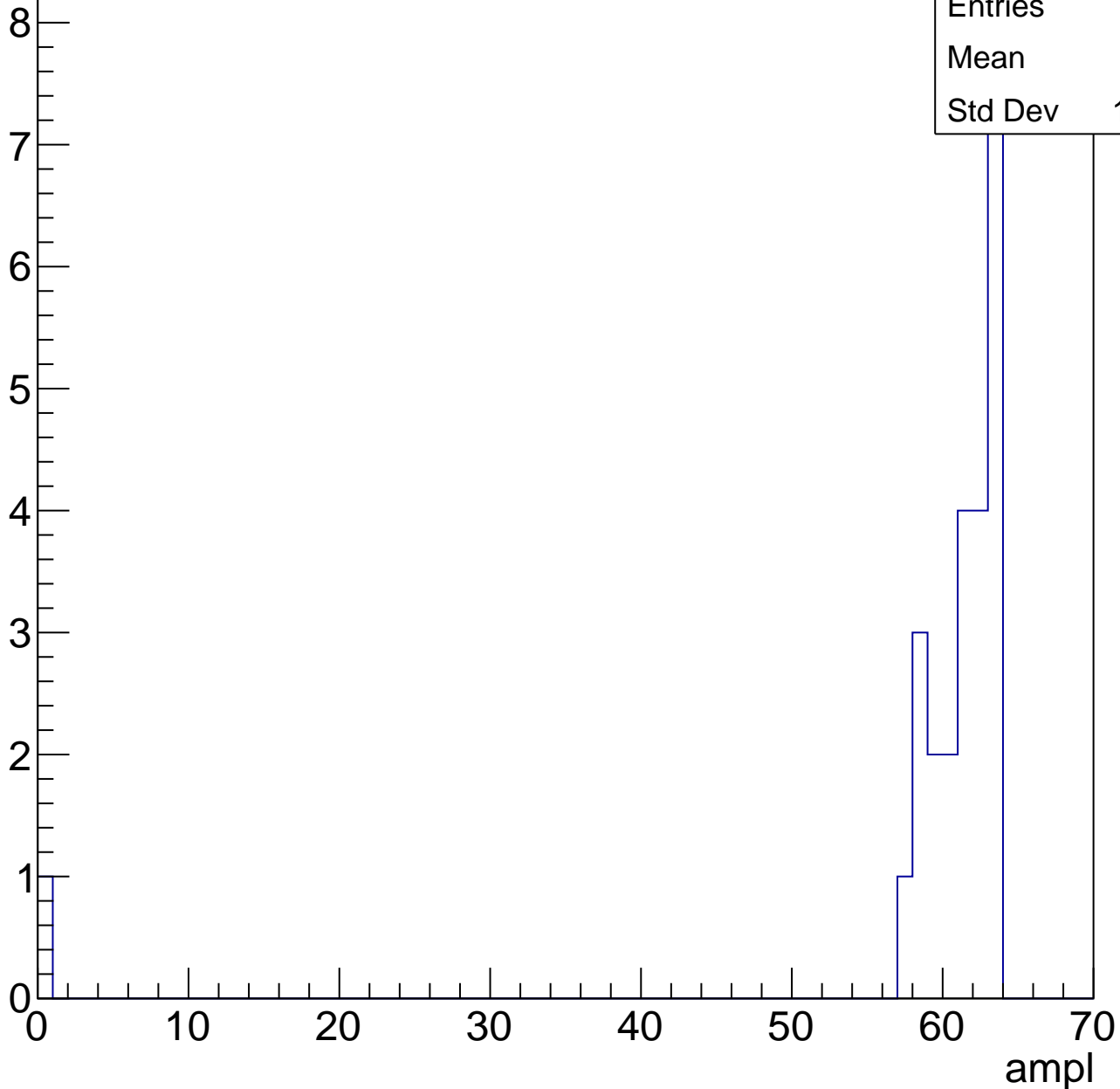


B0L101S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	25
Mean	58.6
Std Dev	12.11



B0L101S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch103, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.94
Std Dev	4.441

Gaus mean : 30.8366

Gaus Width: 3.6710

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

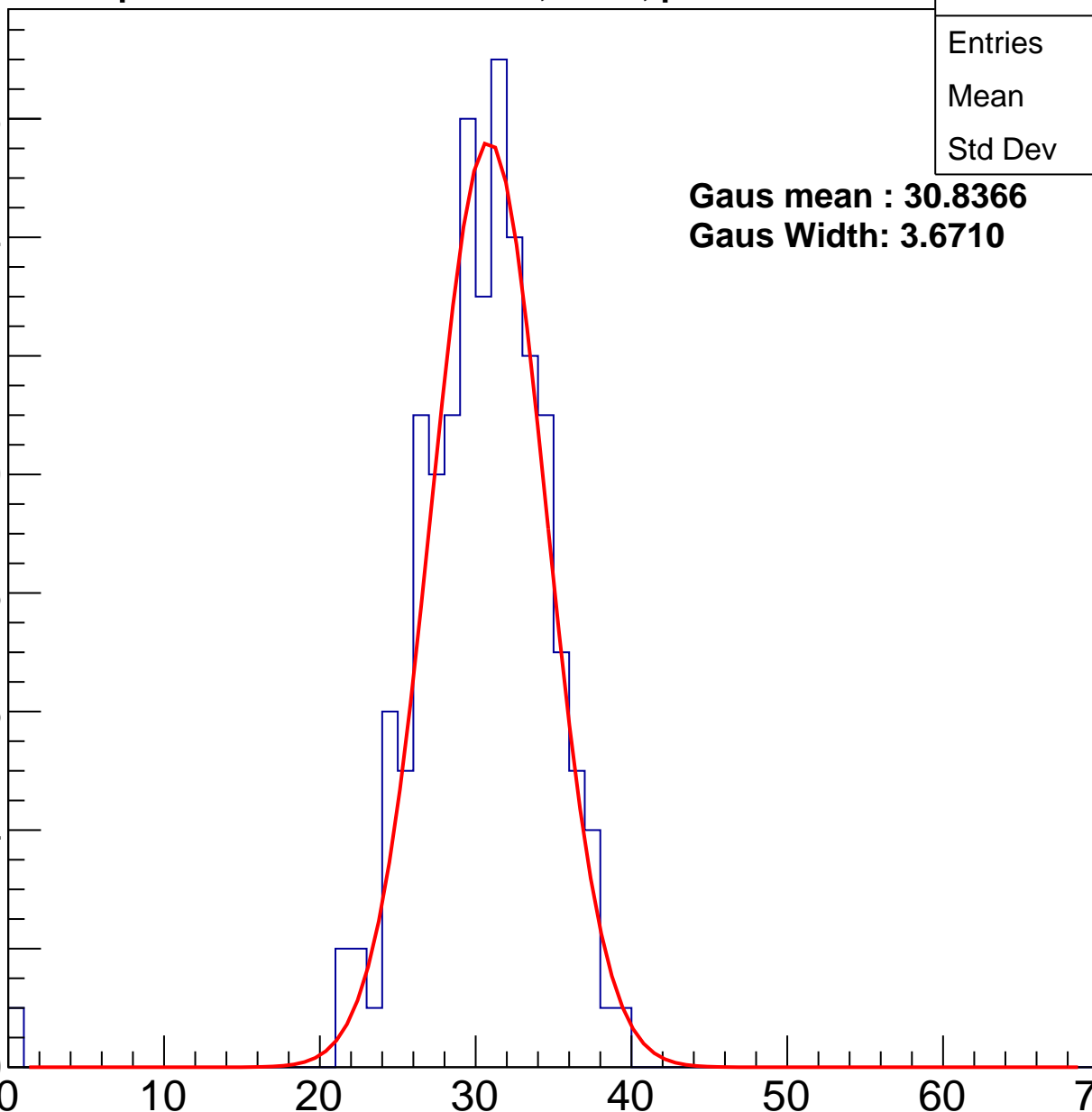
40

50

60

70

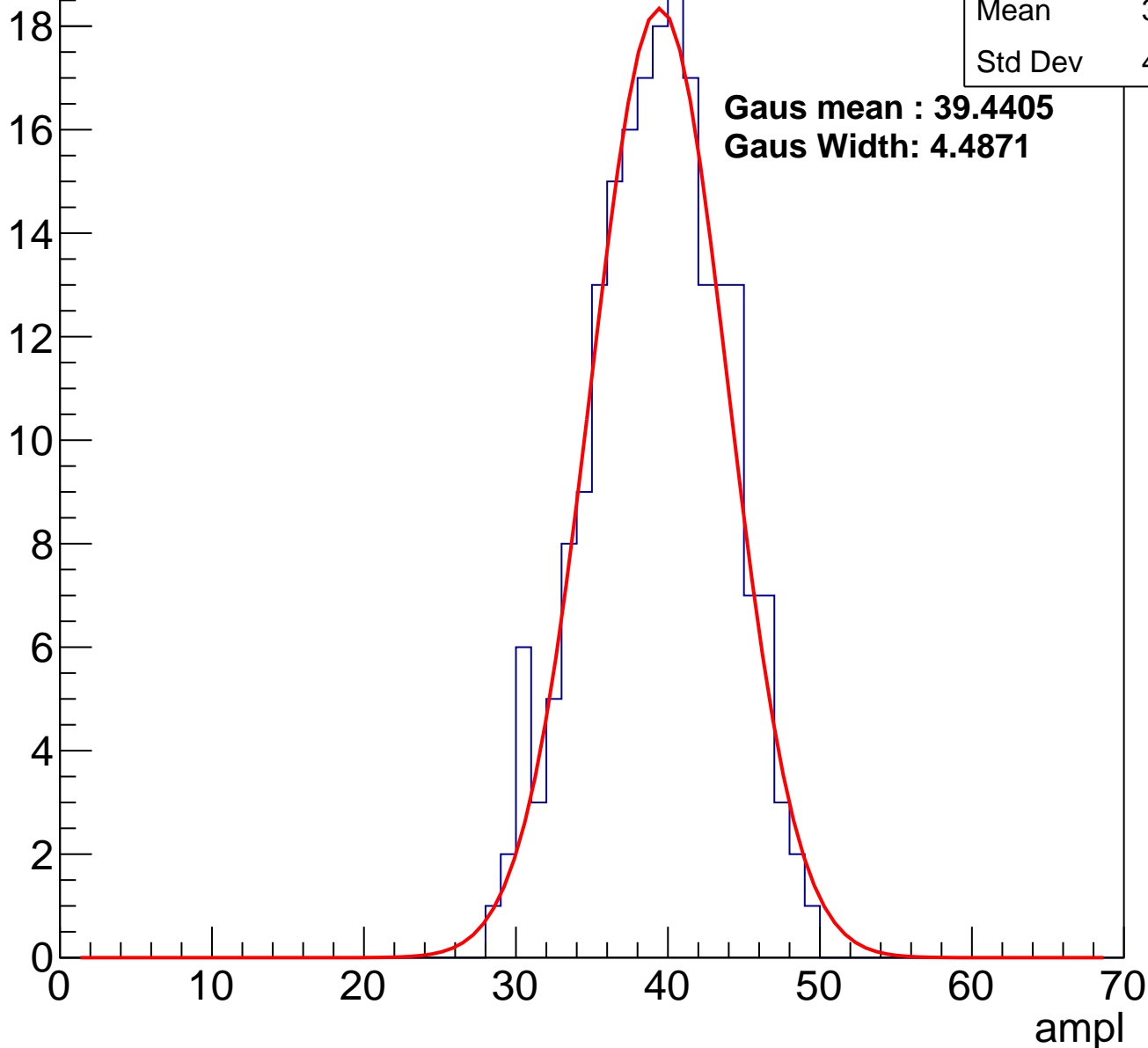
ampl



B0L101S, U14-ch103, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch103, adc2

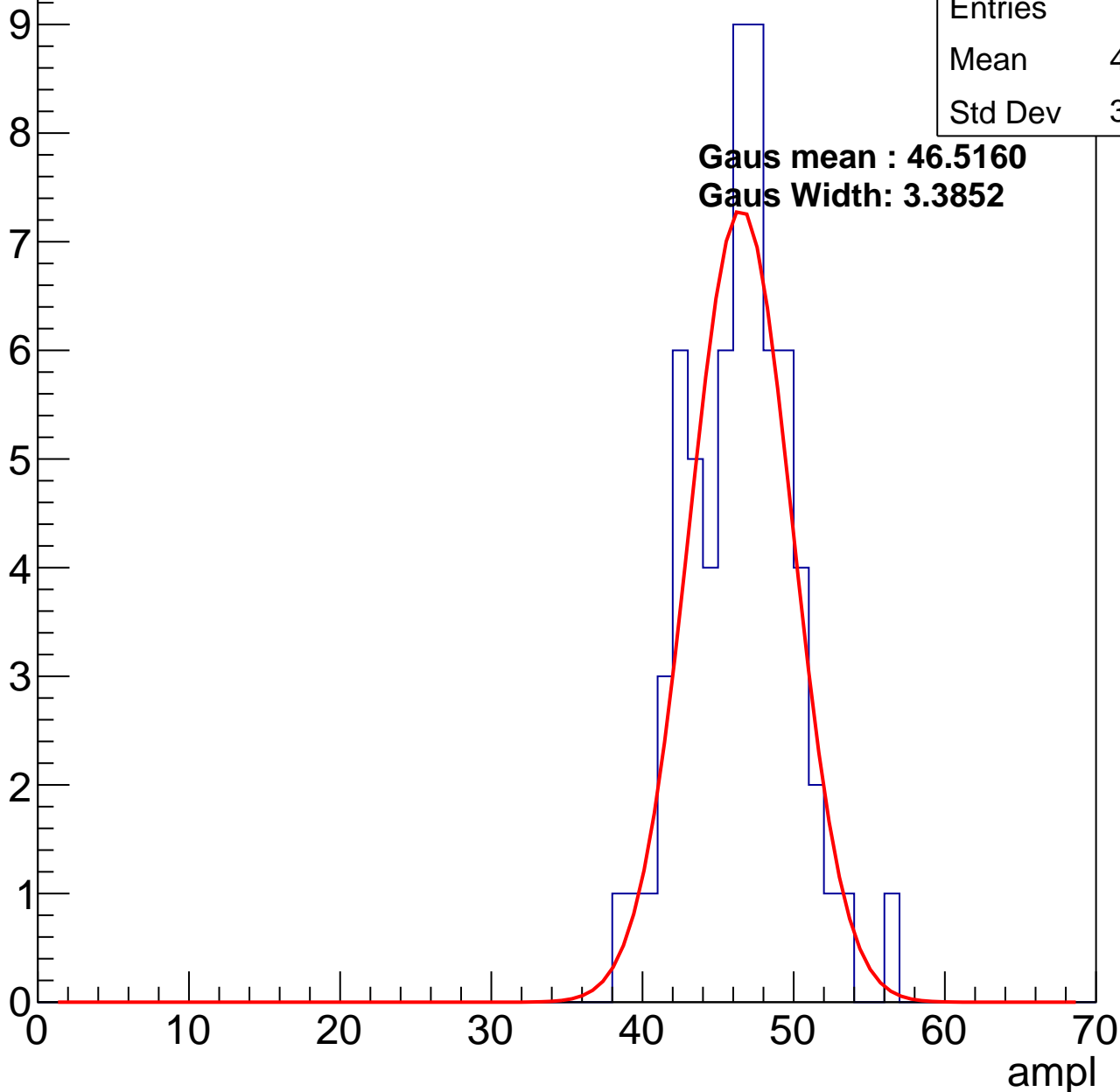
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	66
Mean	45.98
Std Dev	3.422

Gaus mean : 46.5160

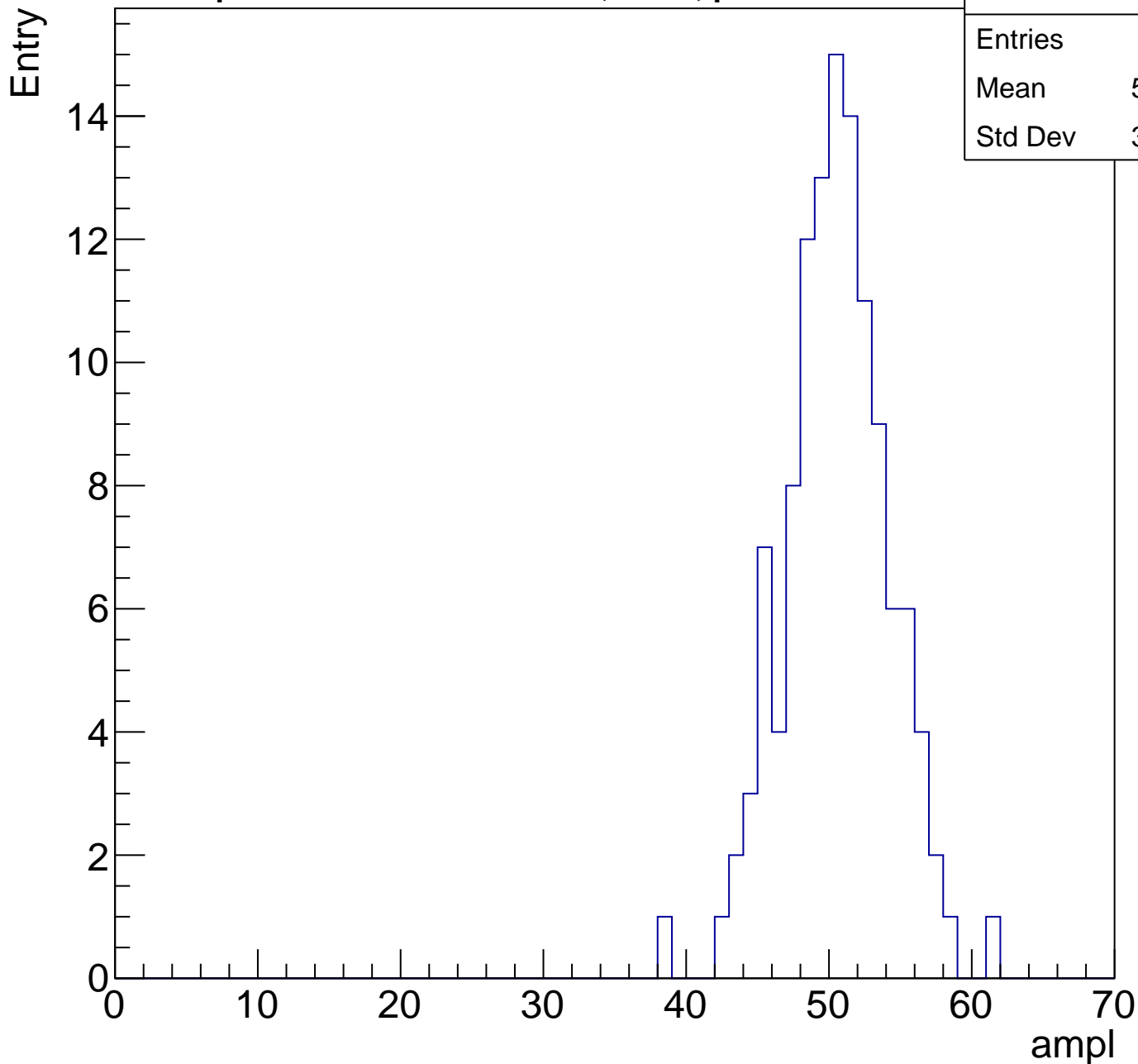
Gaus Width: 3.3852



B0L101S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	50.08
Std Dev	3.648



B0L101S, U14-ch103, adc4

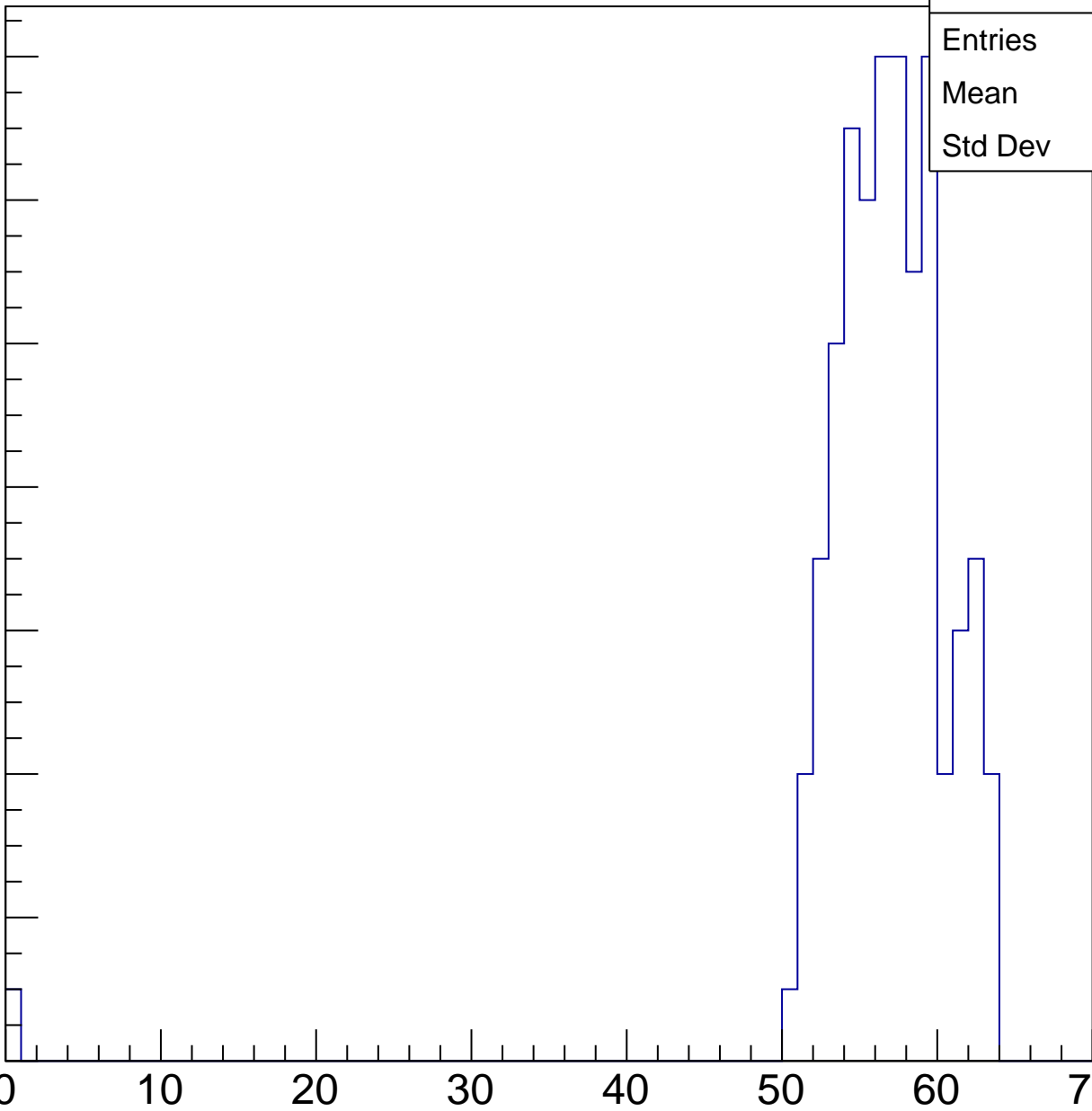
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	56.13
Std Dev	5.993

ampl



B0L101S, U14-ch103, adc5

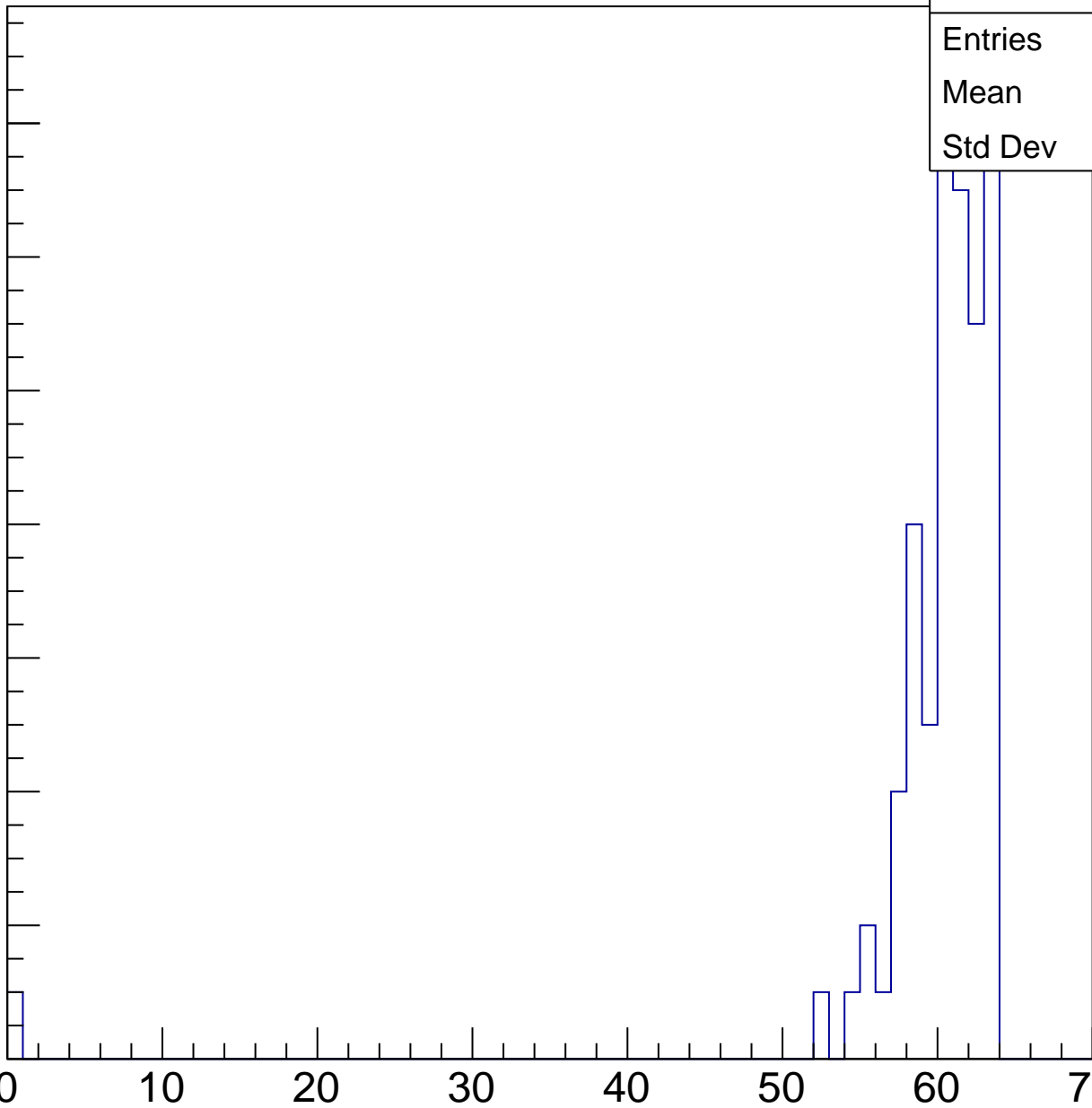
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.42
Std Dev	7.252

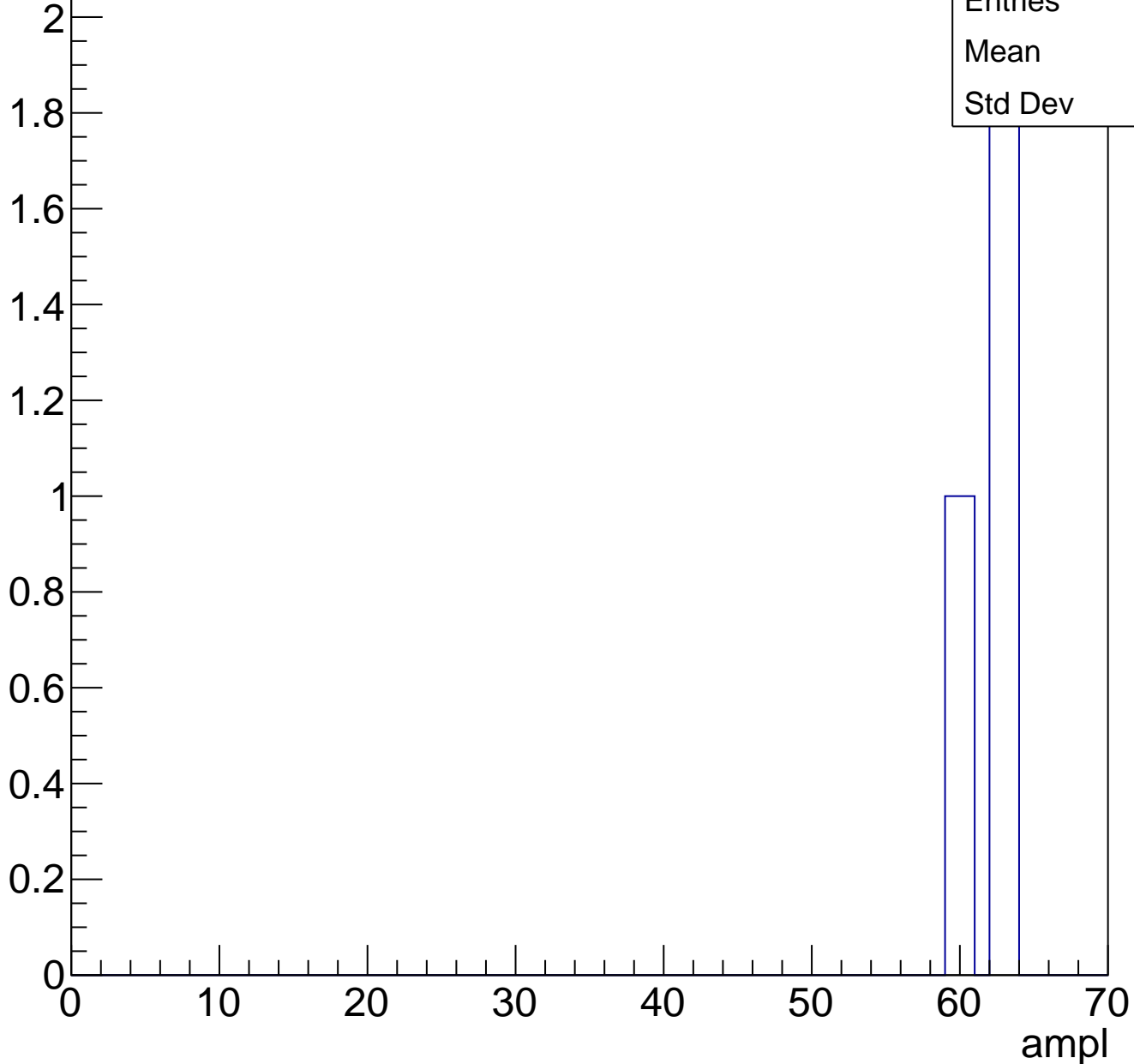
ampl



B0L101S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch104, adc0

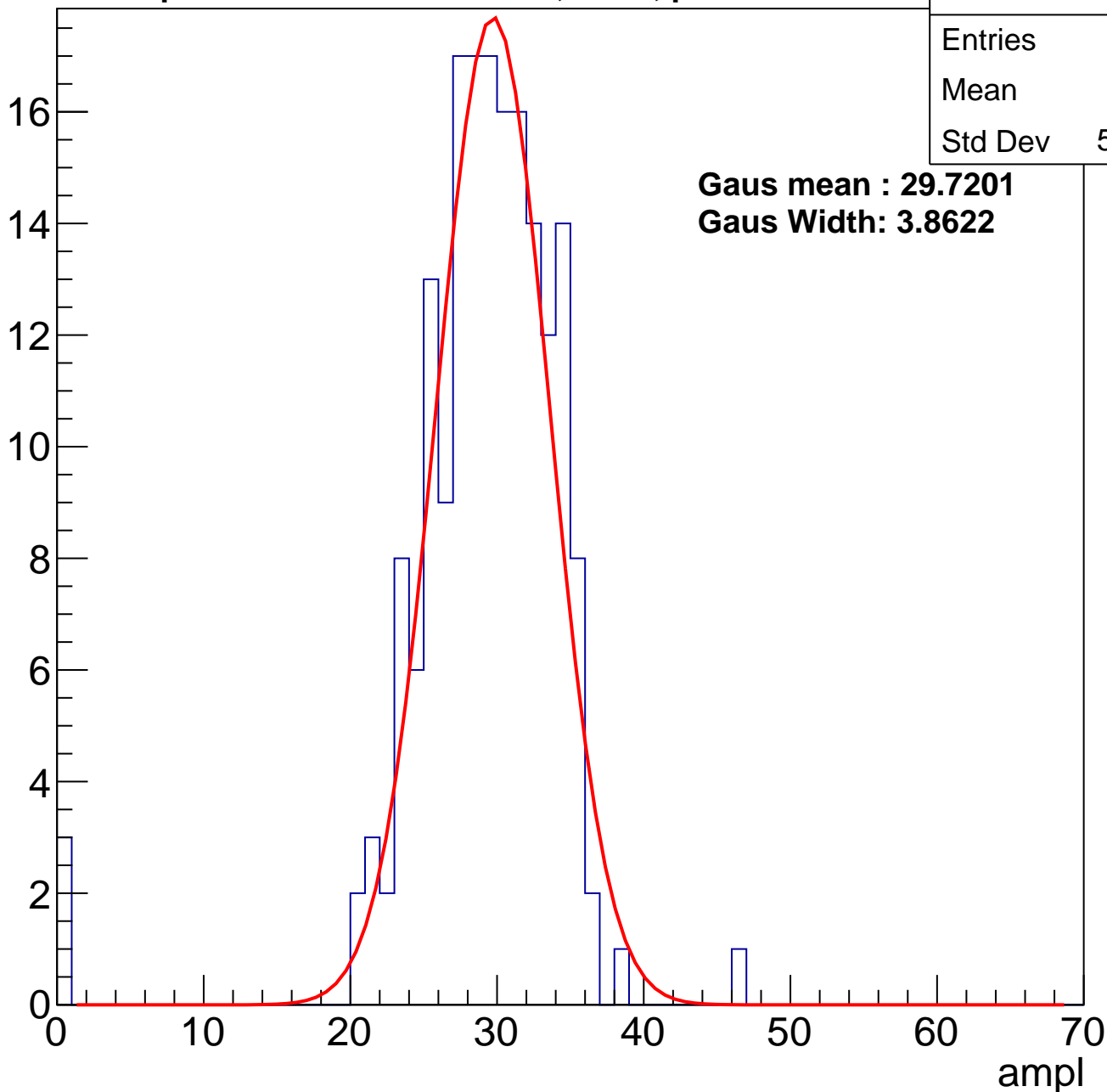
calib_packv5_042523_0143.root, FC#1, port C1

Entries	181
Mean	28.7
Std Dev	5.398

Gaus mean : 29.7201

Gaus Width: 3.8622

Entry



B0L101S, U14-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

20
18
16
14
12
10
8
6
4
2
0

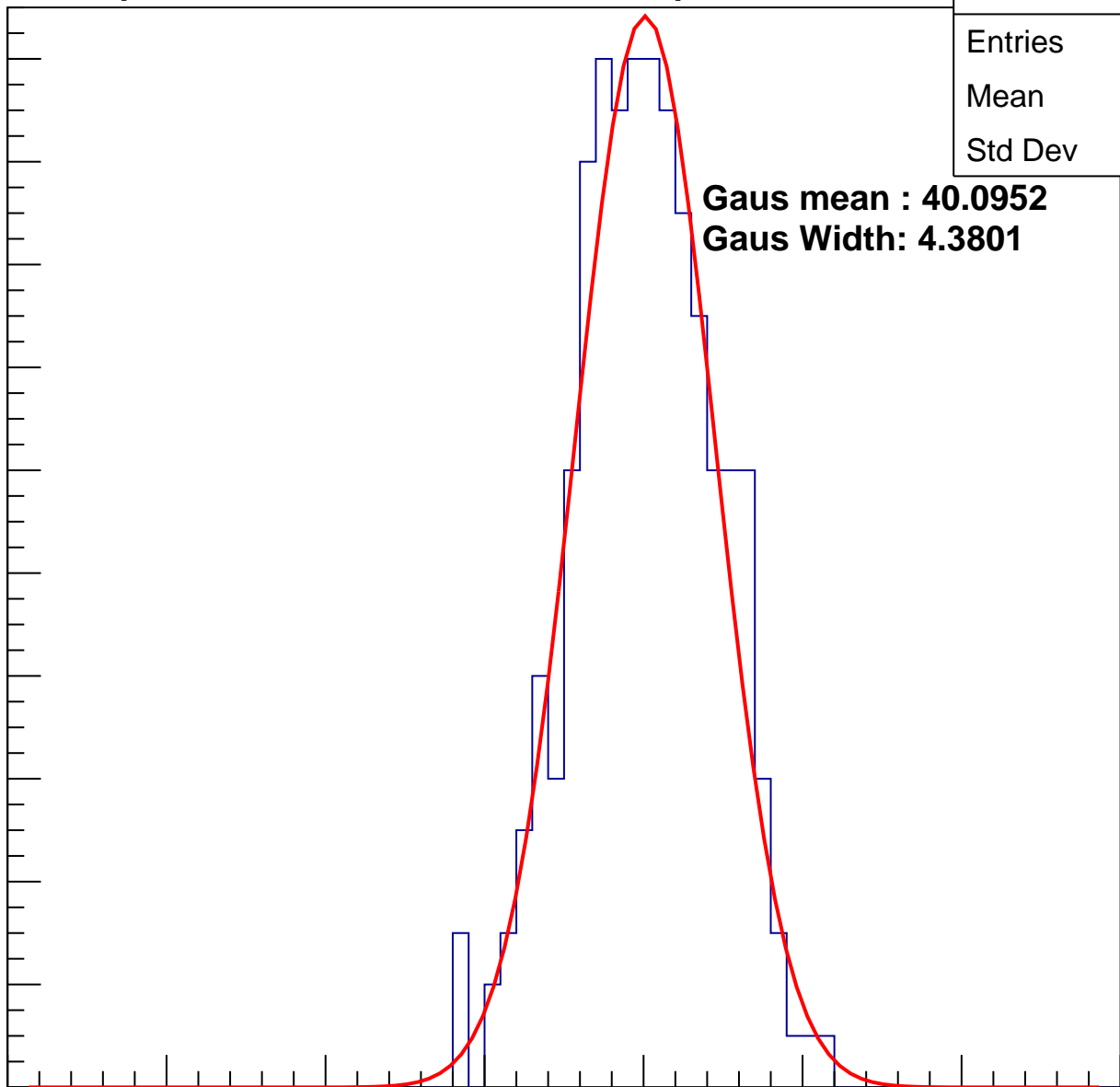
Entries	235
Mean	39.61
Std Dev	4.408

Gaus mean : 40.0952

Gaus Width: 4.3801

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch104, adc2

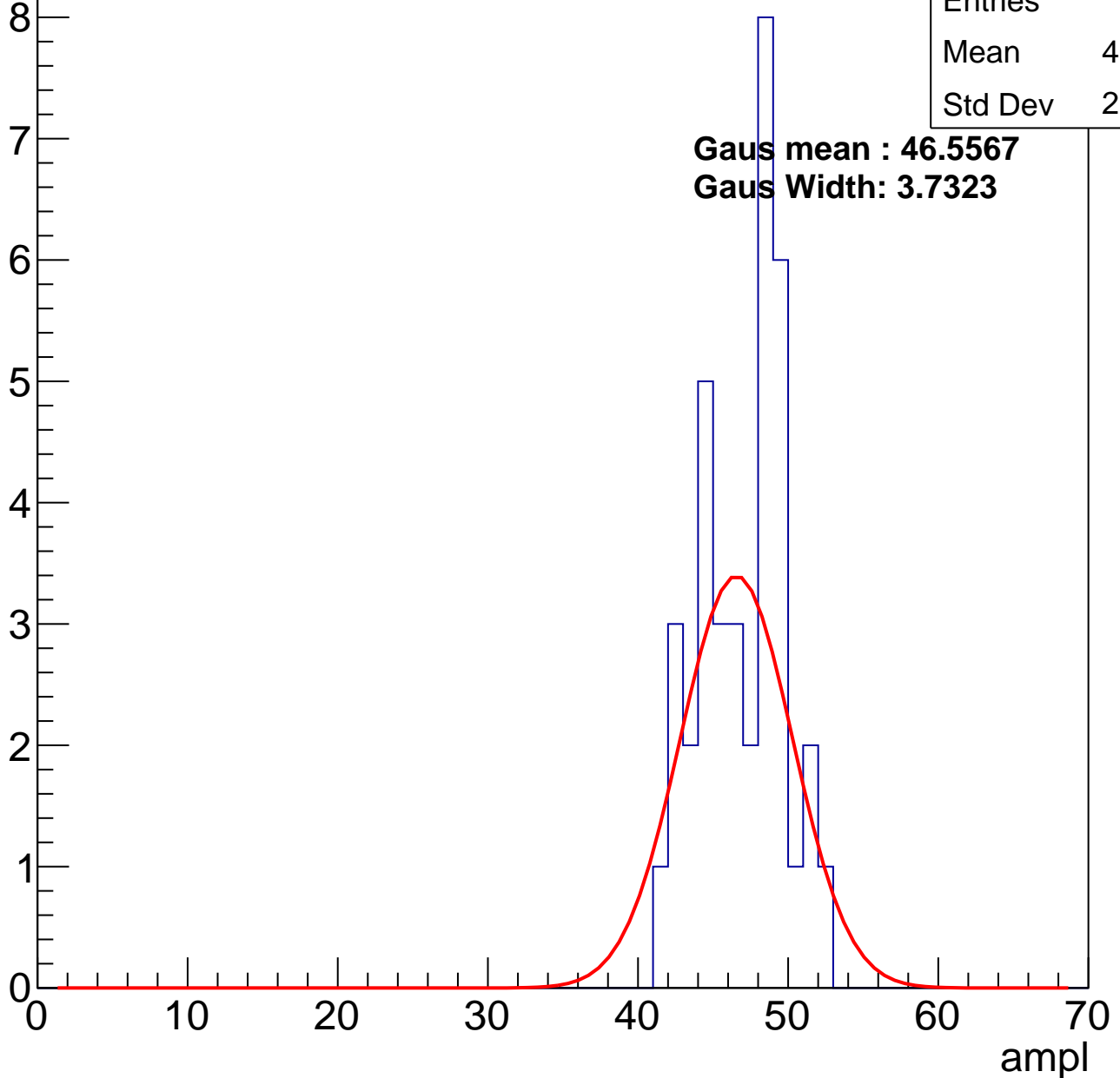
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	37
Mean	46.54
Std Dev	2.815

Gaus mean : 46.5567

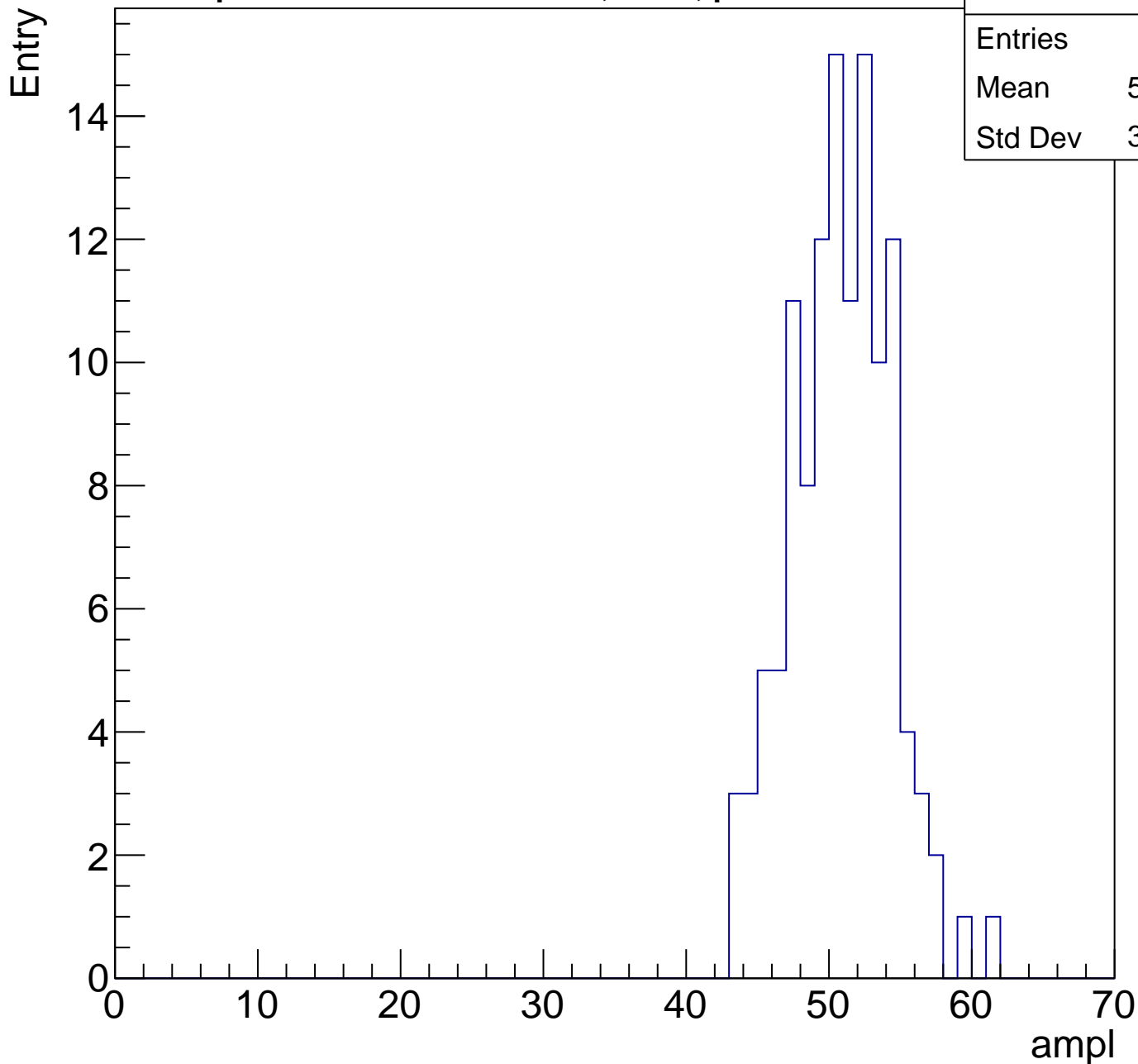
Gaus Width: 3.7323



B0L101S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	50.38
Std Dev	3.458



B0L101S, U14-ch104, adc4

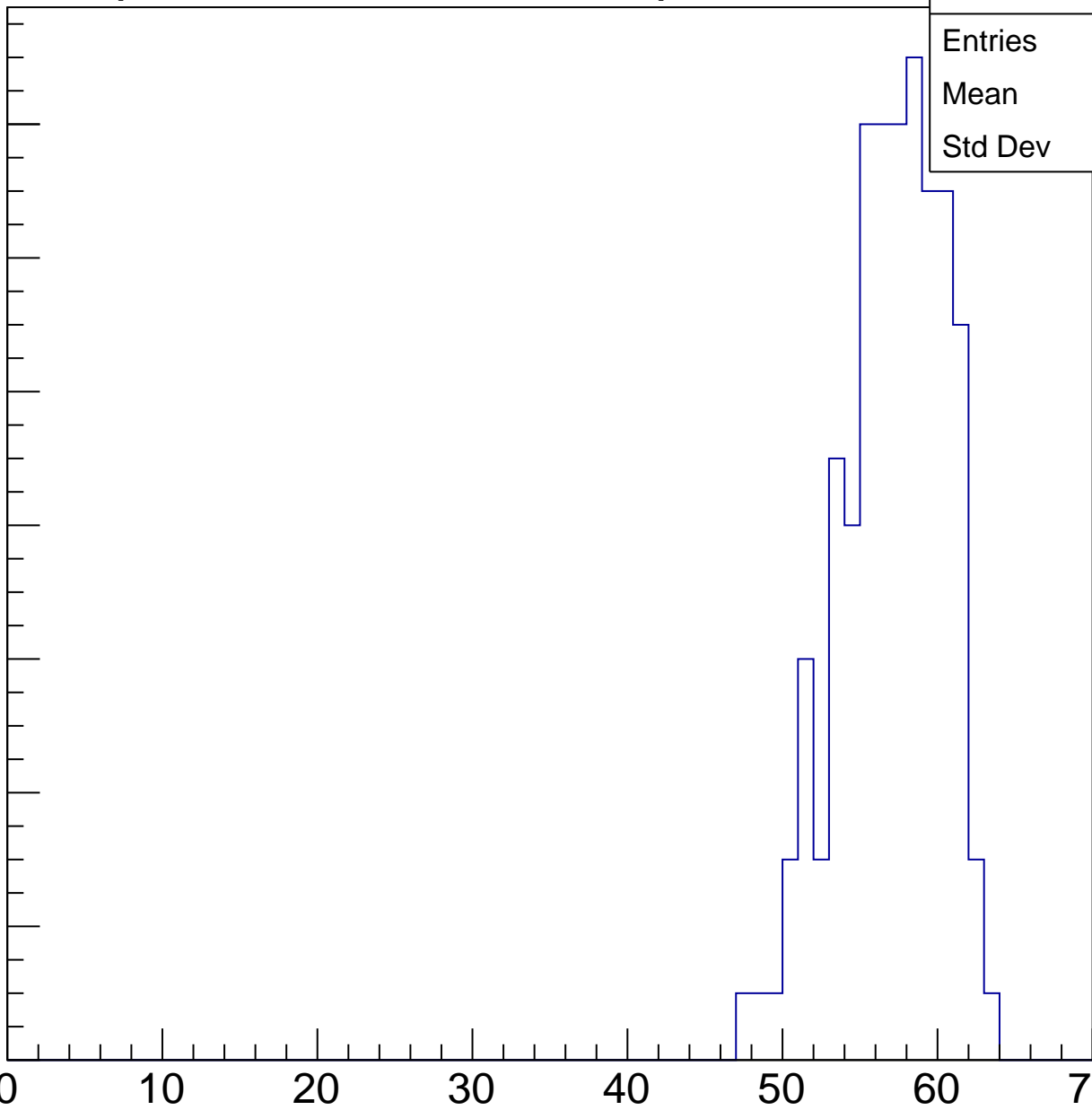
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	56.57
Std Dev	3.3

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch104, adc5

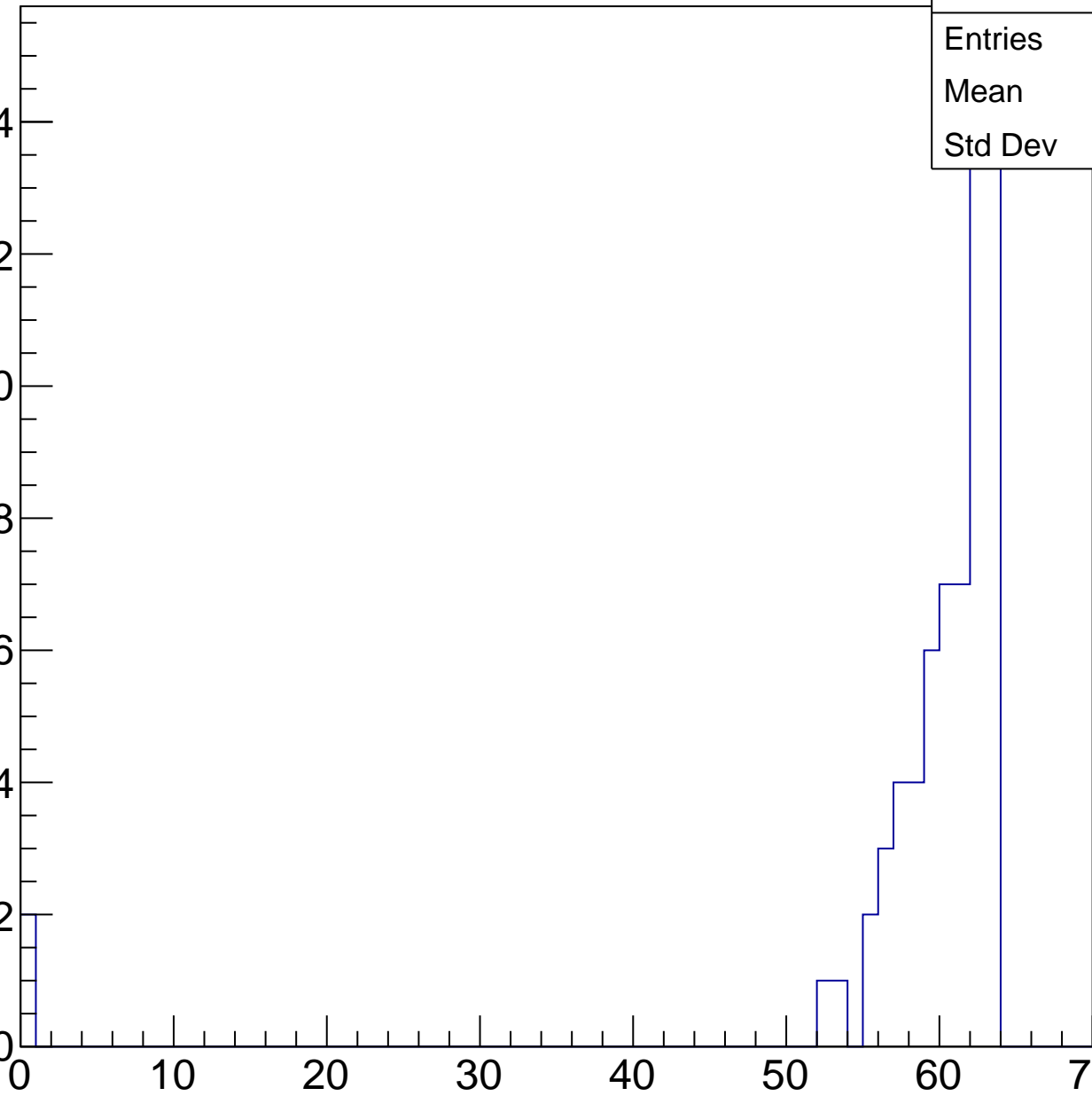
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	58.44
Std Dev	10.66

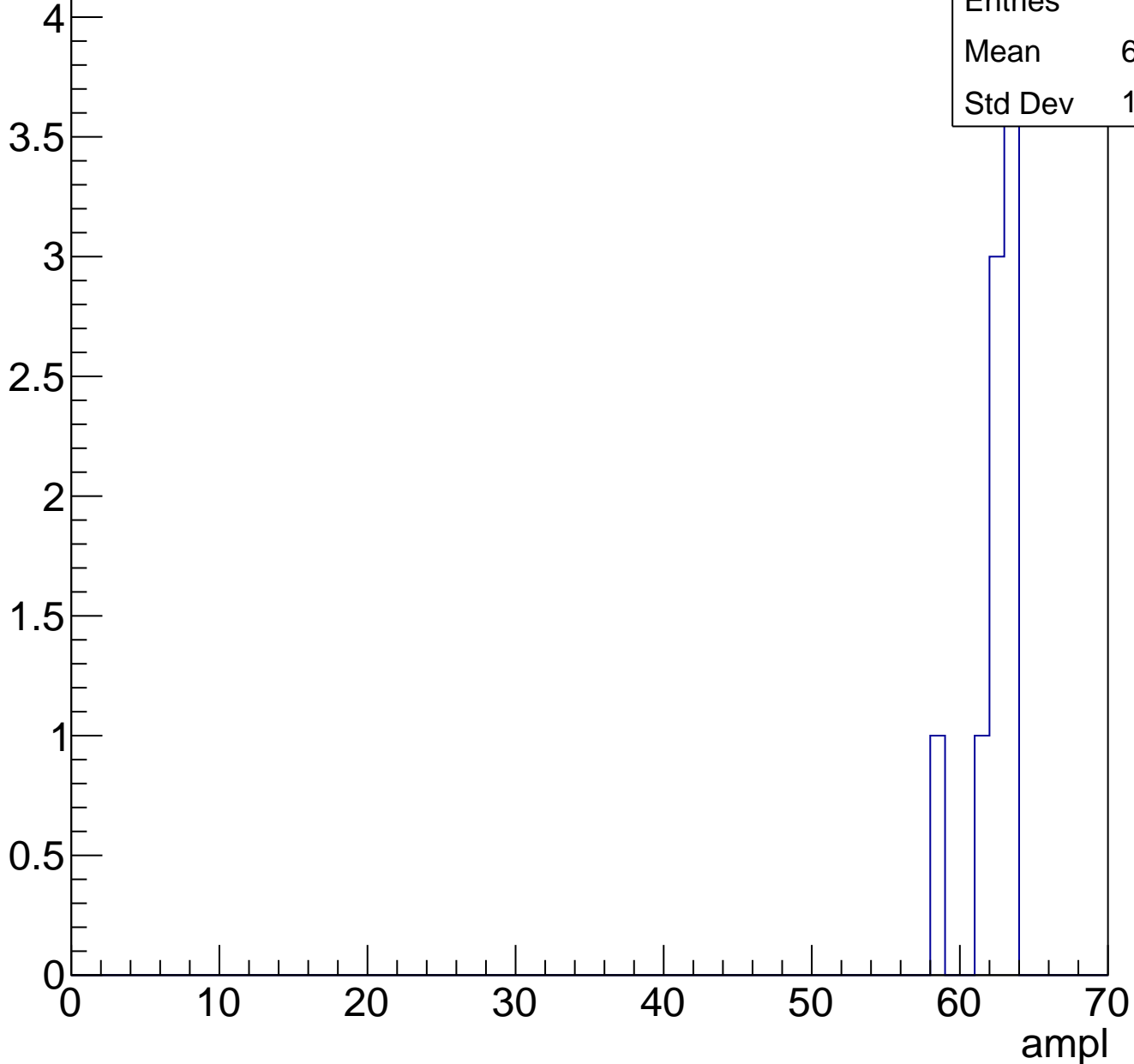
ampl



B0L101S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch105, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	28.57
Std Dev	3.867

Gaus mean : 29.4218

Gaus Width: 3.8732

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

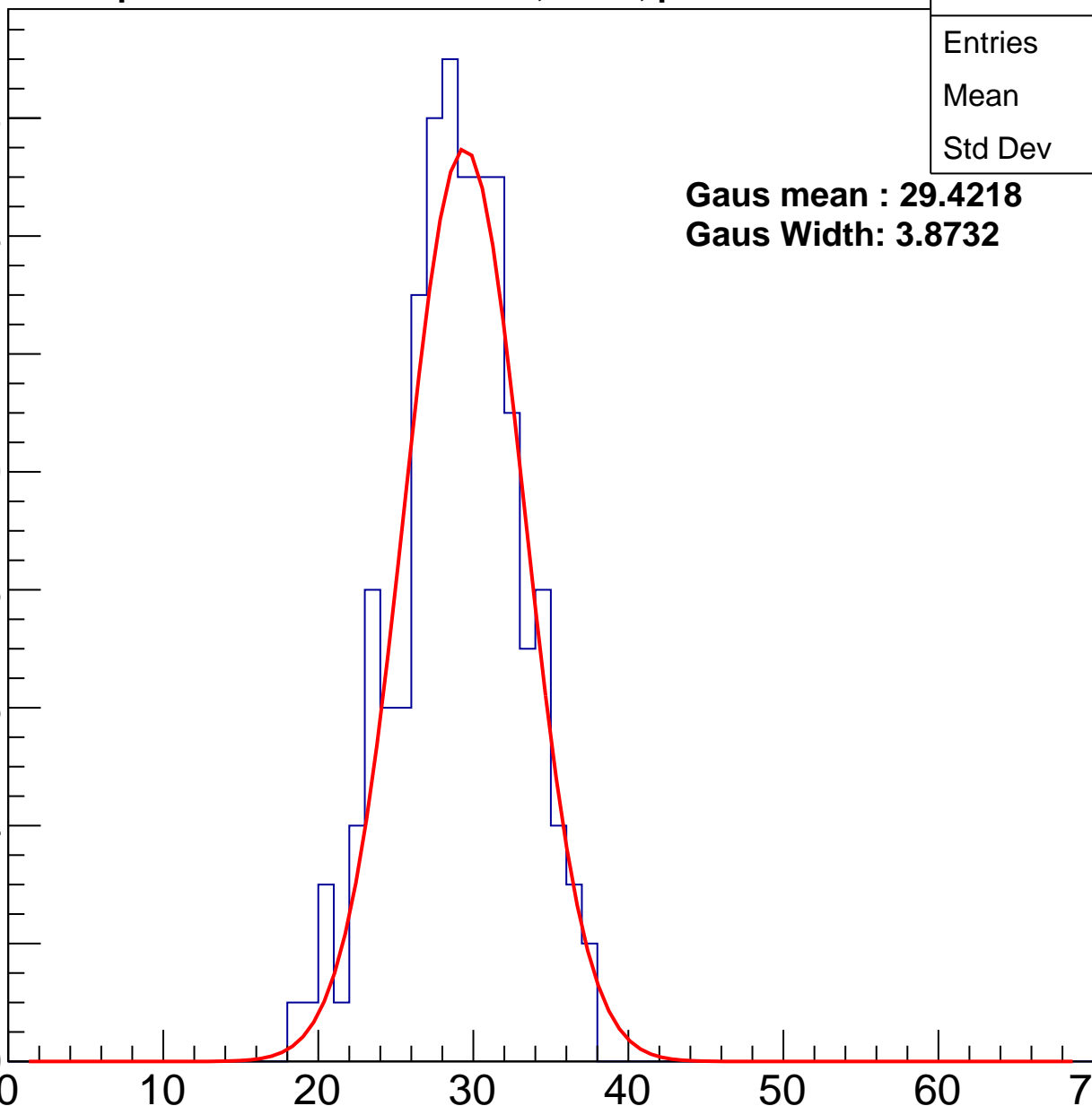
40

50

60

70

ampl



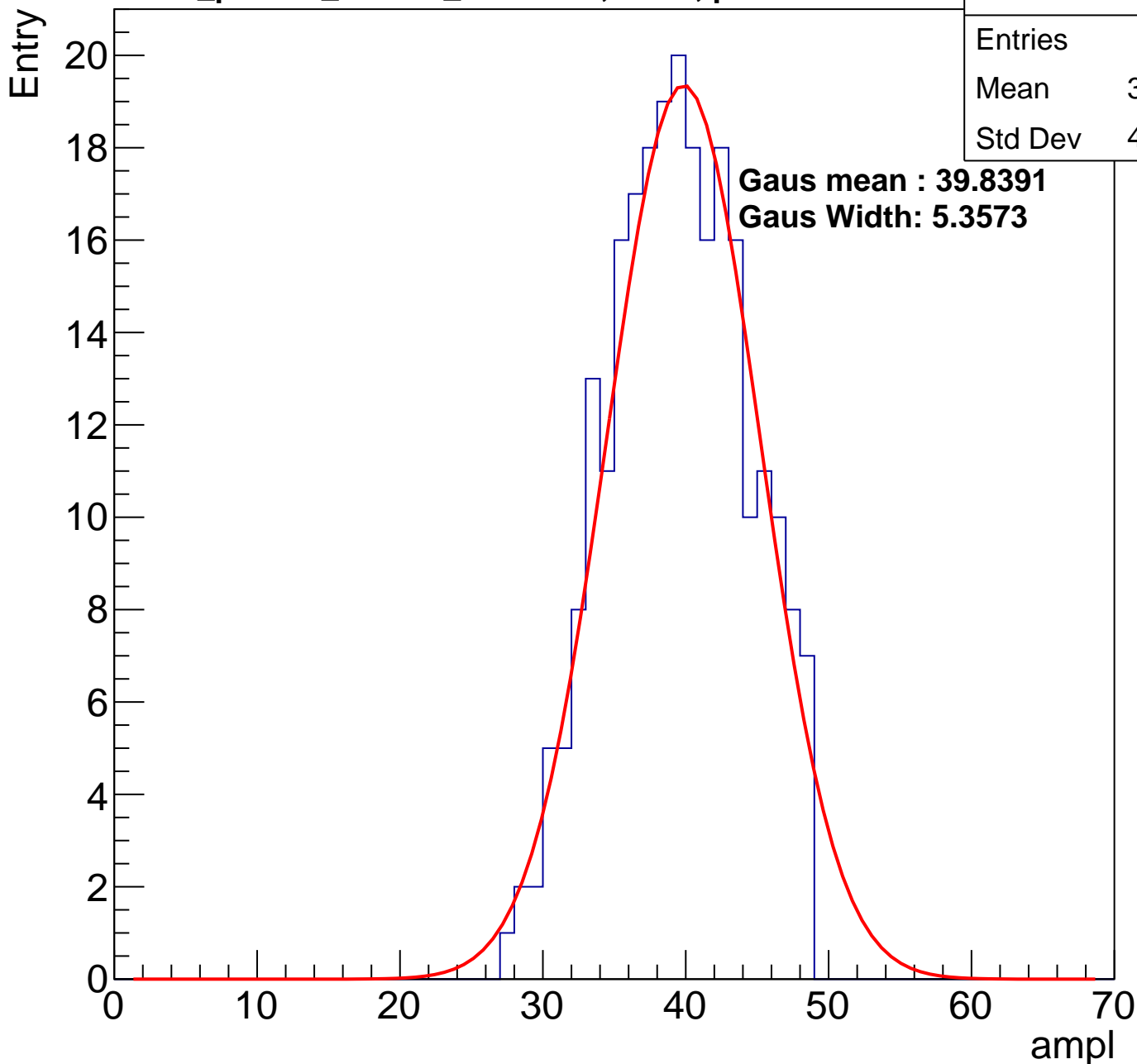
B0L101S, U14-ch105, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	251
Mean	38.93
Std Dev	4.749

Gaus mean : 39.8391

Gaus Width: 5.3573



B0L101S, U14-ch105, adc2

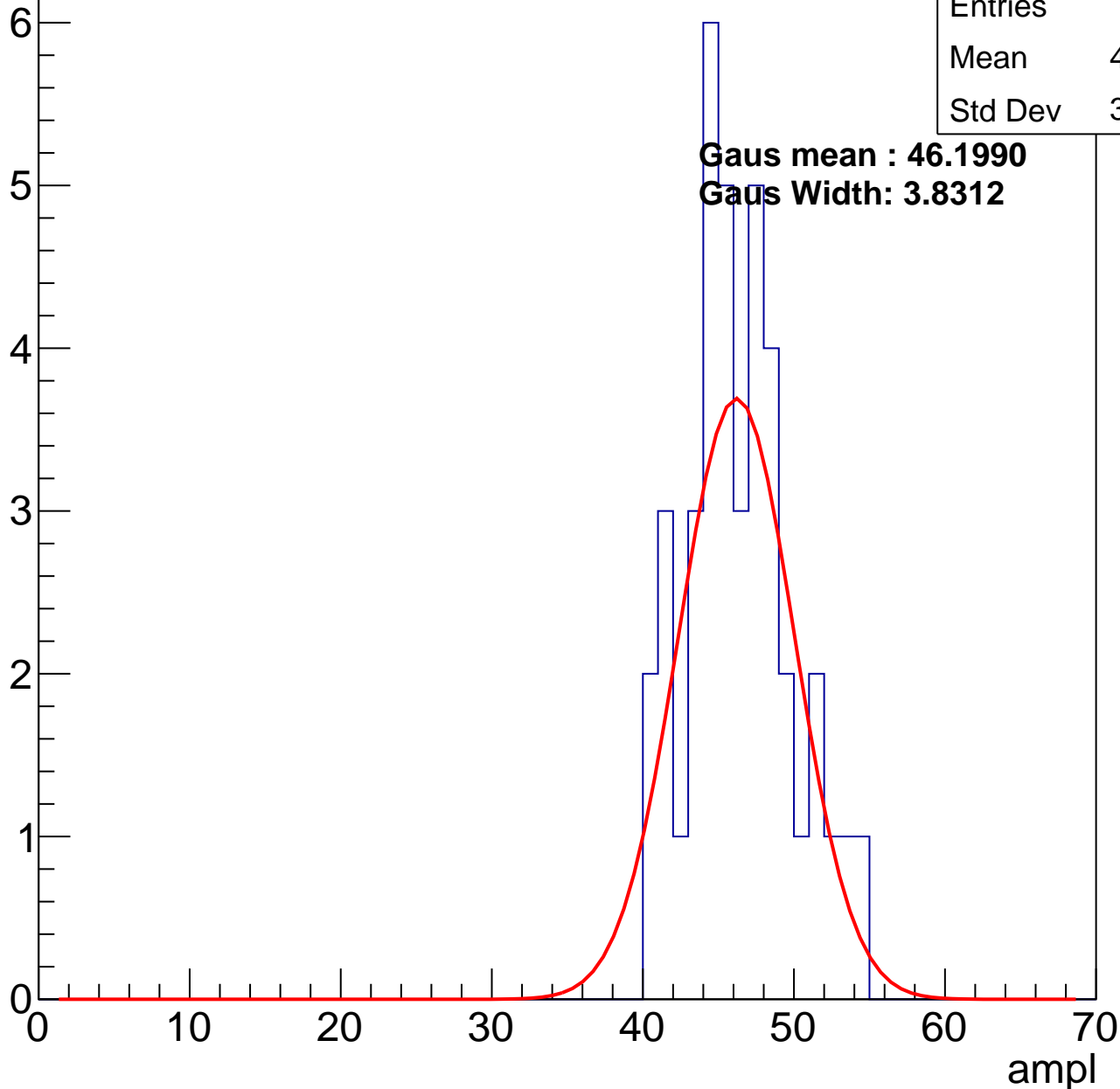
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	40
Mean	45.92
Std Dev	3.438

Gaus mean : 46.1990

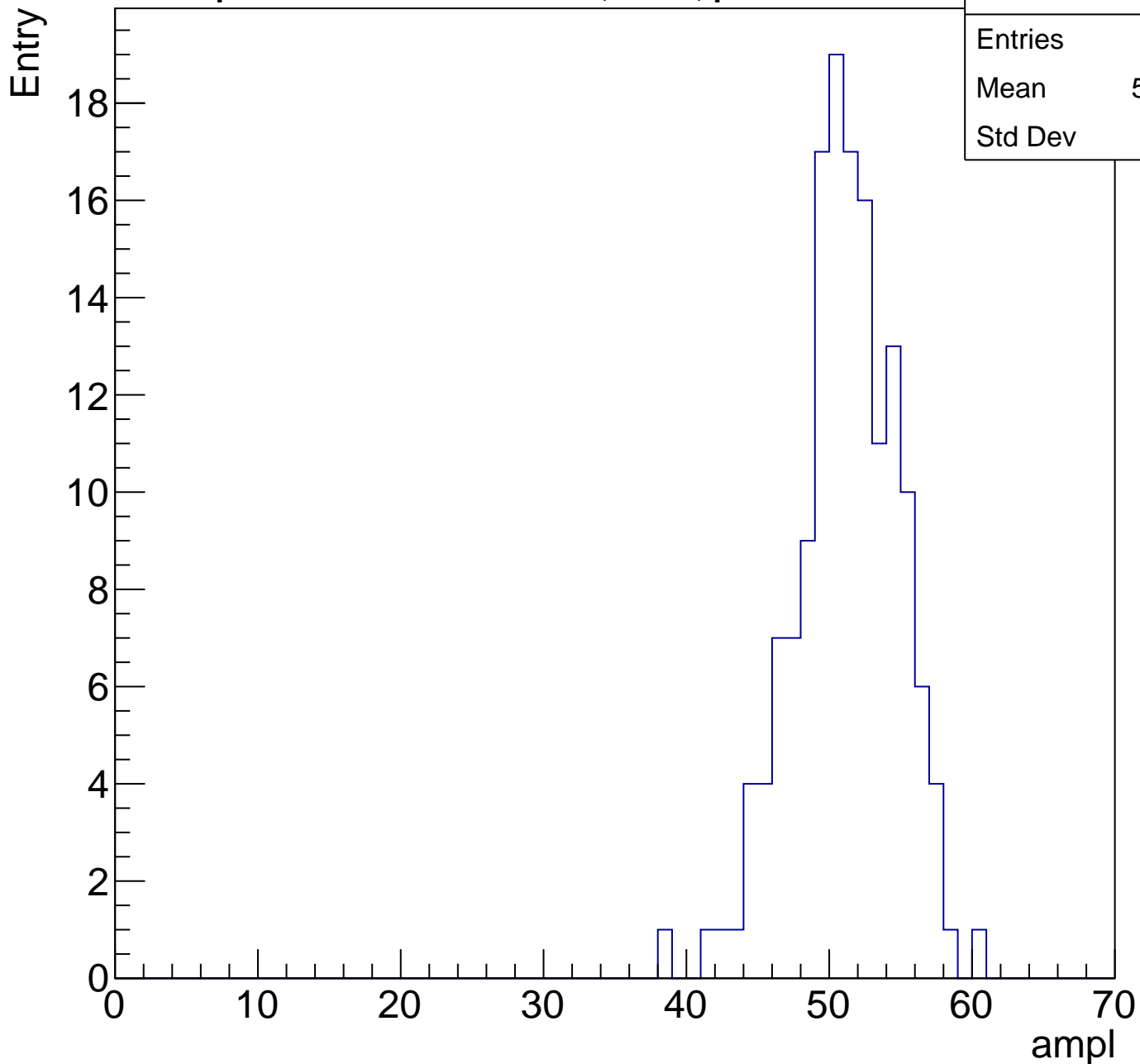
Gaus Width: 3.8312



B0L101S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	50.68
Std Dev	3.61



B0L101S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

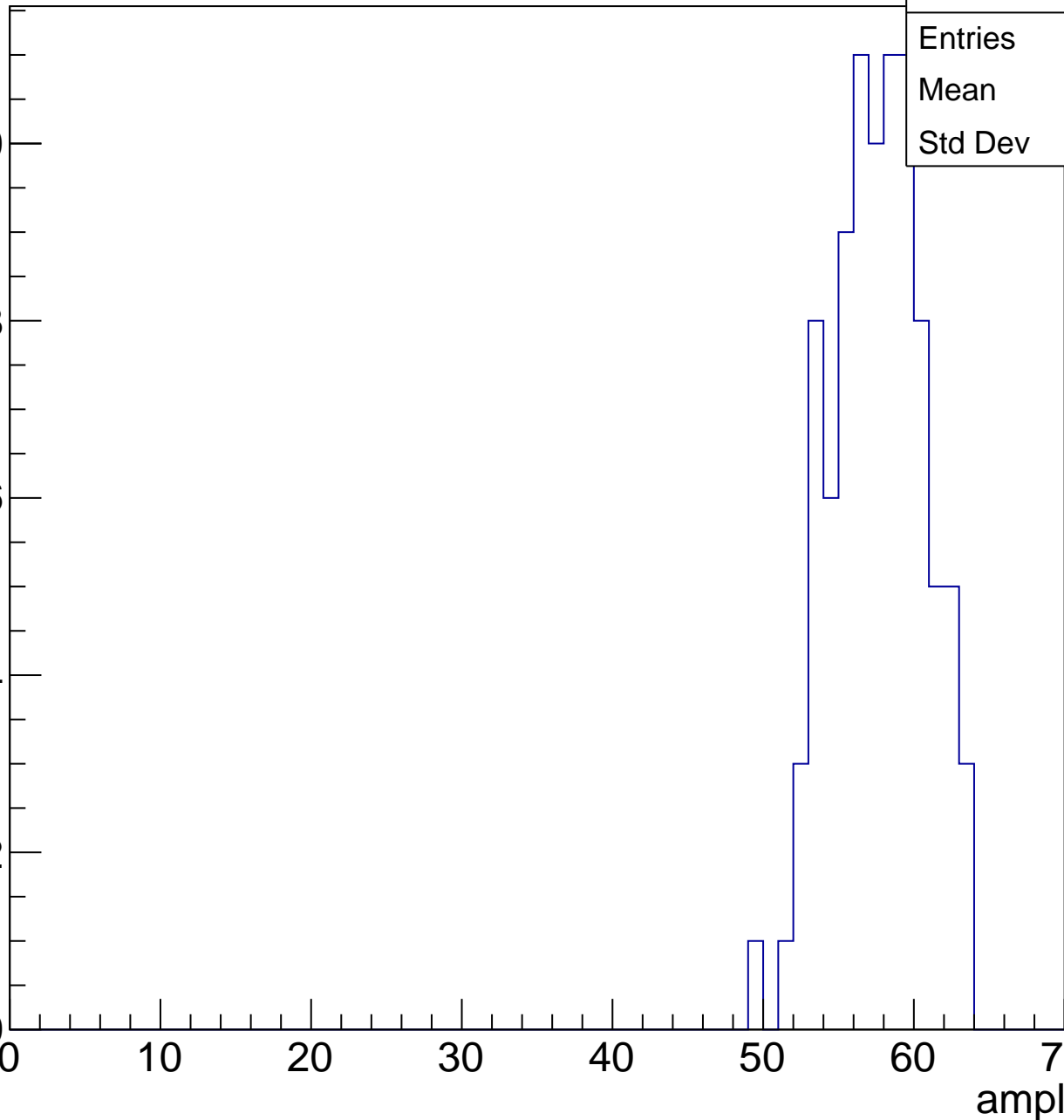
50

60

70

ampl

Entries	92
Mean	57.13
Std Dev	3.026



B0L101S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

74

Mean

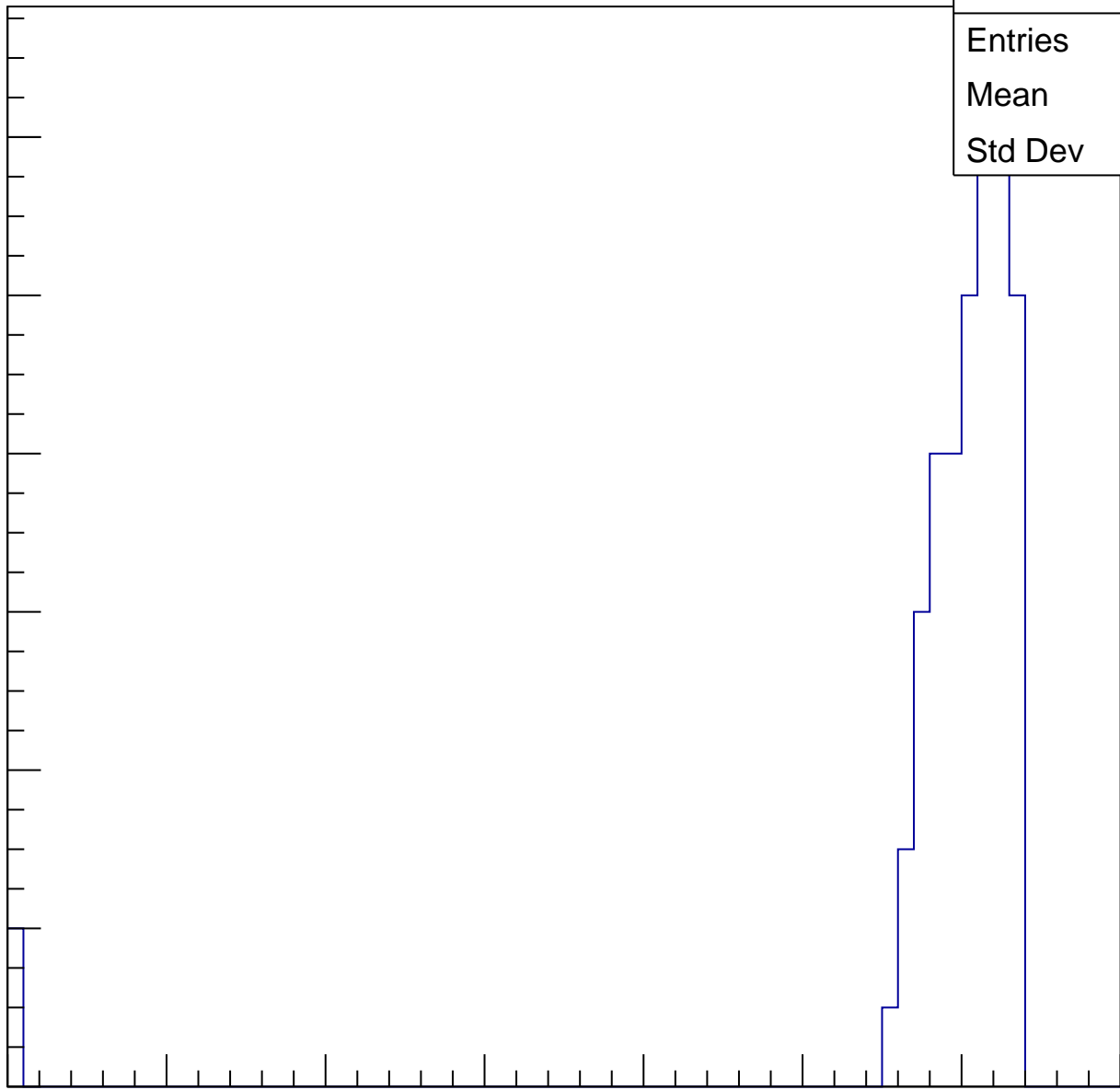
58.51

Std Dev

9.972

ampl

0 10 20 30 40 50 60 70

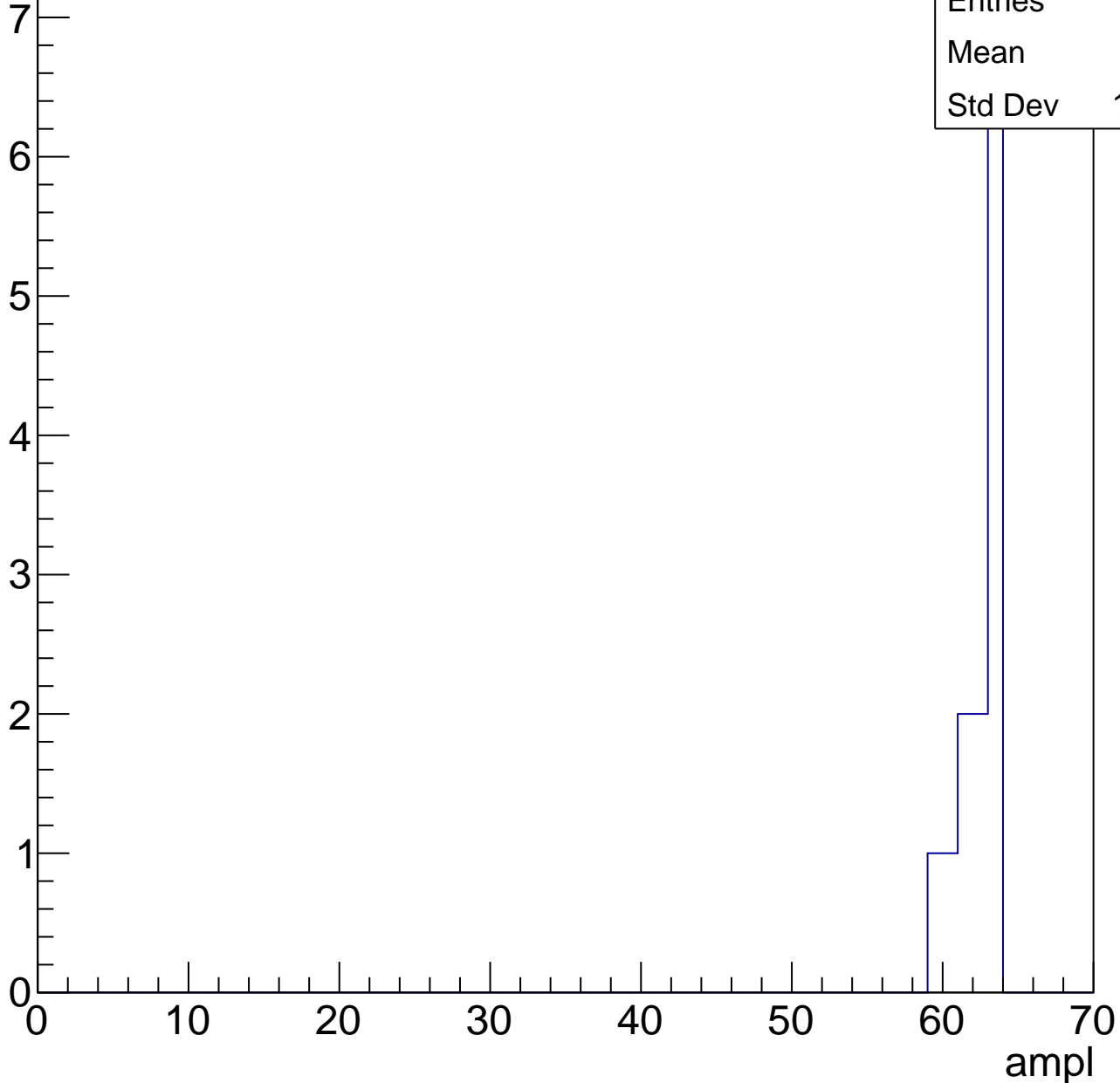


B0L101S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	62
Std Dev	1.301



B0L101S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch106, adc0

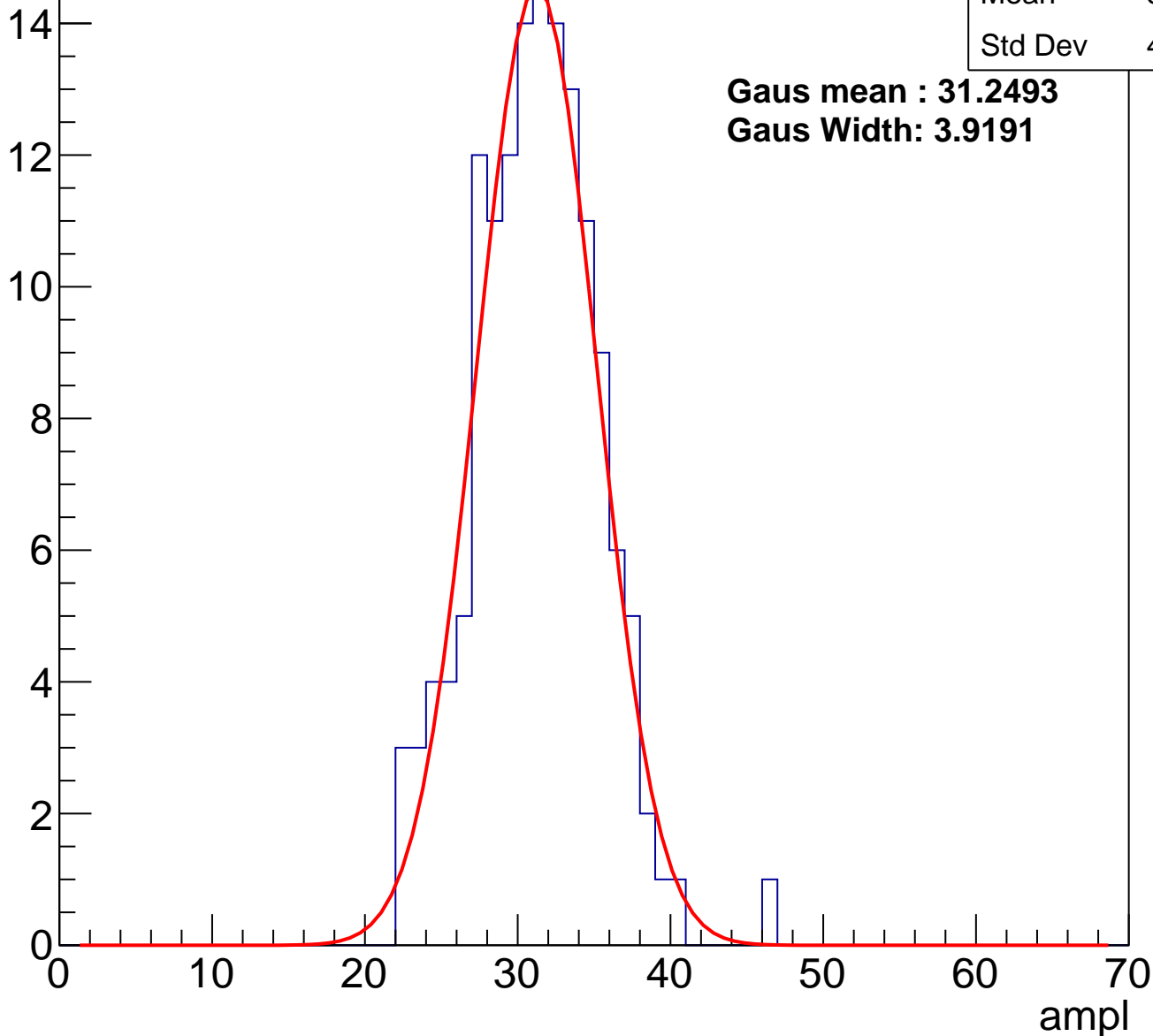
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	30.78
Std Dev	4.015

Gaus mean : 31.2493

Gaus Width: 3.9191

Entry



B0L101S, U14-ch106, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

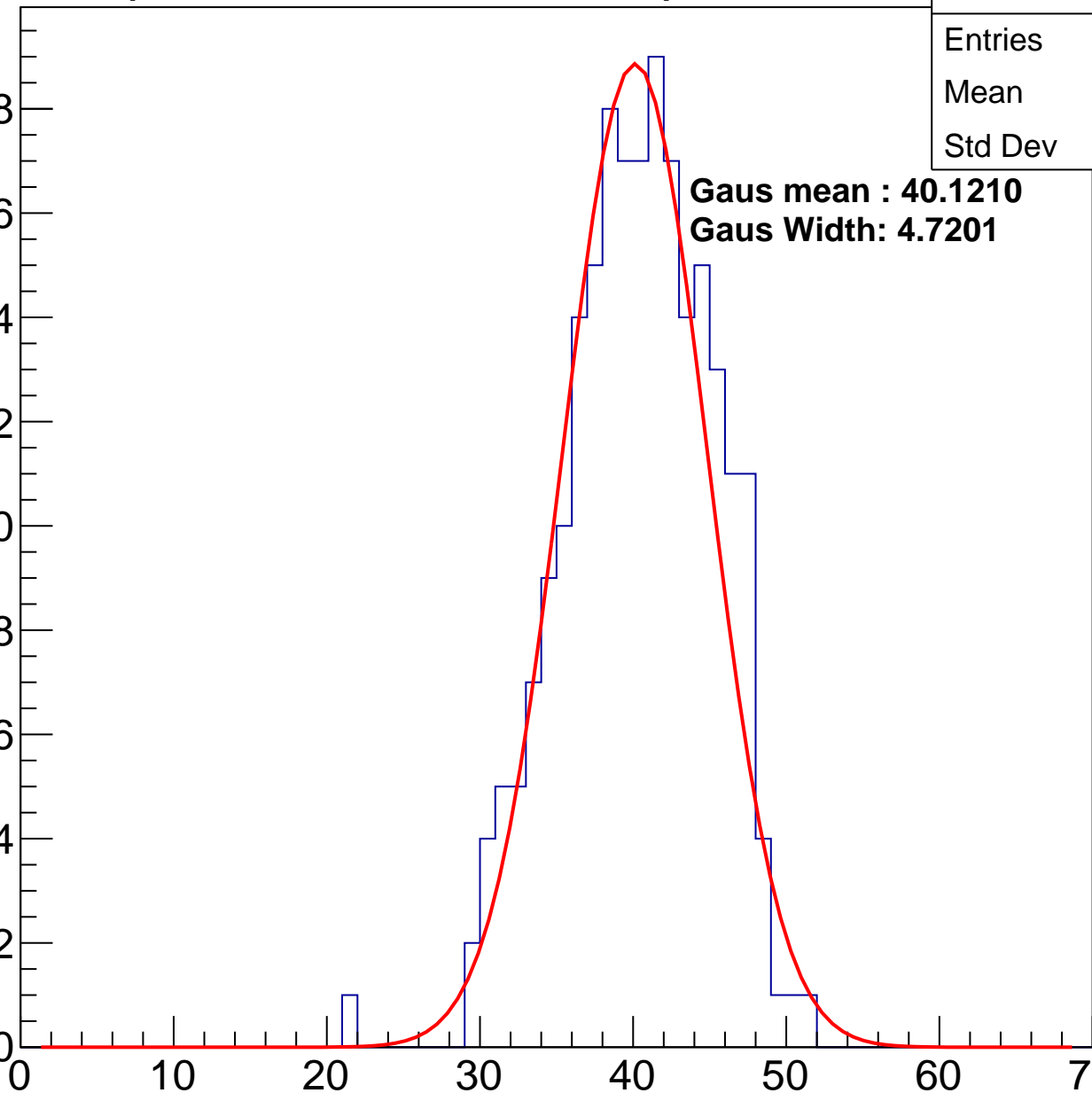
18
16
14
12
10
8
6
4
2
0

Entries	231
Mean	39.79
Std Dev	4.814

Gaus mean : 40.1210

Gaus Width: 4.7201

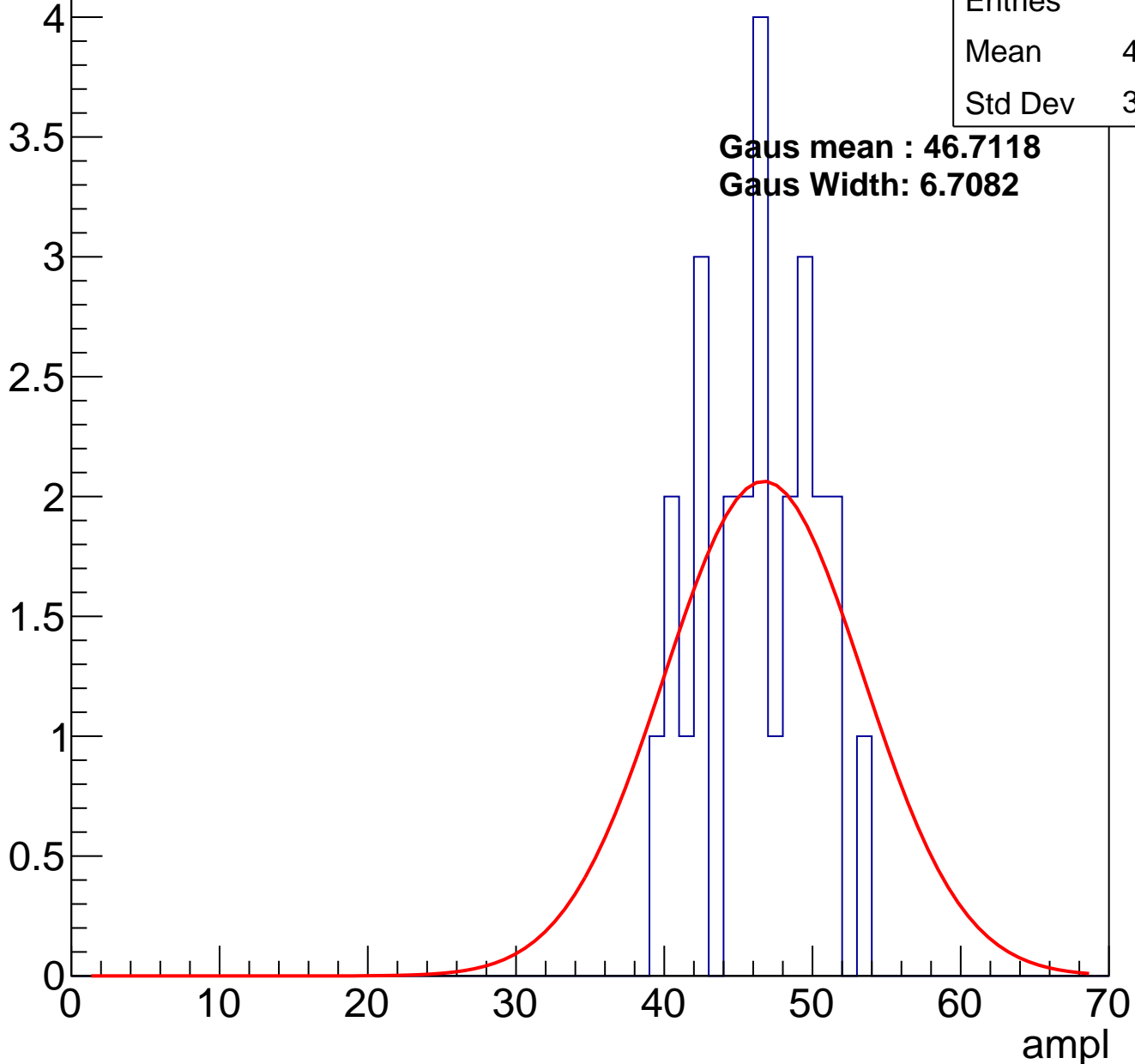
ampl



B0L101S, U14-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	26
Mean	45.88
Std Dev	3.776

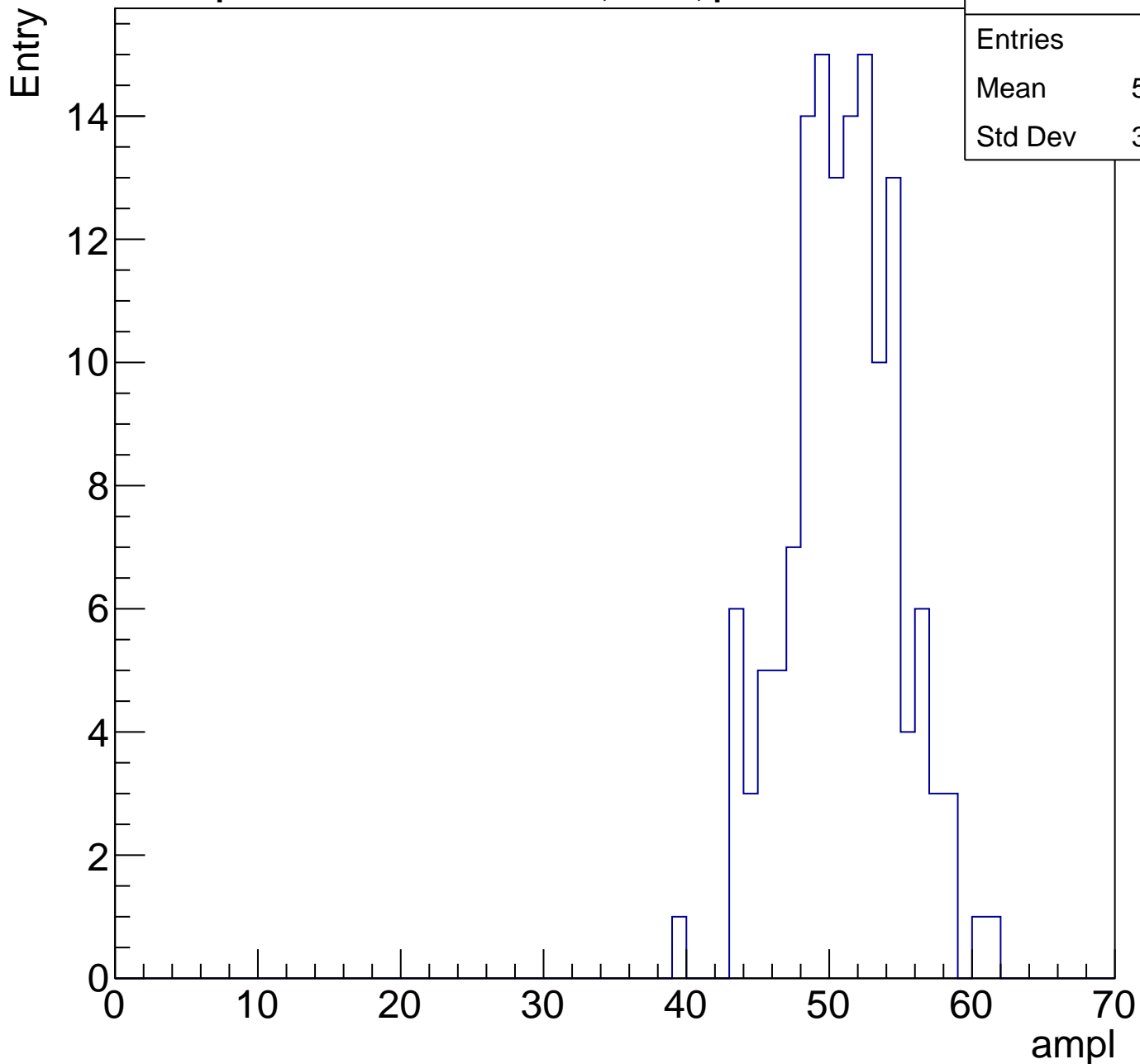
Gaus mean : 46.7118

Gaus Width: 6.7082

B0L101S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	50.49
Std Dev	3.865



B0L101S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

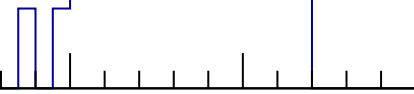
Entries	112
Mean	56.79
Std Dev	3.539

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	62
Mean	59.66
Std Dev	2.718

ampl

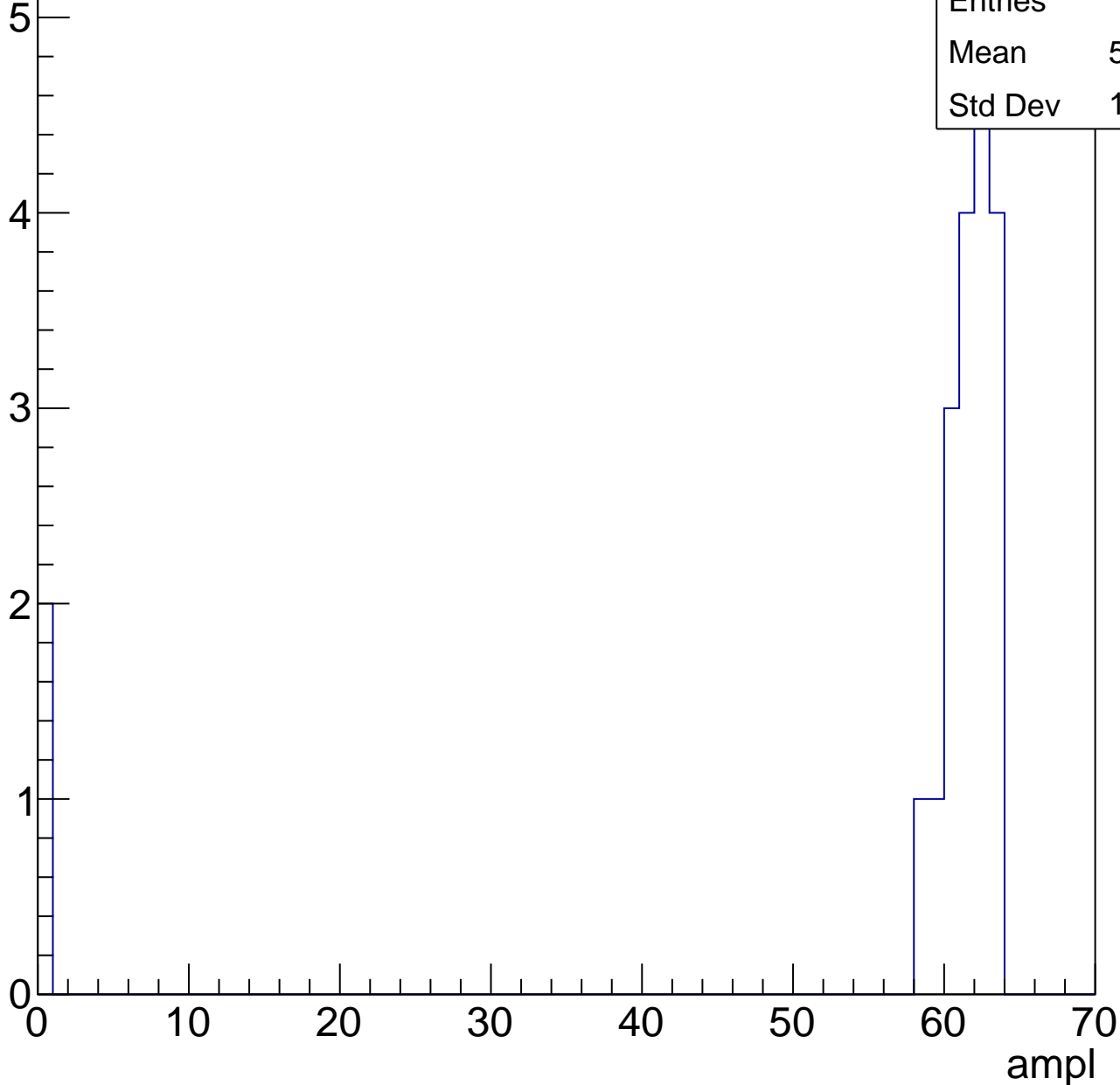
0 10 20 30 40 50 60 70

B0L101S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

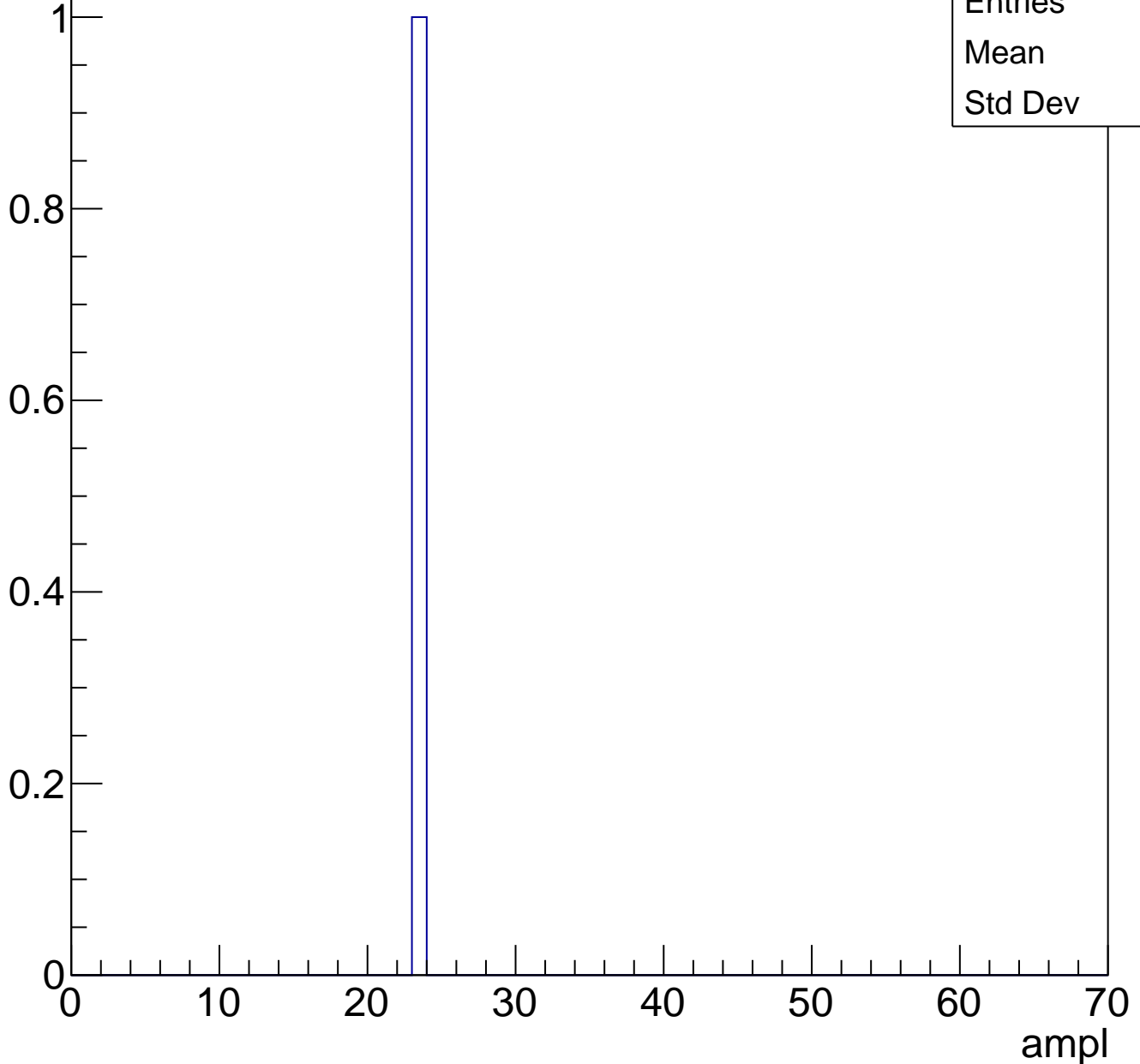
Entries	20
Mean	55.15
Std Dev	18.43



B0L101S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L101S, U14-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	28.01
Std Dev	3.789

Gaus mean : 28.3456

Gaus Width: 3.8143

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

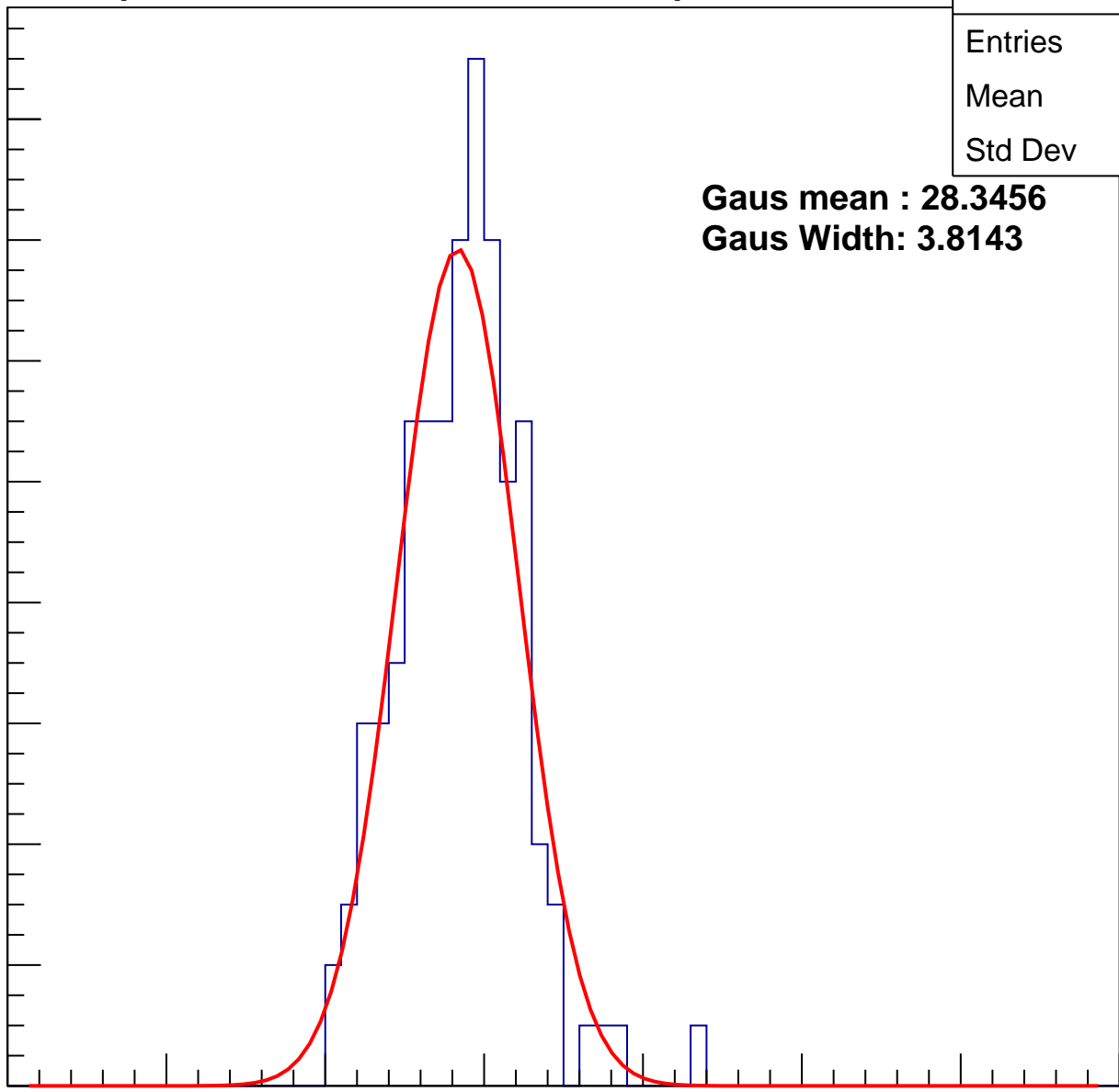
40

50

60

70

ampl



B0L101S, U14-ch107, adc1

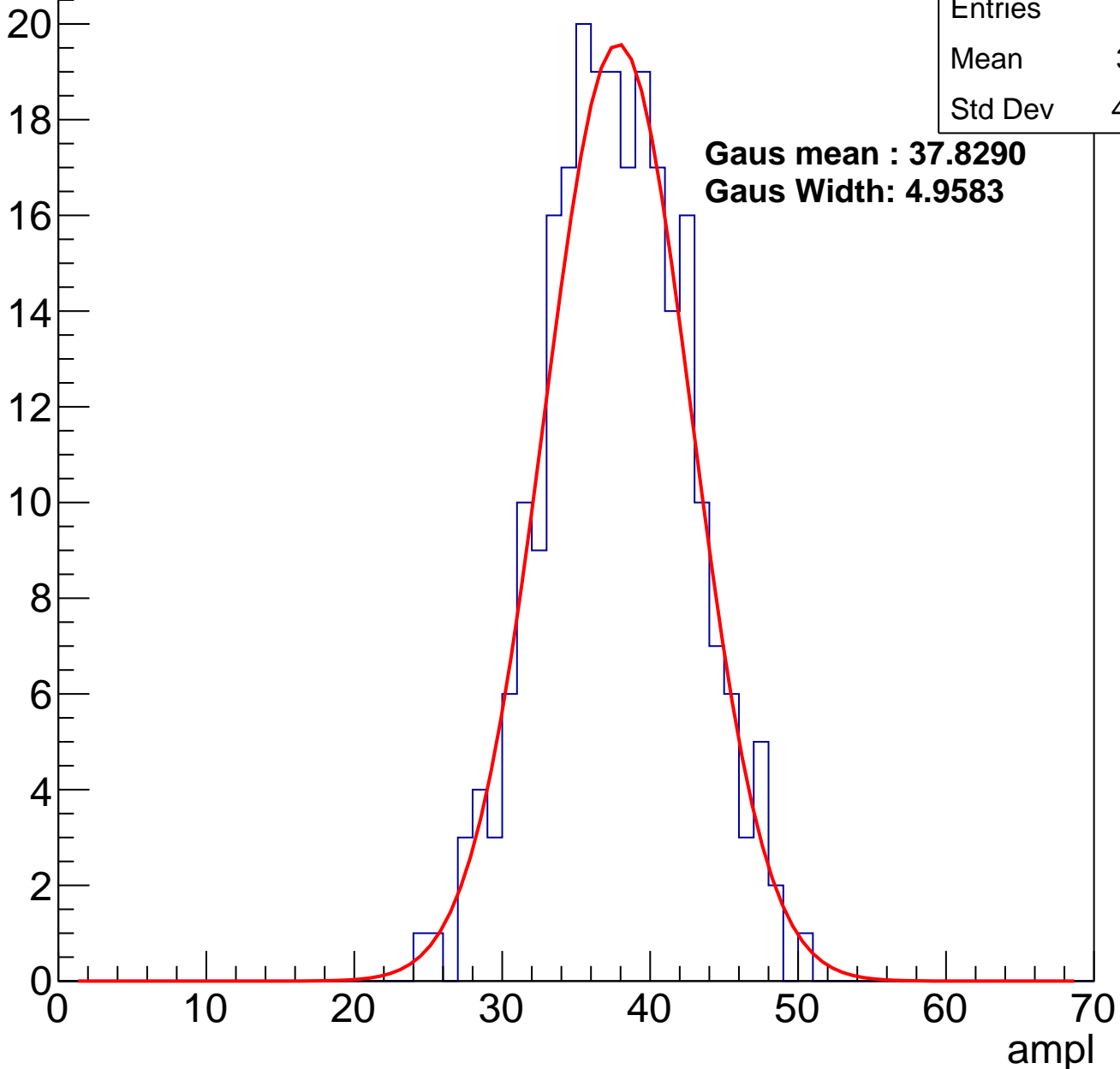
calib_packv5_042523_0143.root, FC#1, port C1

Entries	245
Mean	37.31
Std Dev	4.783

Gaus mean : 37.8290

Gaus Width: 4.9583

Entry



B0L101S, U14-ch107, adc2

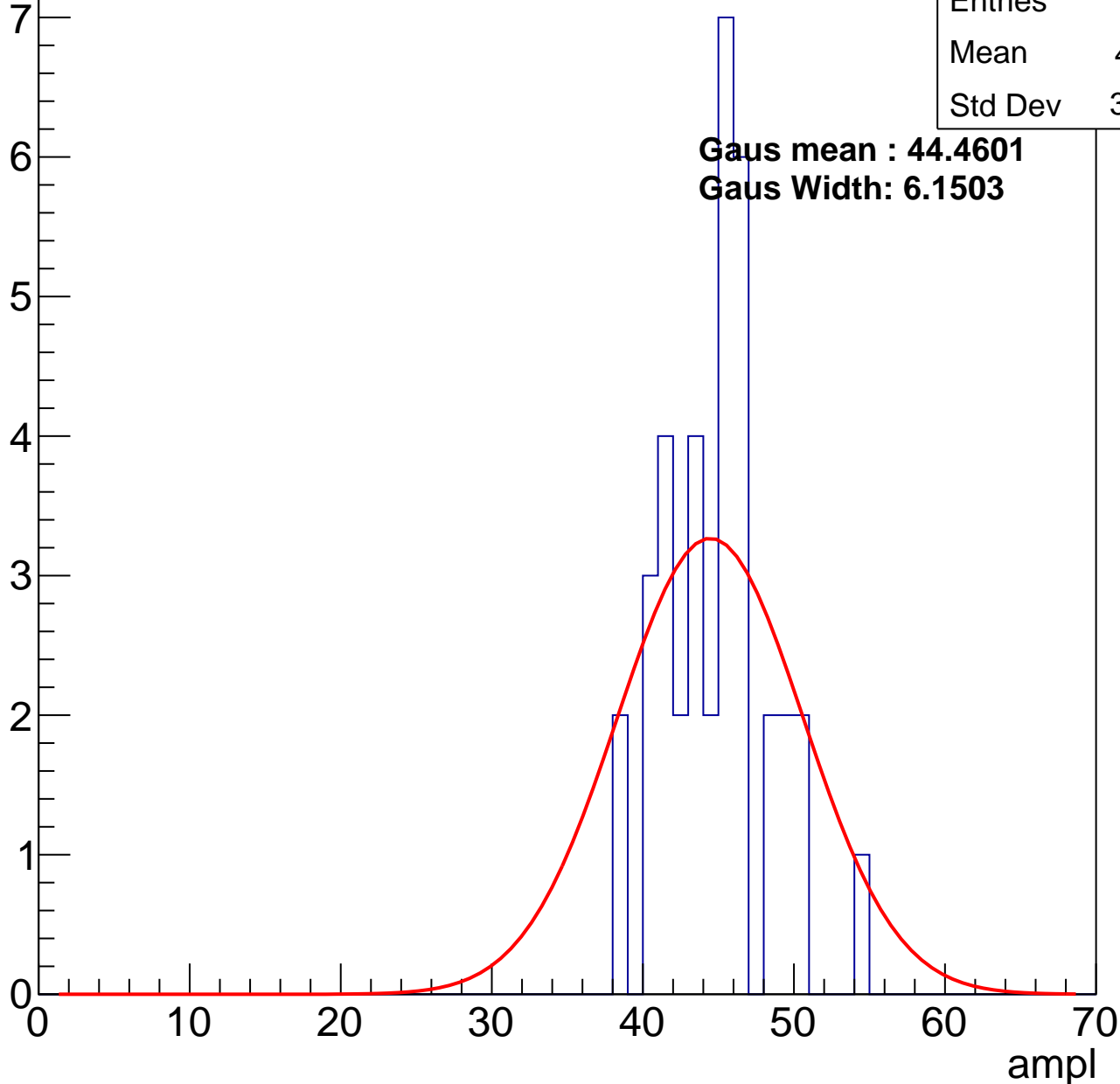
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	37
Mean	44.41
Std Dev	3.475

Gaus mean : 44.4601

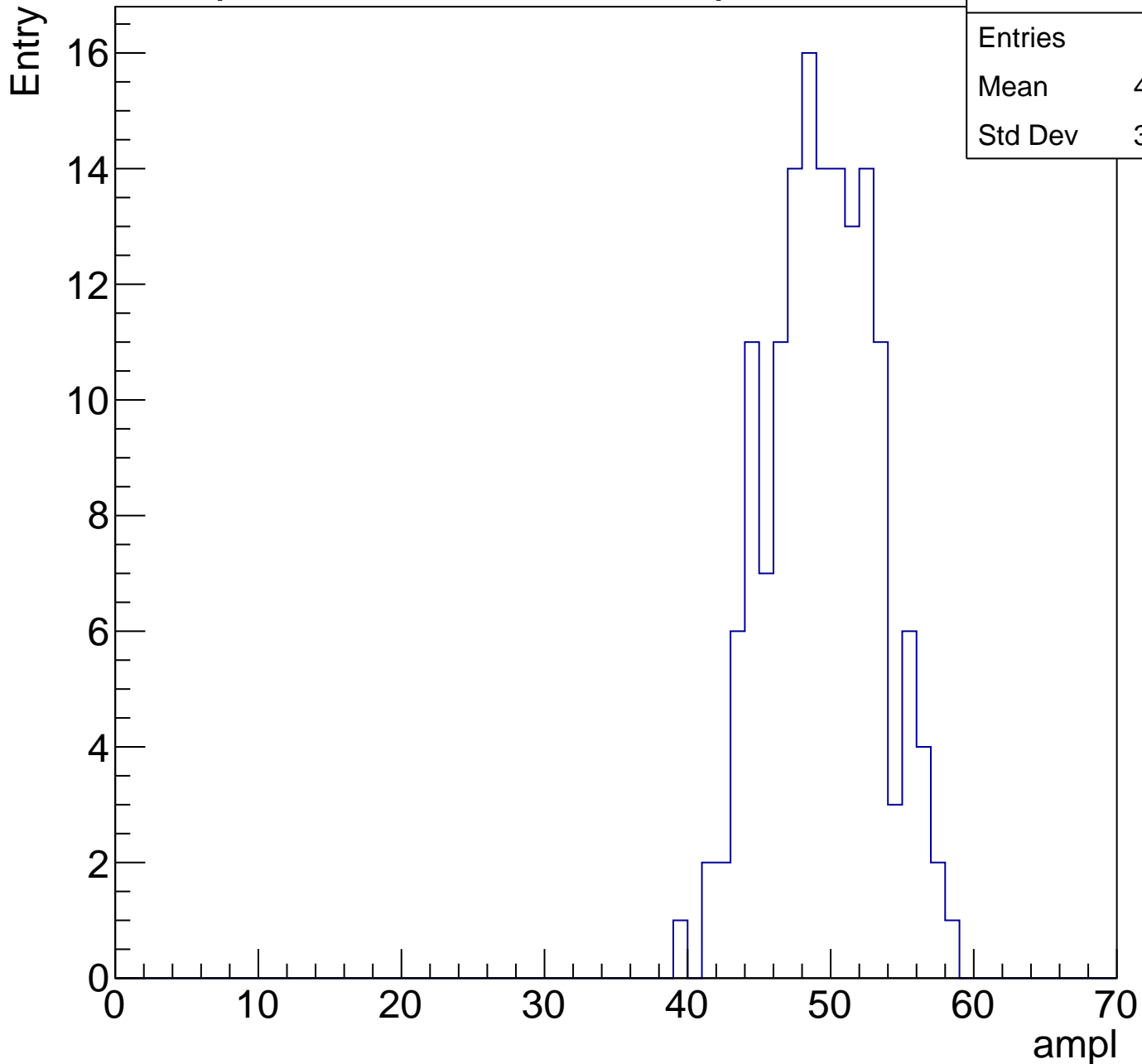
Gaus Width: 6.1503



B0L101S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

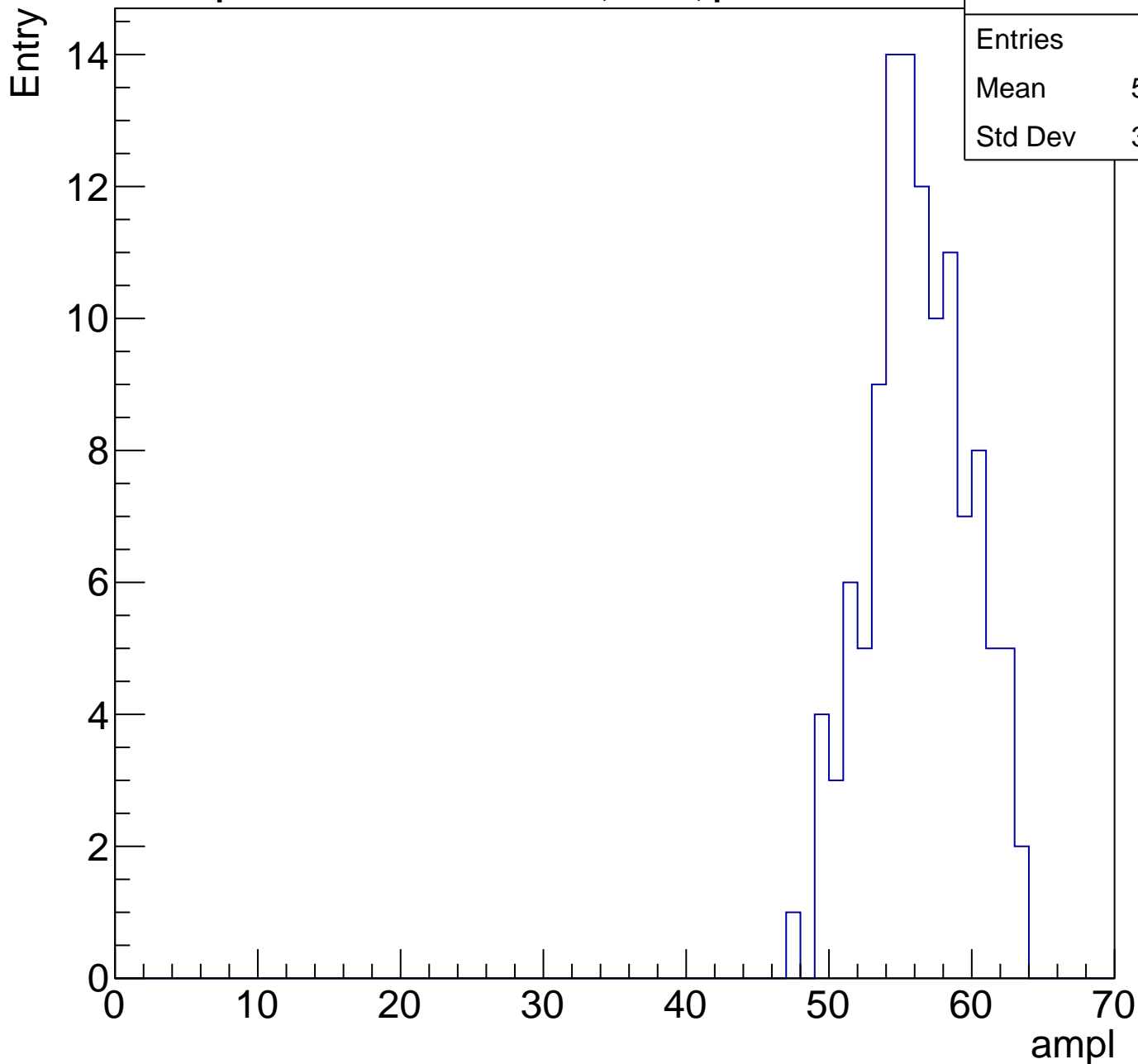
Entries	152
Mean	48.96
Std Dev	3.773



B0L101S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	55.83
Std Dev	3.487



B0L101S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

81

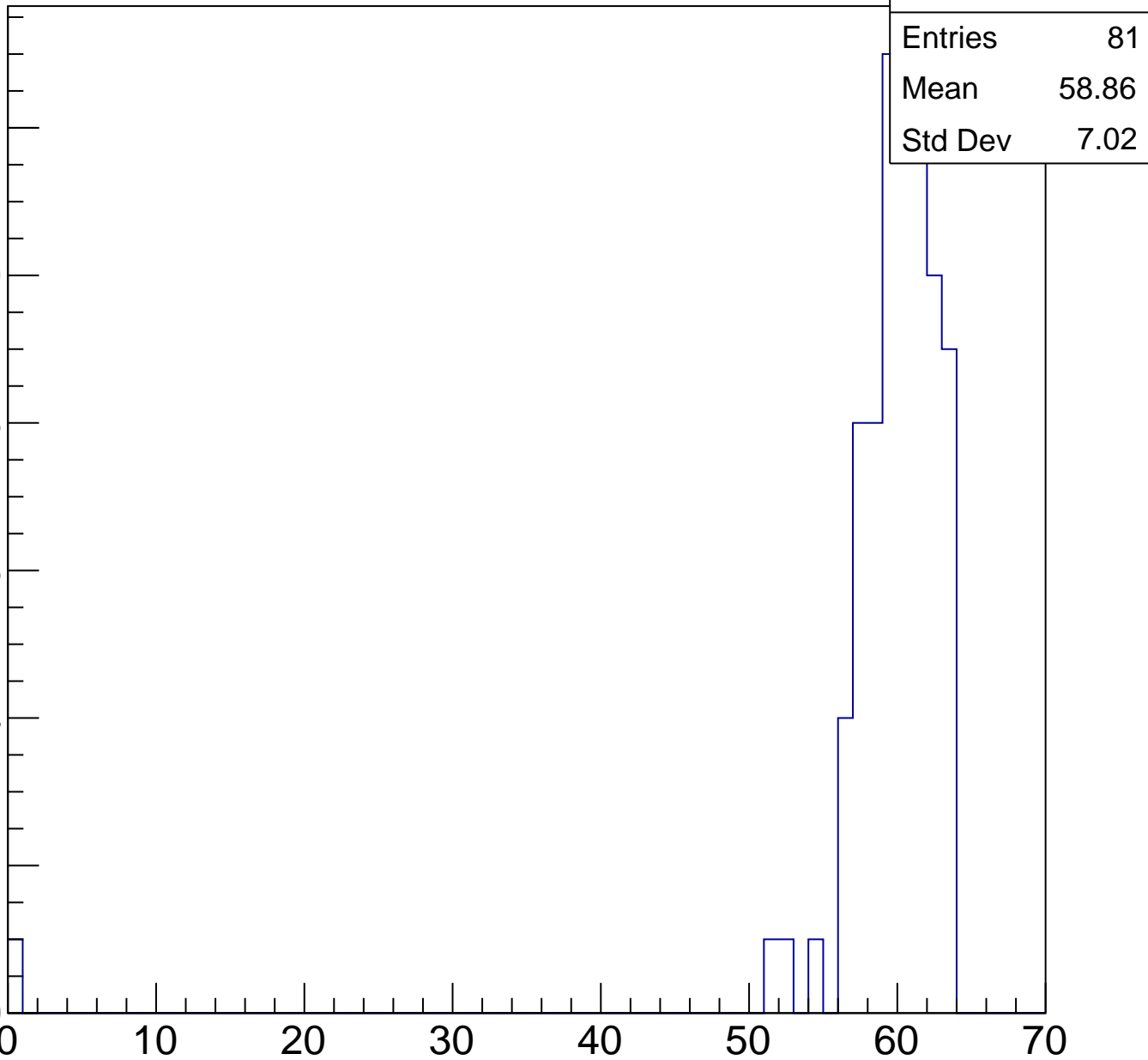
Mean

58.86

Std Dev

7.02

ampl

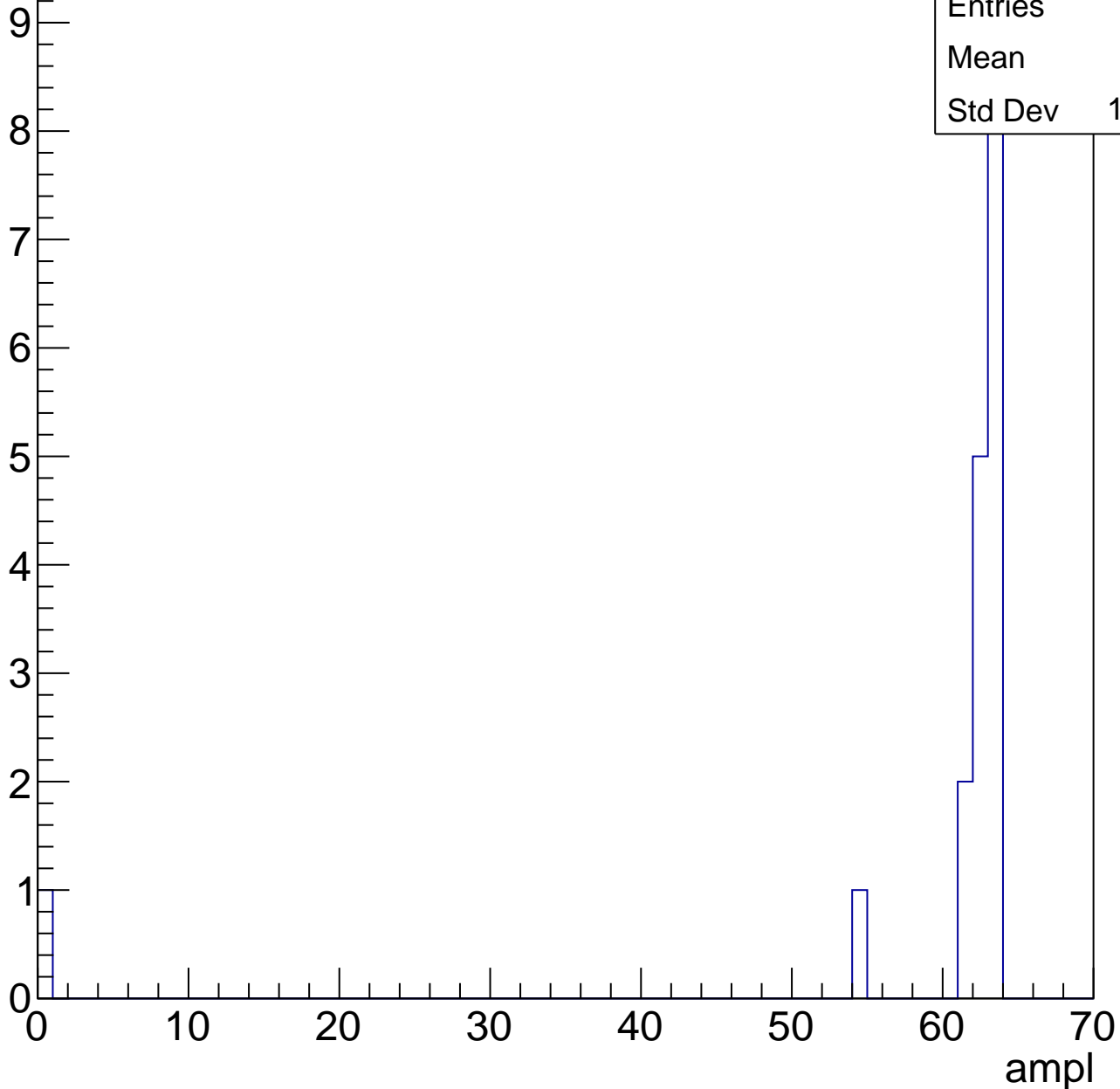


B0L101S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

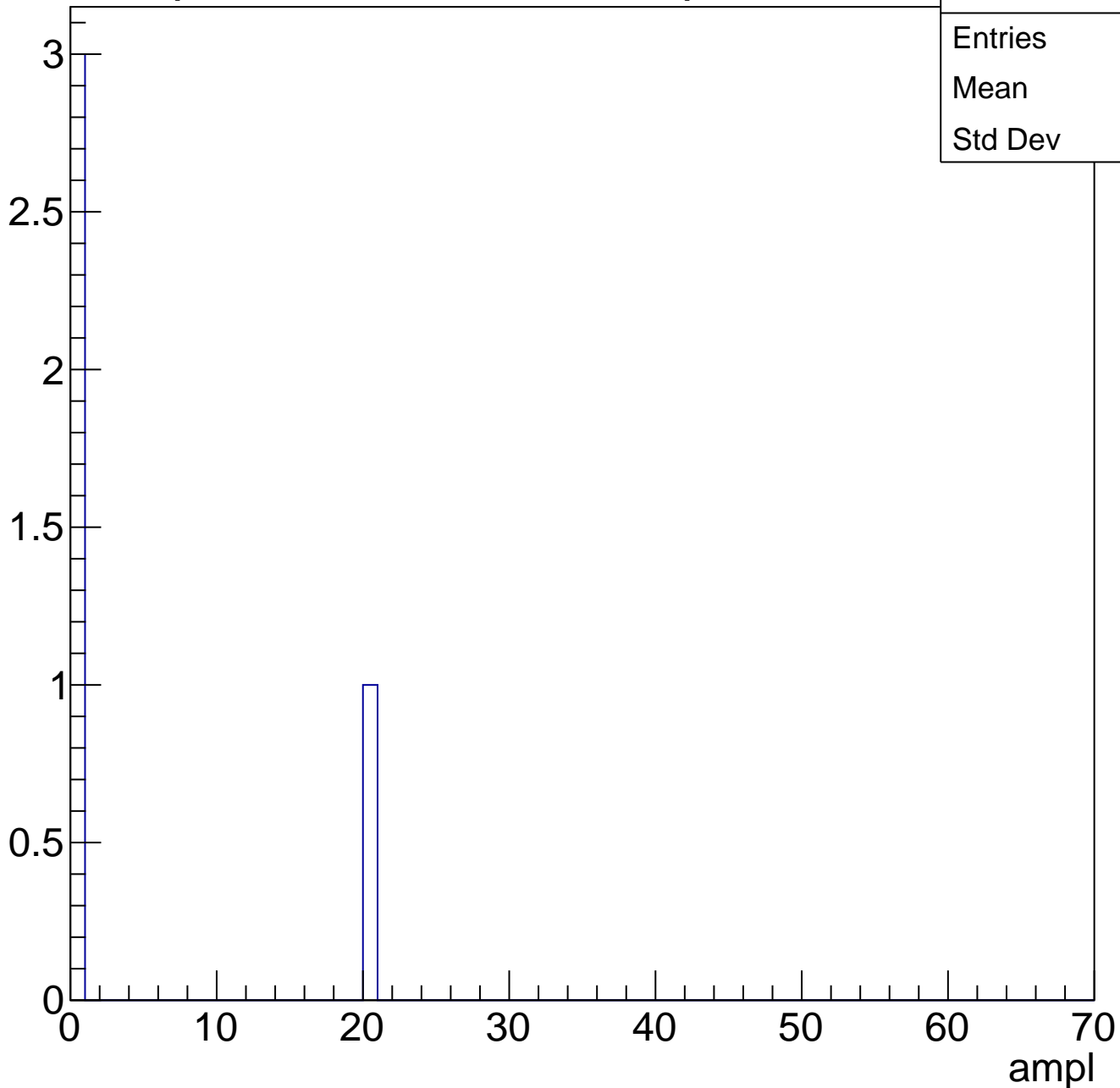
Entries	18
Mean	58.5
Std Dev	14.33



B0L101S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	5
Std Dev	8.66

B0L101S, U14-ch108, adc0

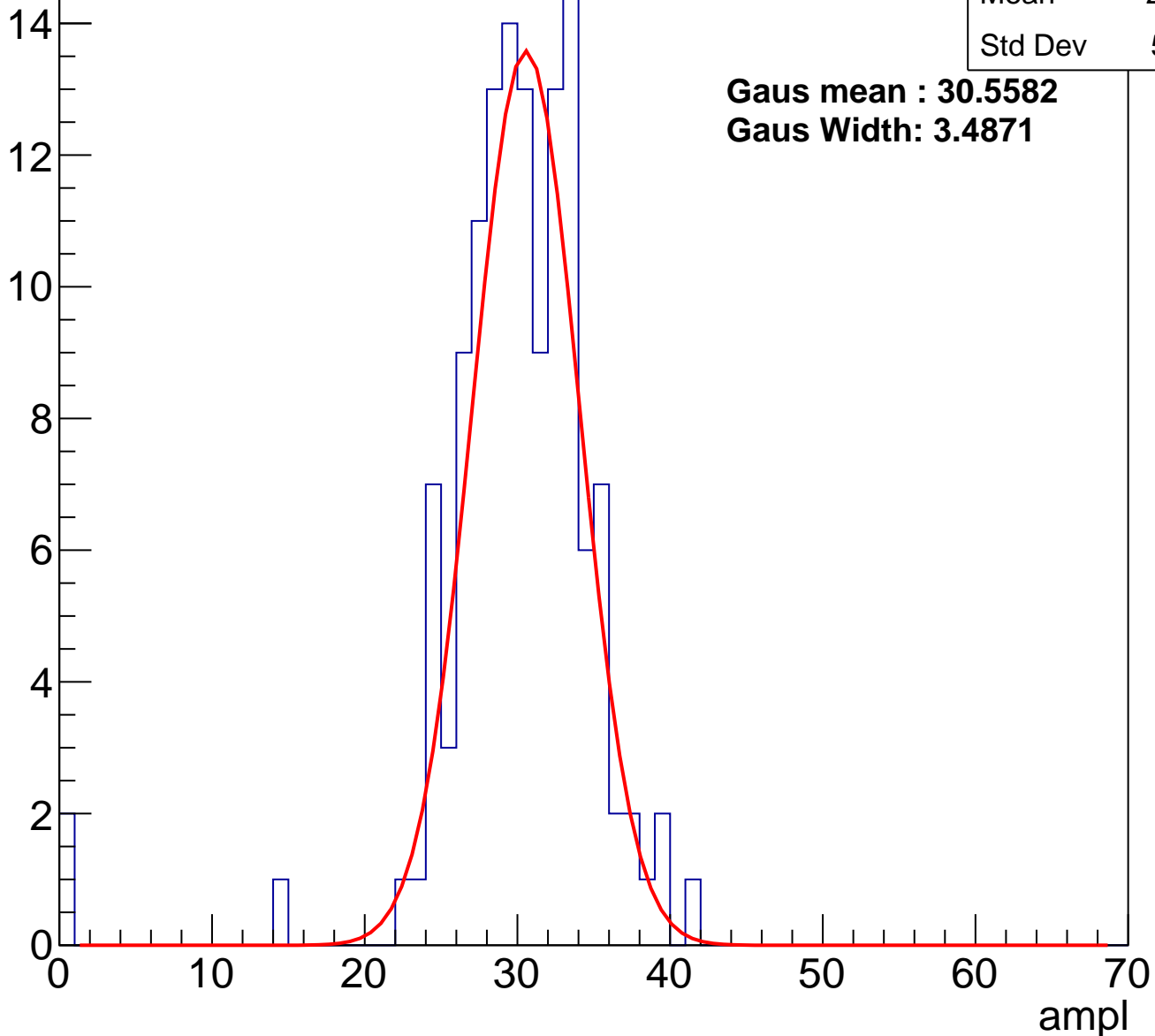
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	29.59
Std Dev	5.321

Gaus mean : 30.5582

Gaus Width: 3.4871

Entry



B0L101S, U14-ch108, adc1

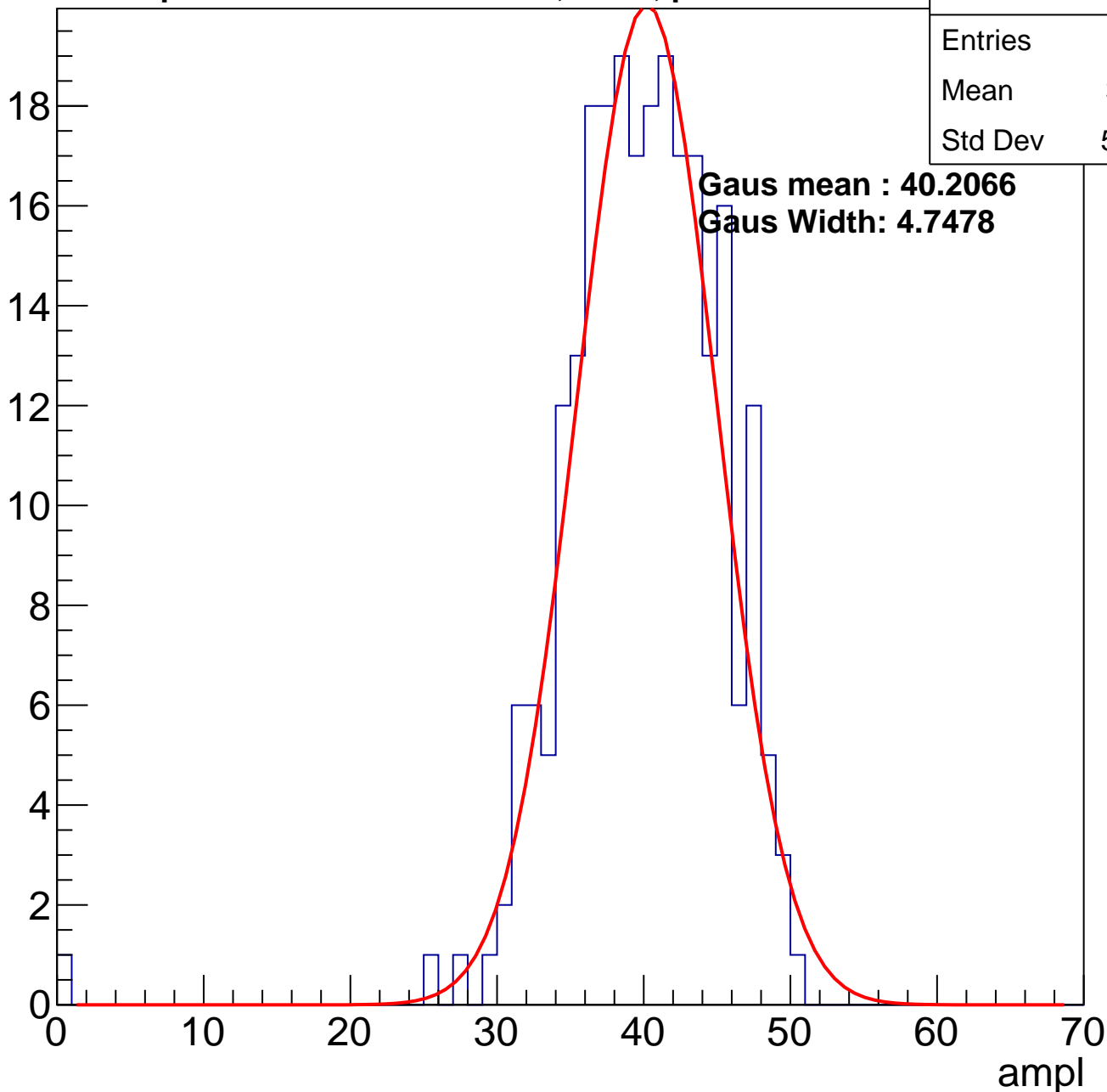
calib_packv5_042523_0143.root, FC#1, port C1

Entries	247
Mean	39.51
Std Dev	5.318

Gaus mean : 40.2066

Gaus Width: 4.7478

Entry



B0L101S, U14-ch108, adc2

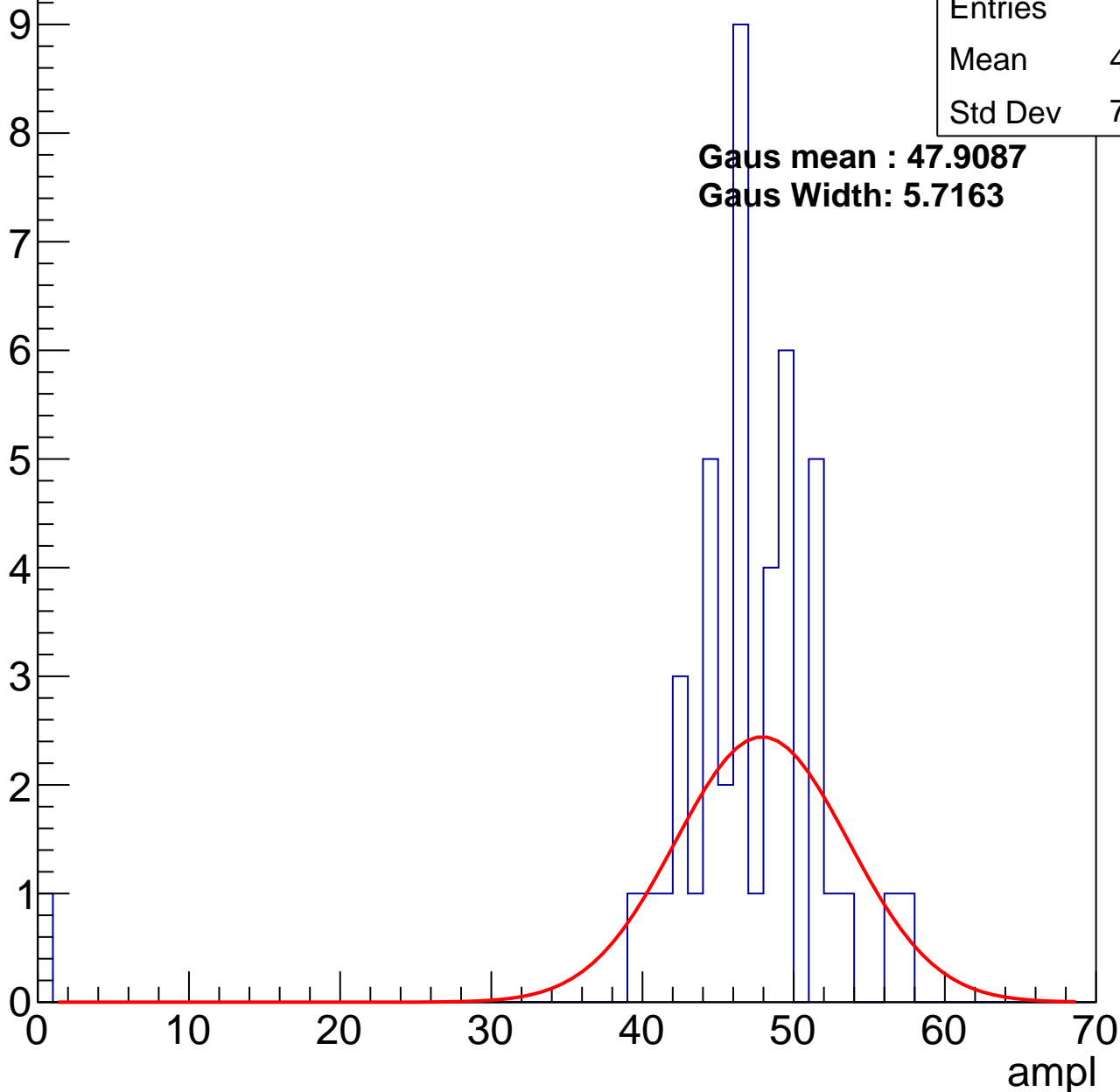
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	44
Mean	45.89
Std Dev	7.986

Gaus mean : 47.9087

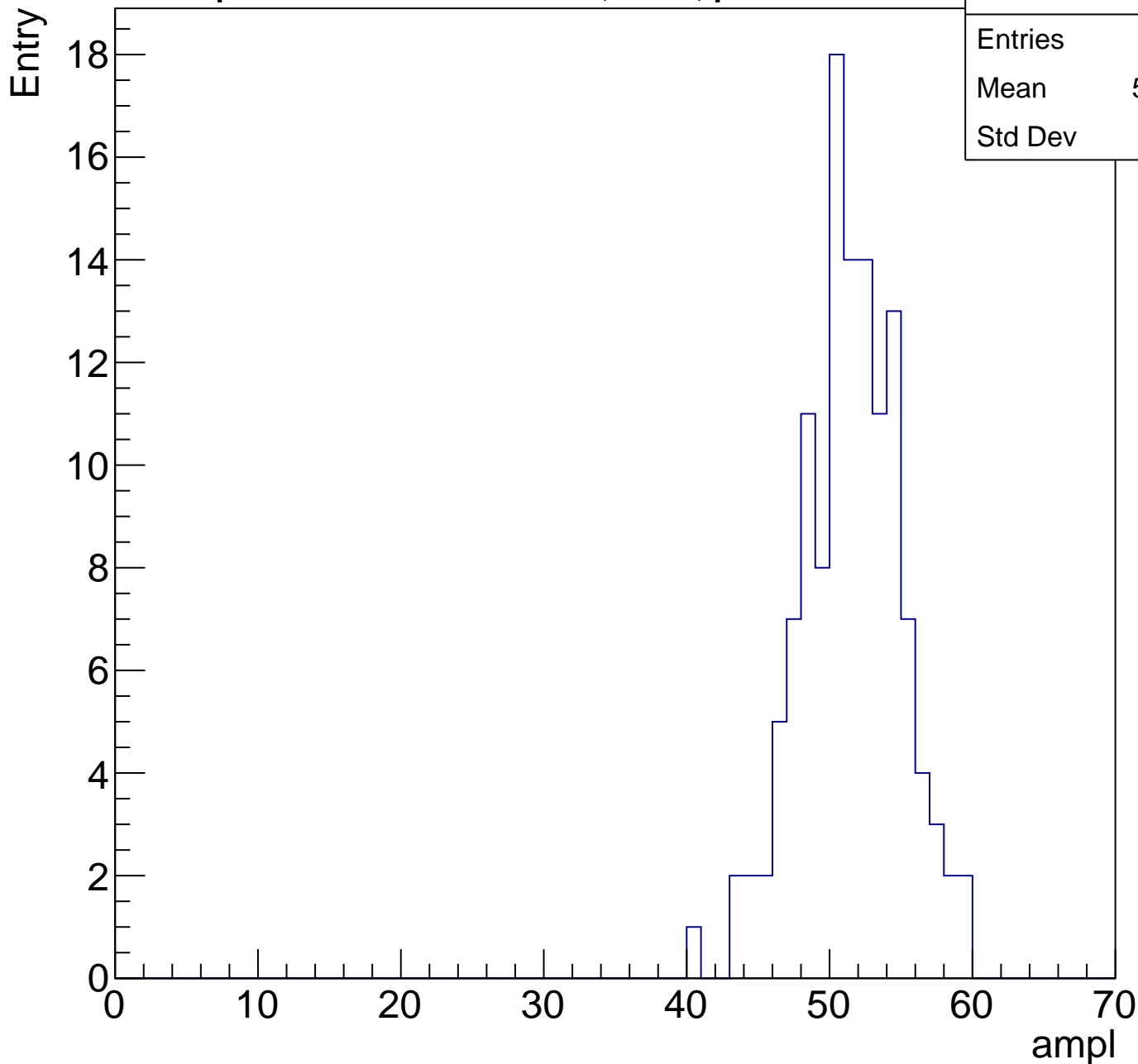
Gaus Width: 5.7163



B0L101S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	50.98
Std Dev	3.48



B0L101S, U14-ch108, adc4

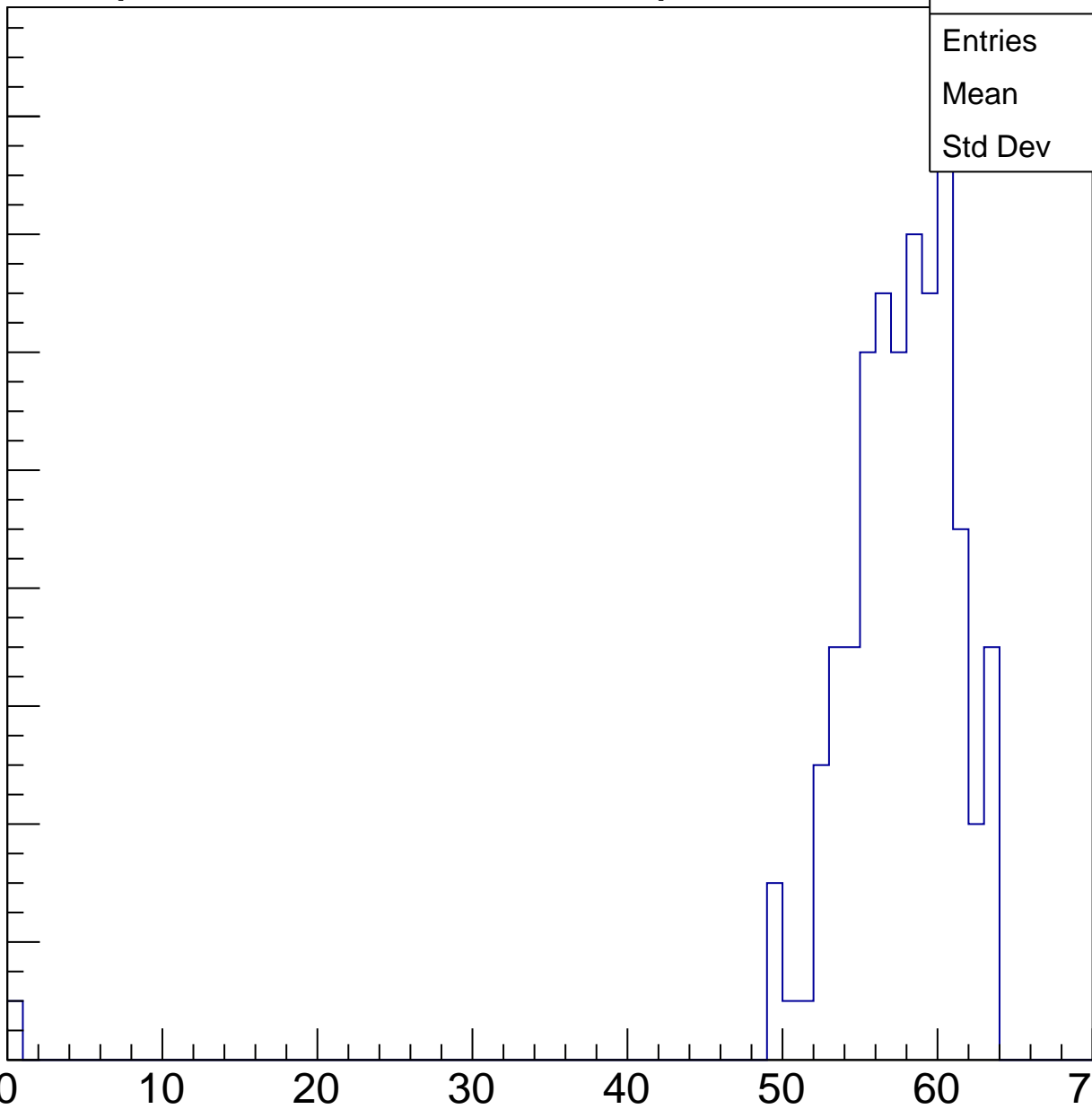
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	56.87
Std Dev	6.042

ampl



B0L101S, U14-ch108, adc5

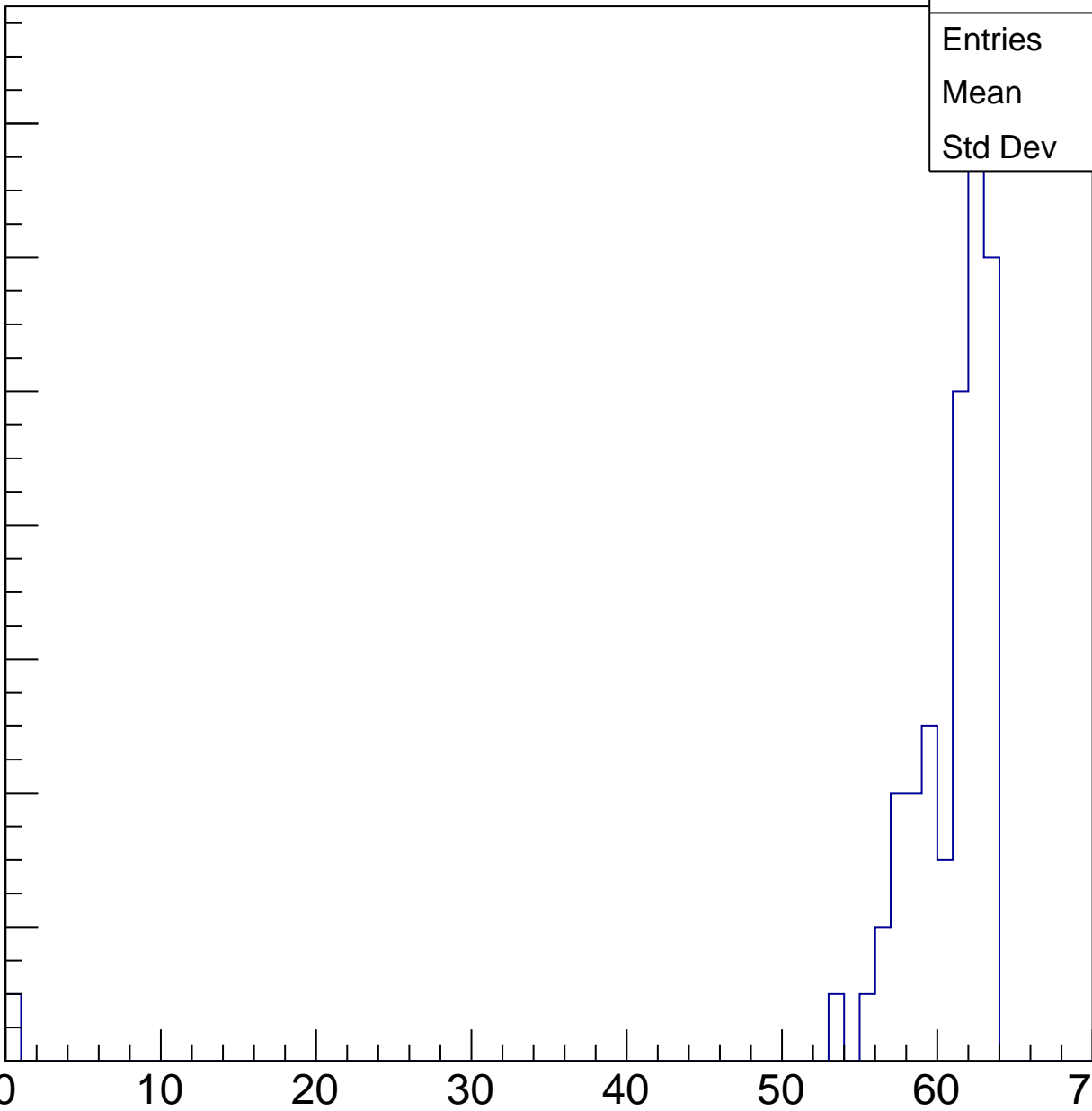
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	59.5
Std Dev	8.228

ampl



B0L101S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch109, adc0

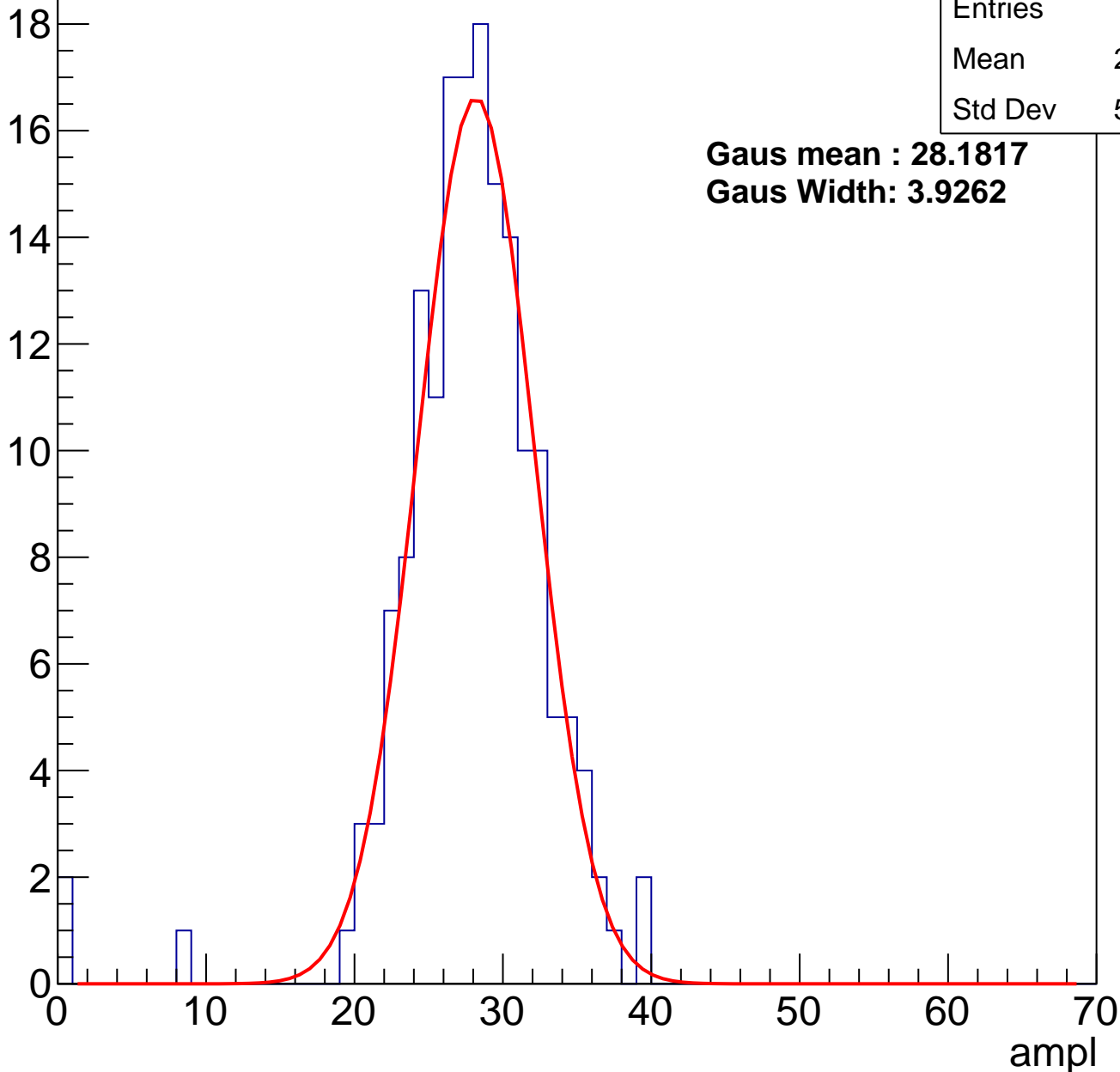
calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	27.38
Std Dev	5.126

Gaus mean : 28.1817

Gaus Width: 3.9262

Entry



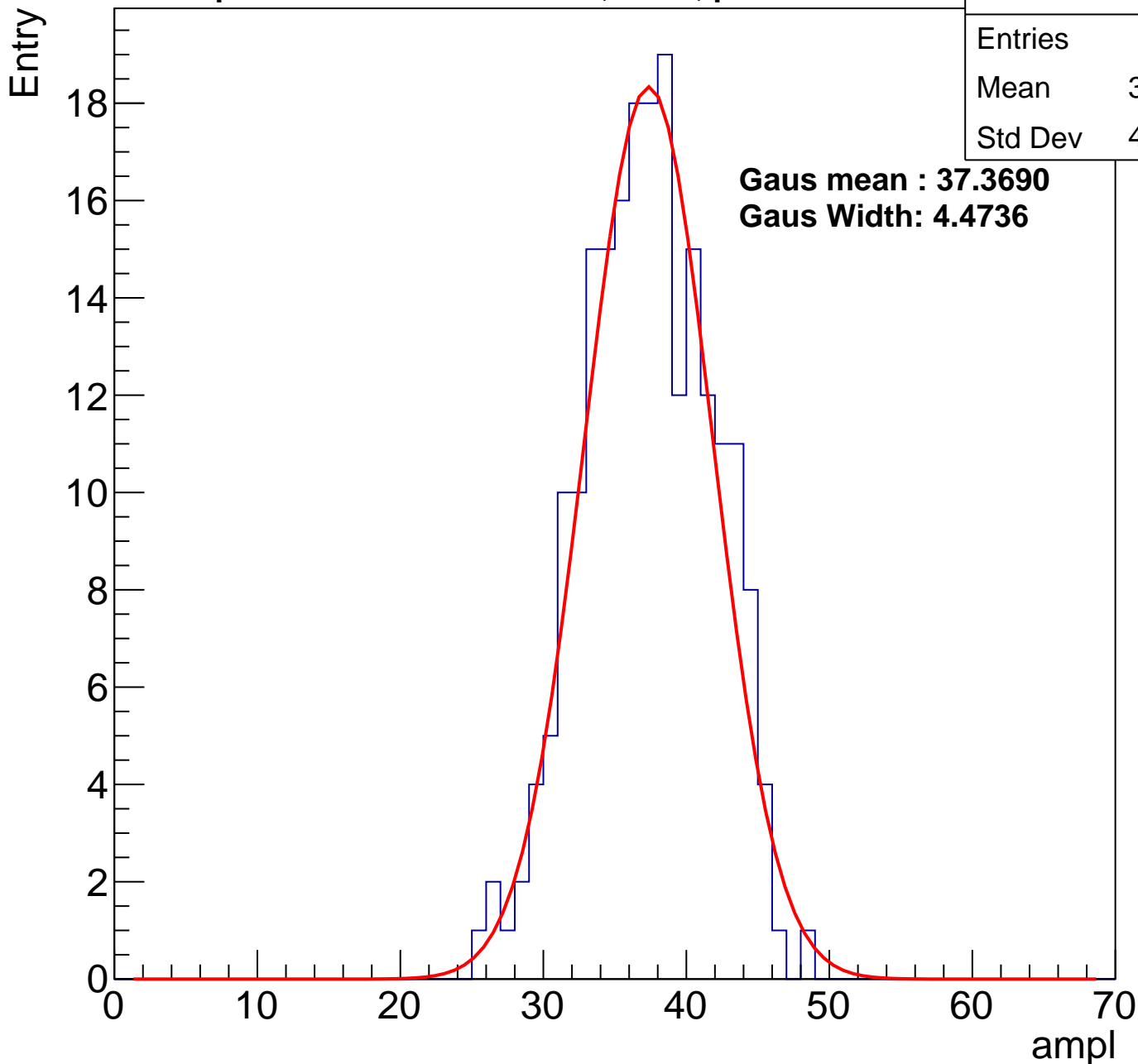
B0L101S, U14-ch109, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	211
Mean	36.86
Std Dev	4.423

Gaus mean : 37.3690

Gaus Width: 4.4736



B0L101S, U14-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

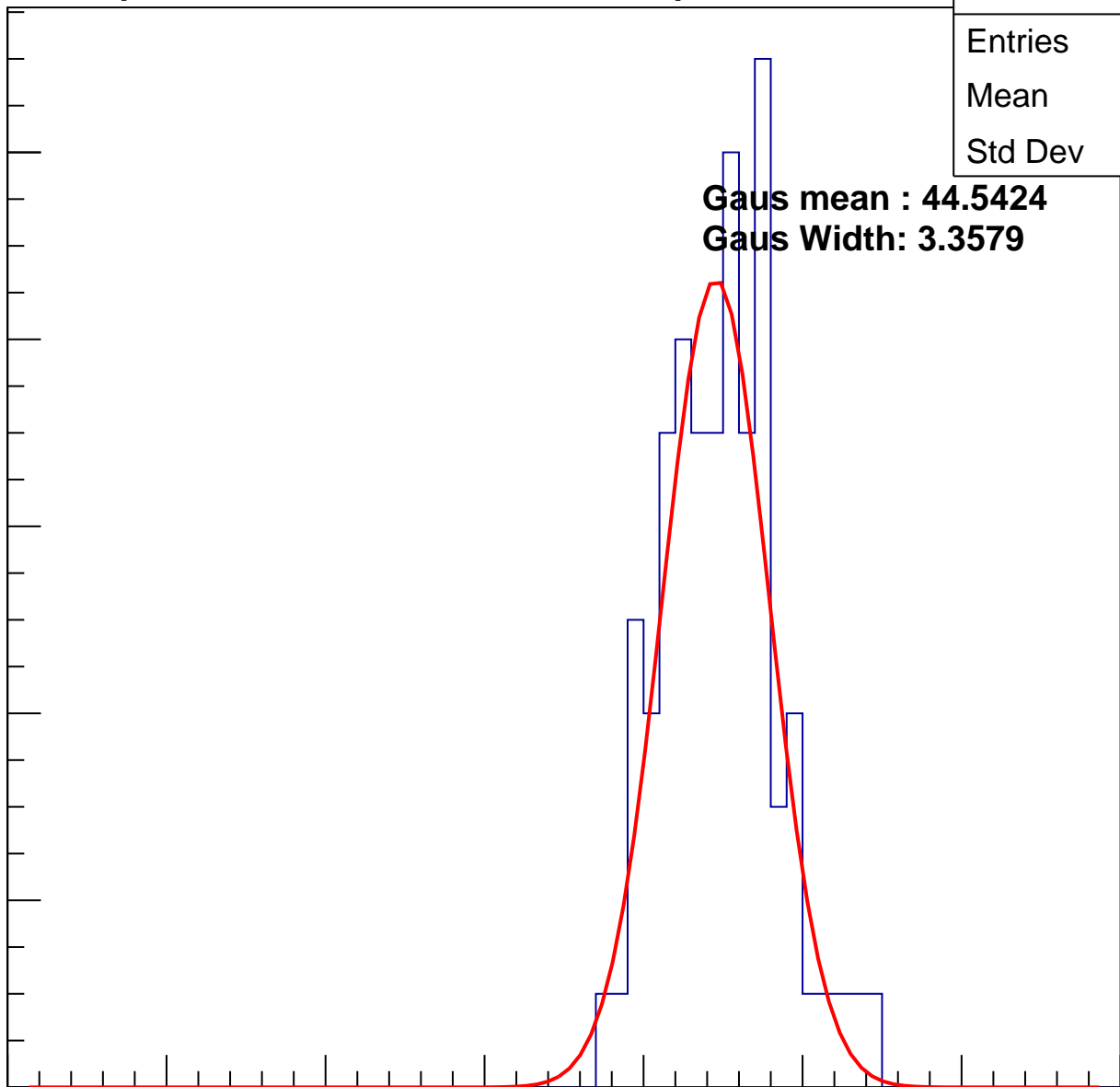
70

ampl

Entries	80
Mean	44.39
Std Dev	3.498

Gaus mean : 44.5424

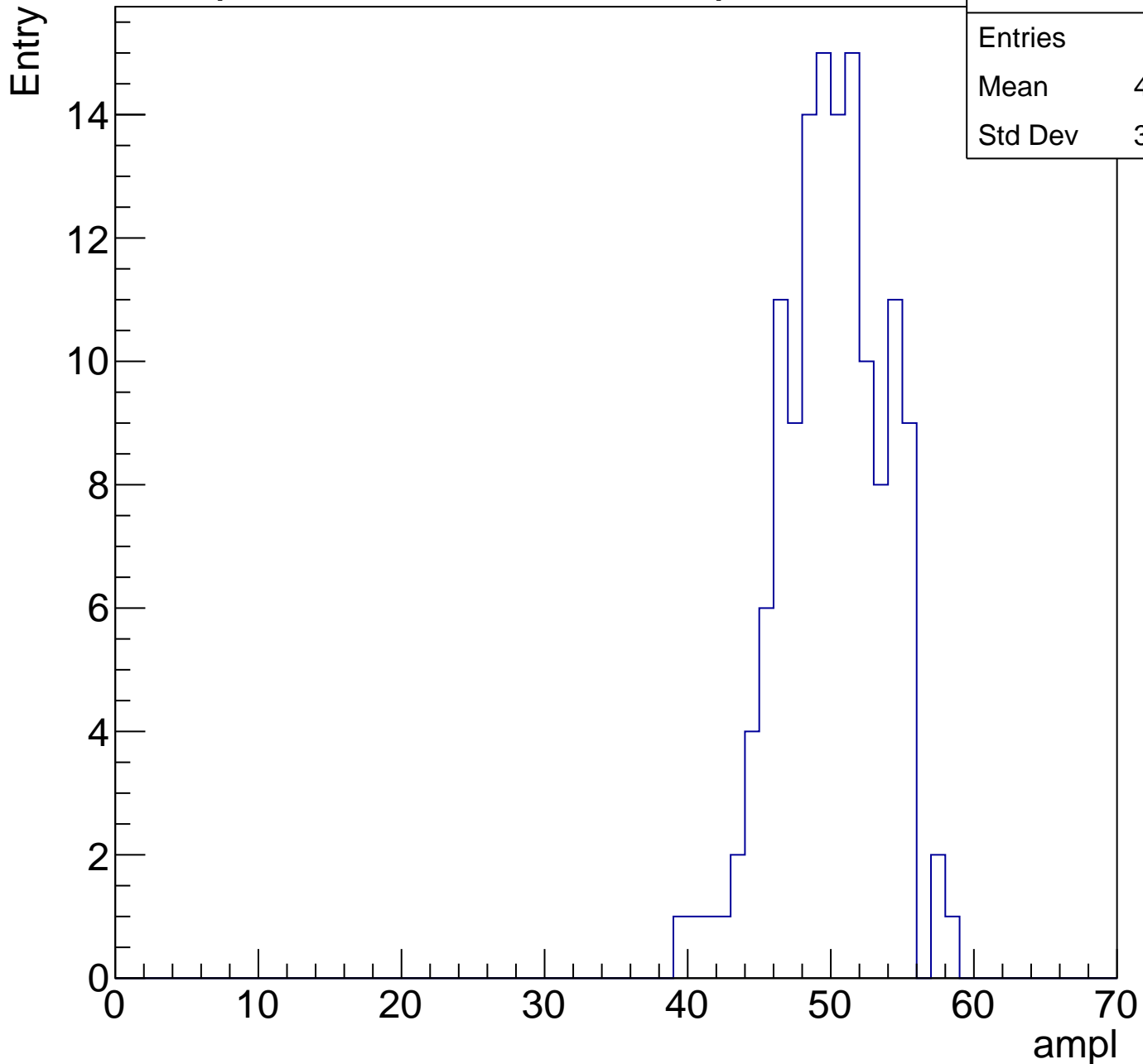
Gaus Width: 3.3579



B0L101S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

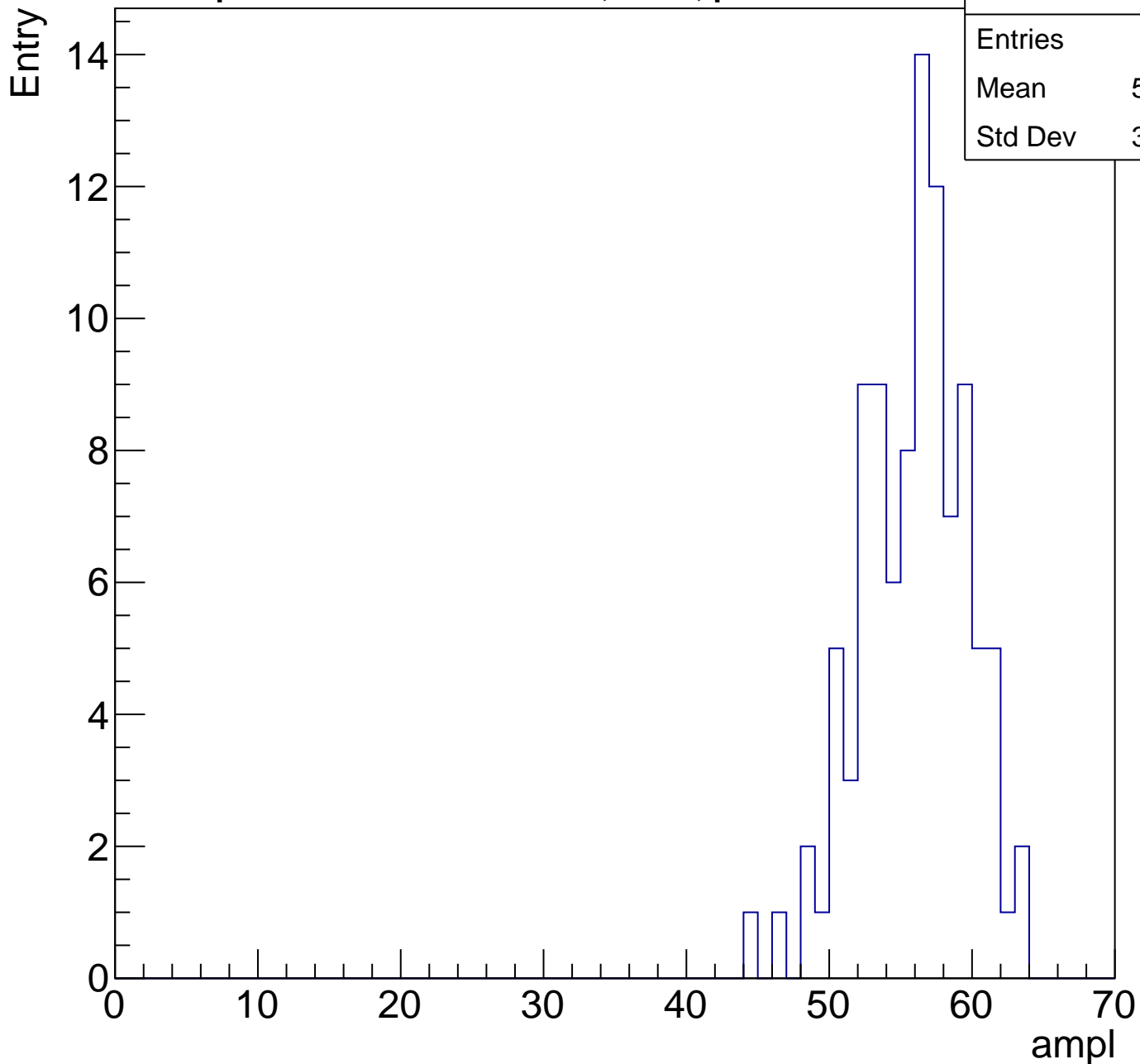
Entries	135
Mean	49.63
Std Dev	3.608



B0L101S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	100
Mean	55.45
Std Dev	3.692



B0L101S, U14-ch109, adc5

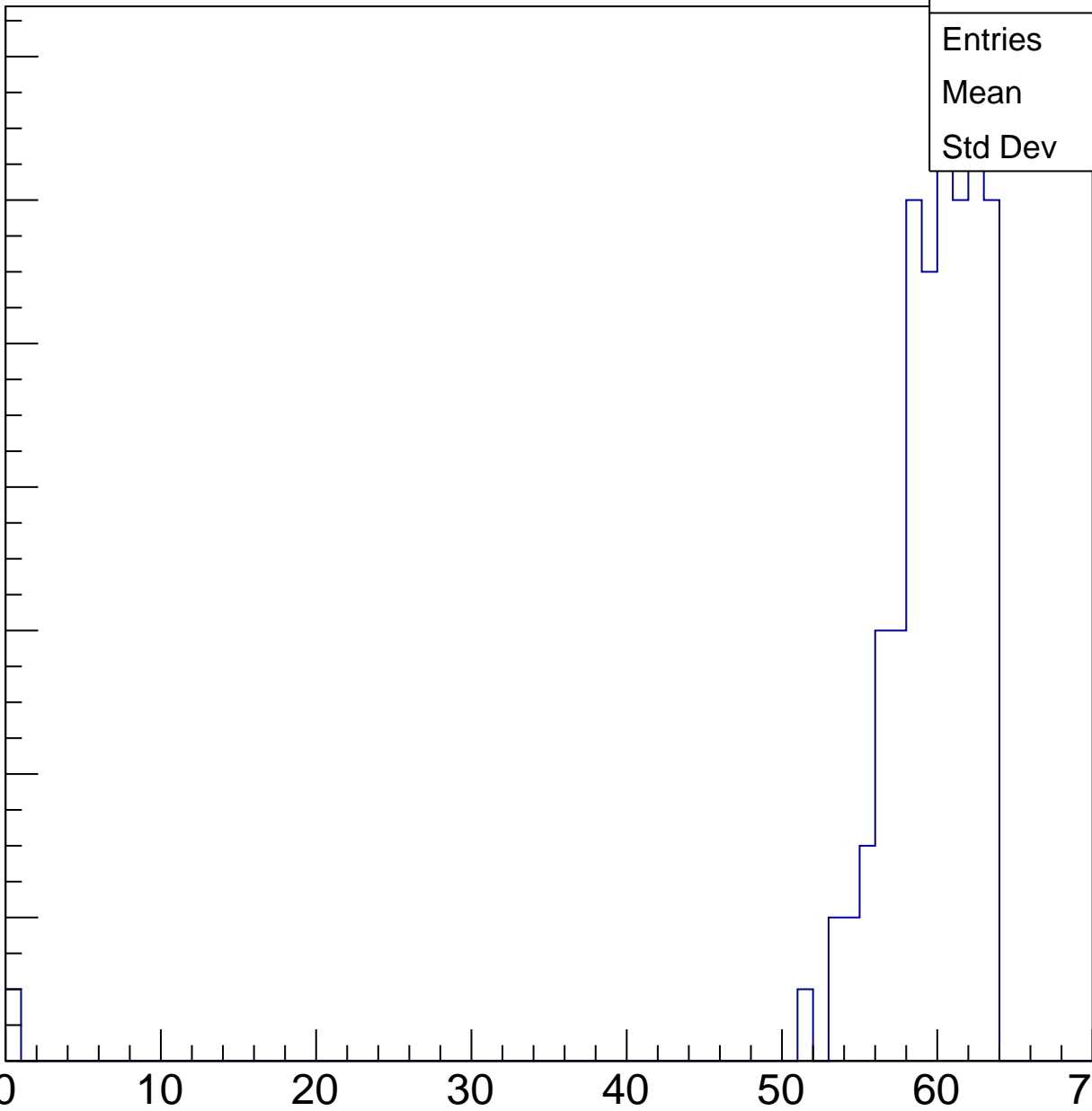
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.84
Std Dev	6.601

ampl

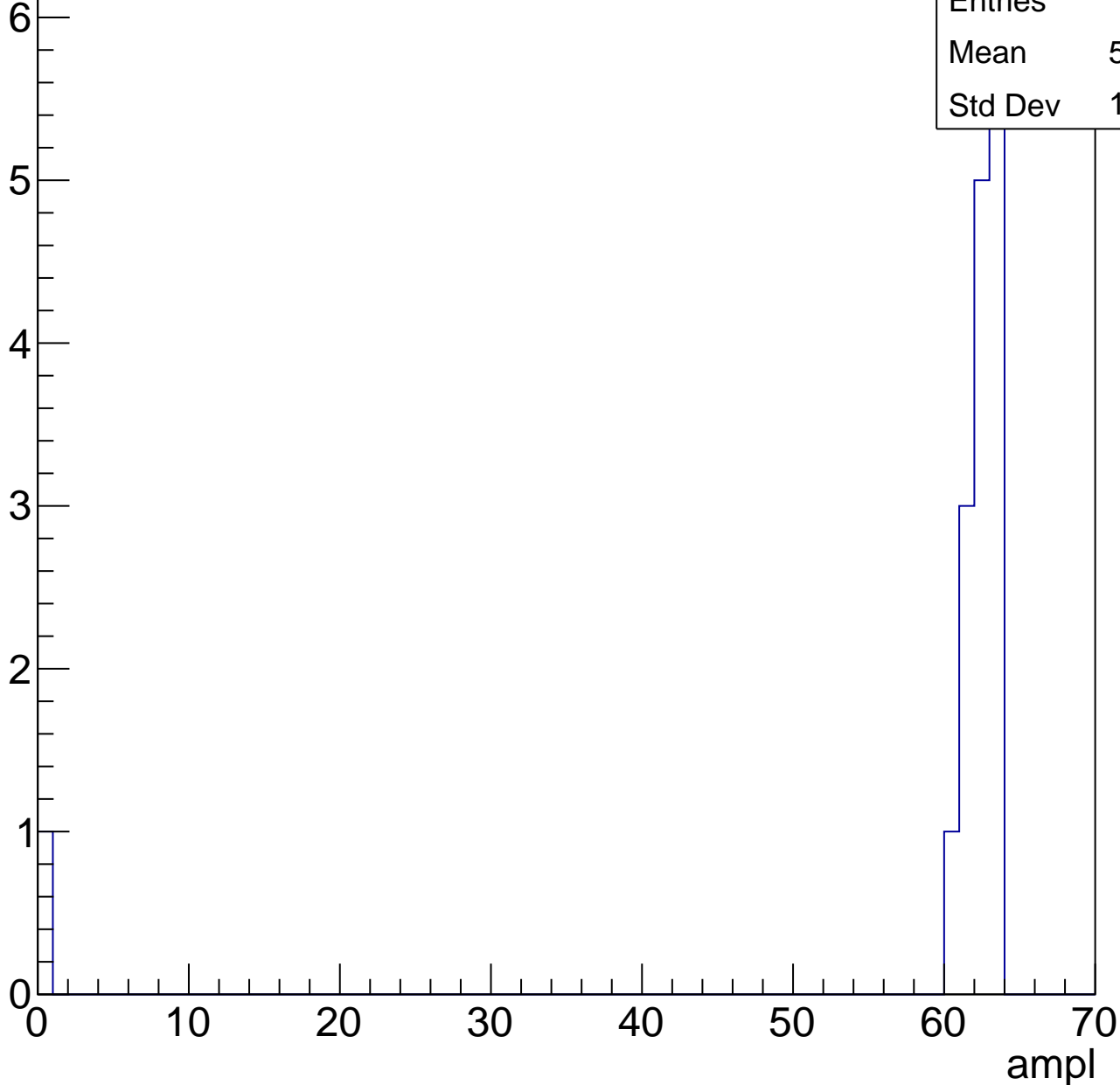


B0L101S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	58.19
Std Dev	15.05



B0L101S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



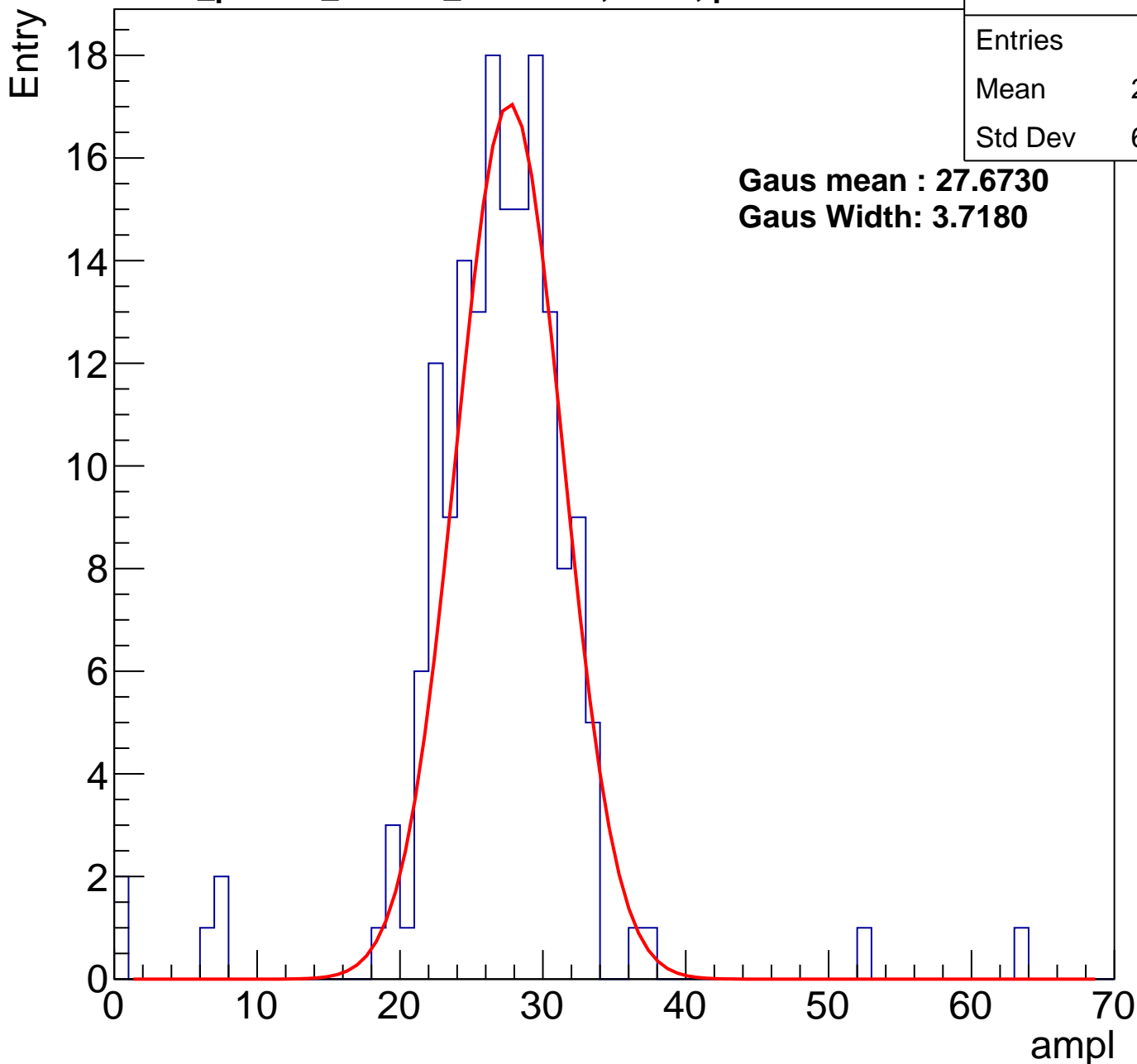
B0L101S, U14-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	26.43
Std Dev	6.278

Gaus mean : 27.6730

Gaus Width: 3.7180



B0L101S, U14-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	207
Mean	35.86
Std Dev	5.004

Gaus mean : 36.6513

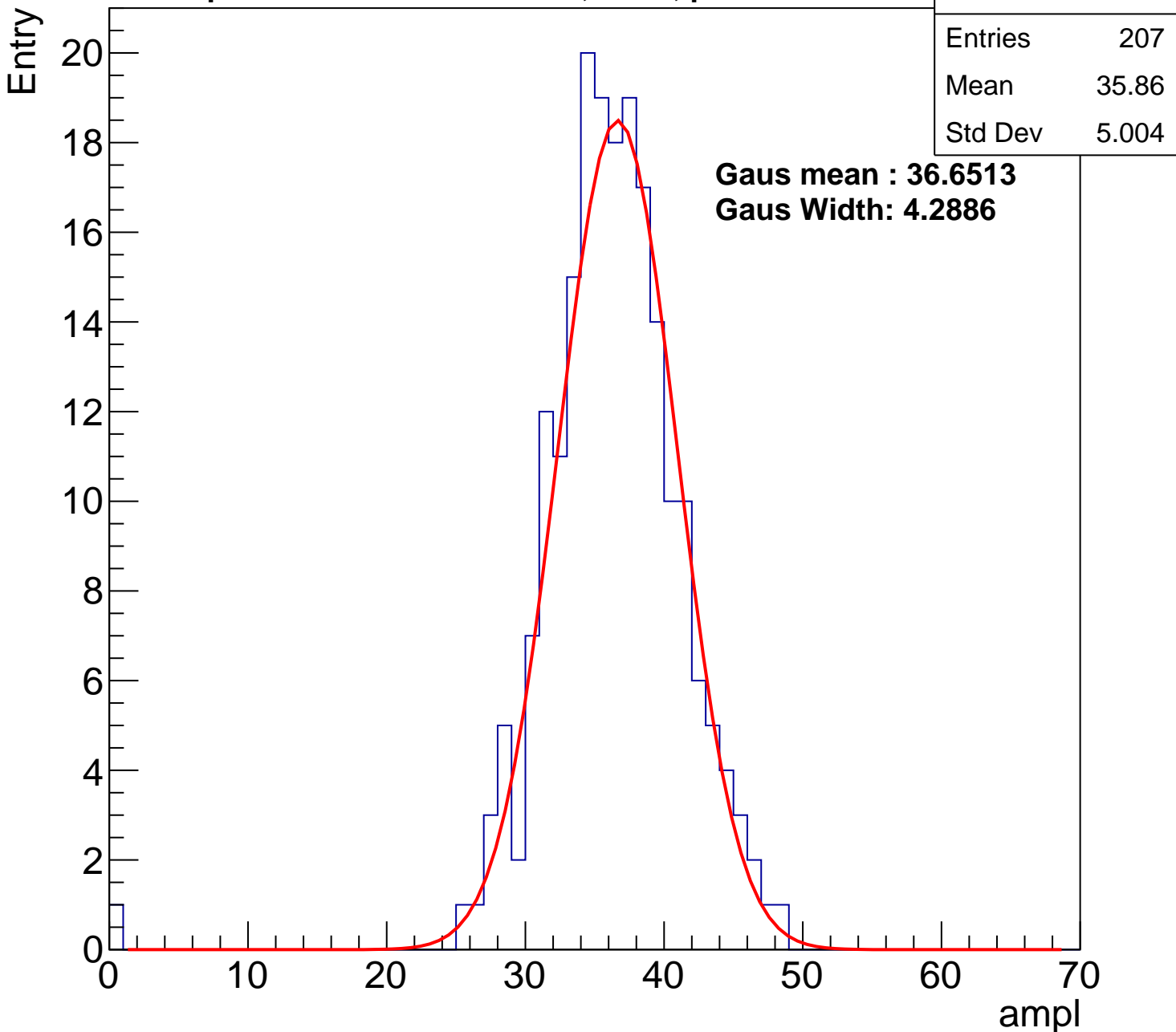
Gaus Width: 4.2886

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	57
Mean	42.33
Std Dev	2.799

Gaus mean : 42.7392

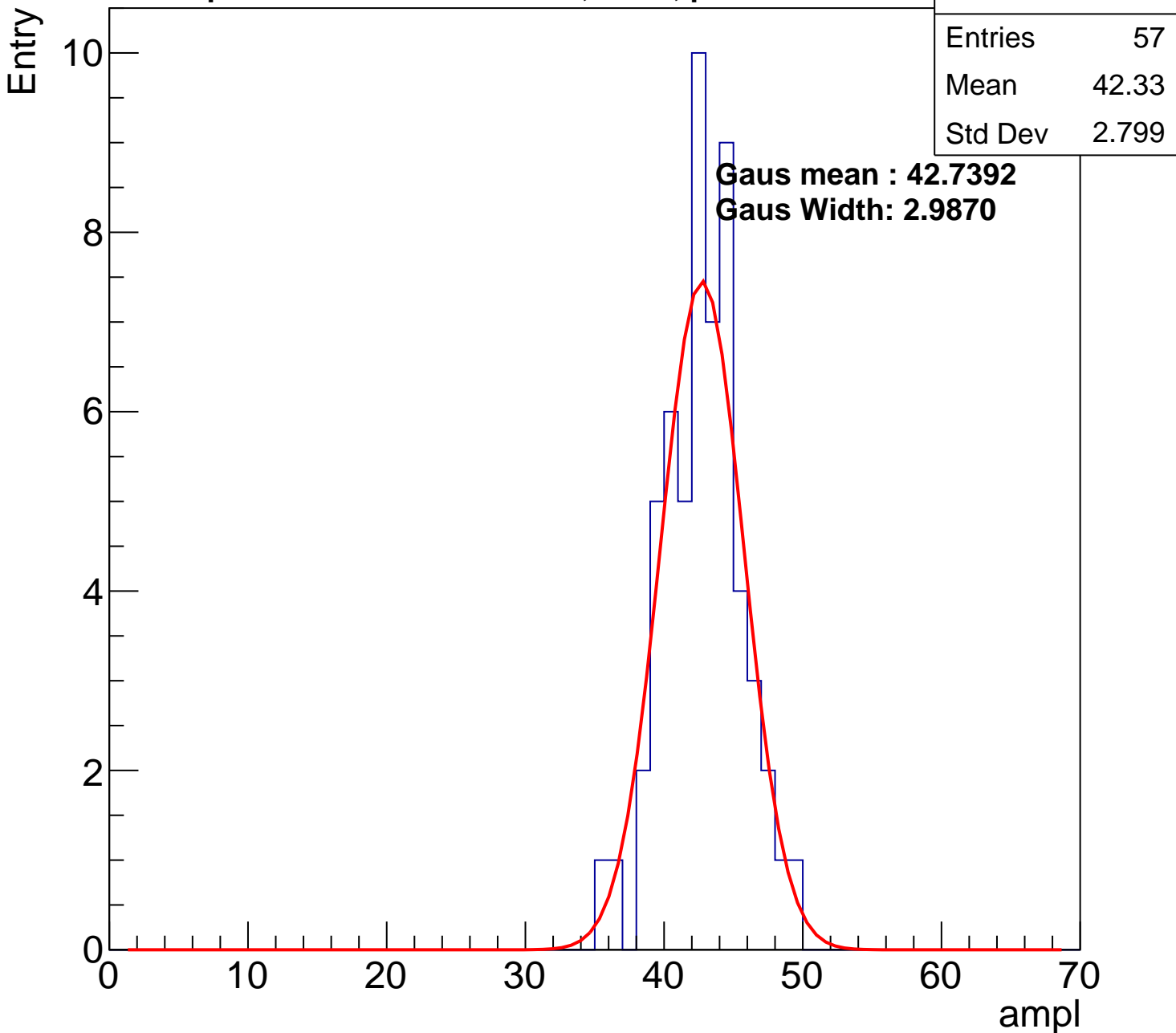
Gaus Width: 2.9870

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

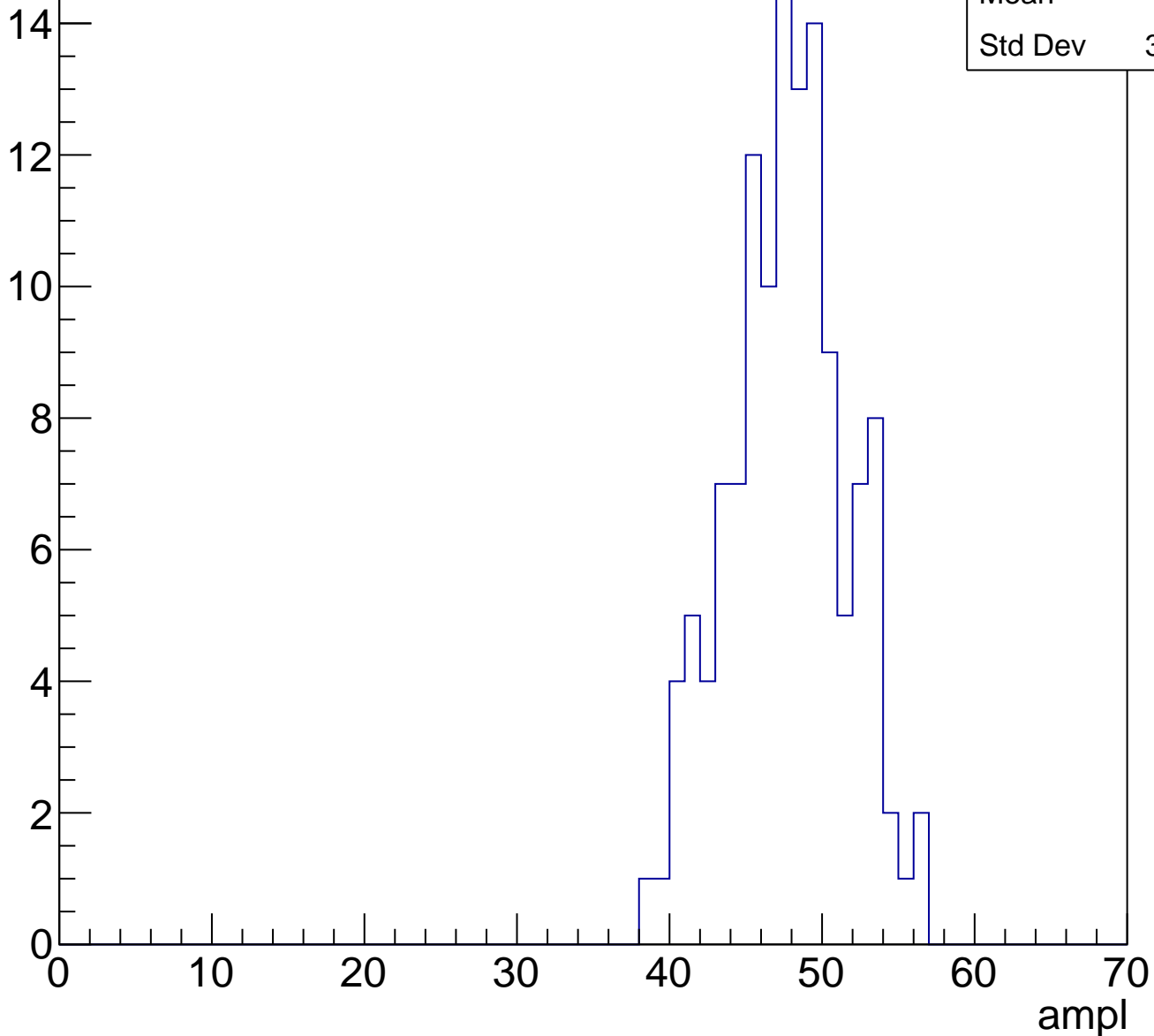


B0L101S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	47.26
Std Dev	3.829

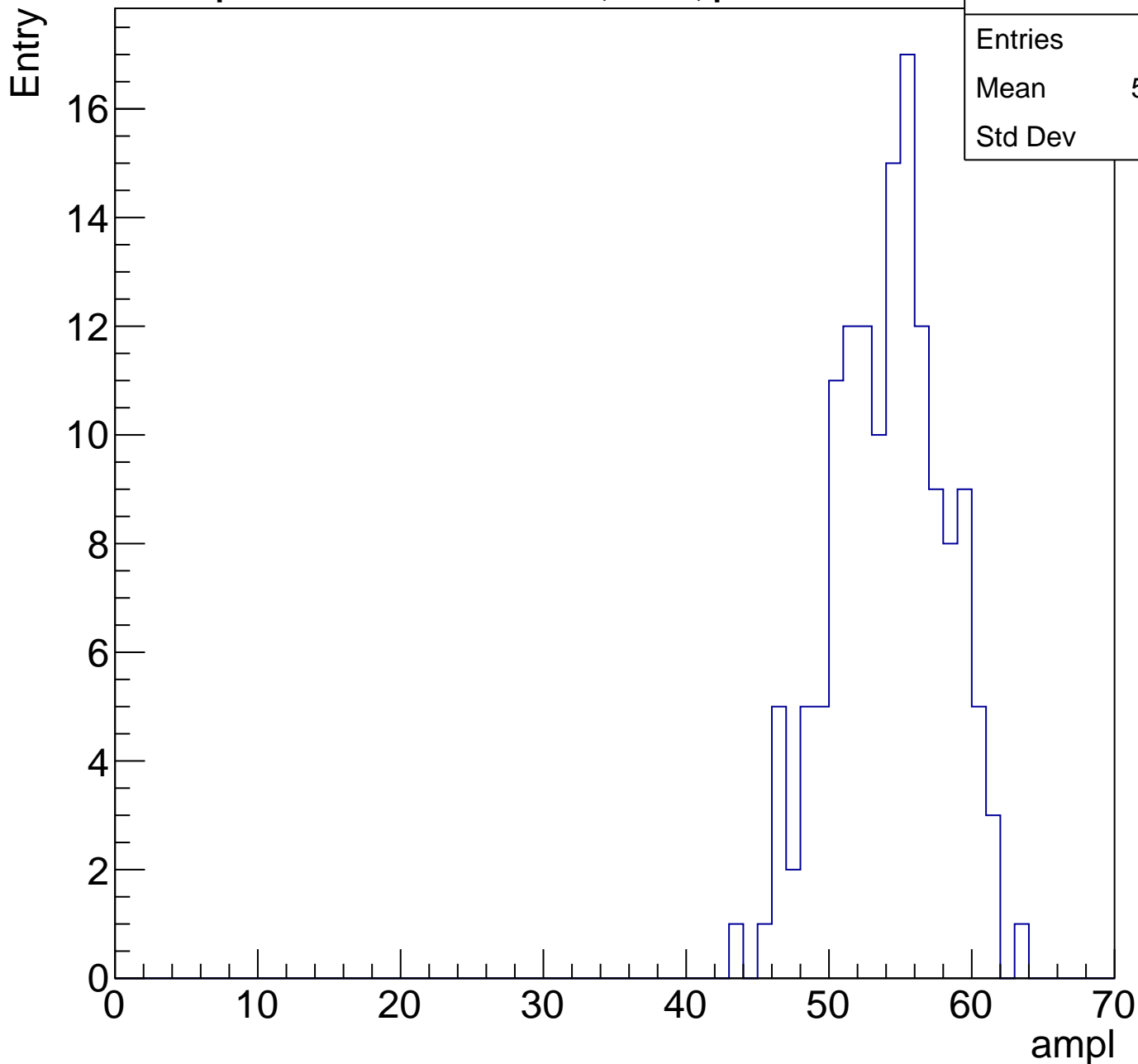
Entry



B0L101S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	53.73
Std Dev	3.89



B0L101S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

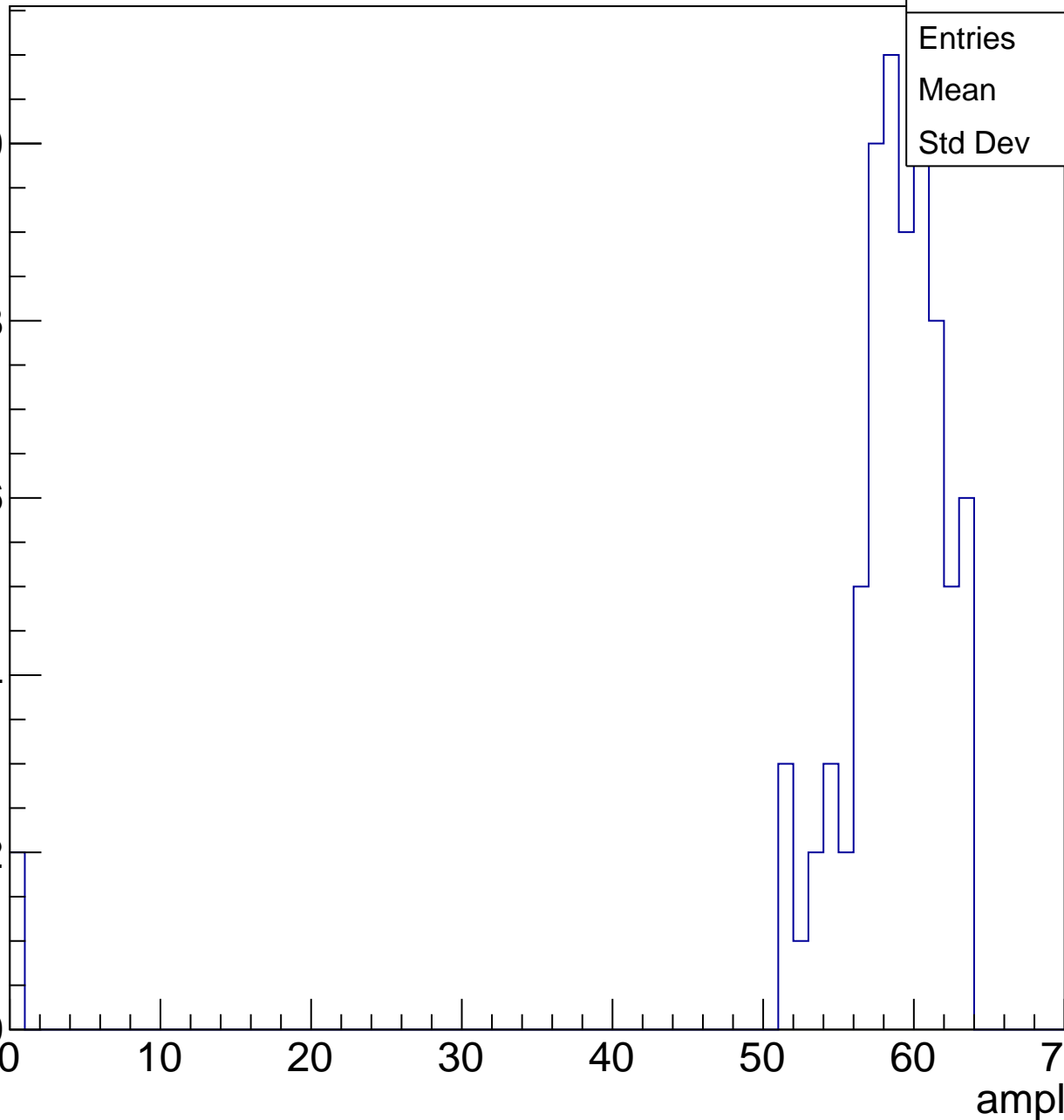
50

60

70

ampl

Entries	77
Mean	56.86
Std Dev	9.742



B0L101S, U14-ch110, adc6

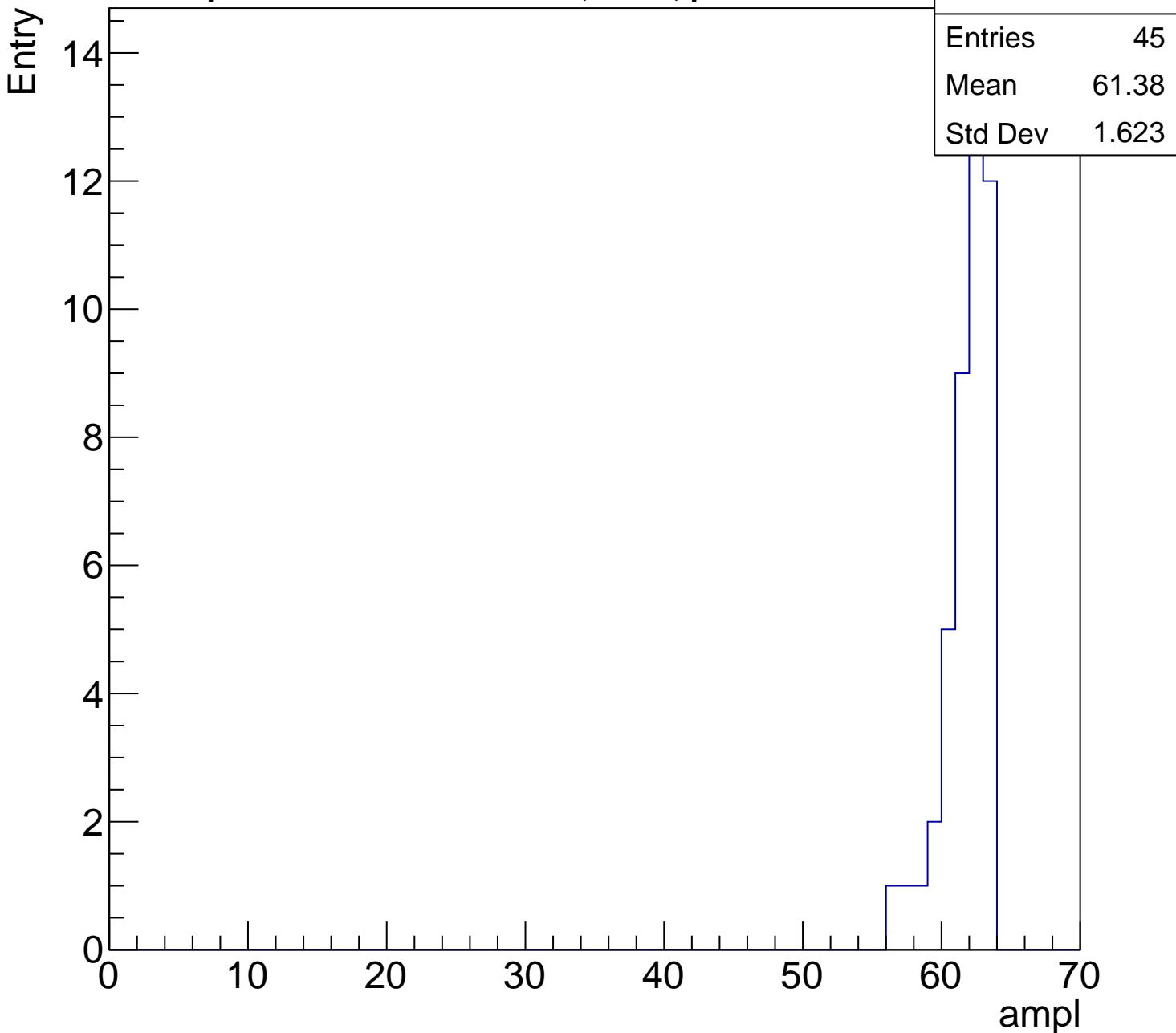
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	45
Mean	61.38
Std Dev	1.623

ampl



B0L101S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	47.33
Std Dev	21.45

ampl

B0L101S, U14-ch111, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	27.9
Std Dev	5.879

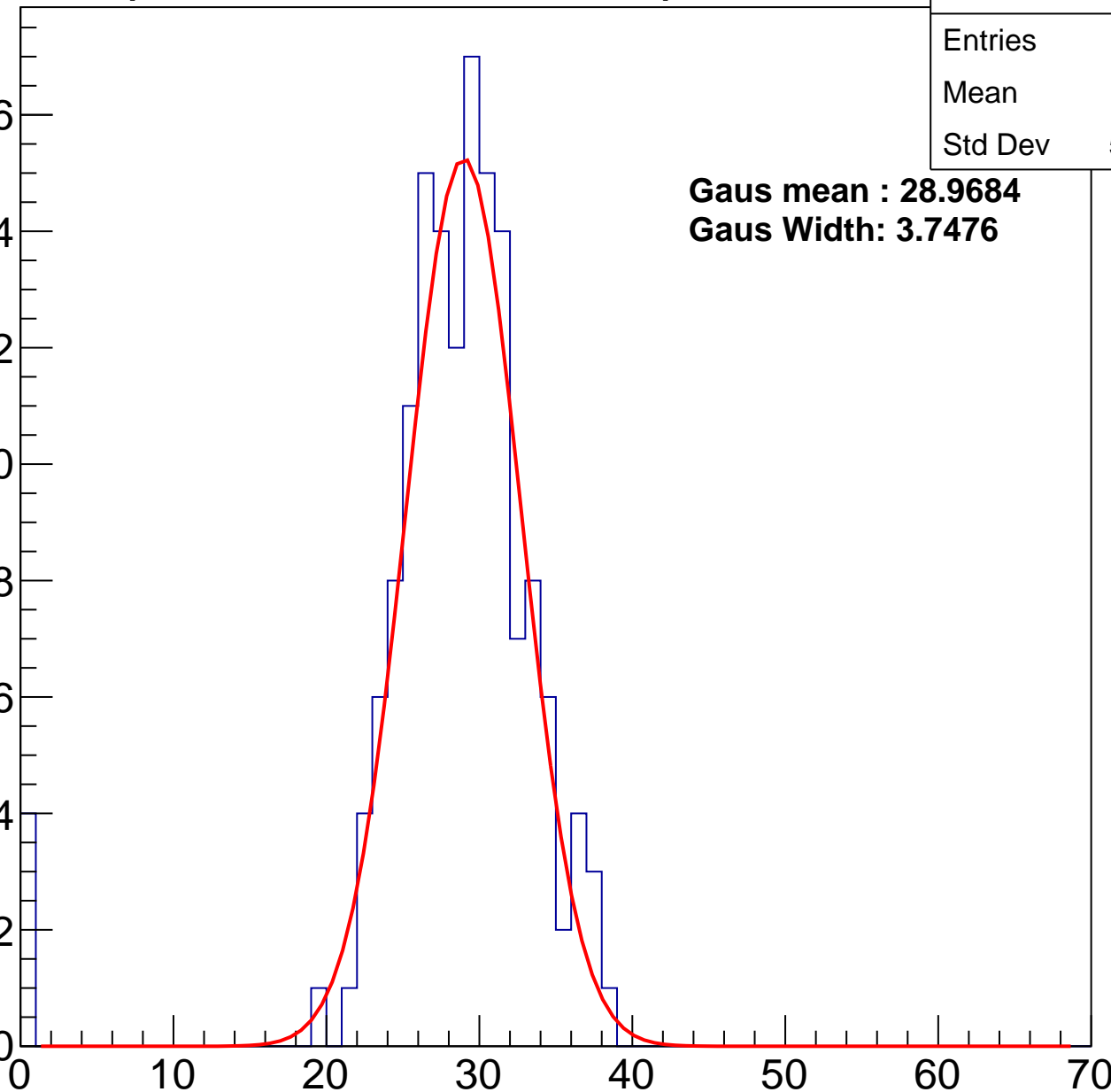
Gaus mean : 28.9684

Gaus Width: 3.7476

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch111, adc1

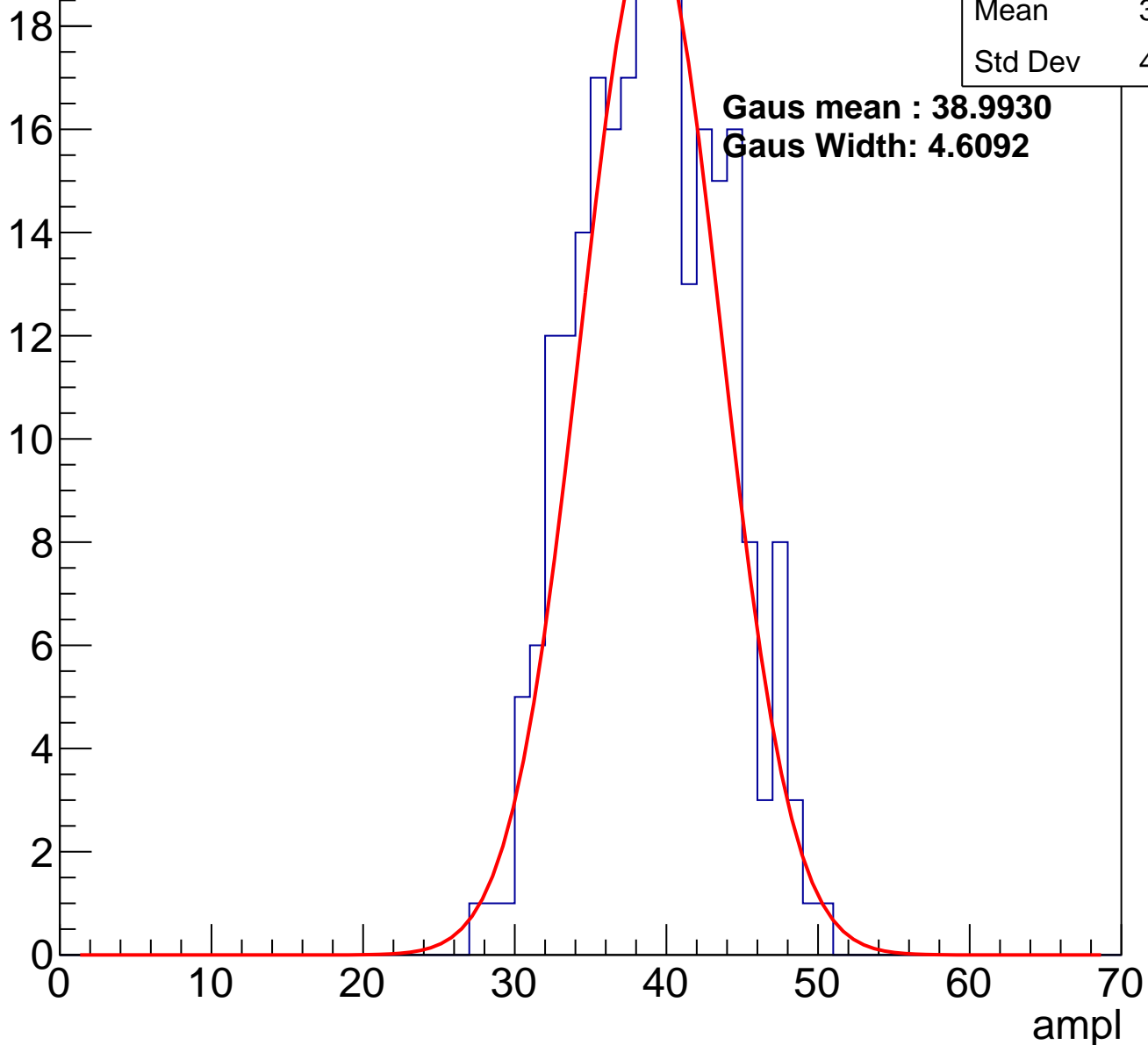
calib_packv5_042523_0143.root, FC#1, port C1

Entries	243
Mean	38.56
Std Dev	4.646

Gaus mean : 38.9930

Gaus Width: 4.6092

Entry



B0L101S, U14-ch111, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	55
Mean	45.31
Std Dev	3.346

Gaus mean : 45.8632

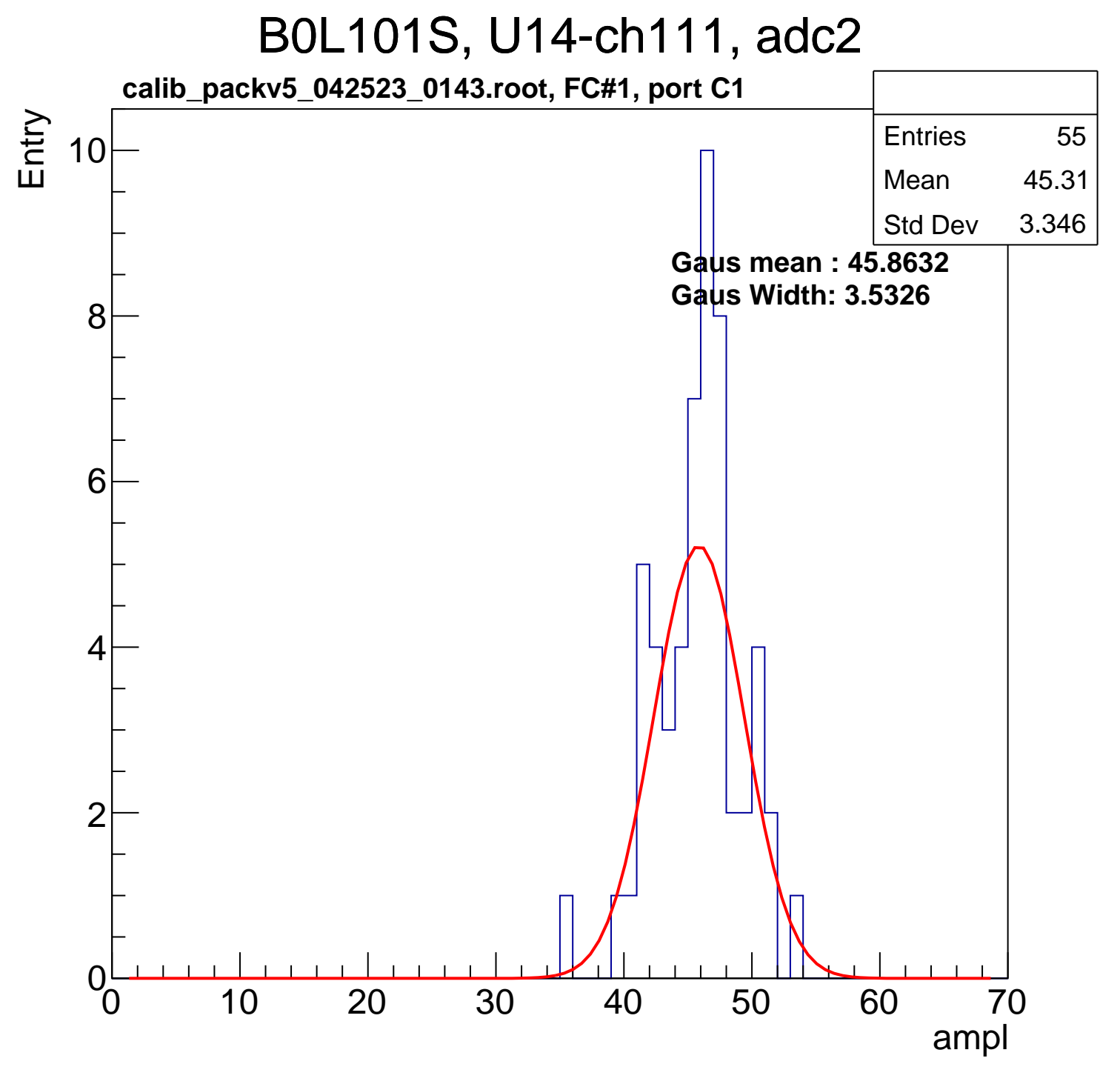
Gaus Width: 3.5326

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

128

Mean

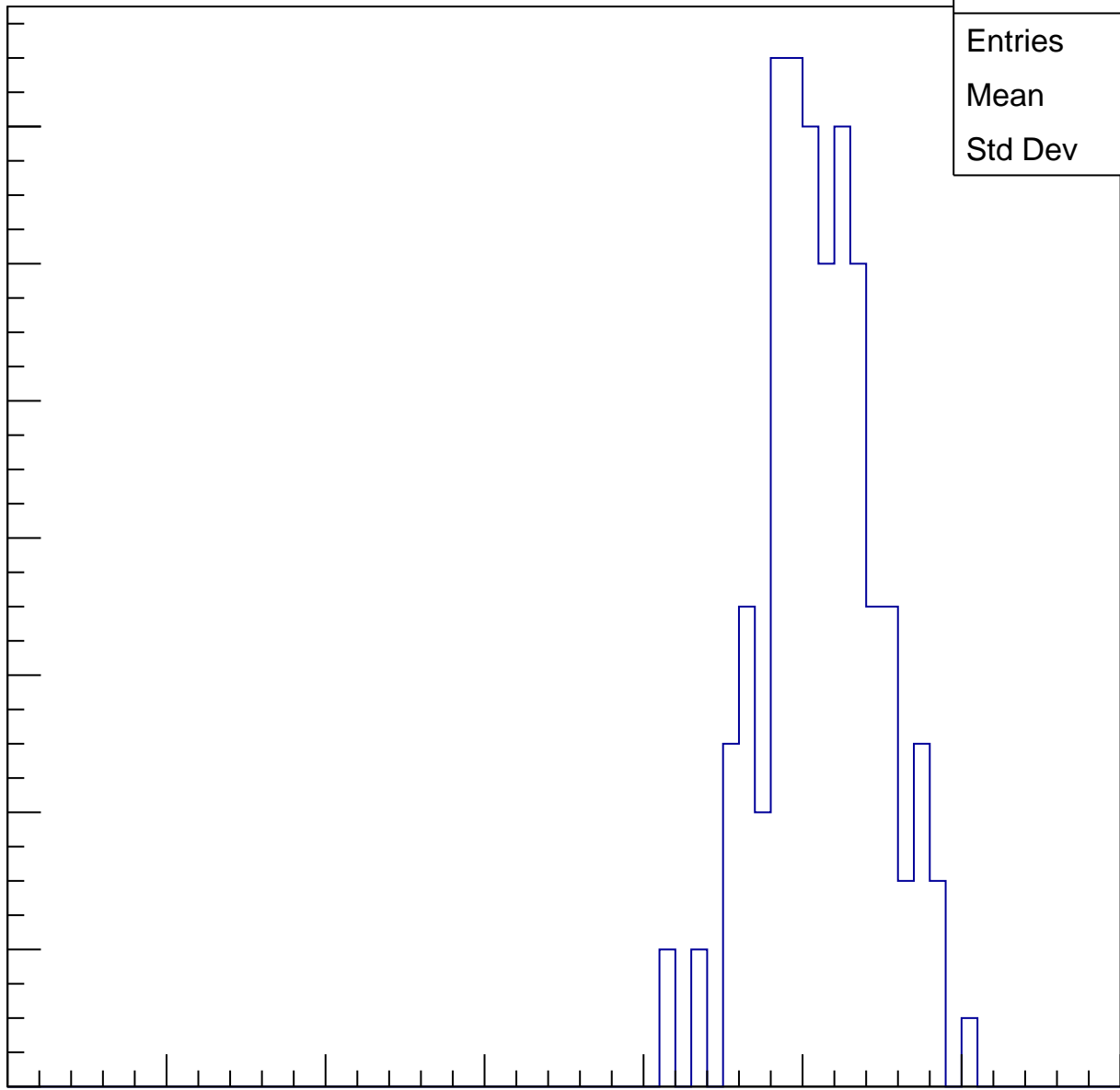
50.66

Std Dev

3.602

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch111, adc4

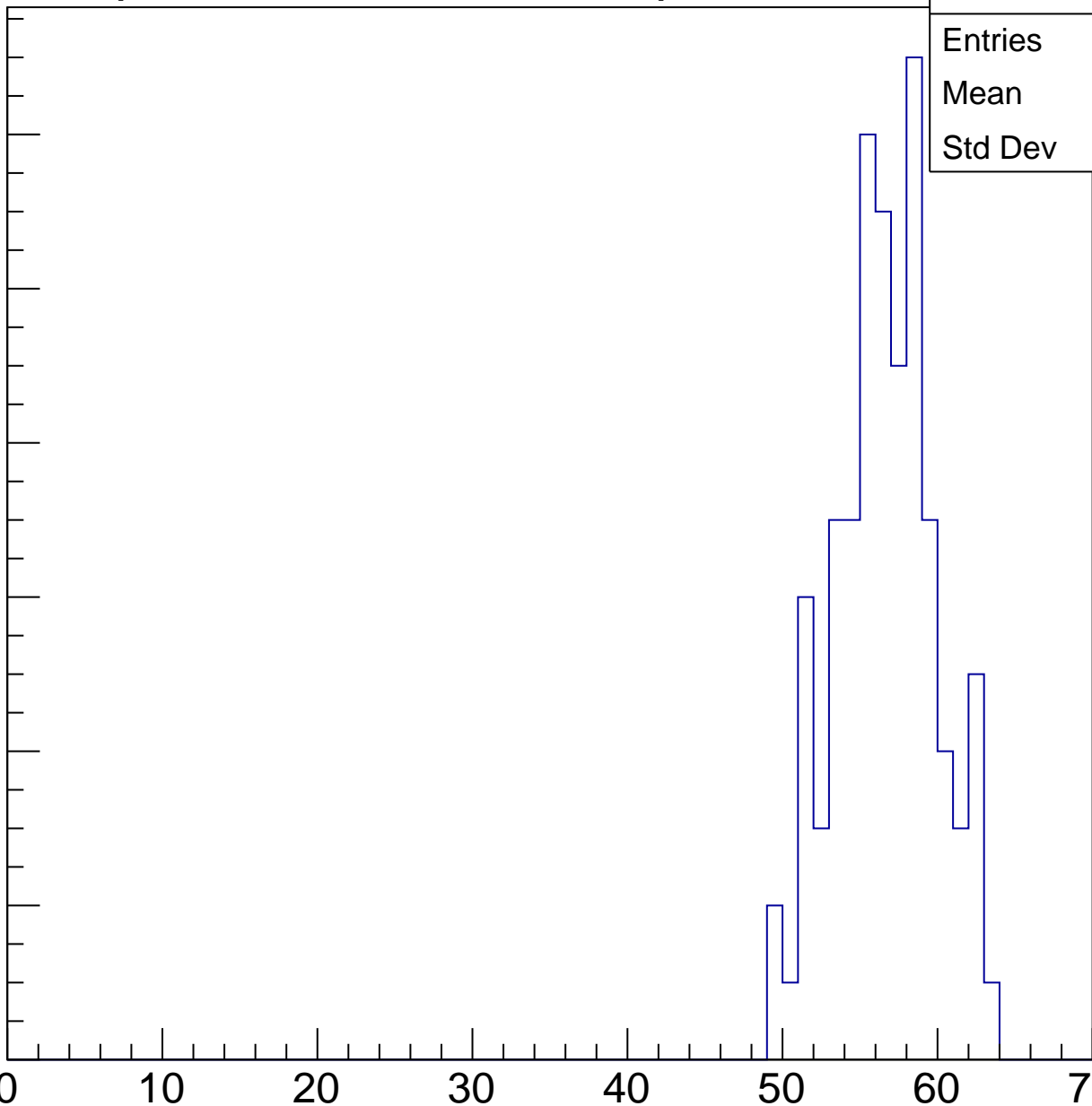
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	91
Mean	56.16
Std Dev	3.187

ampl



B0L101S, U14-ch111, adc5

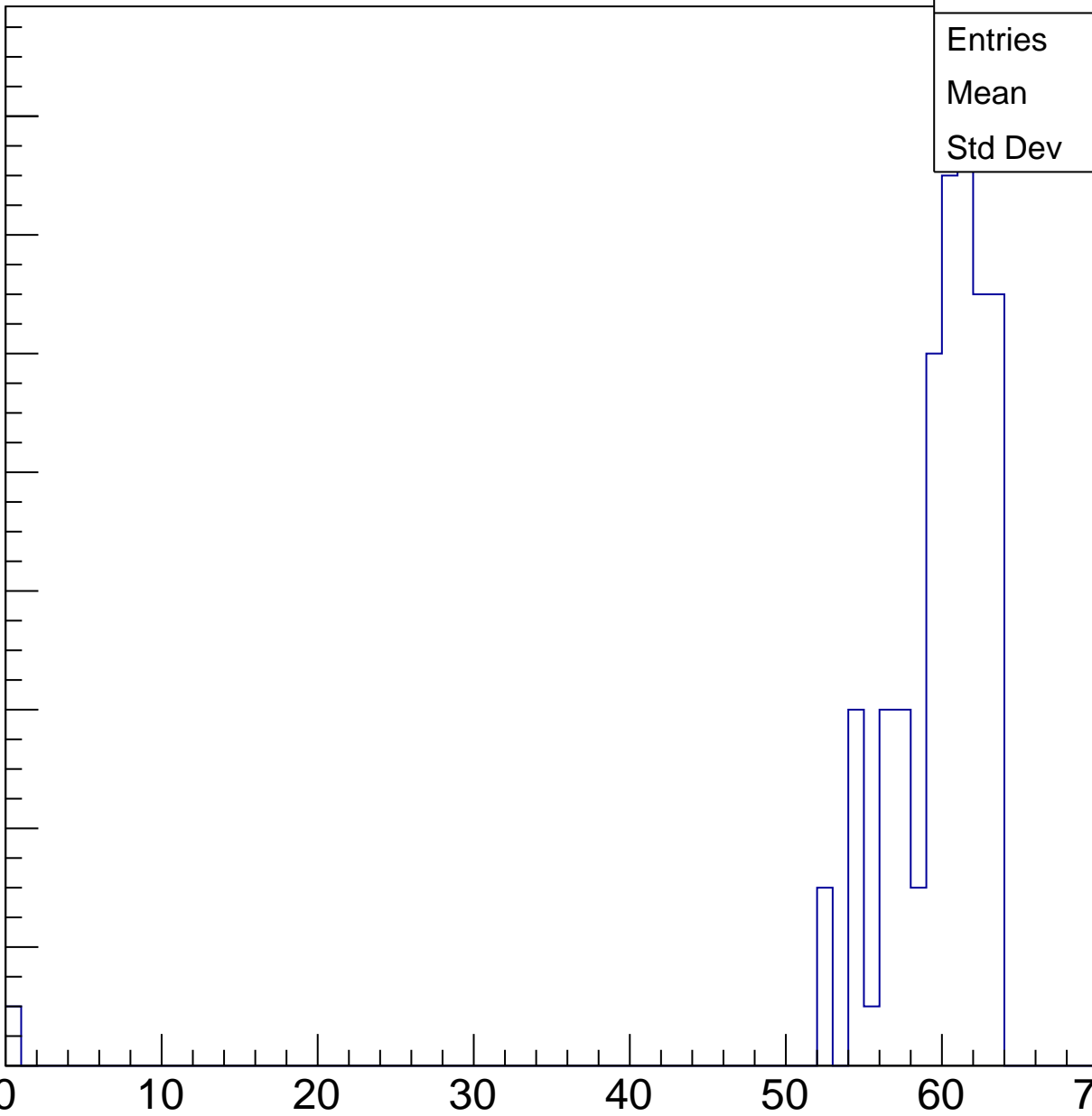
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.93
Std Dev	6.679

ampl

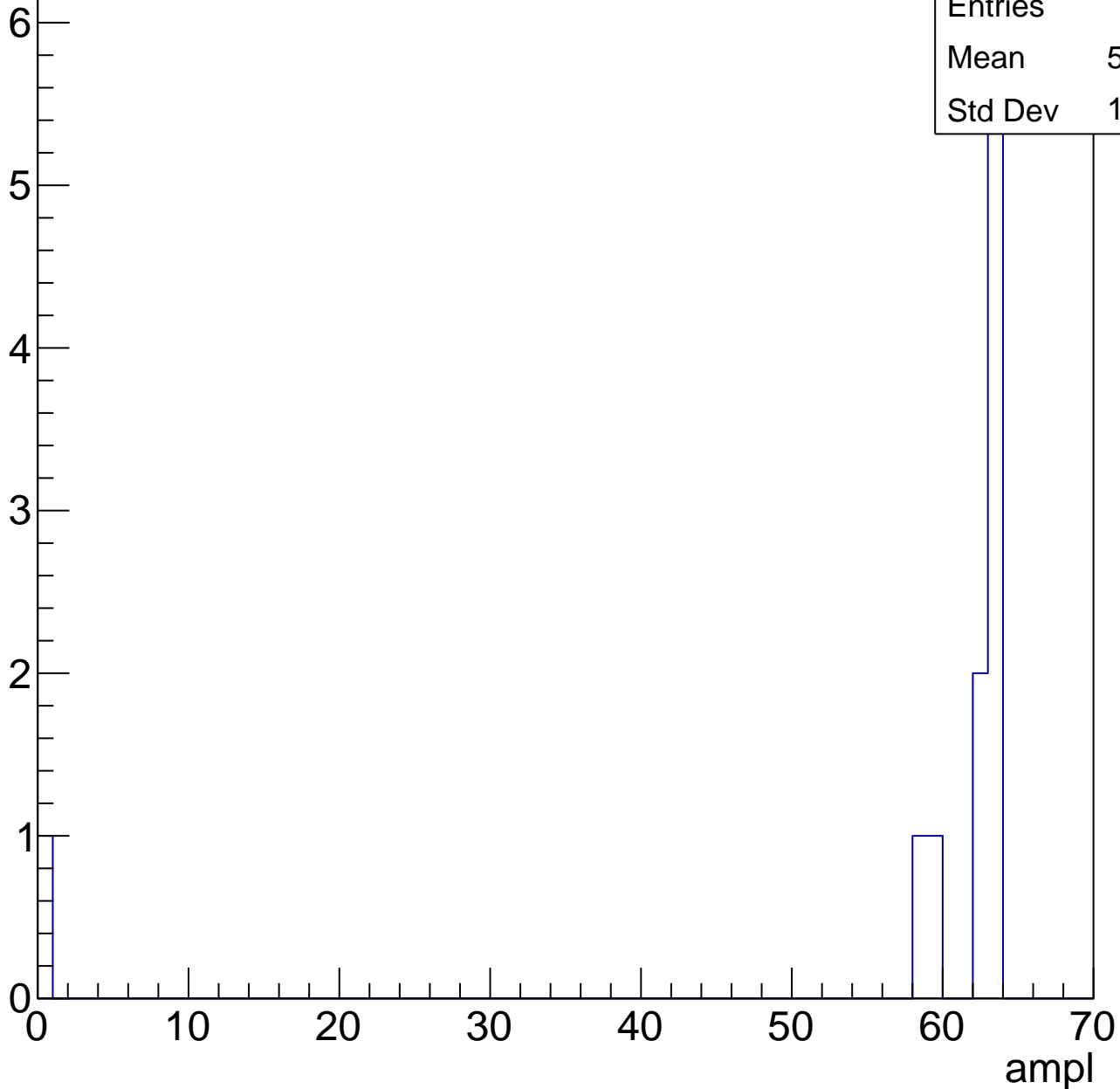


B0L101S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	56.27
Std Dev	17.87



B0L101S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch112, adc0

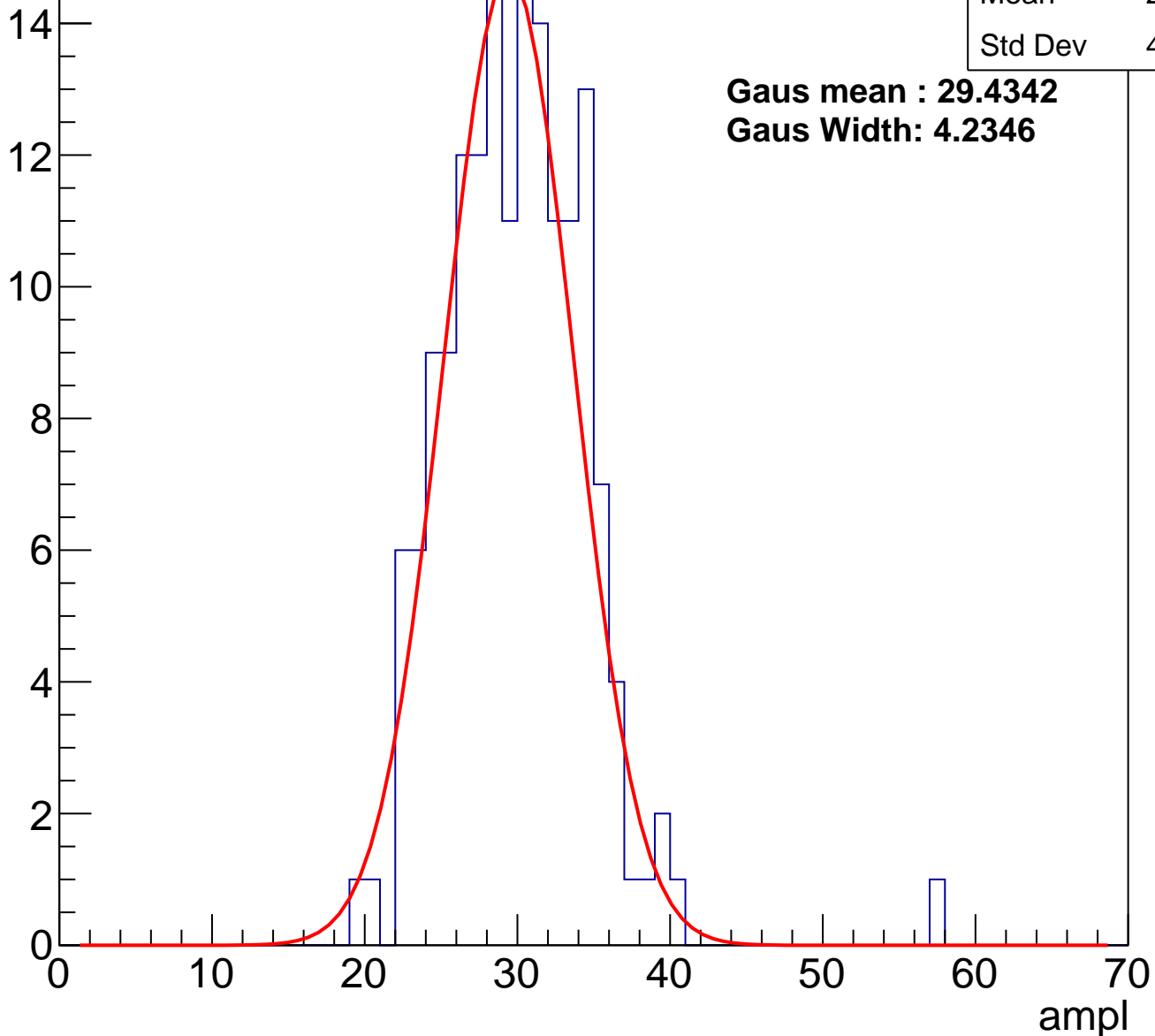
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	29.48
Std Dev	4.675

Gaus mean : 29.4342

Gaus Width: 4.2346

Entry



B0L101S, U14-ch112, adc1

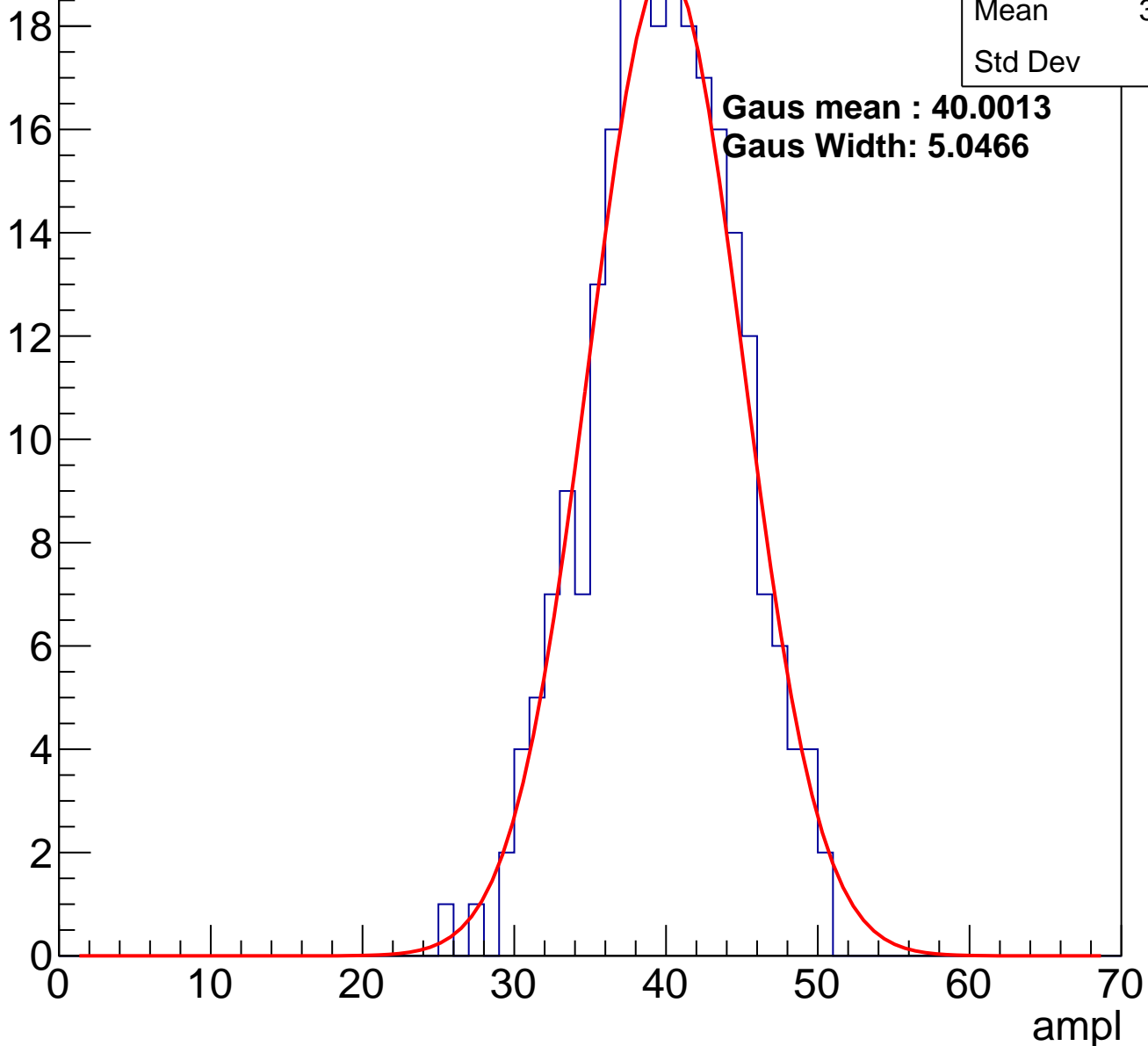
calib_packv5_042523_0143.root, FC#1, port C1

Entries	240
Mean	39.38
Std Dev	4.75

Gaus mean : 40.0013

Gaus Width: 5.0466

Entry



B0L101S, U14-ch112, adc2

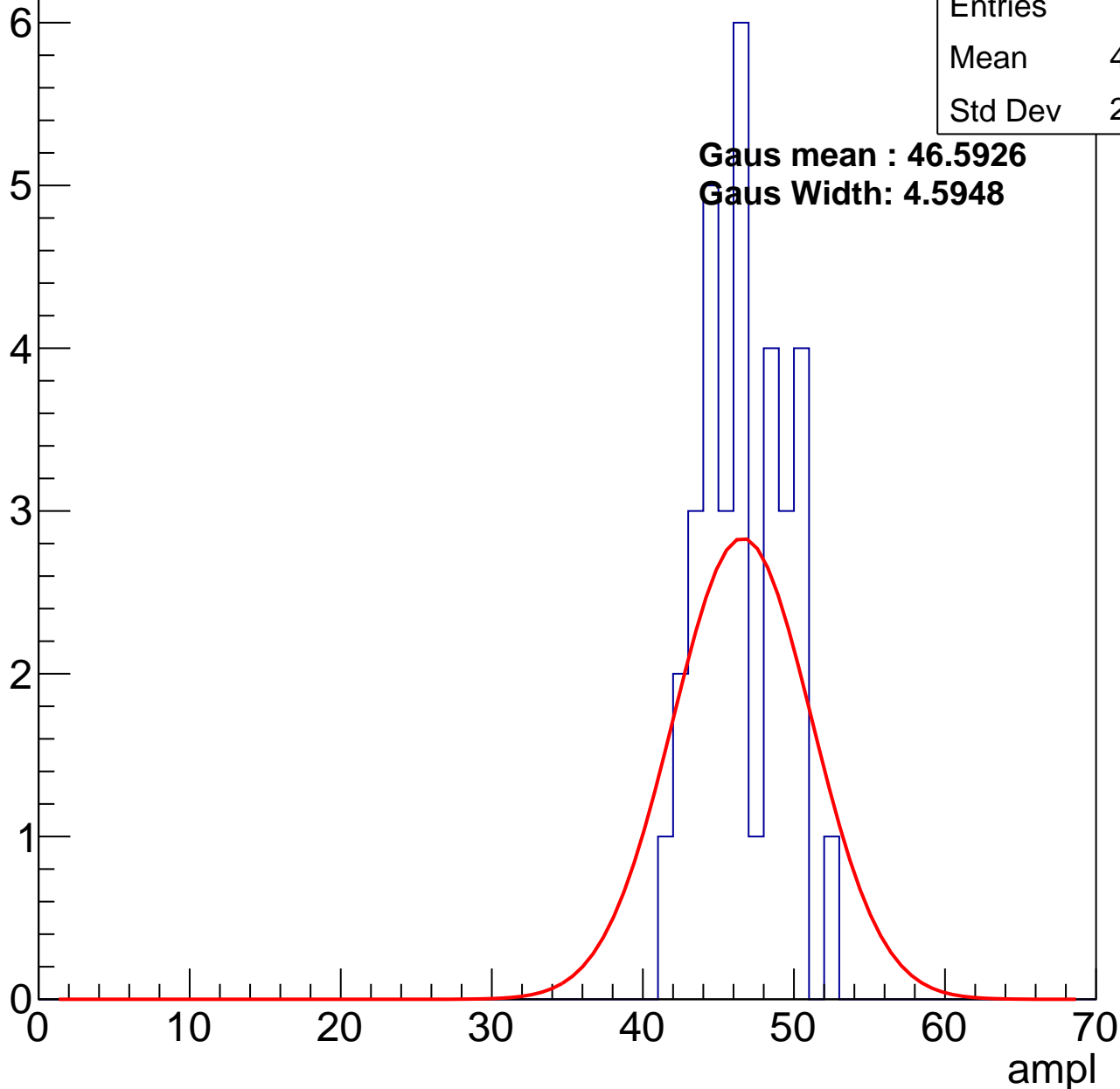
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	33
Mean	46.15
Std Dev	2.754

Gaus mean : 46.5926

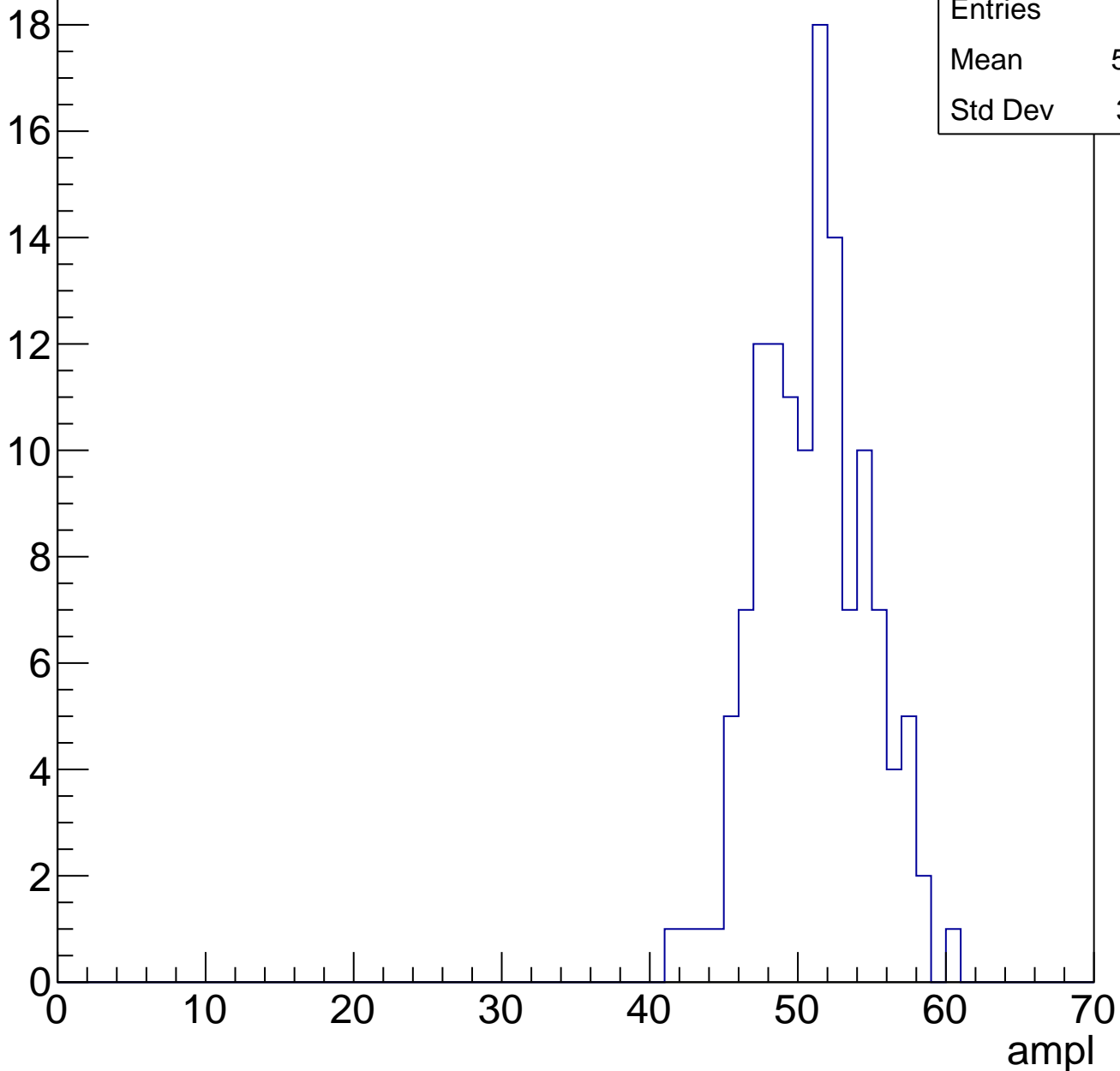
Gaus Width: 4.5948



B0L101S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

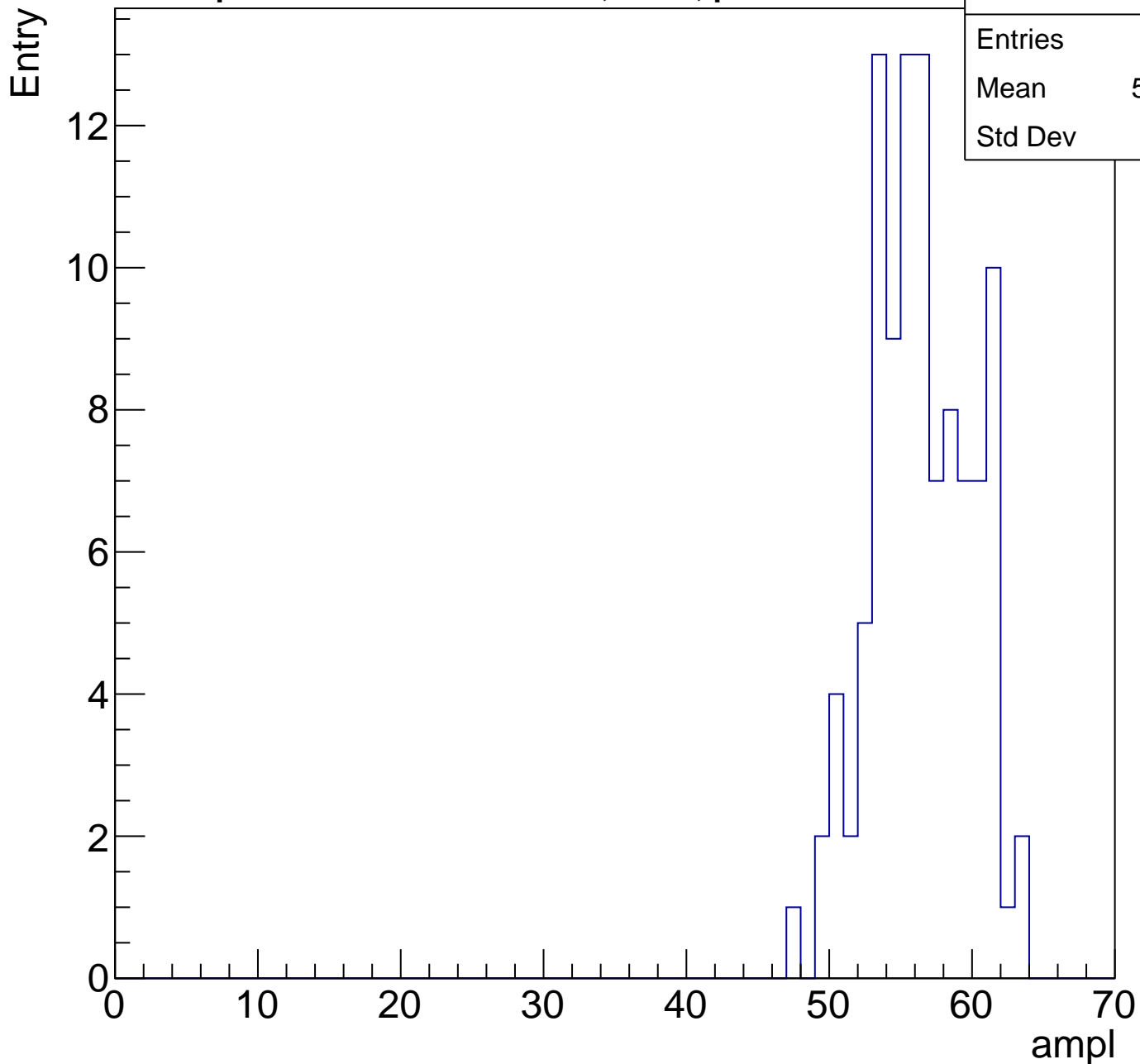


Entries	129
Mean	50.57
Std Dev	3.611

B0L101S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	55.95
Std Dev	3.42



B0L101S, U14-ch112, adc5

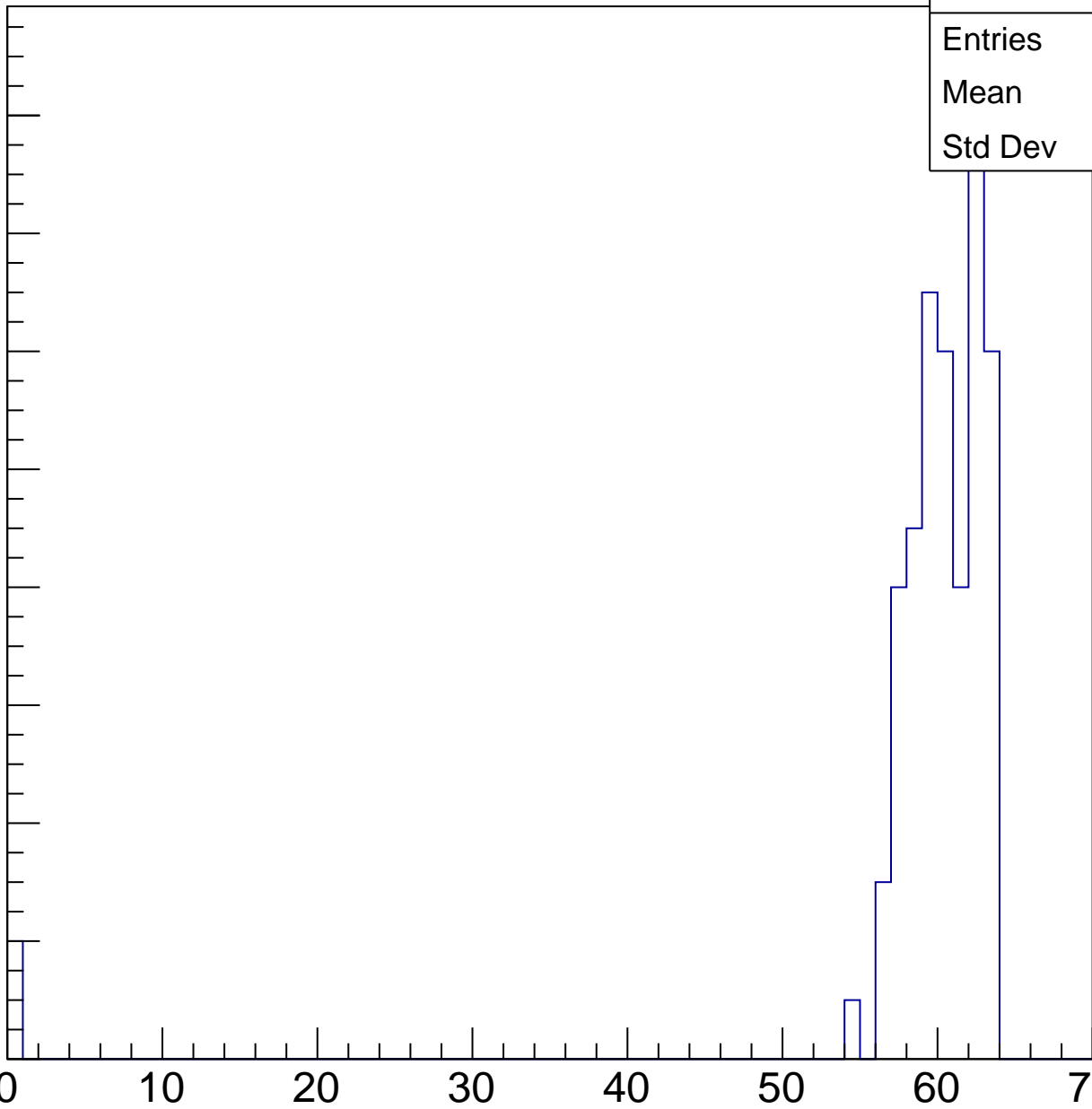
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.65
Std Dev	9.352

ampl

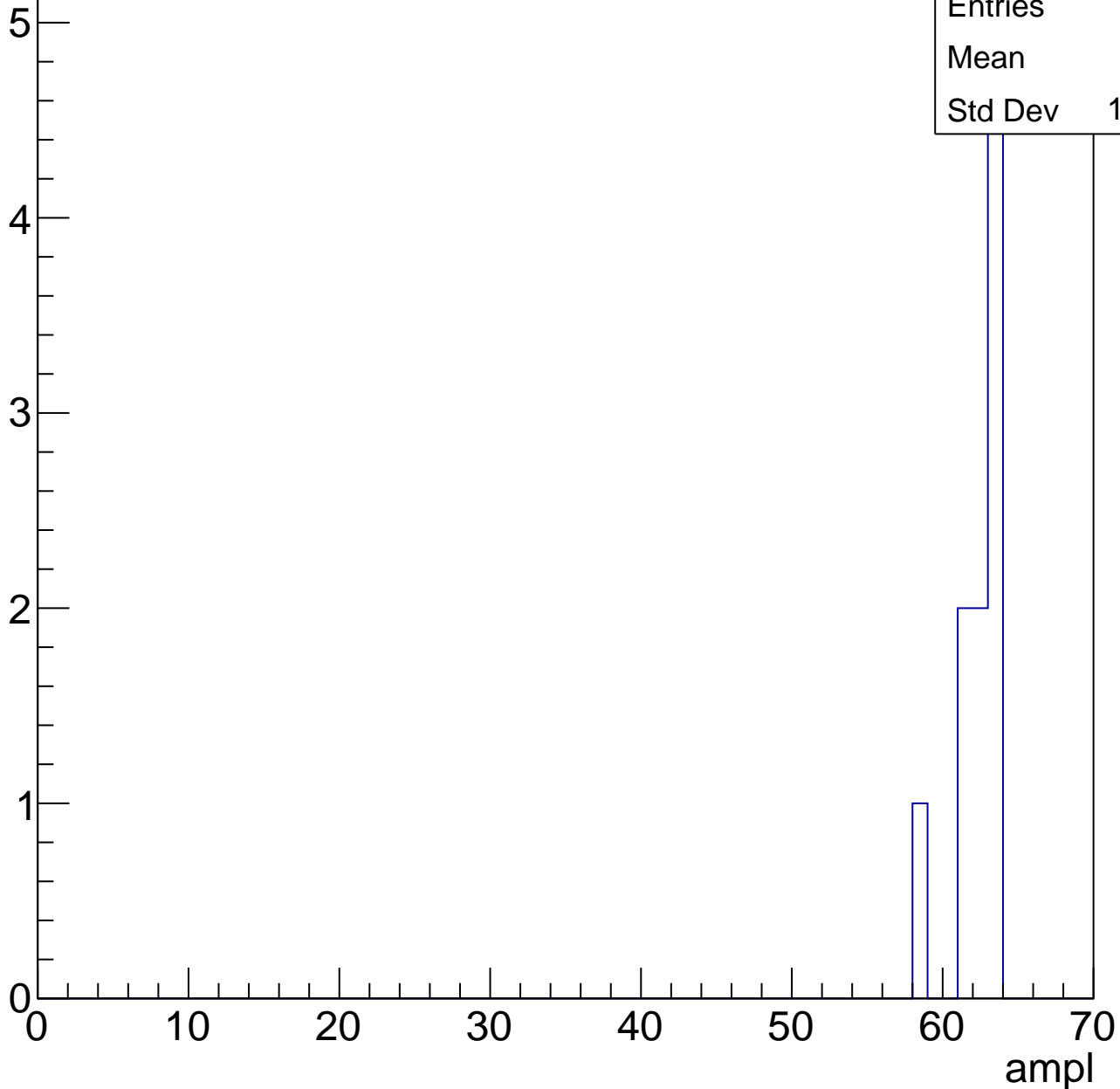


B0L101S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

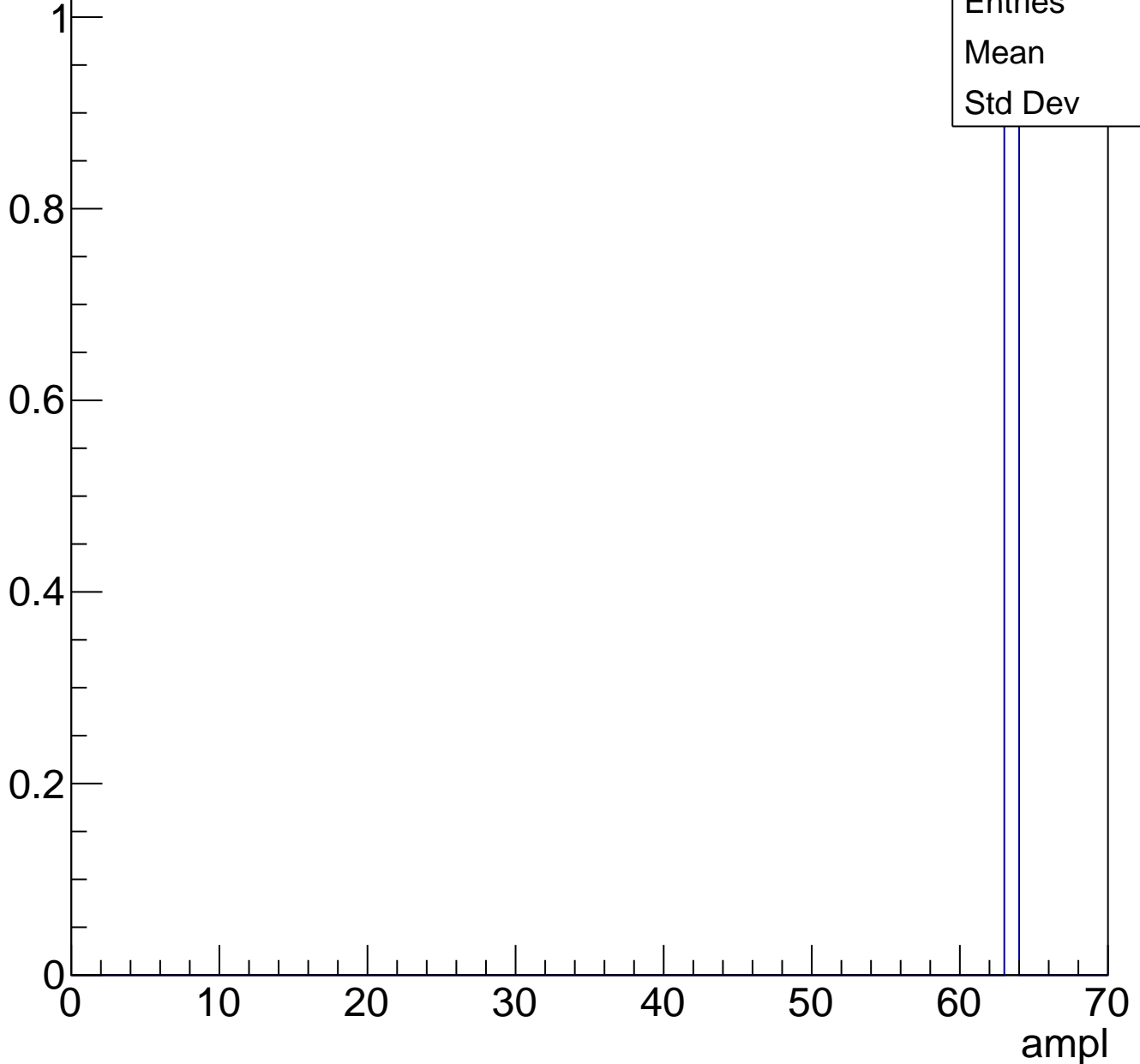
Entries	10
Mean	61.9
Std Dev	1.513



B0L101S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch113, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	30.74
Std Dev	3.739

Gaus mean : 31.1603

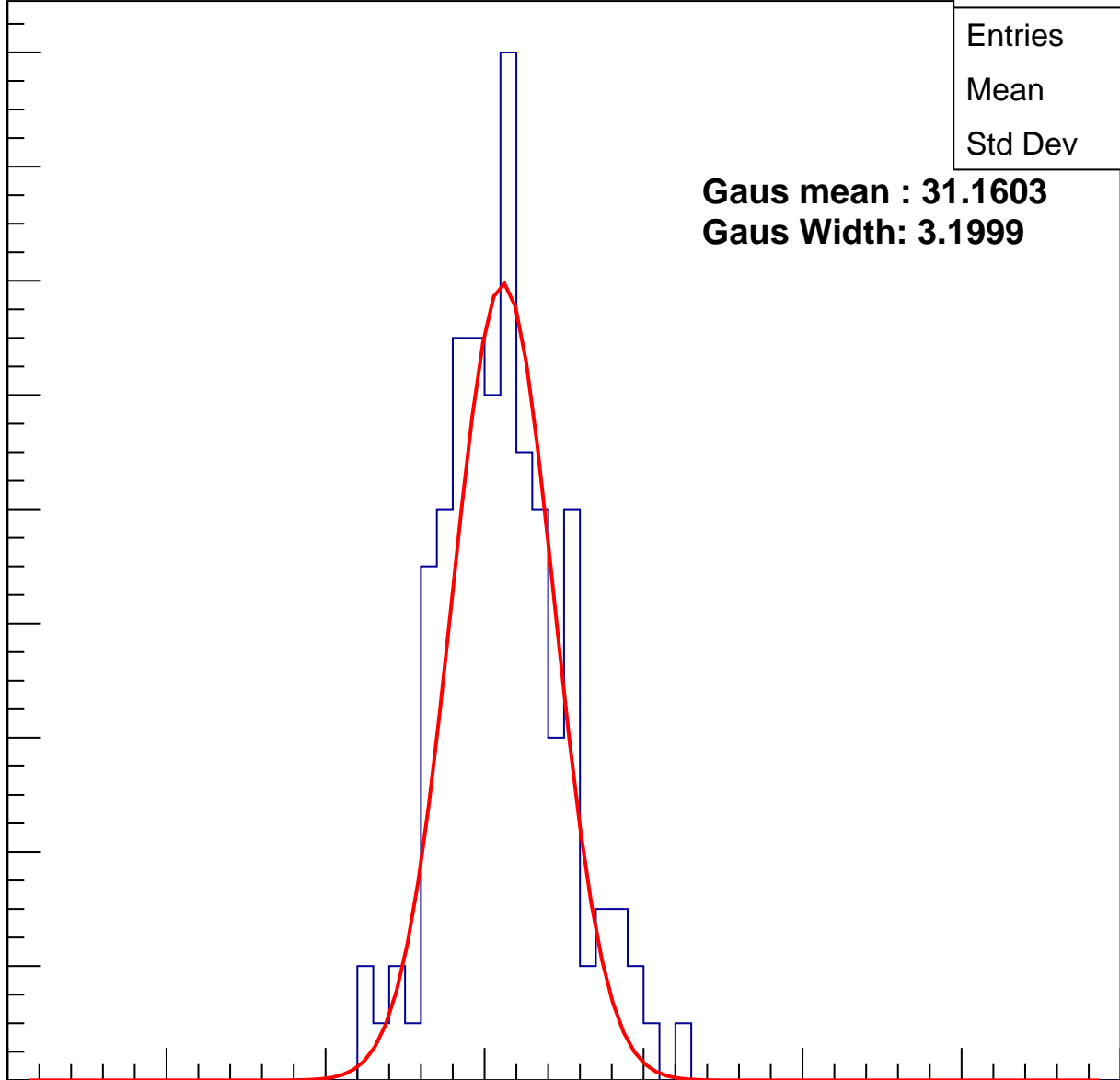
Gaus Width: 3.1999

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch113, adc1

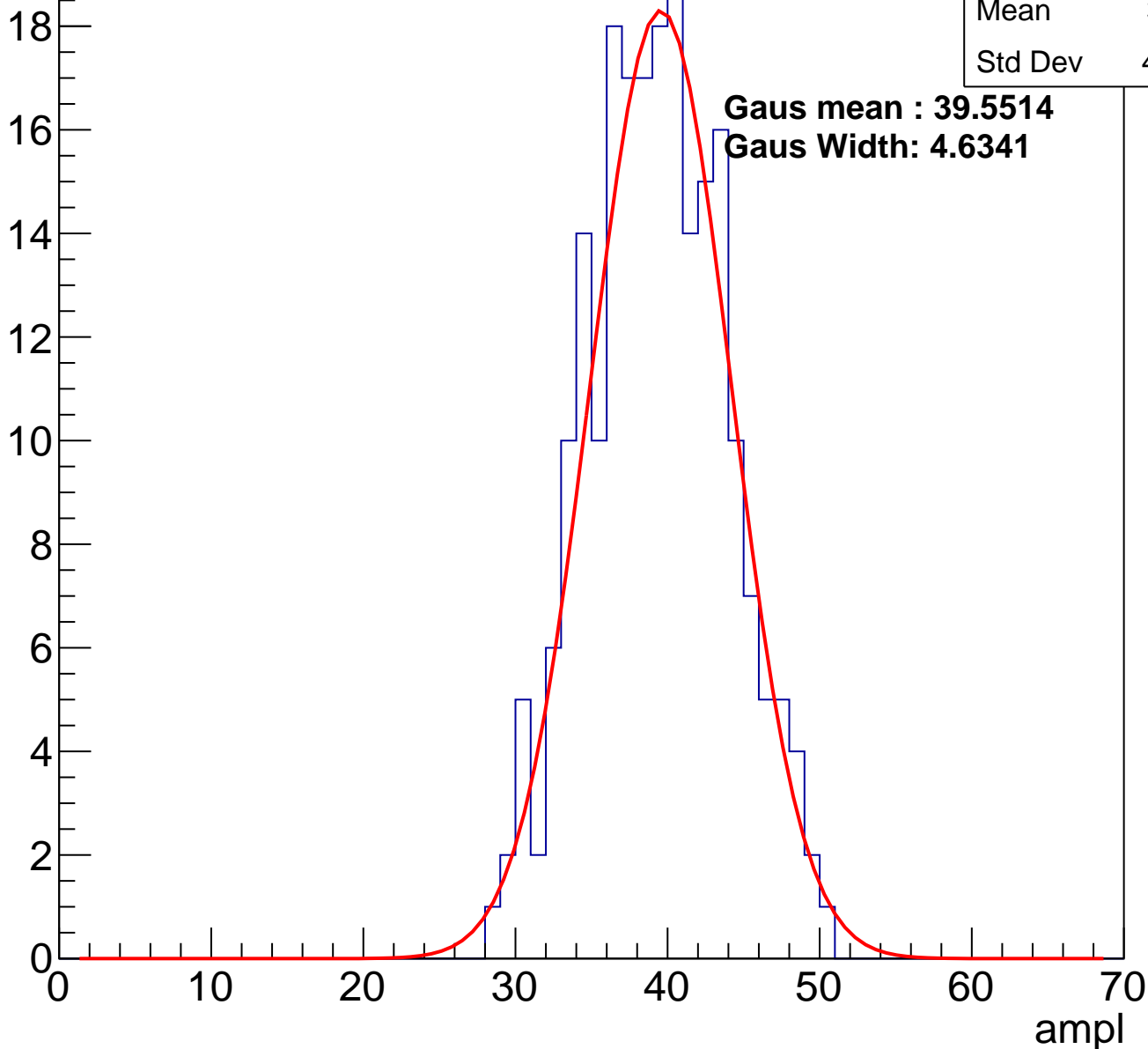
calib_packv5_042523_0143.root, FC#1, port C1

Entries	218
Mean	38.91
Std Dev	4.514

Gaus mean : 39.5514

Gaus Width: 4.6341

Entry



B0L101S, U14-ch113, adc2

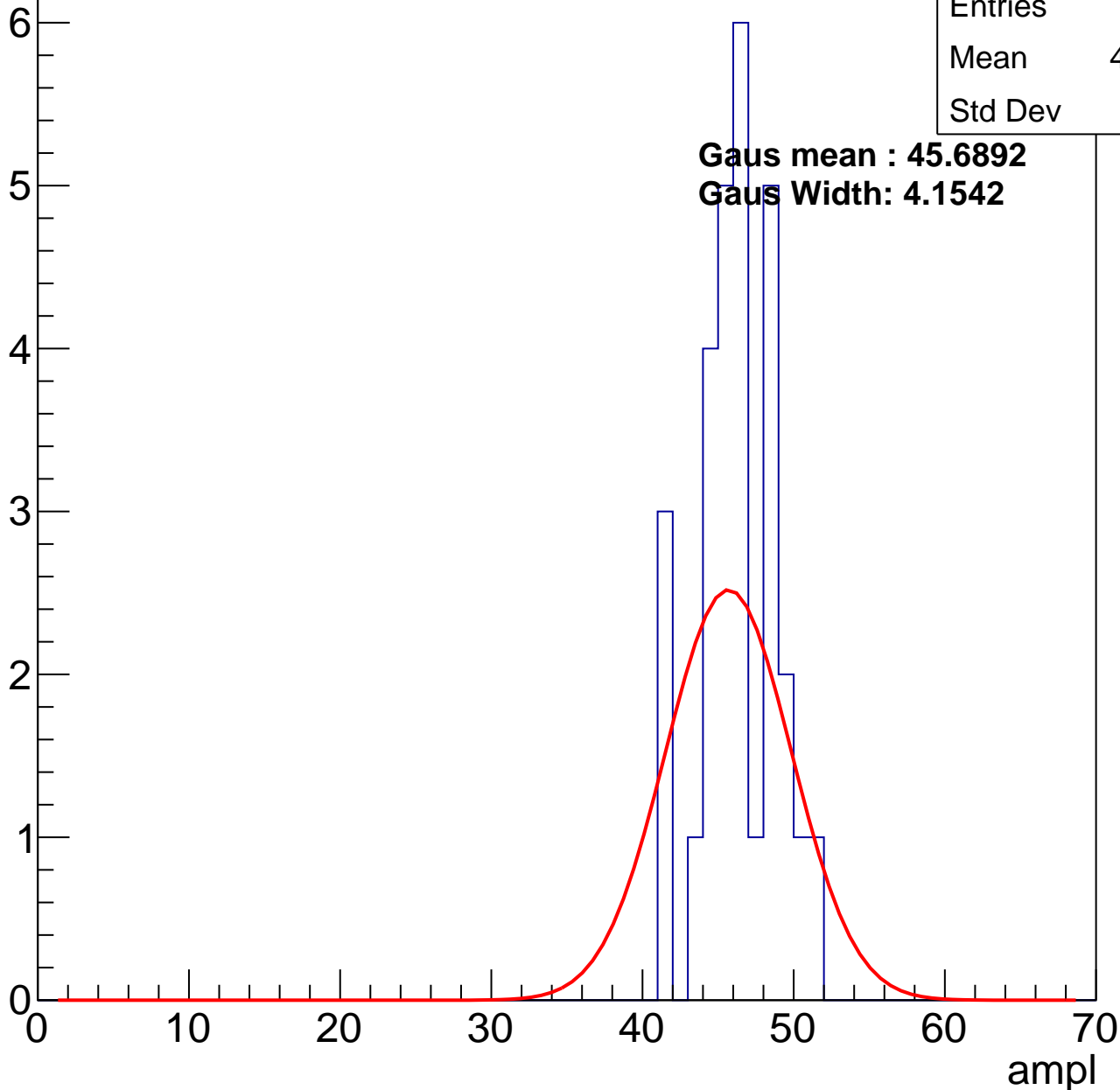
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	29
Mean	45.83
Std Dev	2.52

Gaus mean : 45.6892

Gaus Width: 4.1542



B0L101S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	49.96
Std Dev	3.986

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

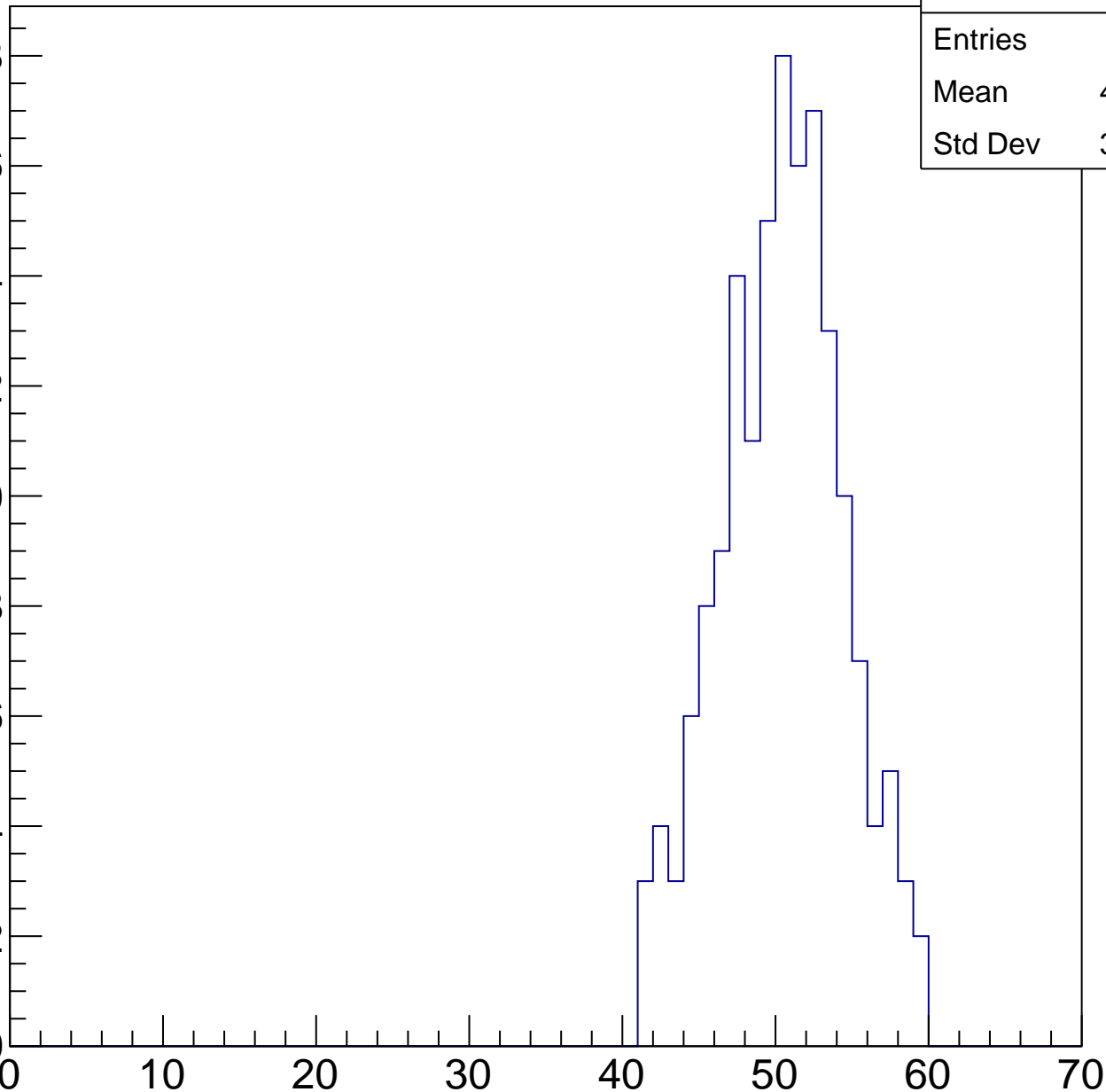
30

40

50

60

ampl



B0L101S, U14-ch113, adc4

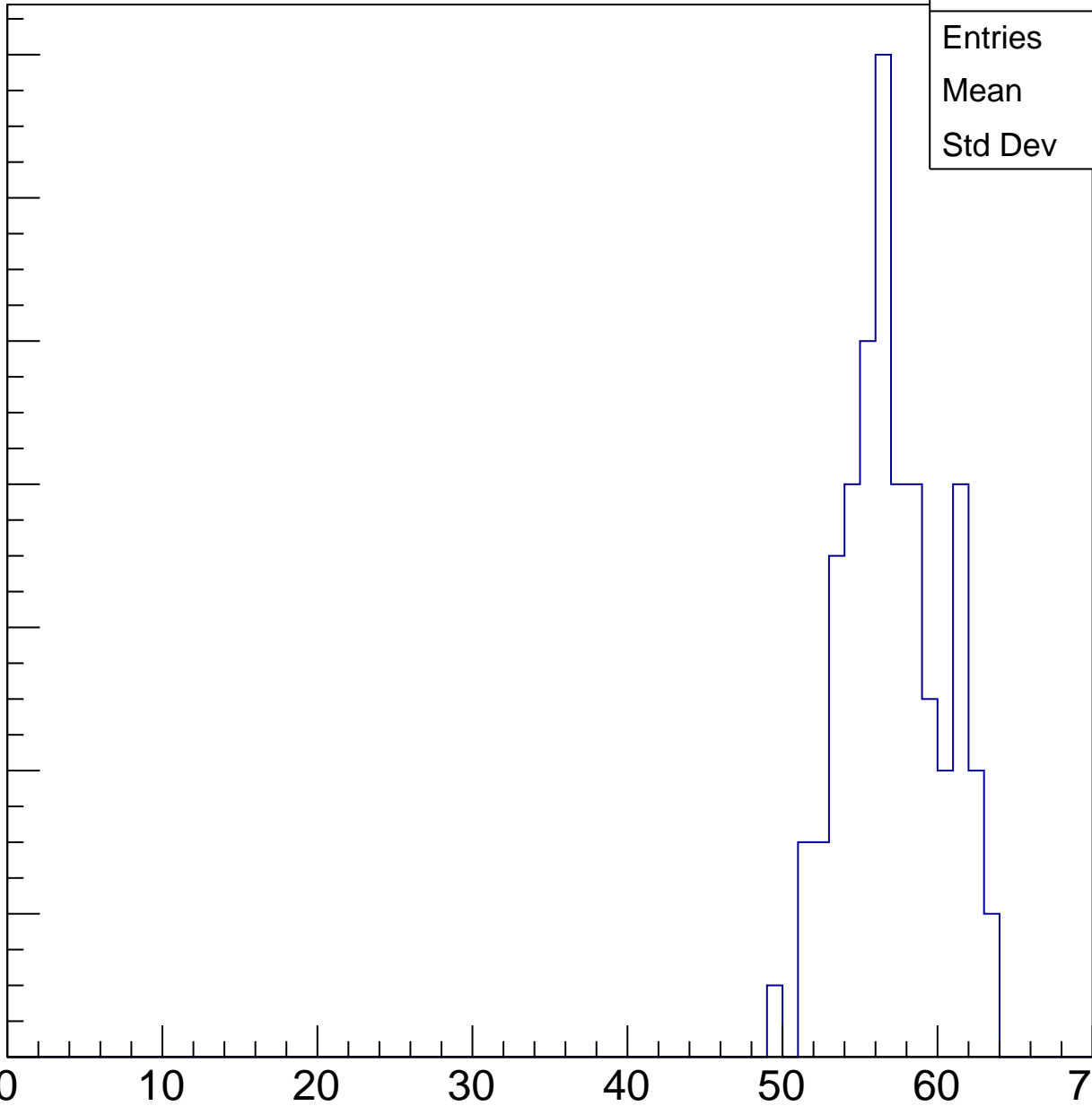
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	56.61
Std Dev	3.136

ampl



B0L101S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	58.84
Std Dev	8.797

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

40

50

ampl

60

70

B0L101S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L101S, U14-ch114, adc0

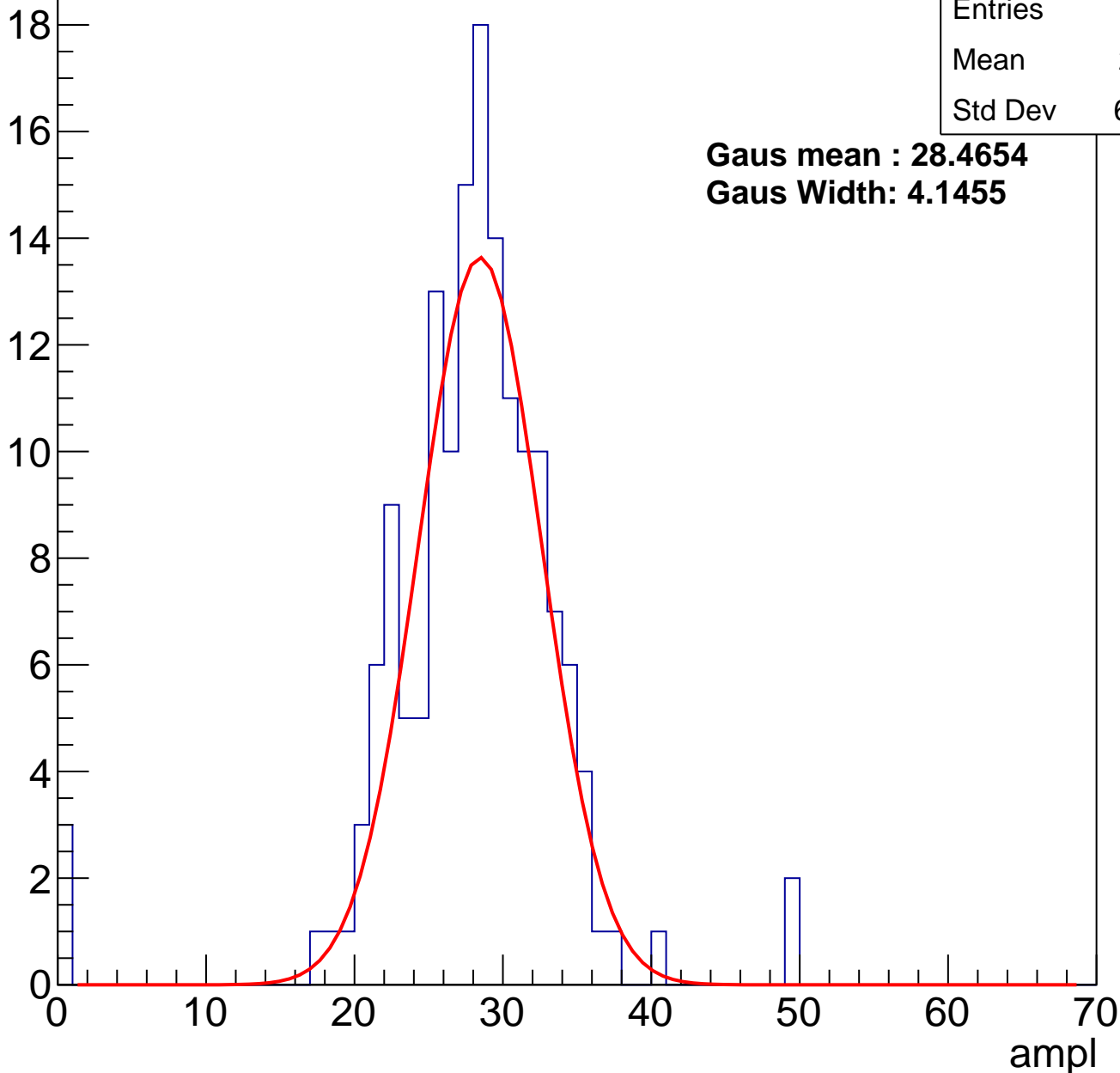
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	27.51
Std Dev	6.114

Gaus mean : 28.4654

Gaus Width: 4.1455

Entry



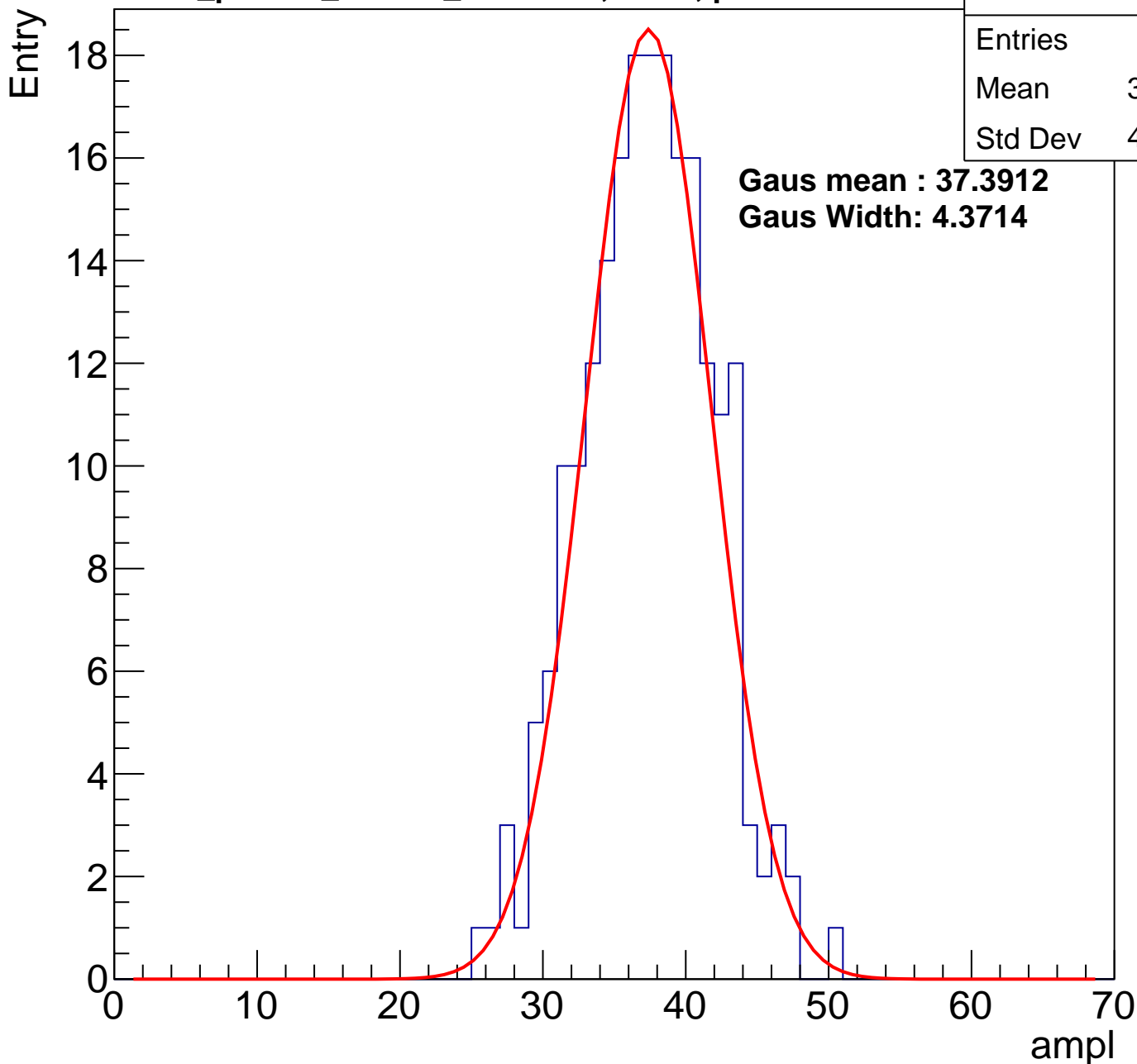
B0L101S, U14-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	211
Mean	36.89
Std Dev	4.503

Gaus mean : 37.3912

Gaus Width: 4.3714



B0L101S, U14-ch114, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	48
Mean	43.21
Std Dev	3.846

Gaus mean : 43.9200

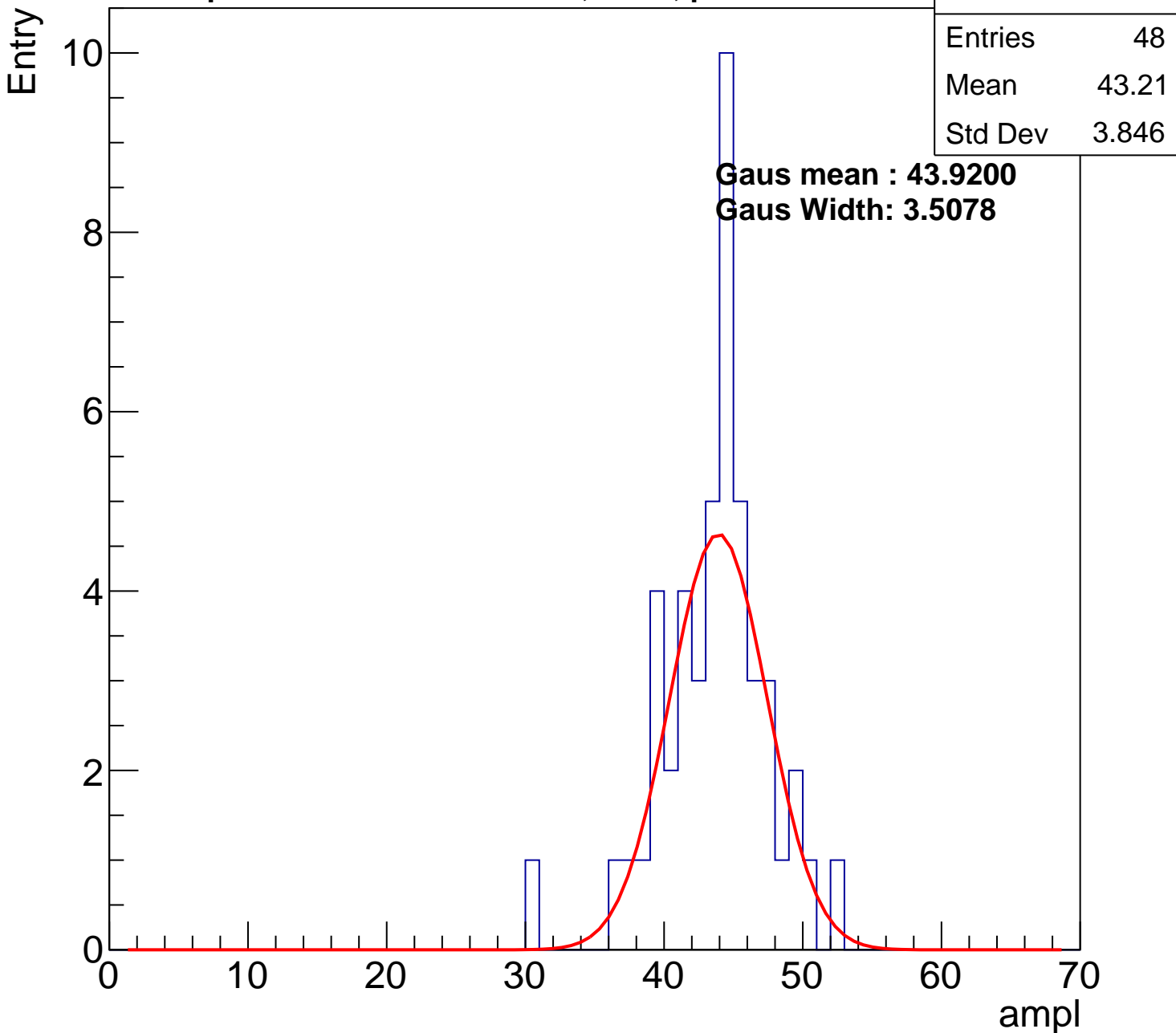
Gaus Width: 3.5078

Entry

10
8
6
4
2
0

ampl

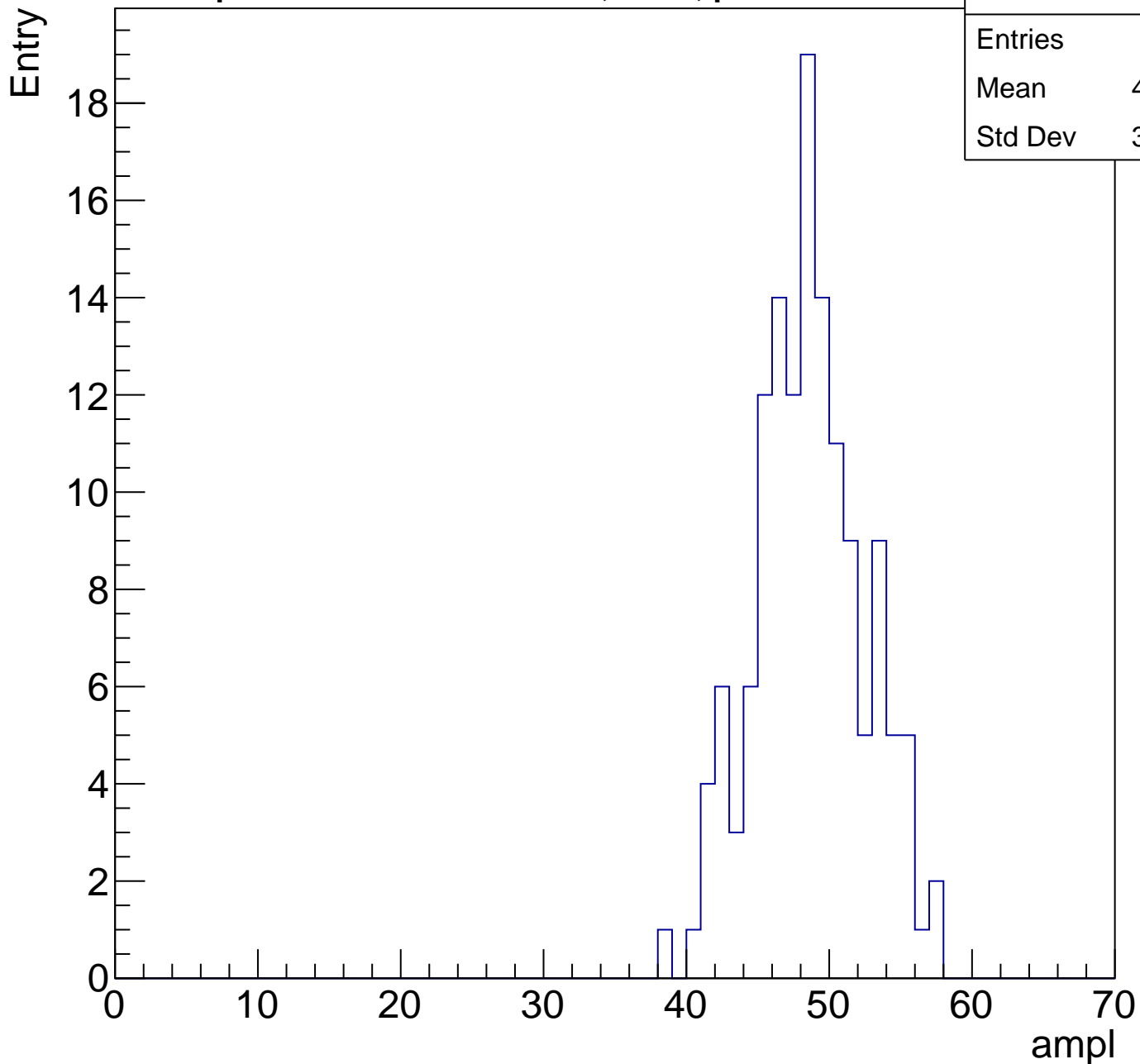
0 10 20 30 40 50 60 70



B0L101S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	48.16
Std Dev	3.807



B0L101S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

114

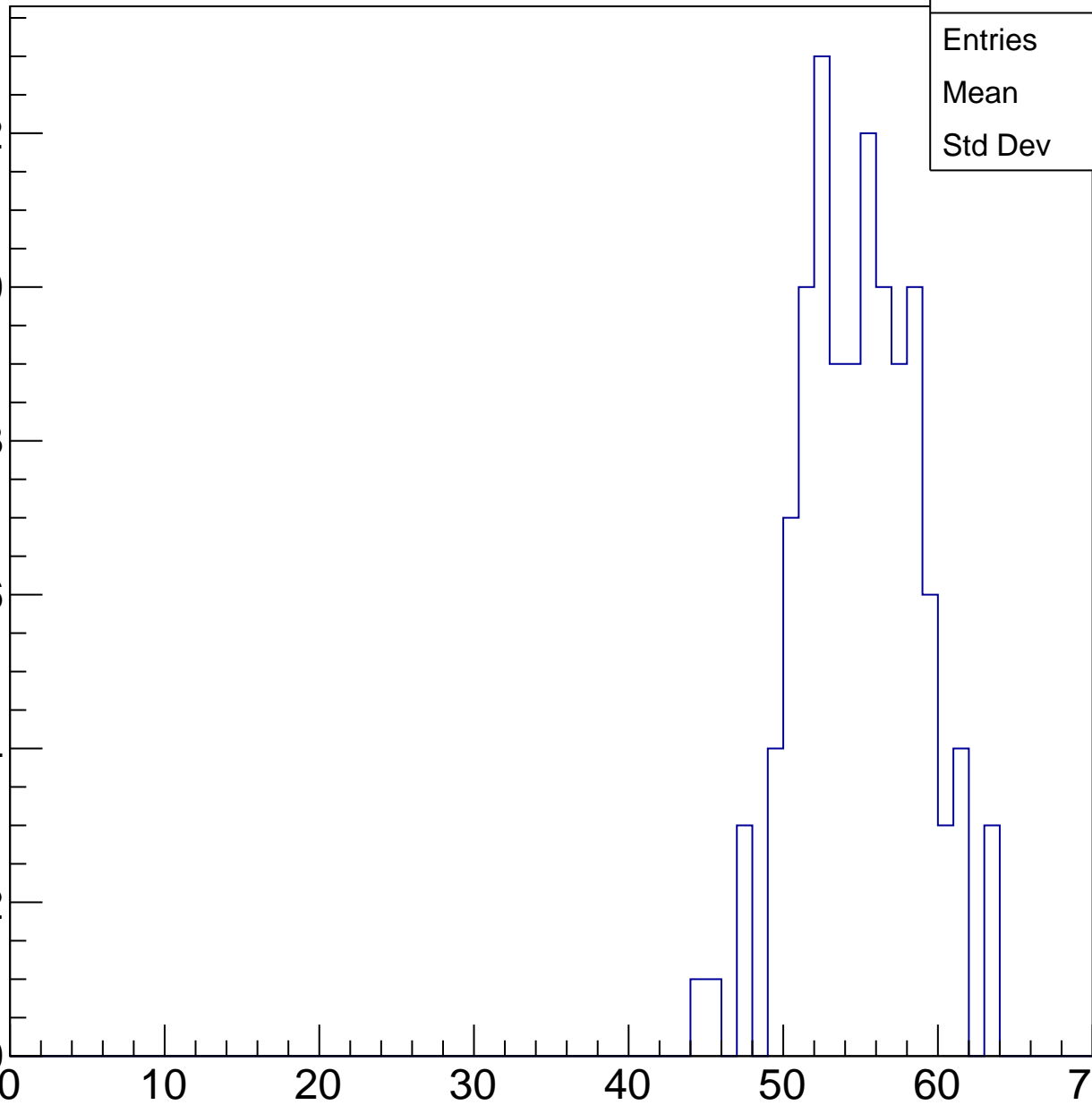
Mean

54.43

Std Dev

3.816

ampl



B0L101S, U14-ch114, adc5

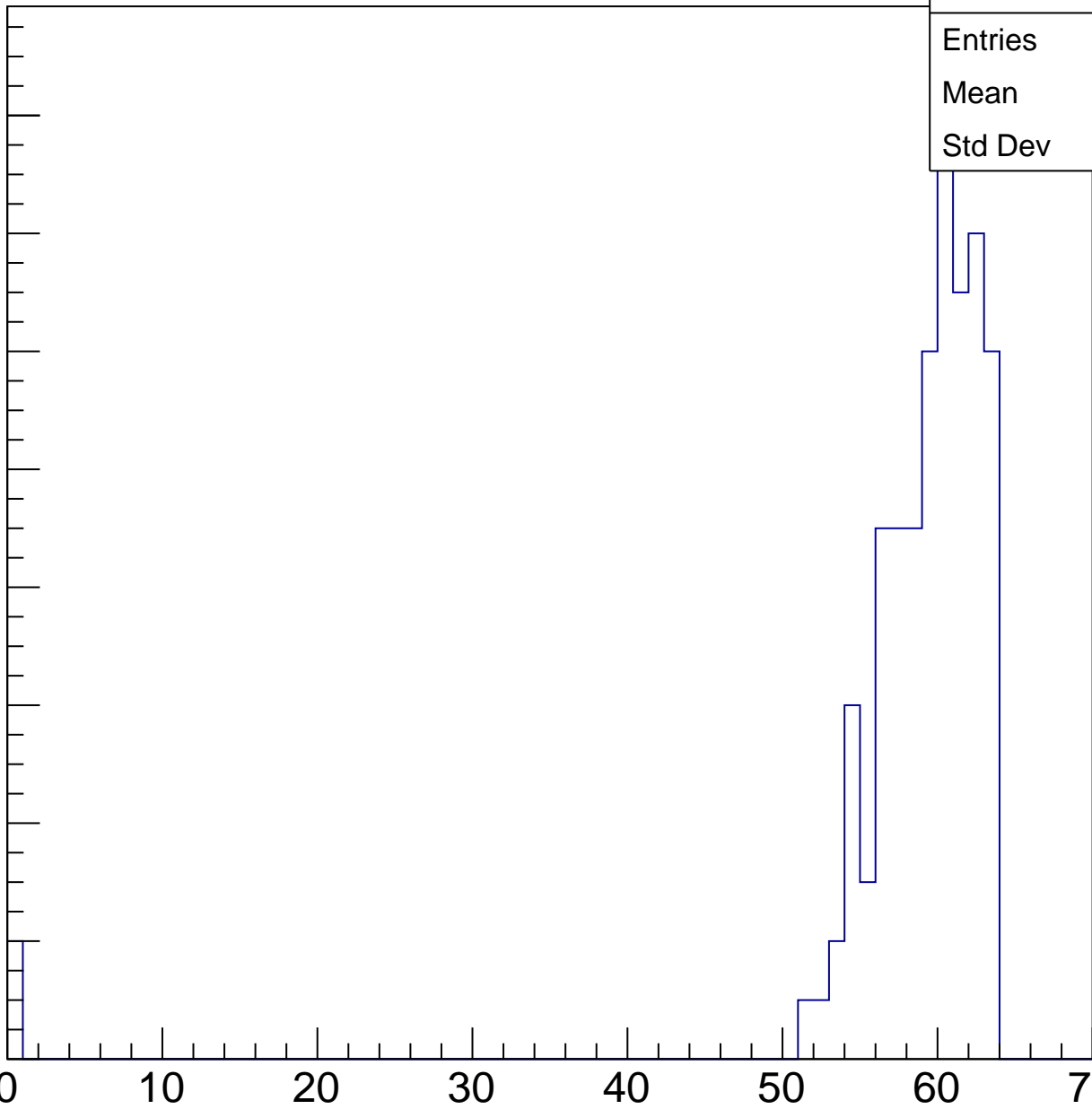
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.02
Std Dev	8.395

ampl

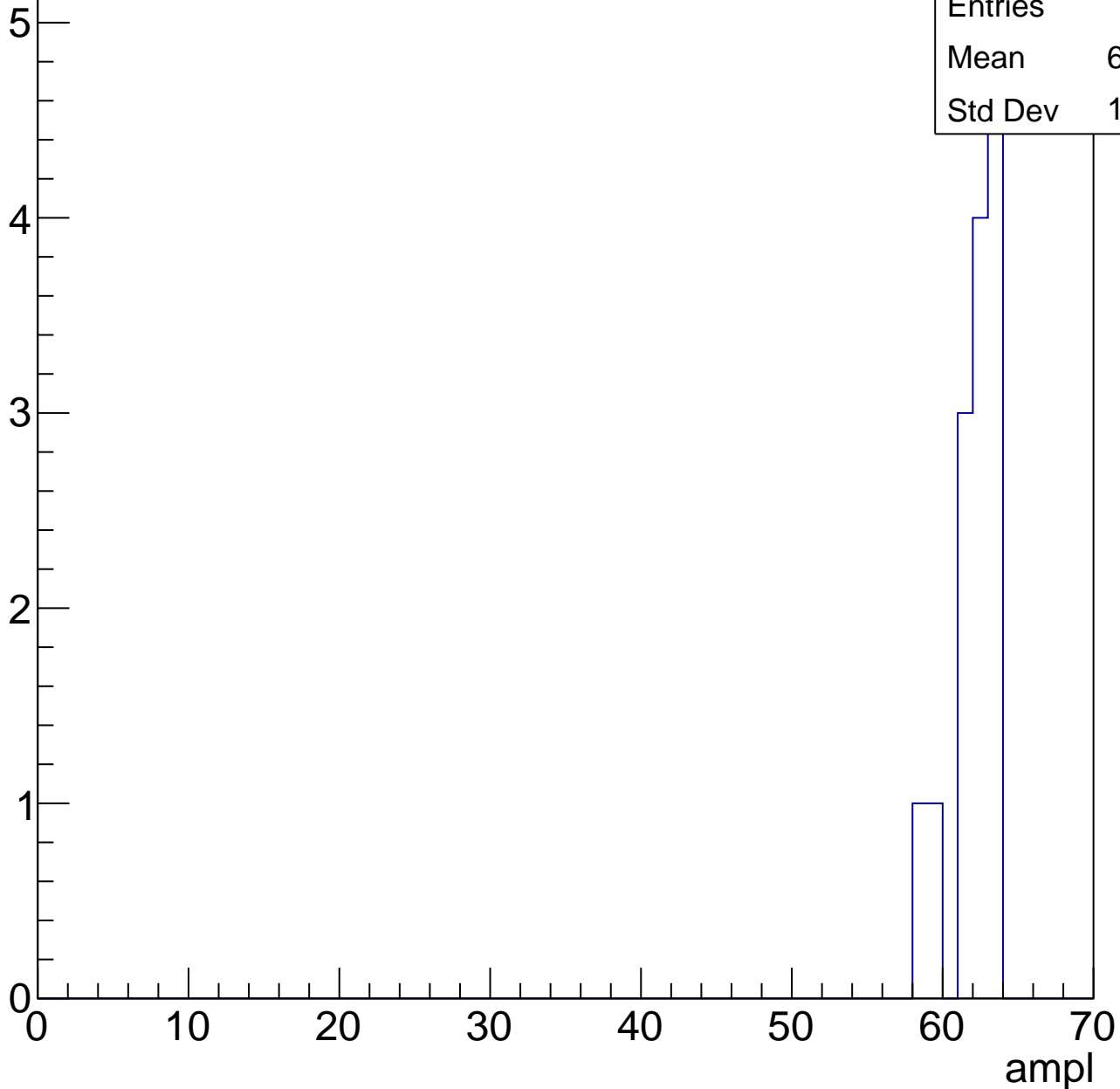


B0L101S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	61.64
Std Dev	1.493



B0L101S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

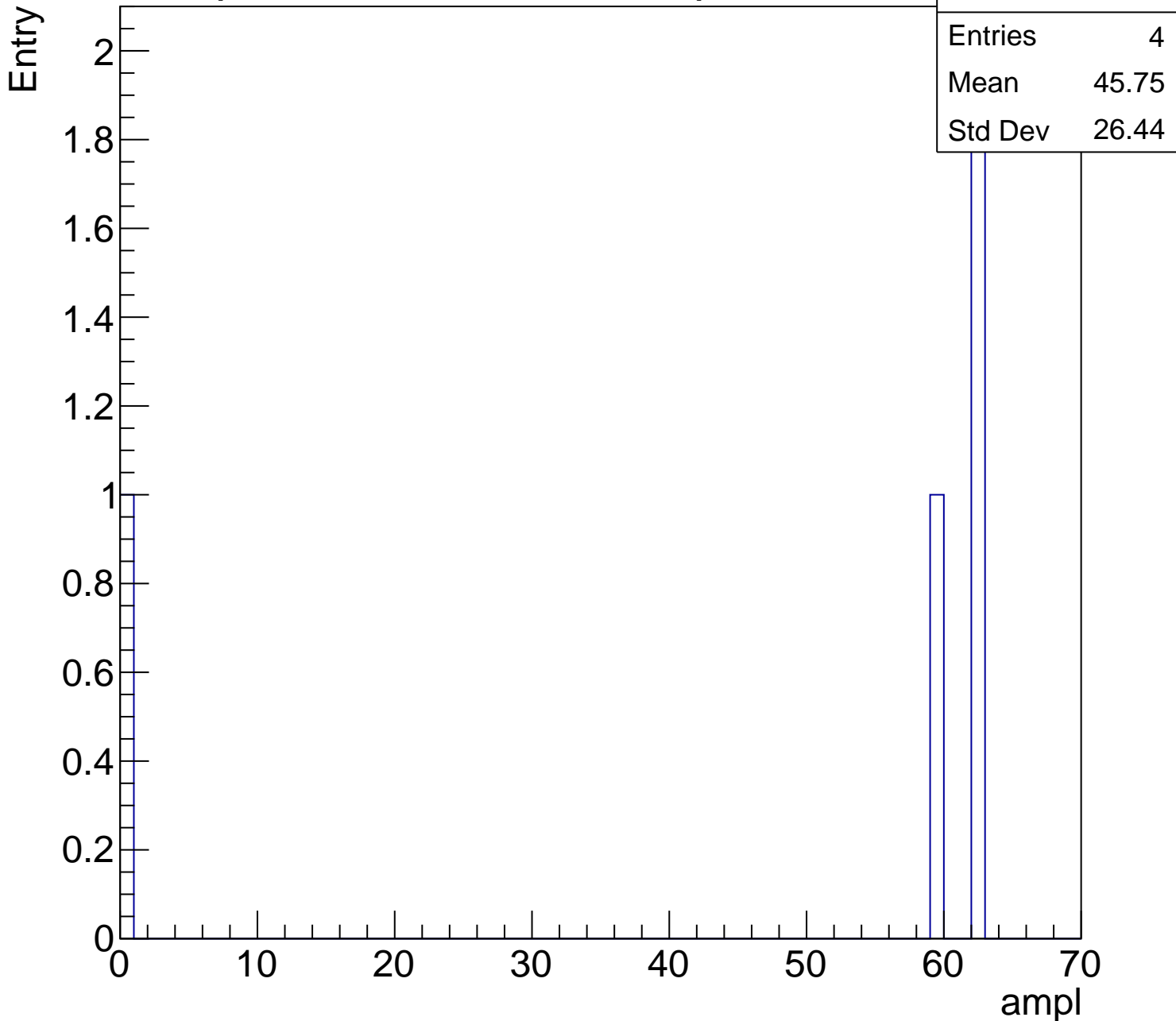
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	45.75
Std Dev	26.44

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch115, adc0

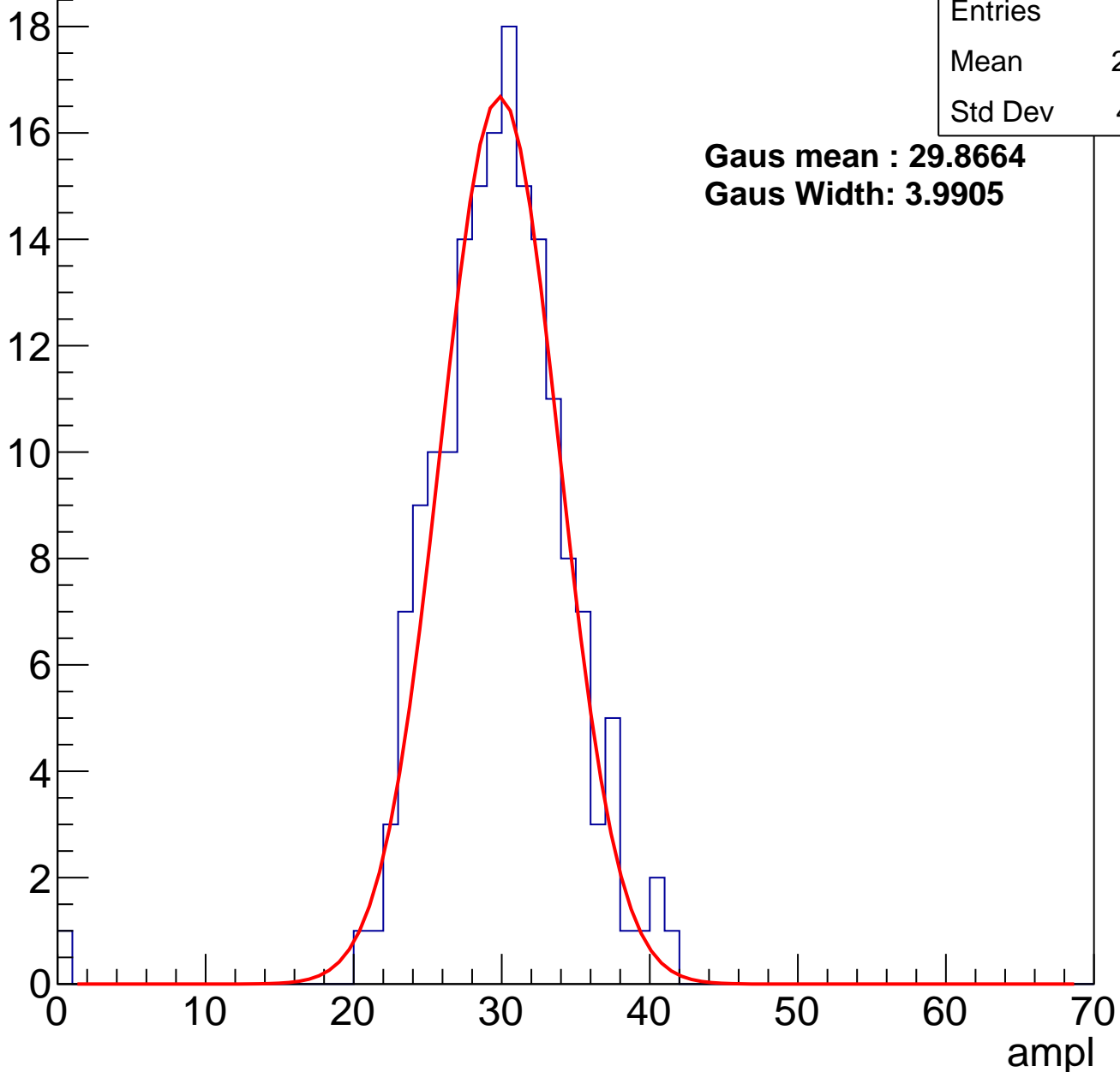
calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	29.36
Std Dev	4.671

Gaus mean : 29.8664

Gaus Width: 3.9905

Entry

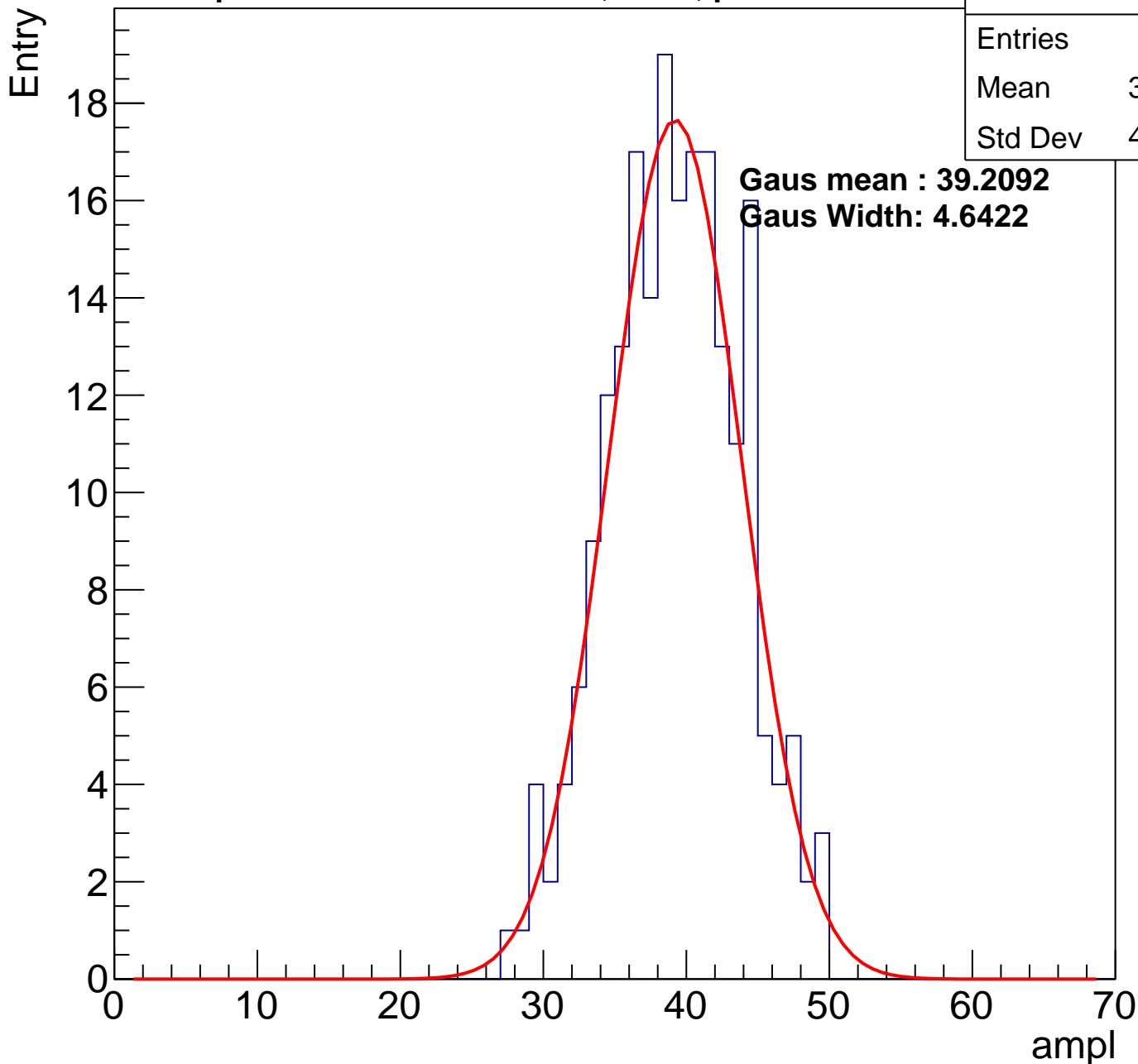


B0L101S, U14-ch115, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	211
Mean	38.72
Std Dev	4.544

Gaus mean : 39.2092
Gaus Width: 4.6422



B0L101S, U14-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	51
Mean	45.06
Std Dev	3.146

Gaus mean : 45.2569

Gaus Width: 2.5535

10

8

6

4

2

0

0

2

4

6

8

10

ampl

0

10

20

30

40

50

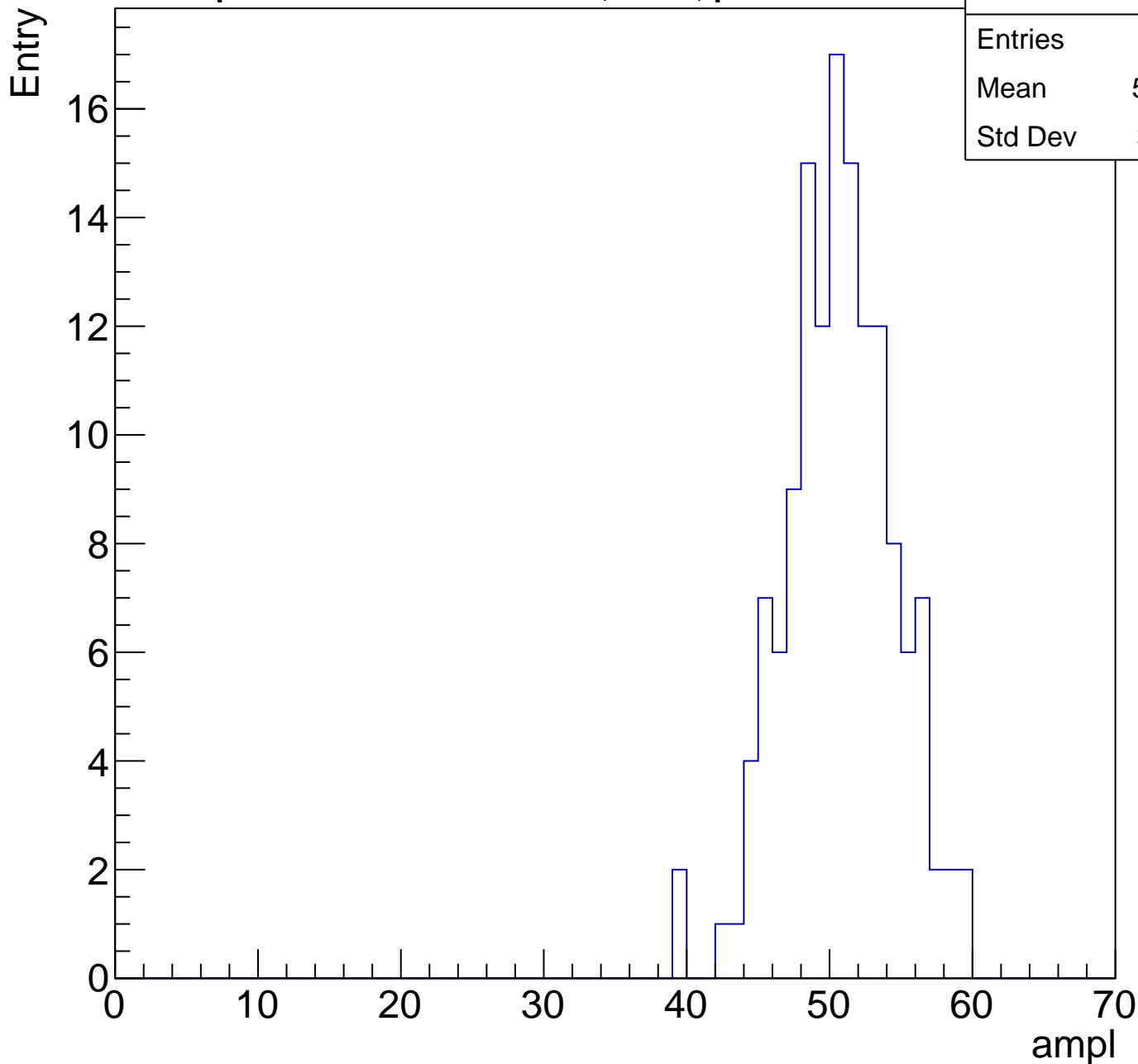
60

70

B0L101S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

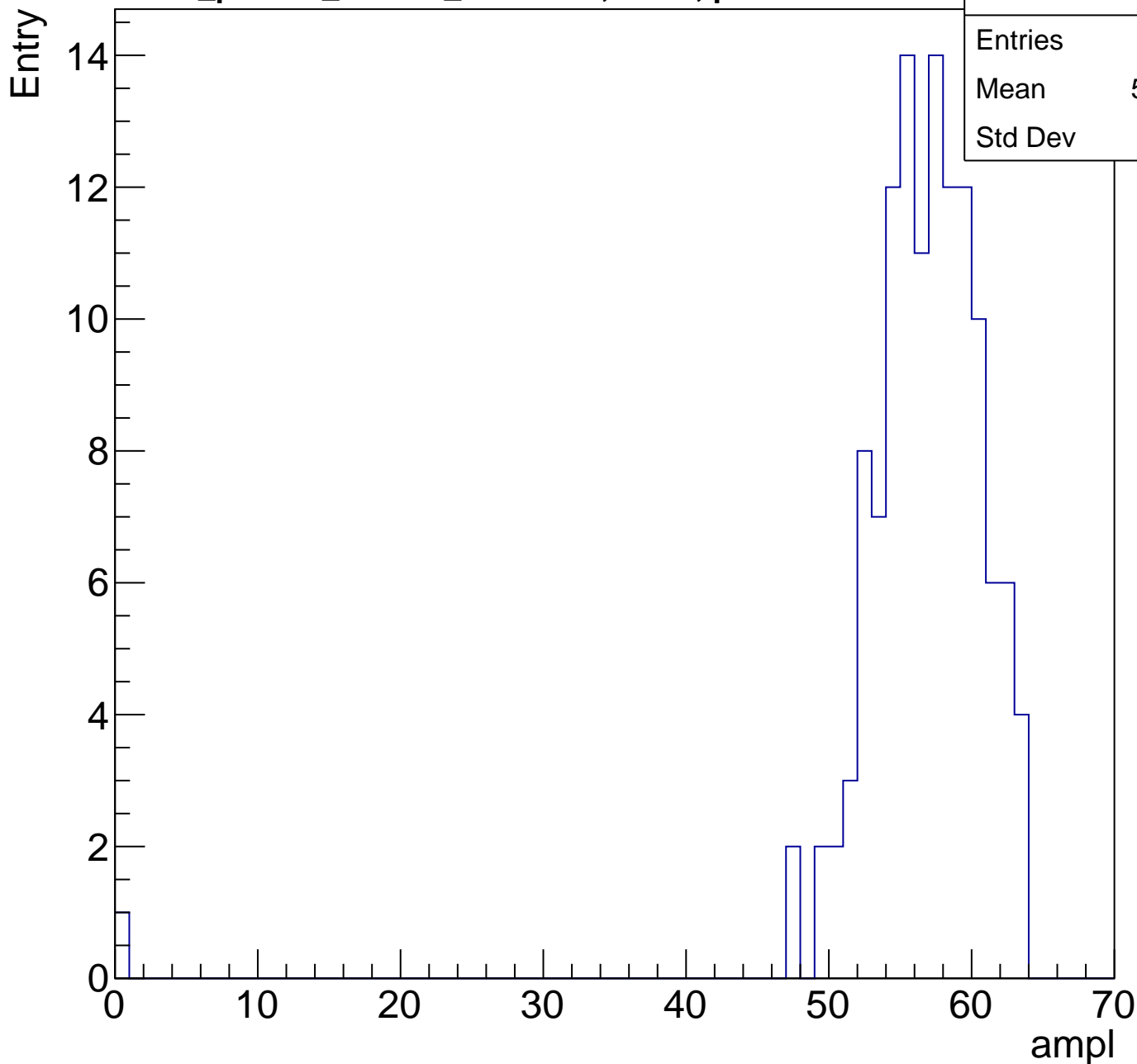
Entries	140
Mean	50.27
Std Dev	3.781



B0L101S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	56.02
Std Dev	6.1



B0L101S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

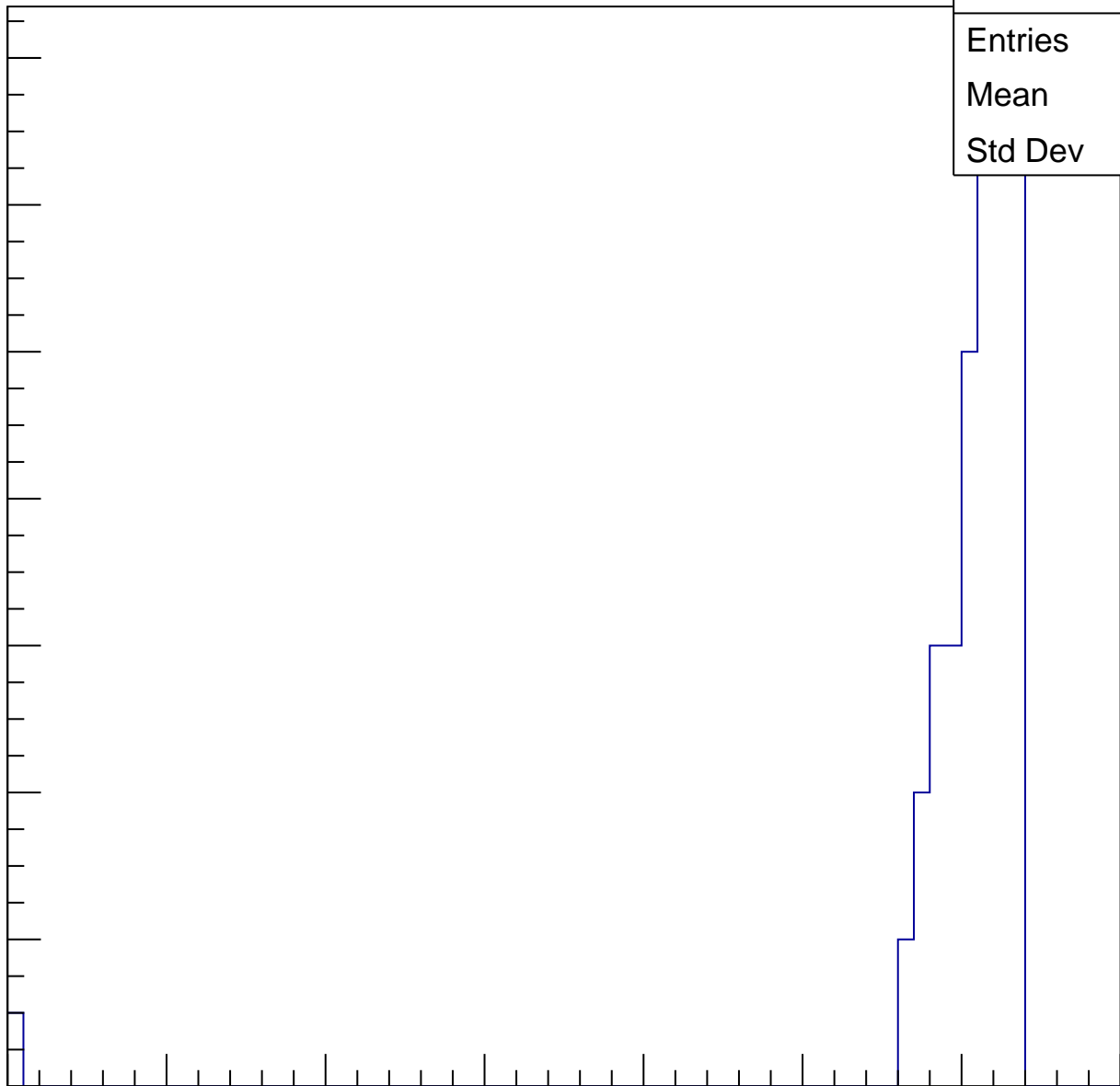
Entries	69
Mean	59.75
Std Dev	7.503

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch116, adc0

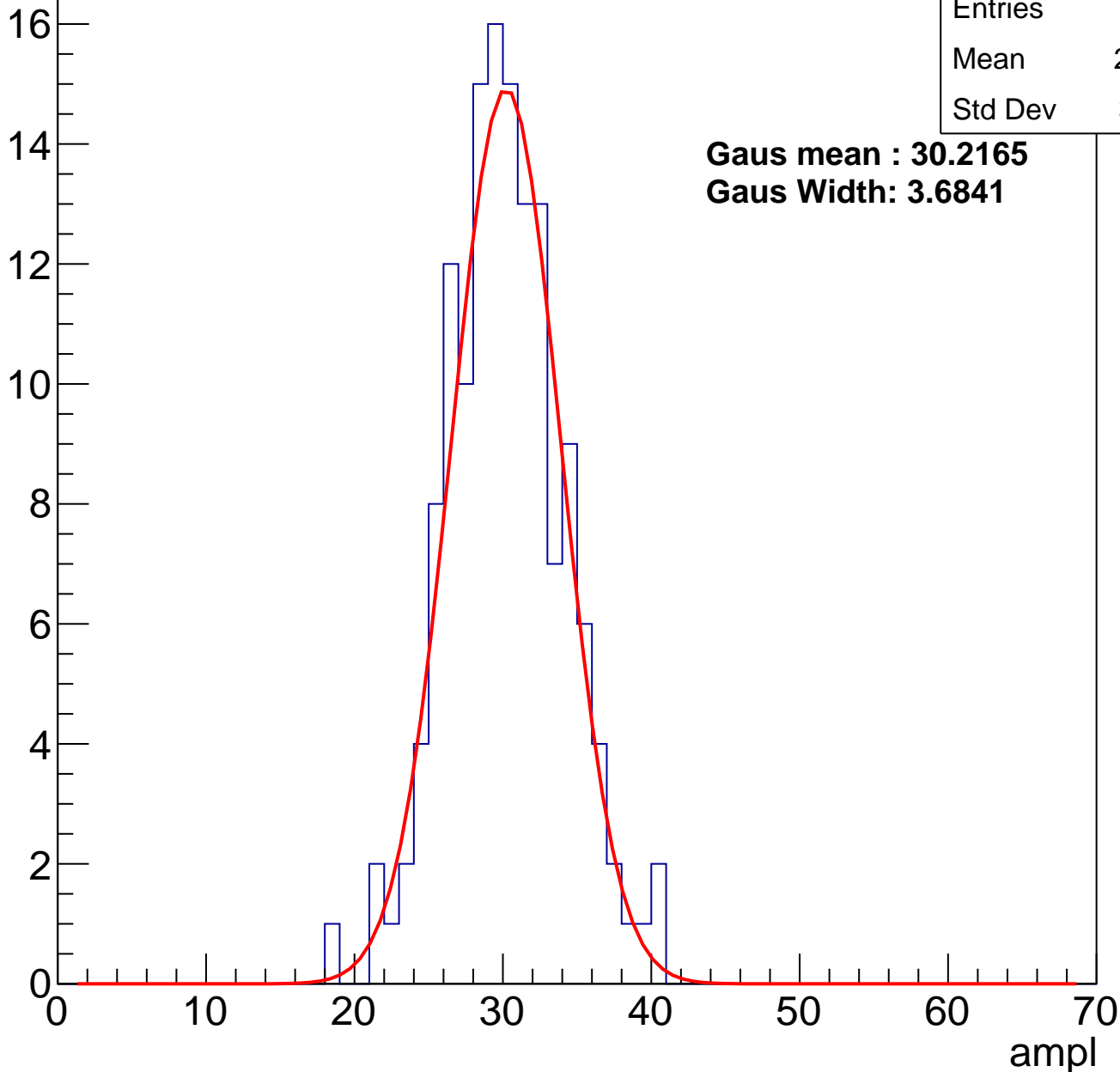
calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	29.73
Std Dev	3.881

Gaus mean : 30.2165

Gaus Width: 3.6841

Entry



B0L101S, U14-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

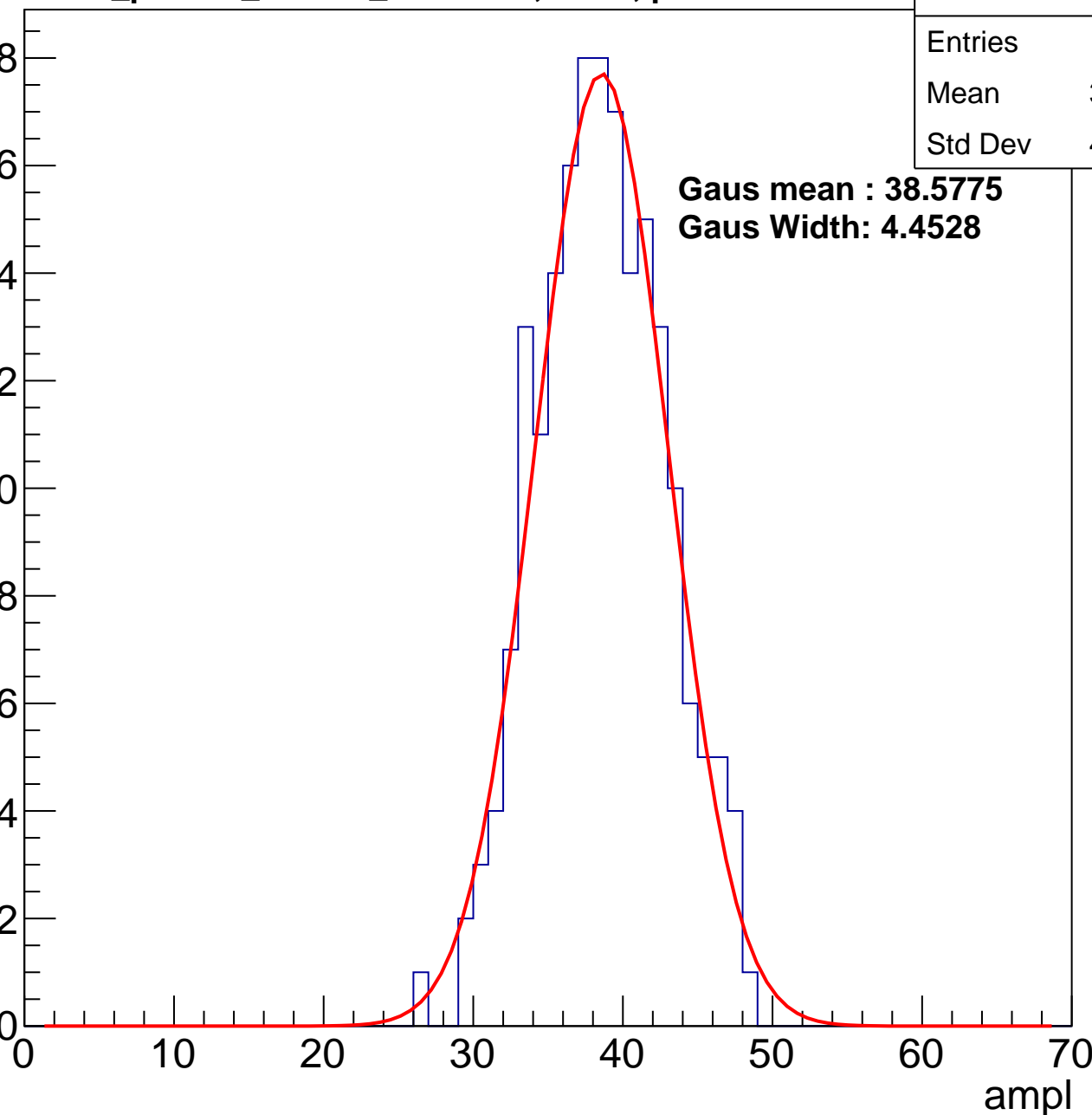
Entry

18
16
14
12
10
8
6
4
2
0

Entries	197
Mean	38.12
Std Dev	4.229

Gaus mean : 38.5775

Gaus Width: 4.4528



B0L101S, U14-ch116, adc2

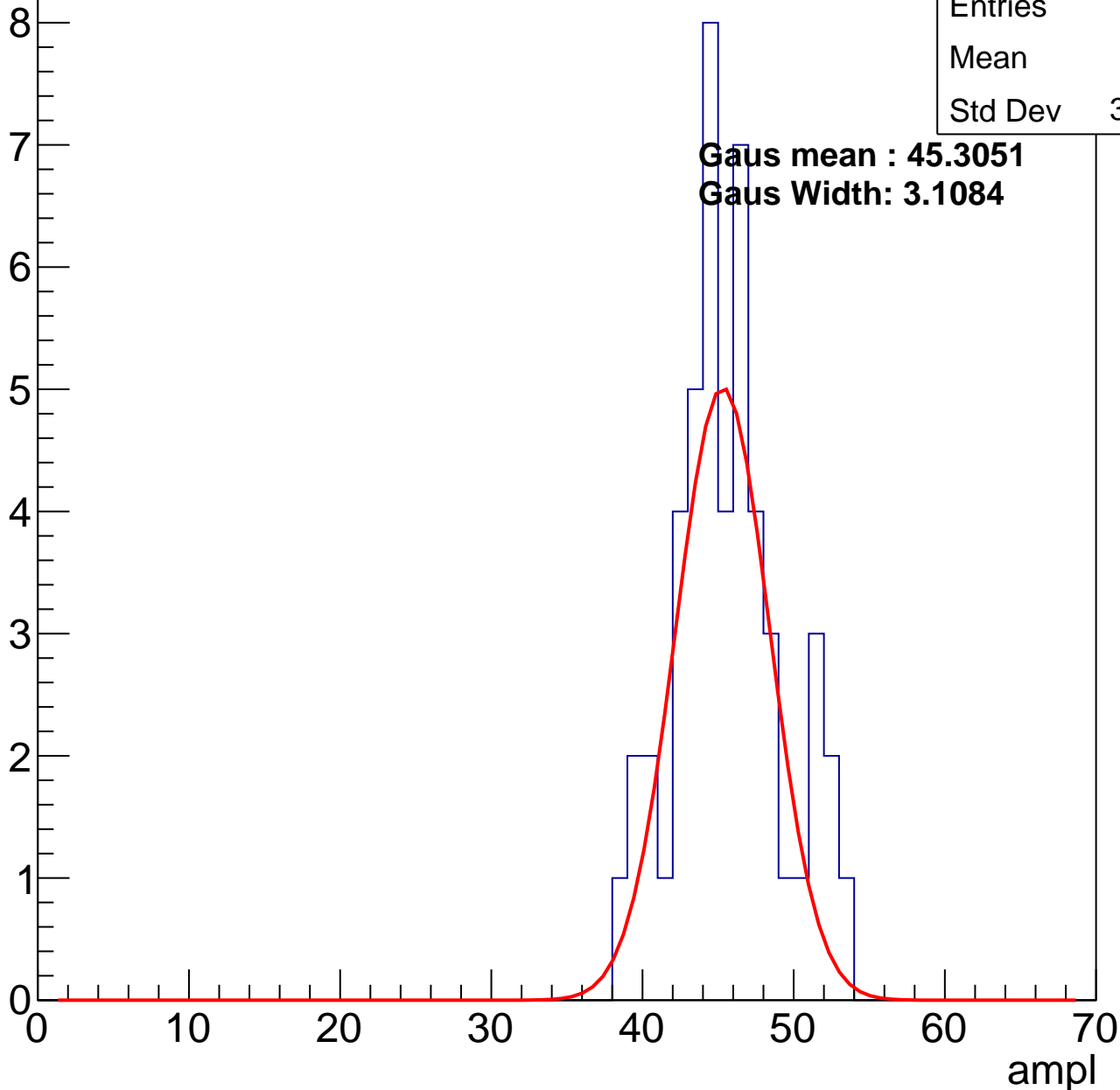
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	49
Mean	45.2
Std Dev	3.528

Gaus mean : 45.3051

Gaus Width: 3.1084



B0L101S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

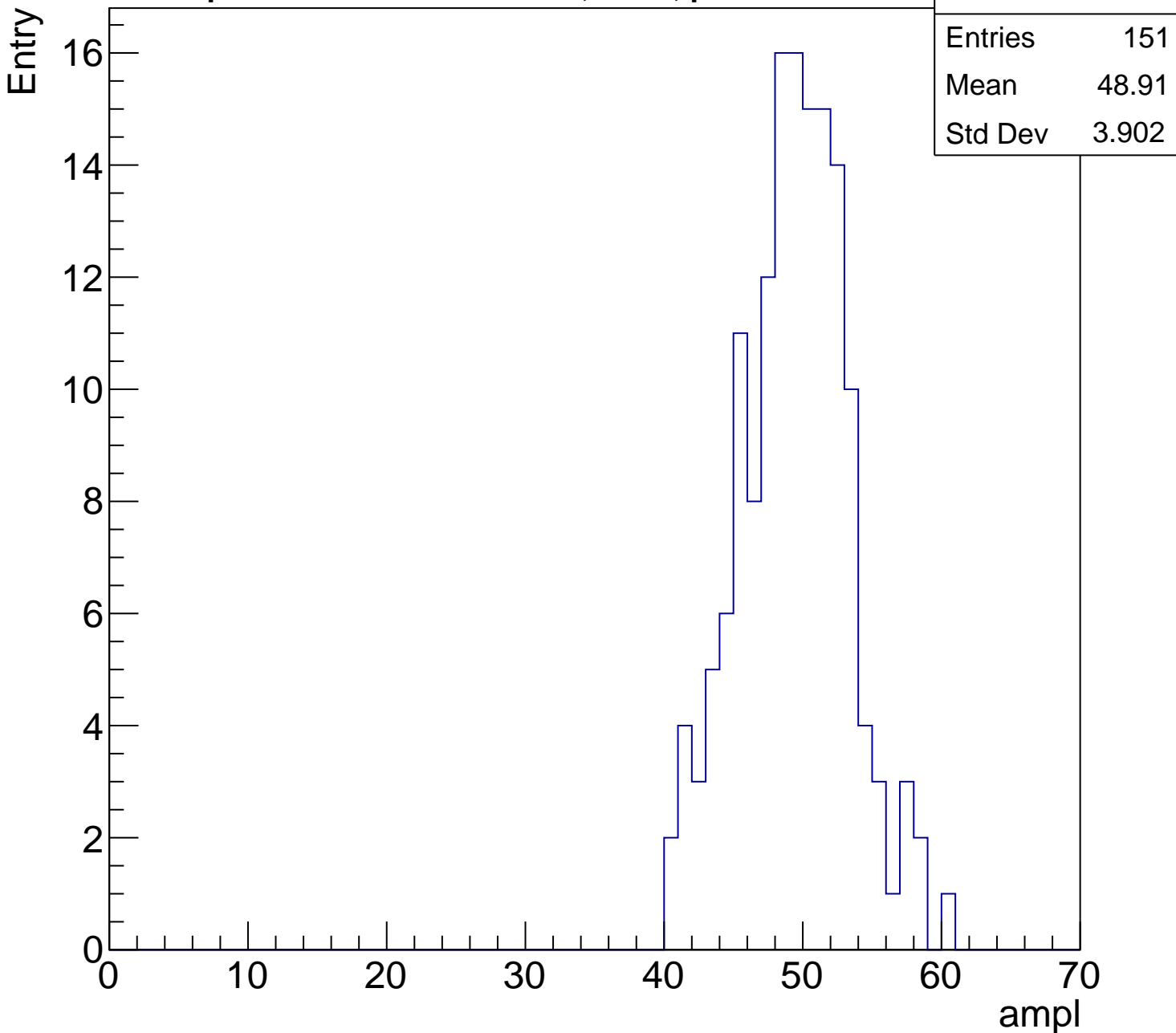
Entries	151
Mean	48.91
Std Dev	3.902

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

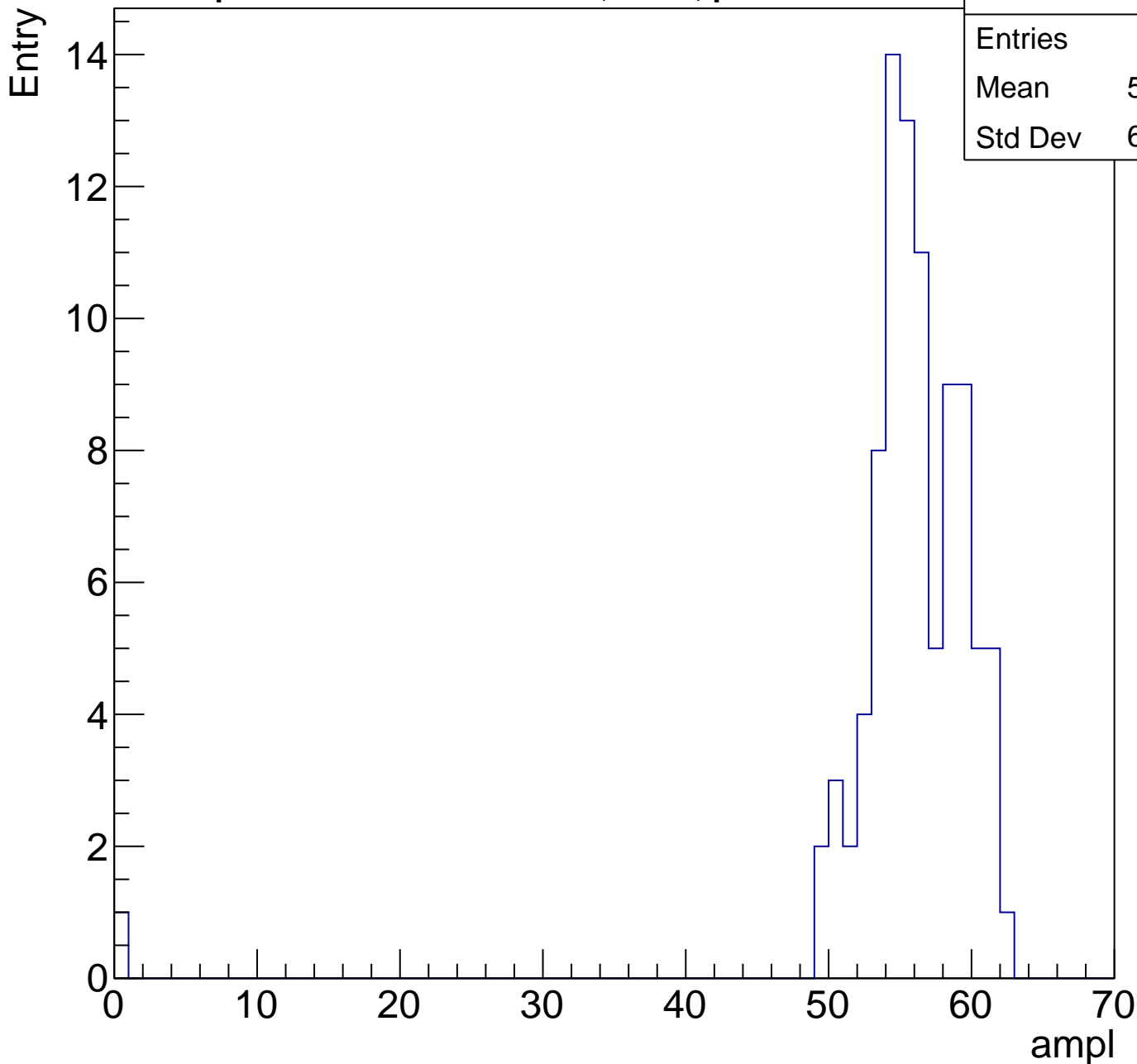
ampl



B0L101S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	92
Mean	55.15
Std Dev	6.506



B0L101S, U14-ch116, adc5

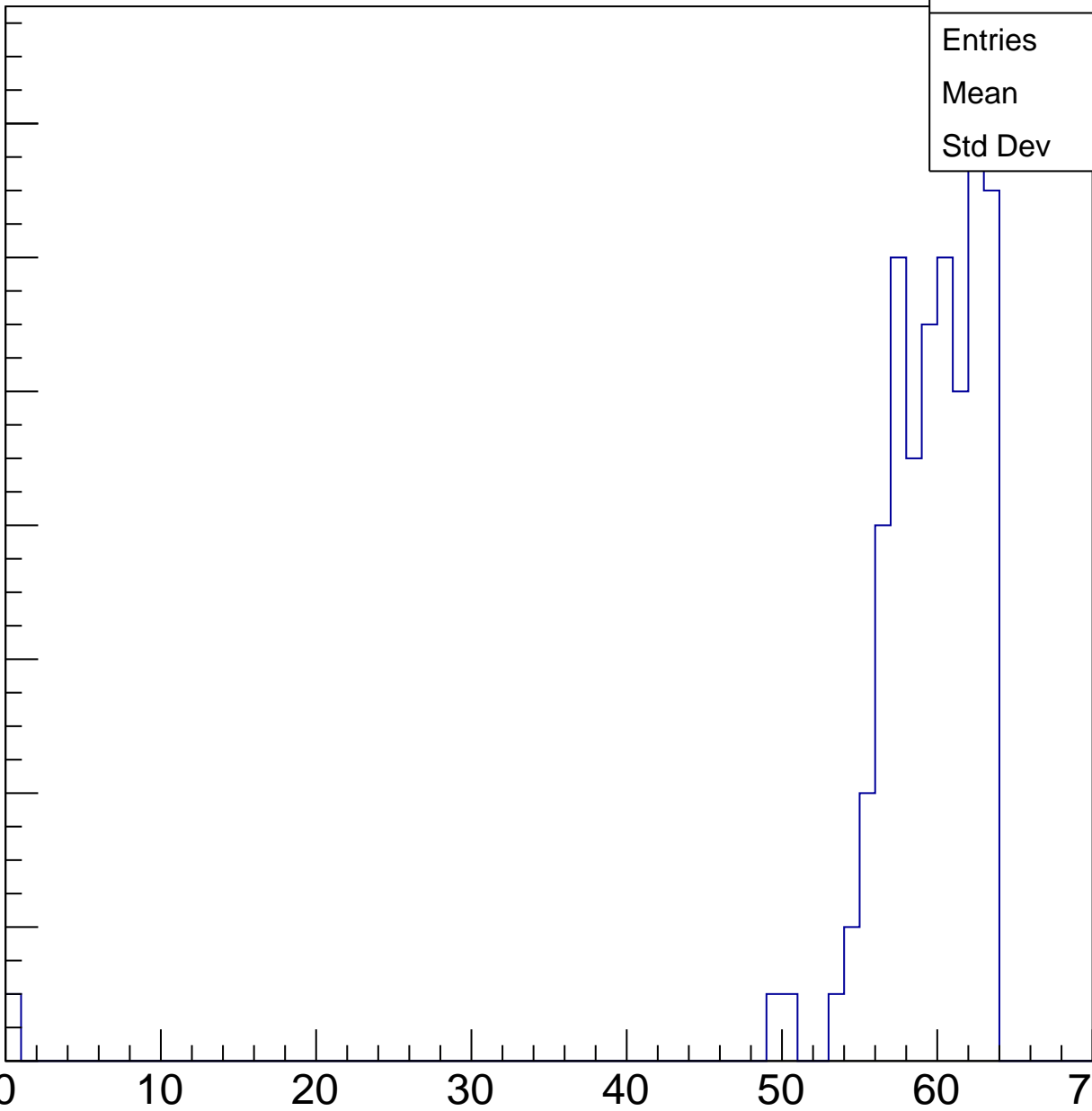
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	58.62
Std Dev	6.574

ampl

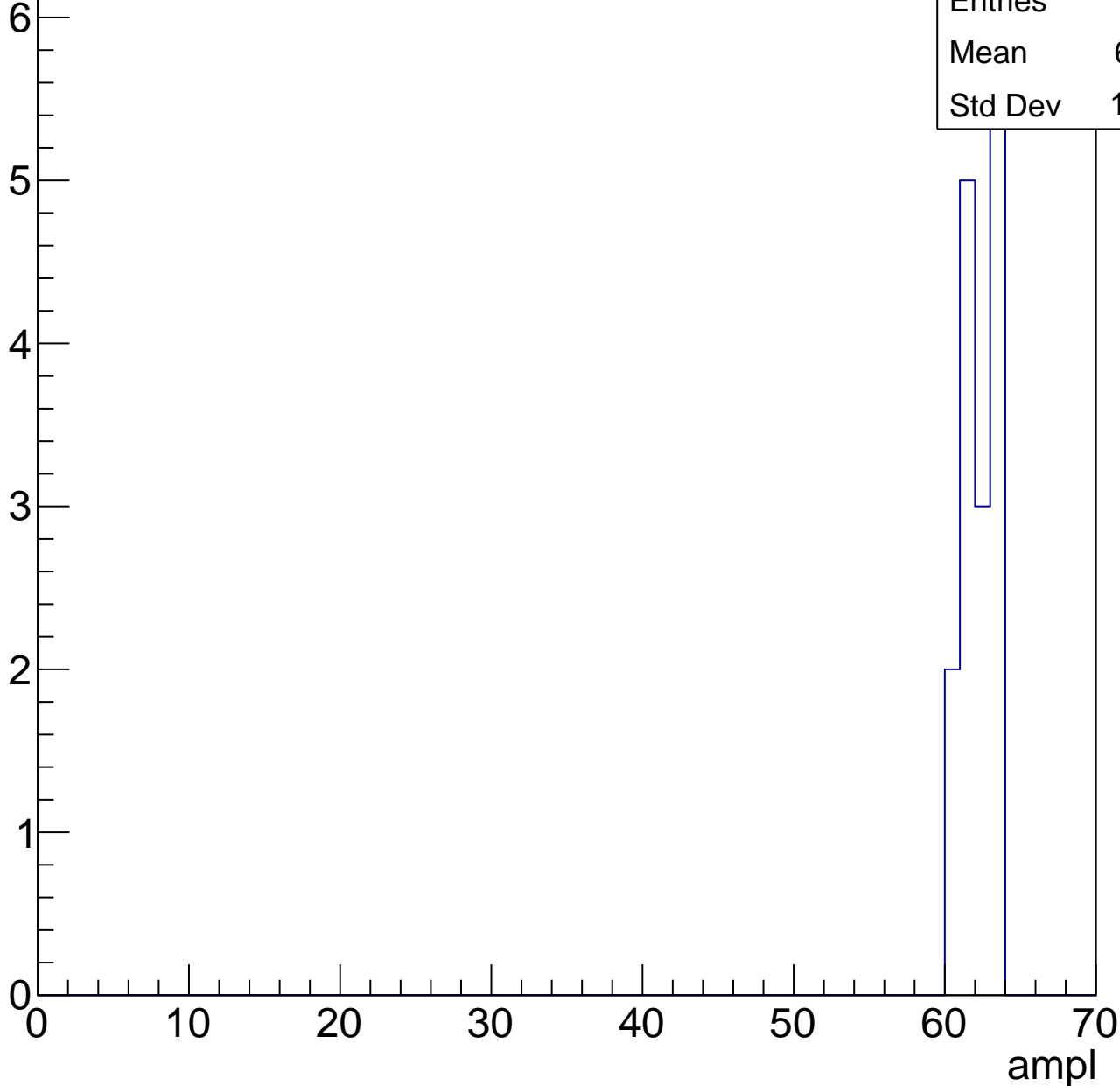


B0L101S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.81
Std Dev	1.073



B0L101S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	31.1
Std Dev	3.711

Gaus mean : 31.8117

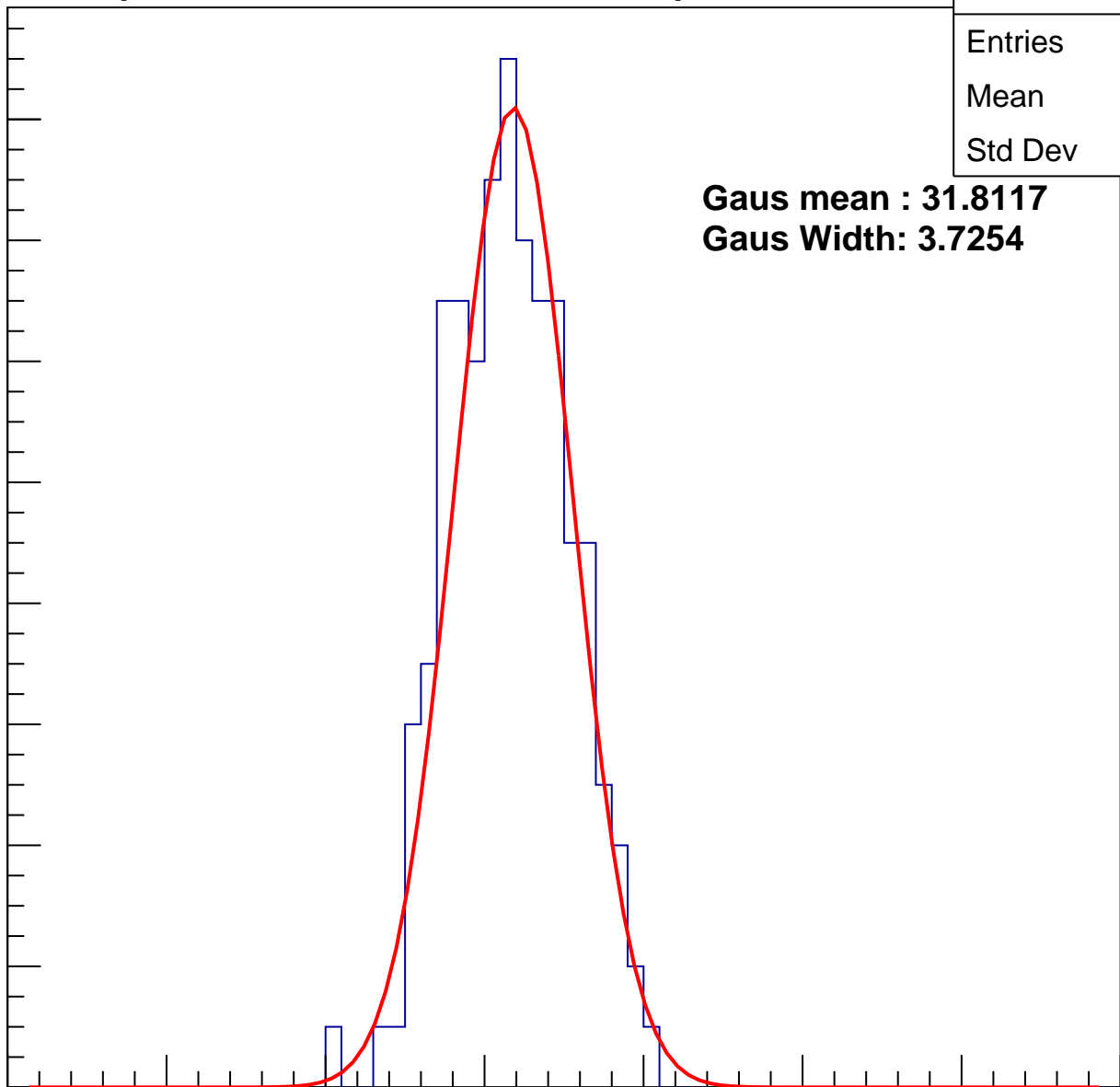
Gaus Width: 3.7254

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch117, adc1

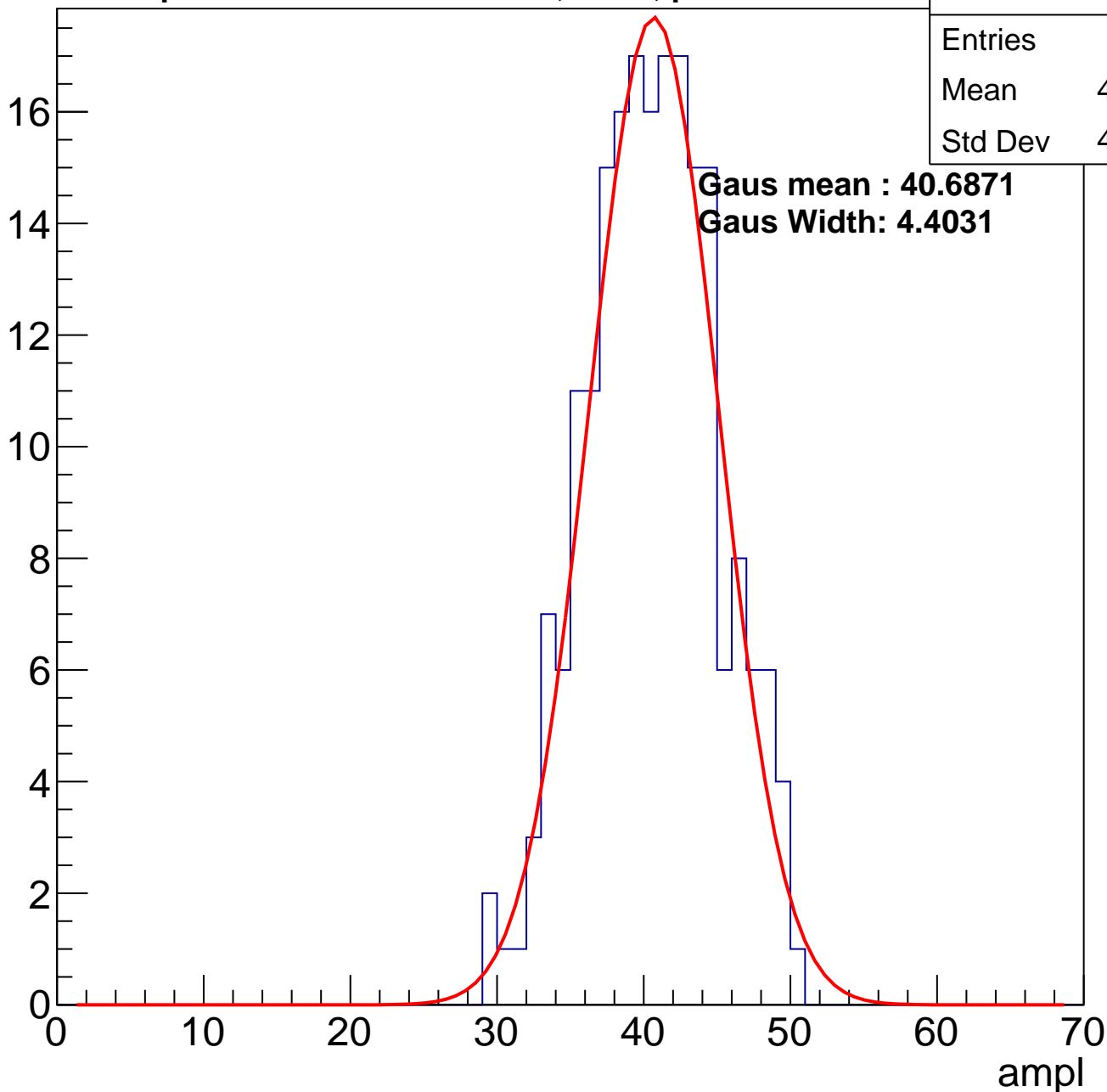
calib_packv5_042523_0143.root, FC#1, port C1

Entries	201
Mean	40.13
Std Dev	4.369

Gaus mean : 40.6871

Gaus Width: 4.4031

Entry



B0L101S, U14-ch117, adc2

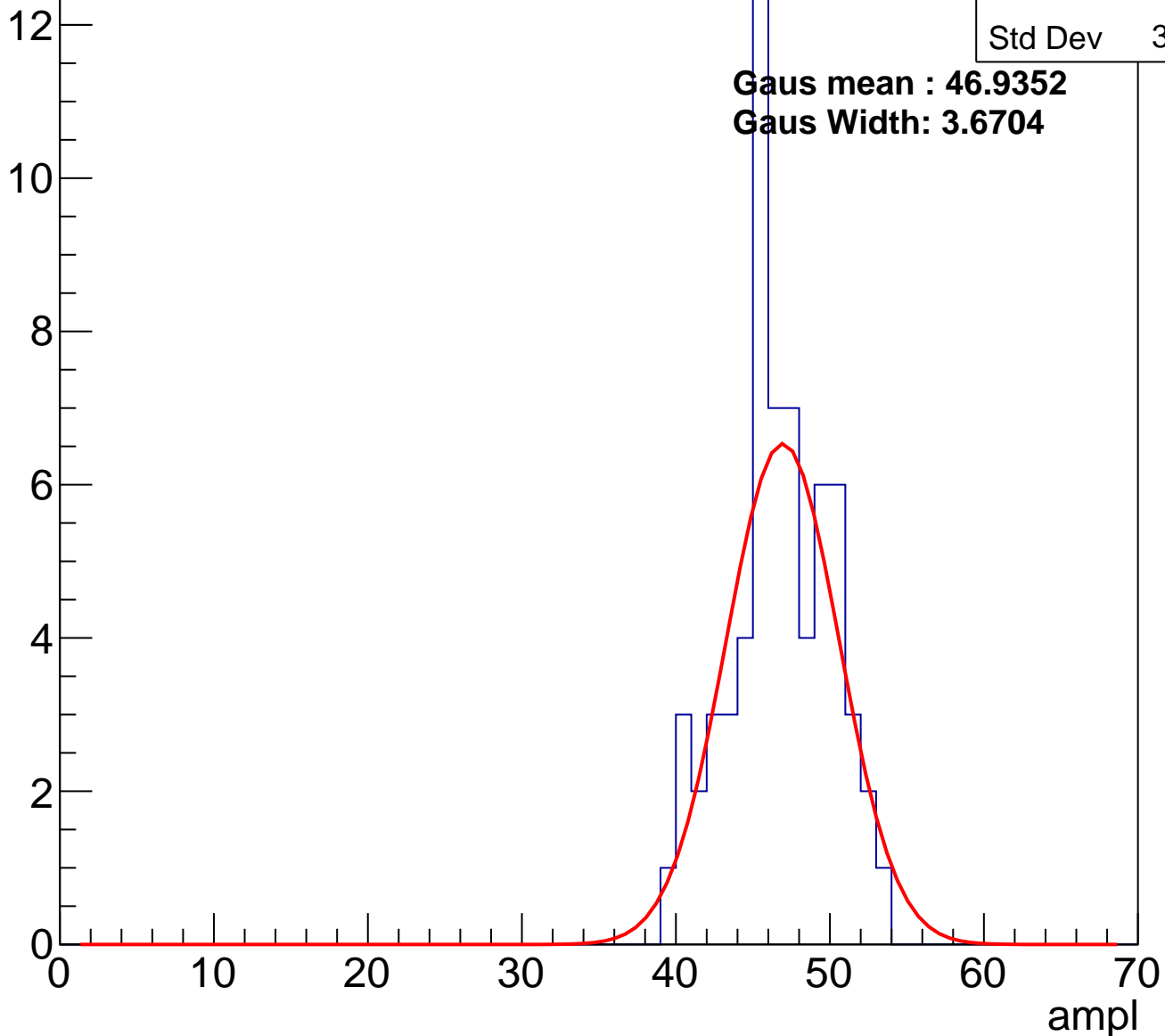
calib_packv5_042523_0143.root, FC#1, port C1

Entries	65
Mean	46.22
Std Dev	3.227

Gaus mean : 46.9352

Gaus Width: 3.6704

Entry



B0L101S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	51.66
Std Dev	3.776

Entry

16
14
12
10
8
6
4
2
0

0

10

20

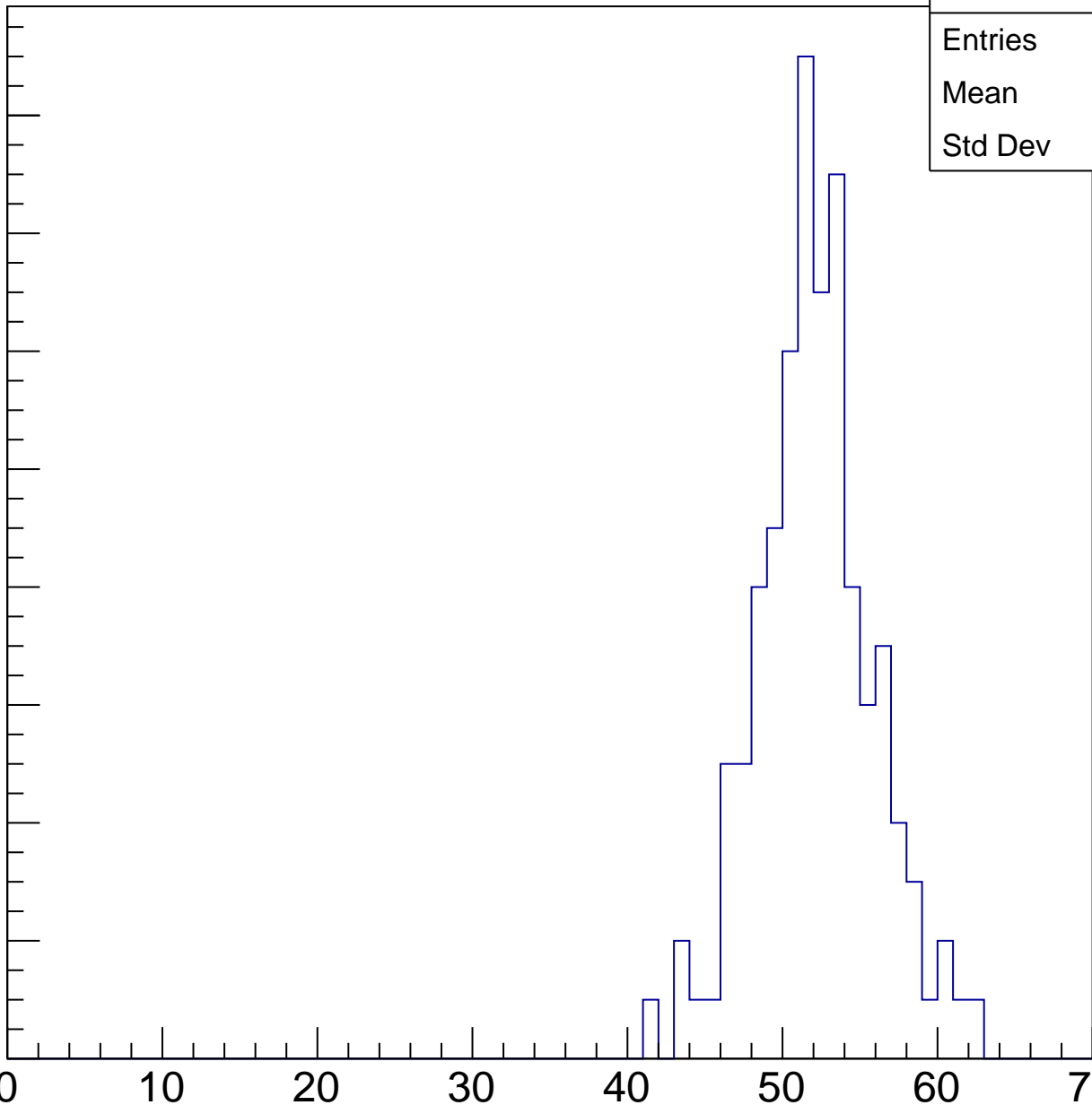
30

40

50

60

ampl



B0L101S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	56.42
Std Dev	3.464

Entry

12

10

8

6

4

2

0

0

10

20

30

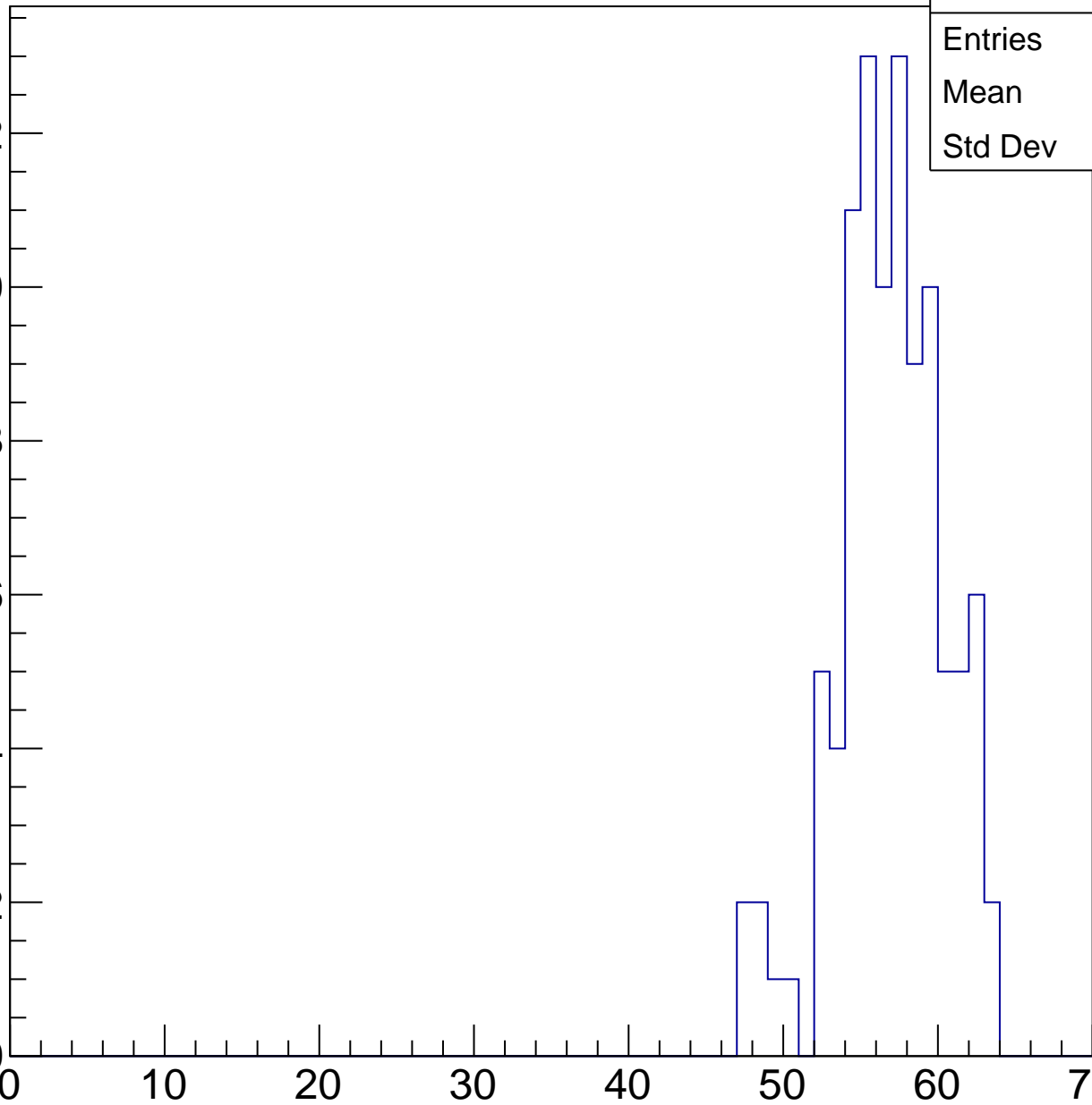
40

50

60

70

ampl



B0L101S, U14-ch117, adc5

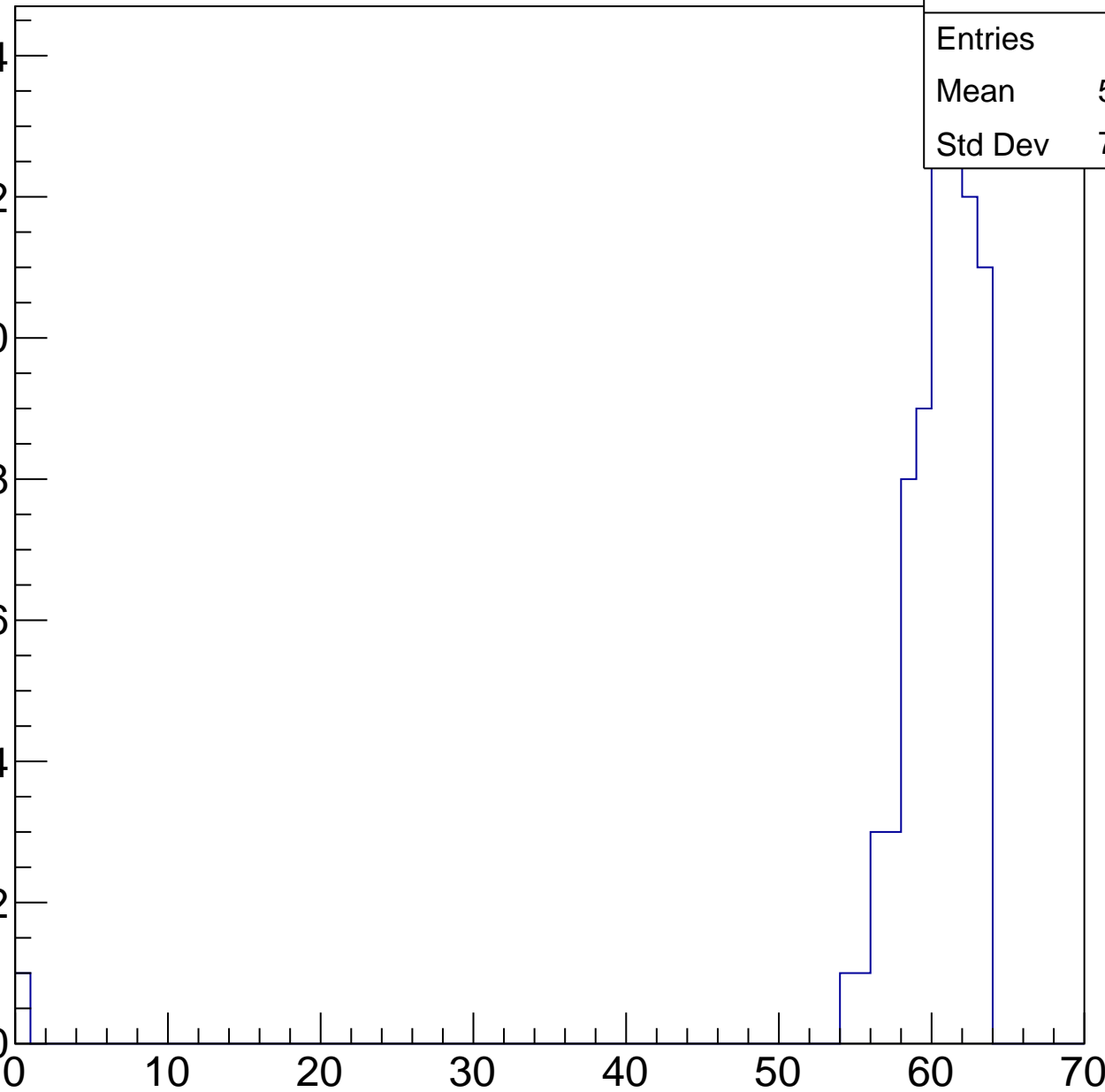
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.39
Std Dev	7.173

ampl

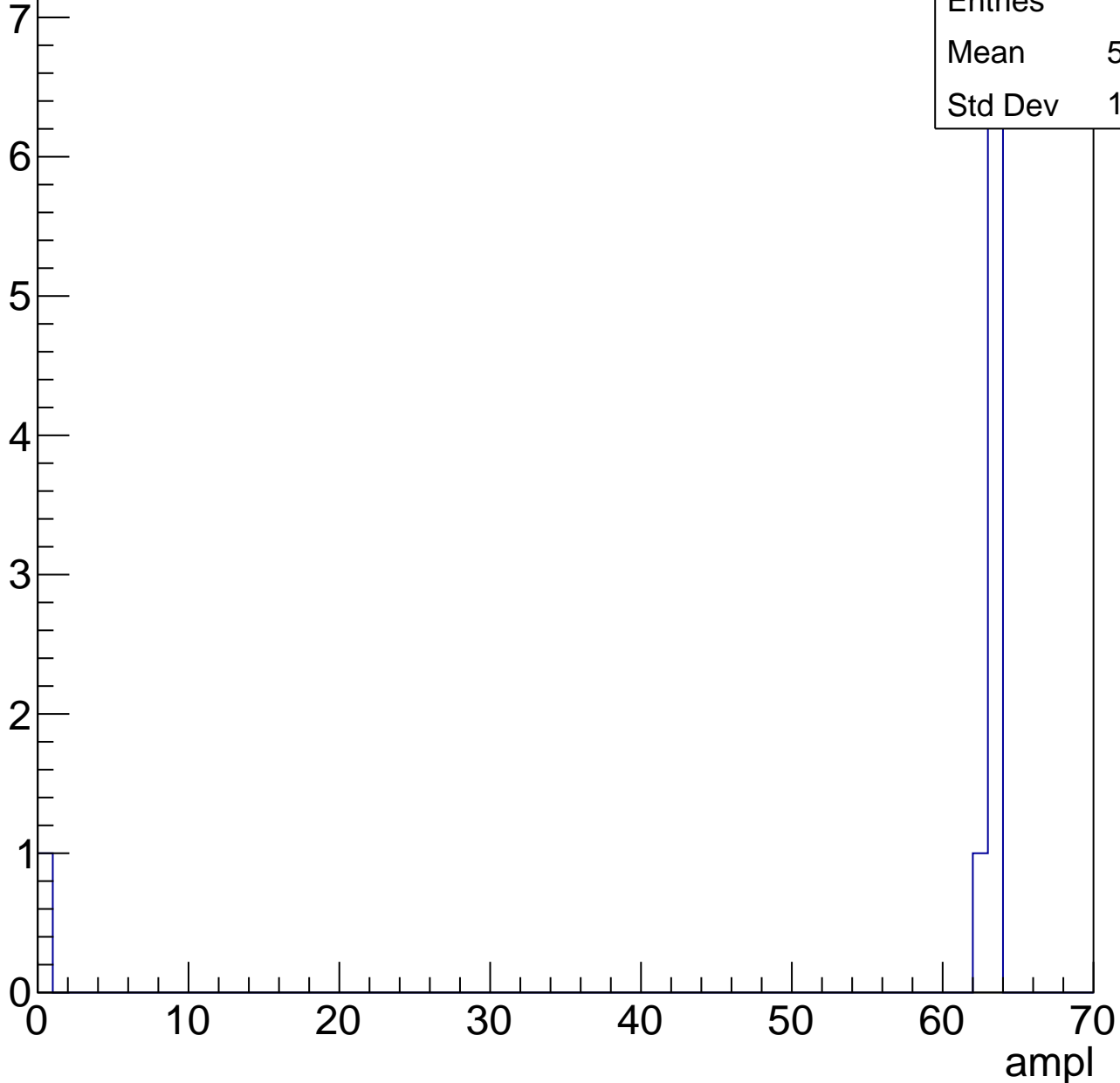


B0L101S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	55.89
Std Dev	19.76



B0L101S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch118, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	30.51
Std Dev	5.194

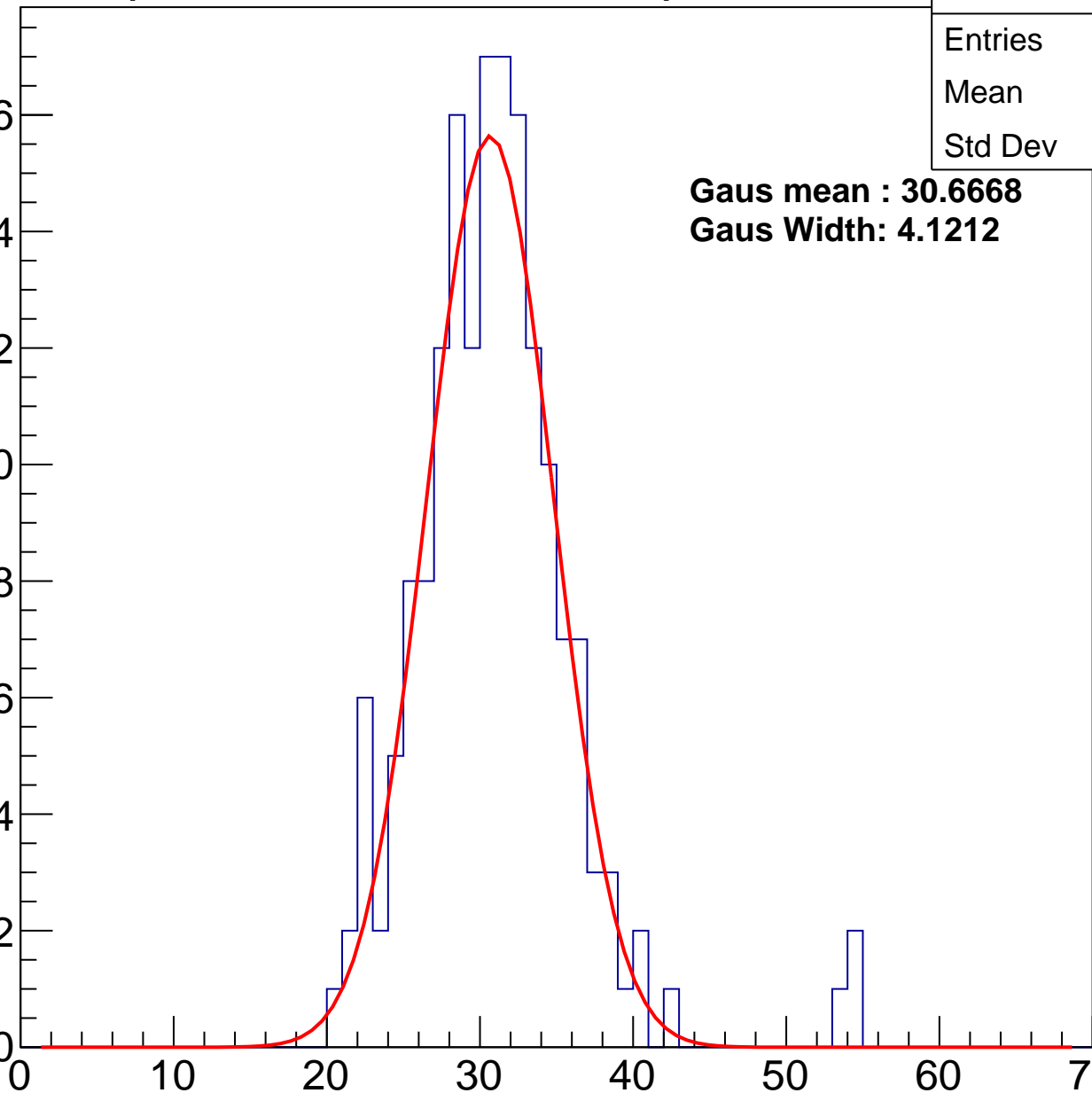
Gaus mean : 30.6668

Gaus Width: 4.1212

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

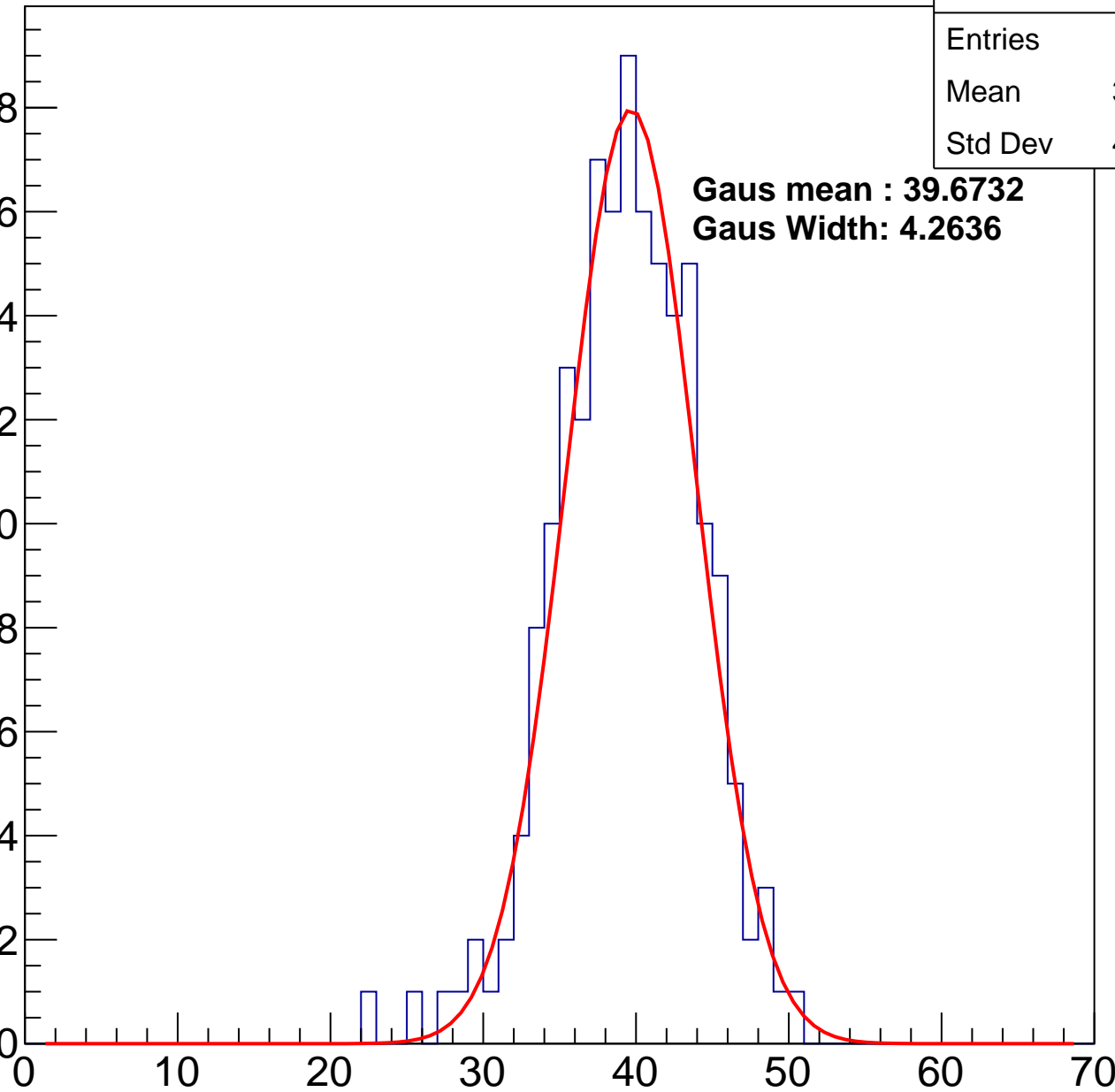
Entry

18
16
14
12
10
8
6
4
2
0

Entries	199
Mean	38.94
Std Dev	4.564

Gaus mean : 39.6732

Gaus Width: 4.2636



ampl

B0L101S, U14-ch118, adc2

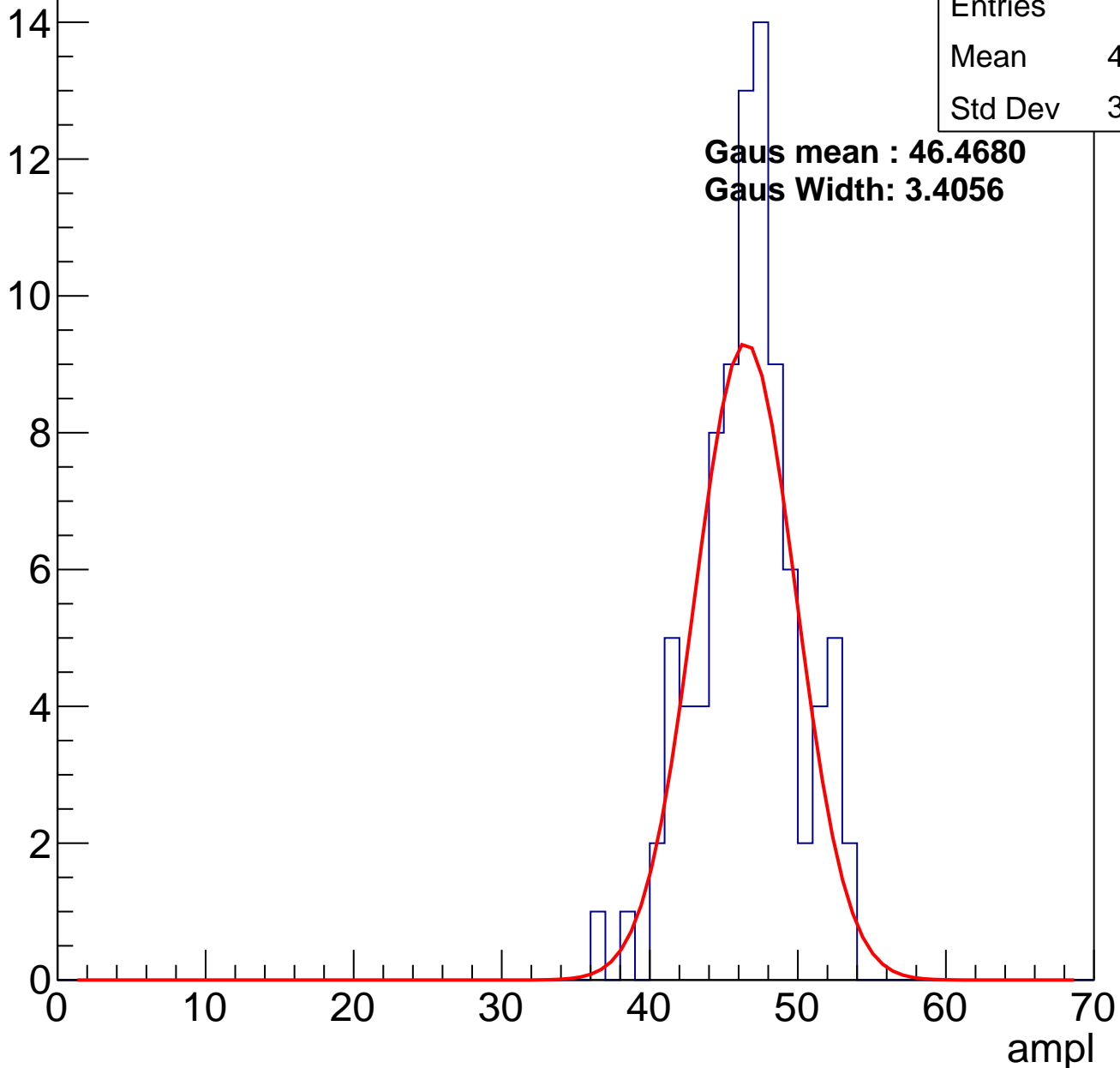
calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	46.16
Std Dev	3.375

Gaus mean : 46.4680

Gaus Width: 3.4056

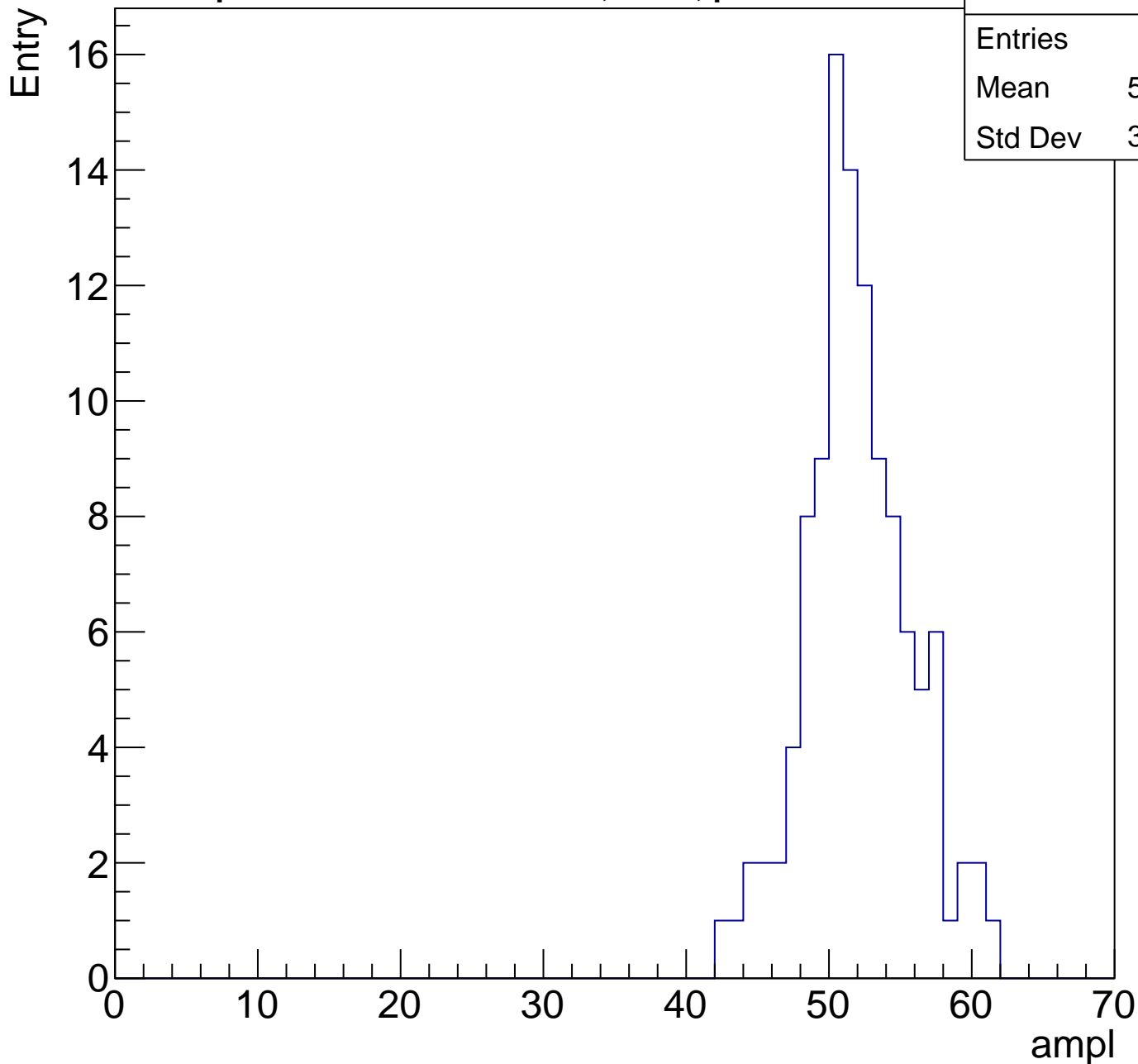
Entry



B0L101S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	51.57
Std Dev	3.692

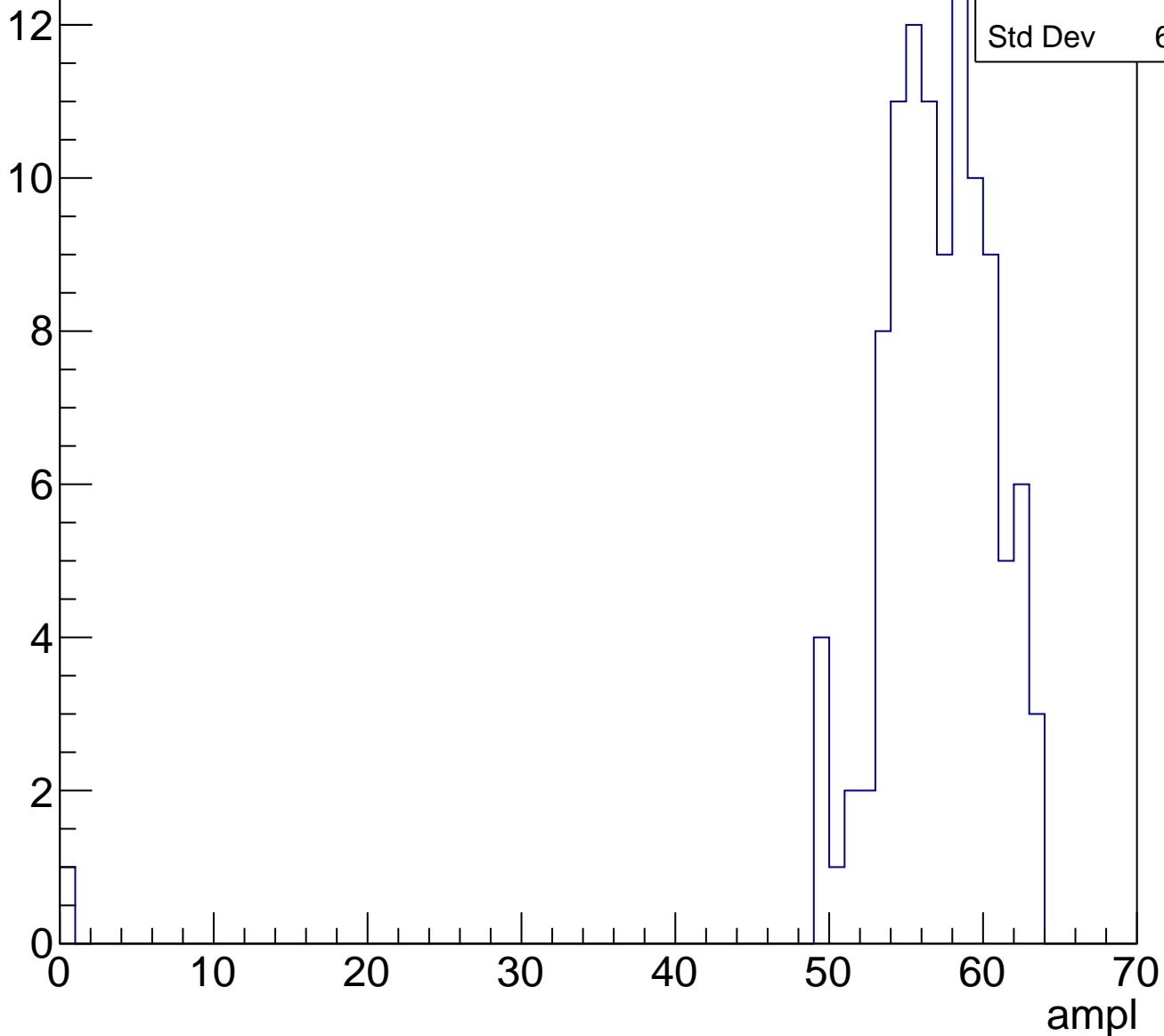


B0L101S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	56.16
Std Dev	6.382

Entry



B0L101S, U14-ch118, adc5

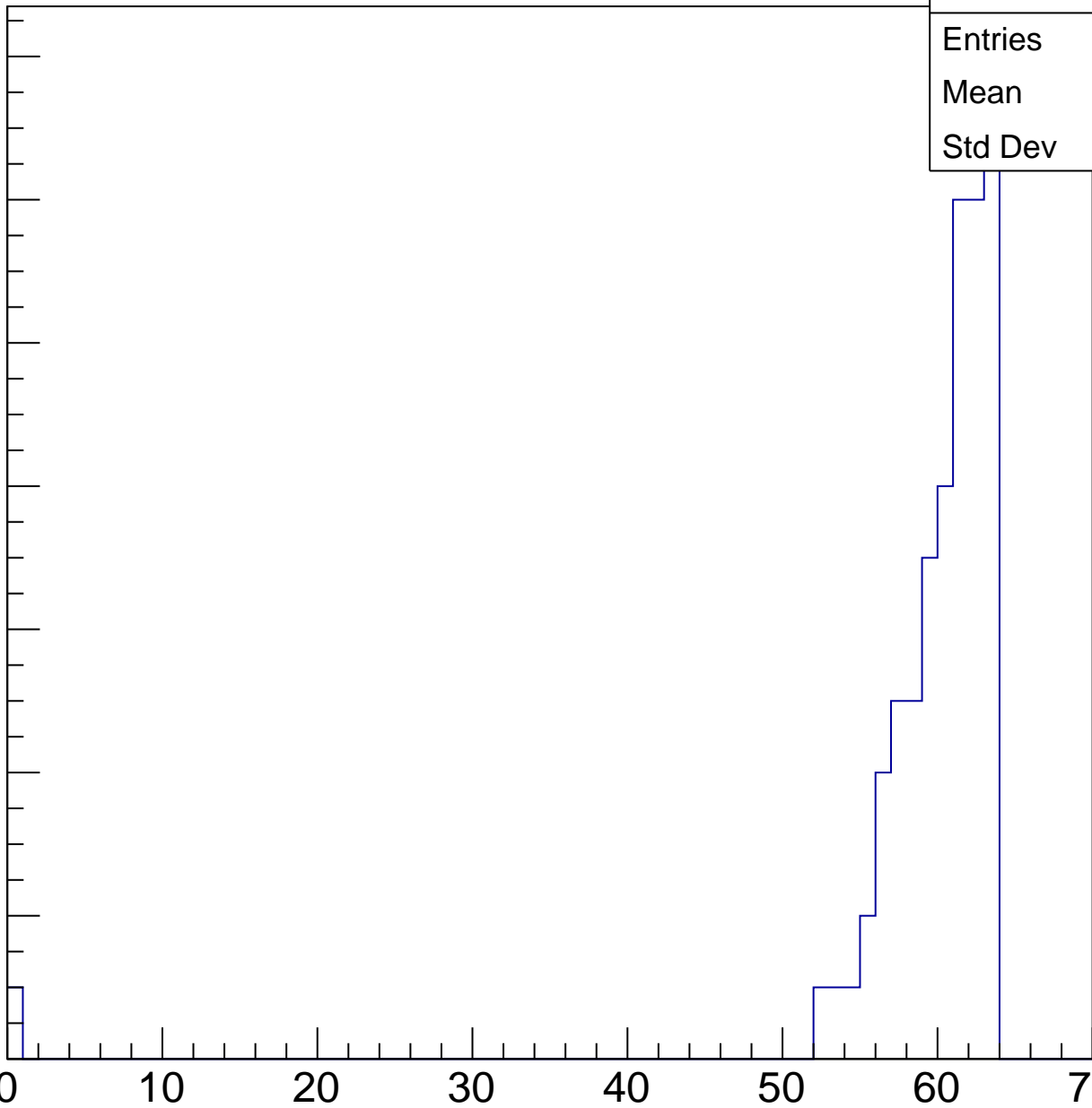
calib_packv5_042523_0143.root, FC#1, port C1

Entries	73
Mean	59.16
Std Dev	7.467

Entry

14
12
10
8
6
4
2
0

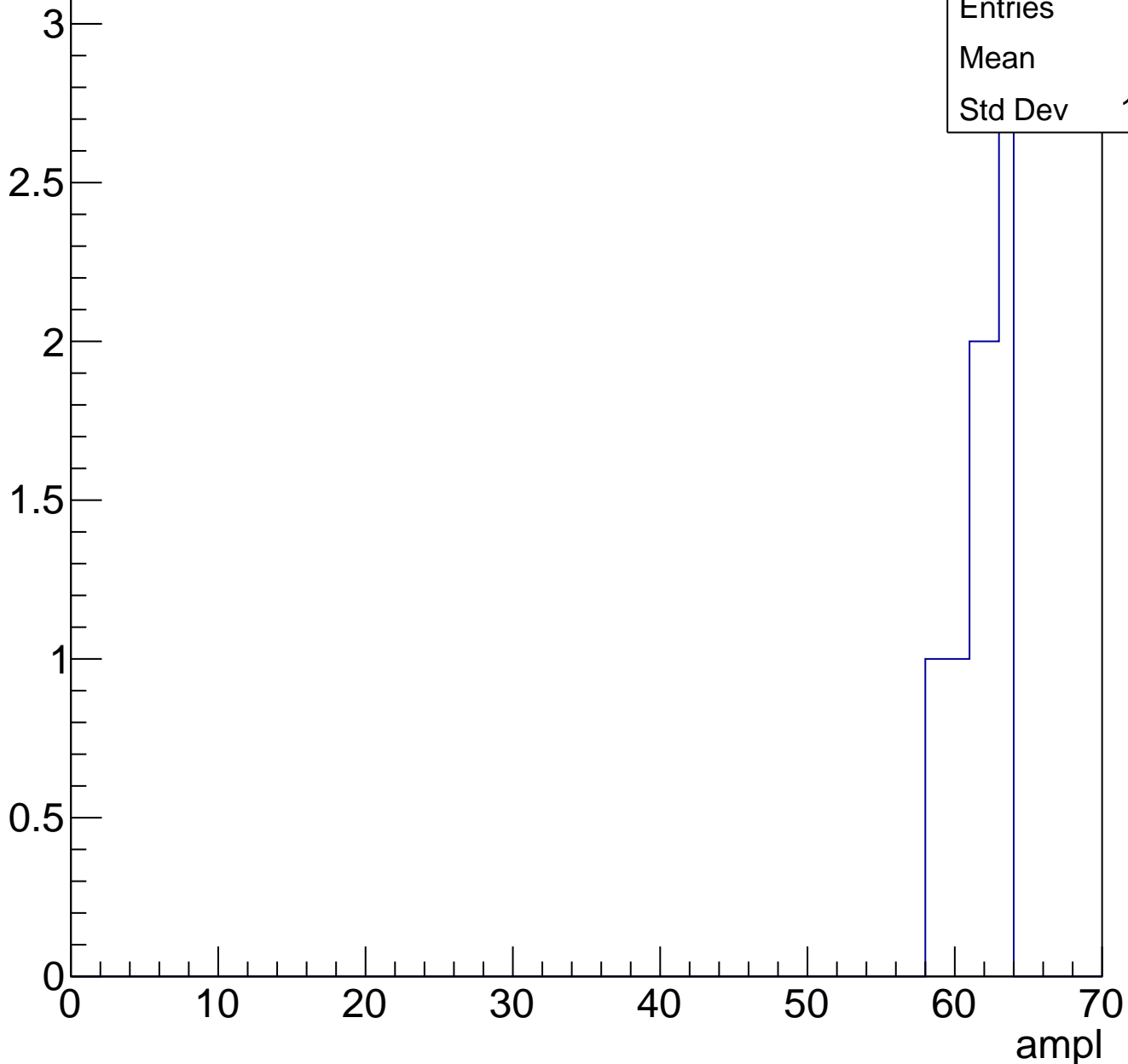
ampl



B0L101S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	10
Mean	61.2
Std Dev	1.661

B0L101S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U14-ch119, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.6
Std Dev	4.621

Gaus mean : 29.1798

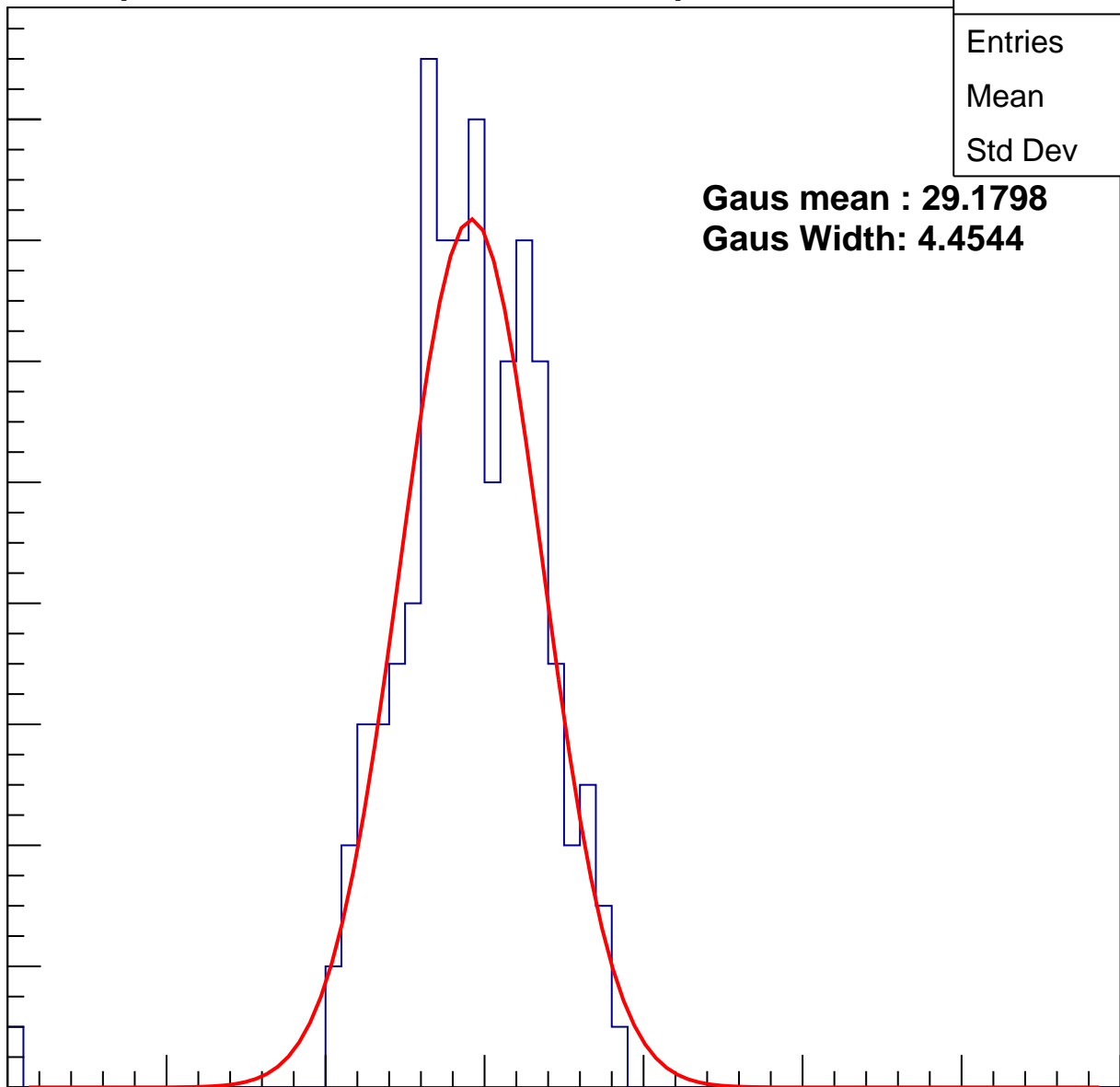
Gaus Width: 4.4544

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch119, adc1

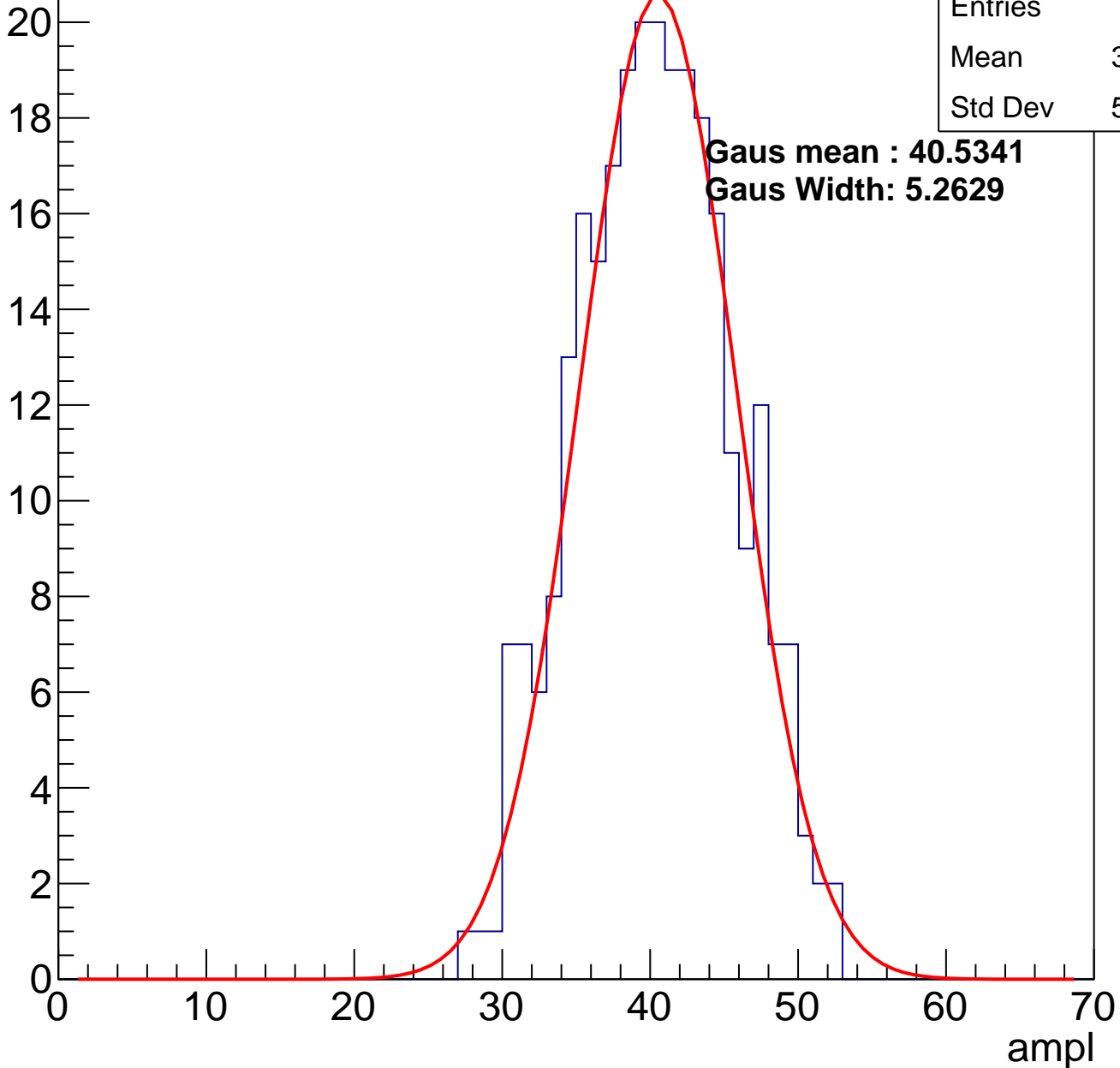
calib_packv5_042523_0143.root, FC#1, port C1

Entries	276
Mean	39.87
Std Dev	5.169

Gaus mean : 40.5341

Gaus Width: 5.2629

Entry



B0L101S, U14-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	49.74
Std Dev	3.579

Entry

12

10

8

6

4

2

0

0

10

20

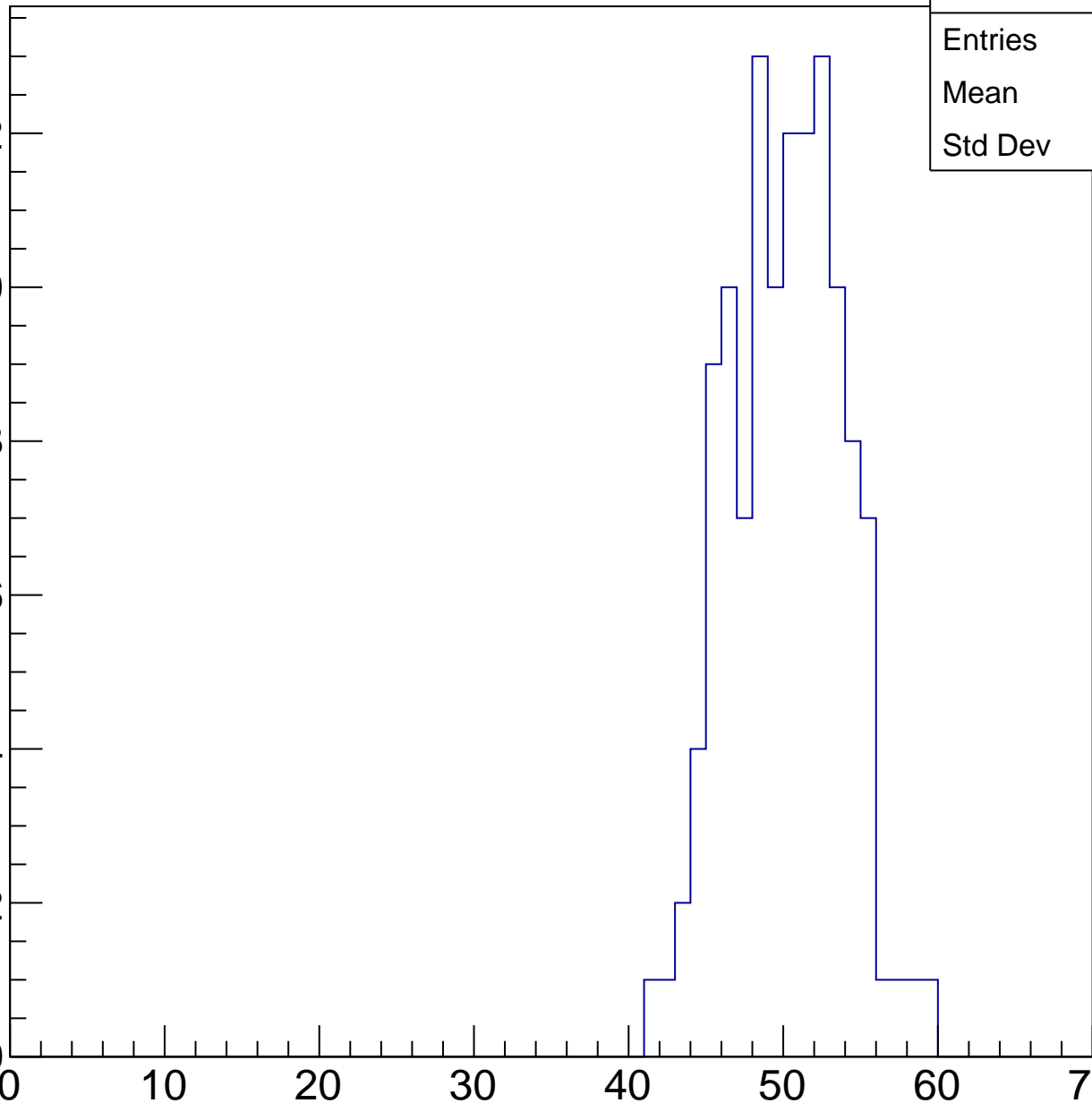
30

40

50

60

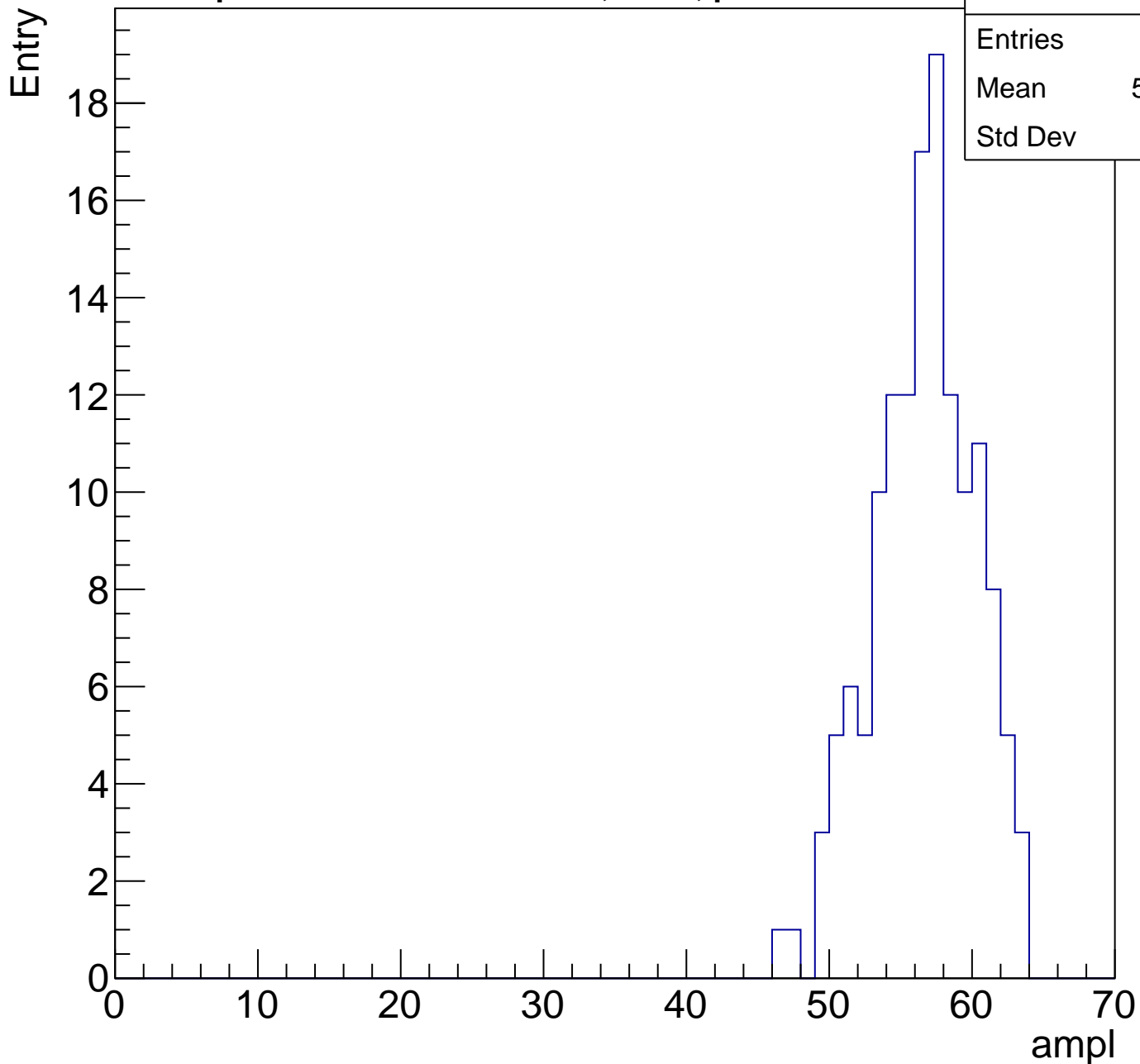
ampl



B0L101S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	56.16
Std Dev	3.53



B0L101S, U14-ch119, adc5

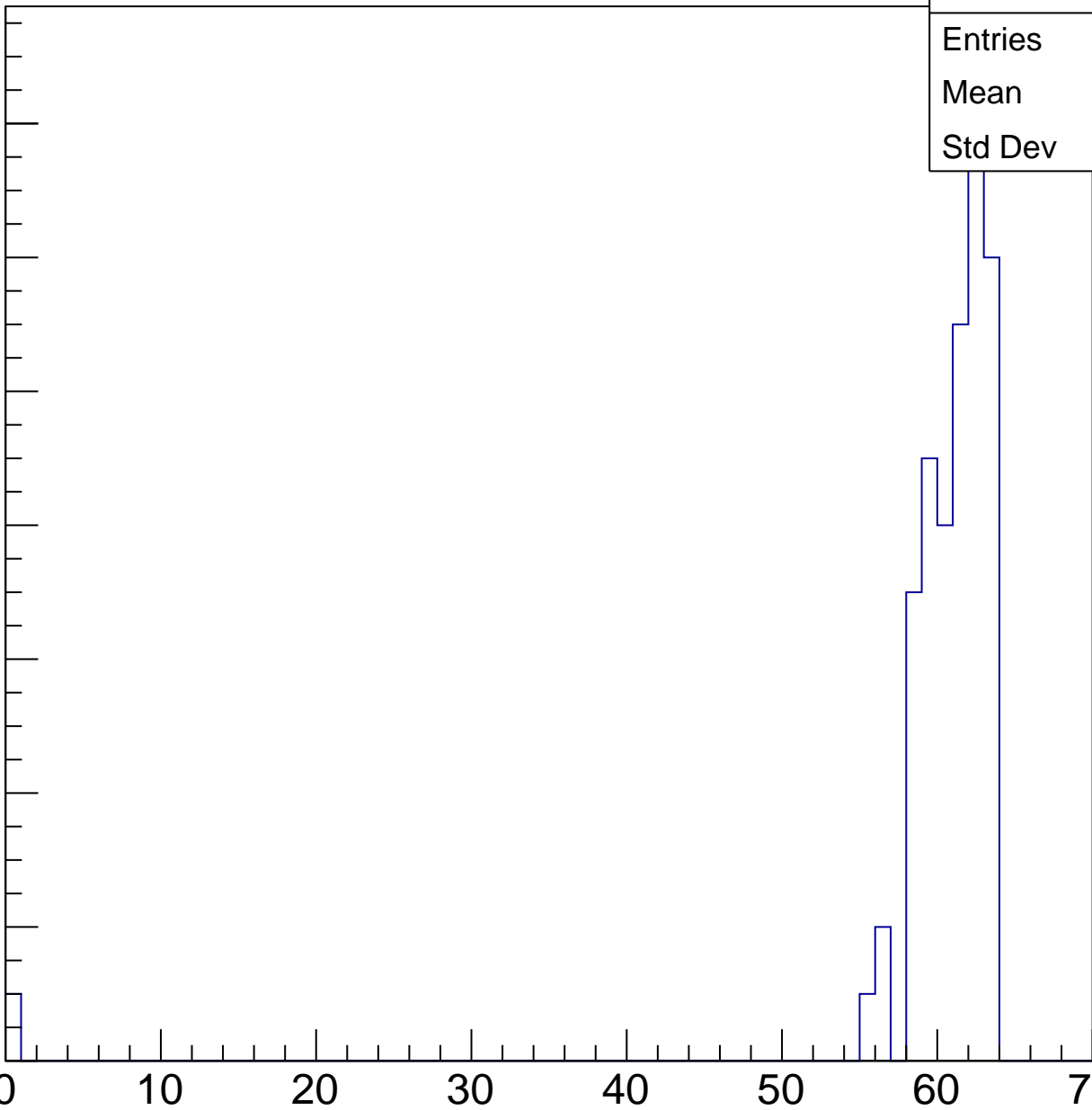
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.71
Std Dev	7.655

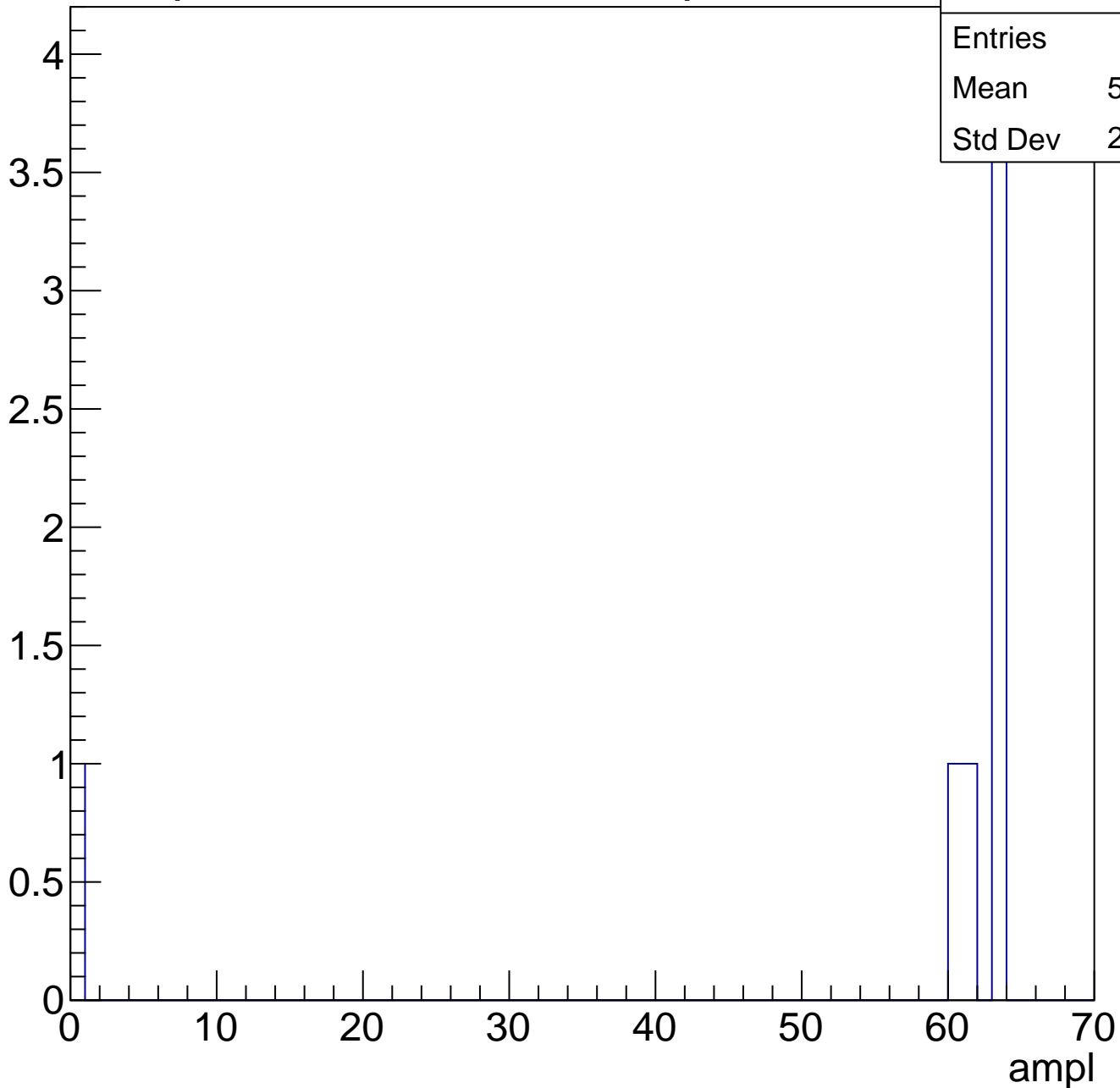
ampl



B0L101S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	40.5
Std Dev	22.5

ampl

B0L101S, U14-ch120, adc0

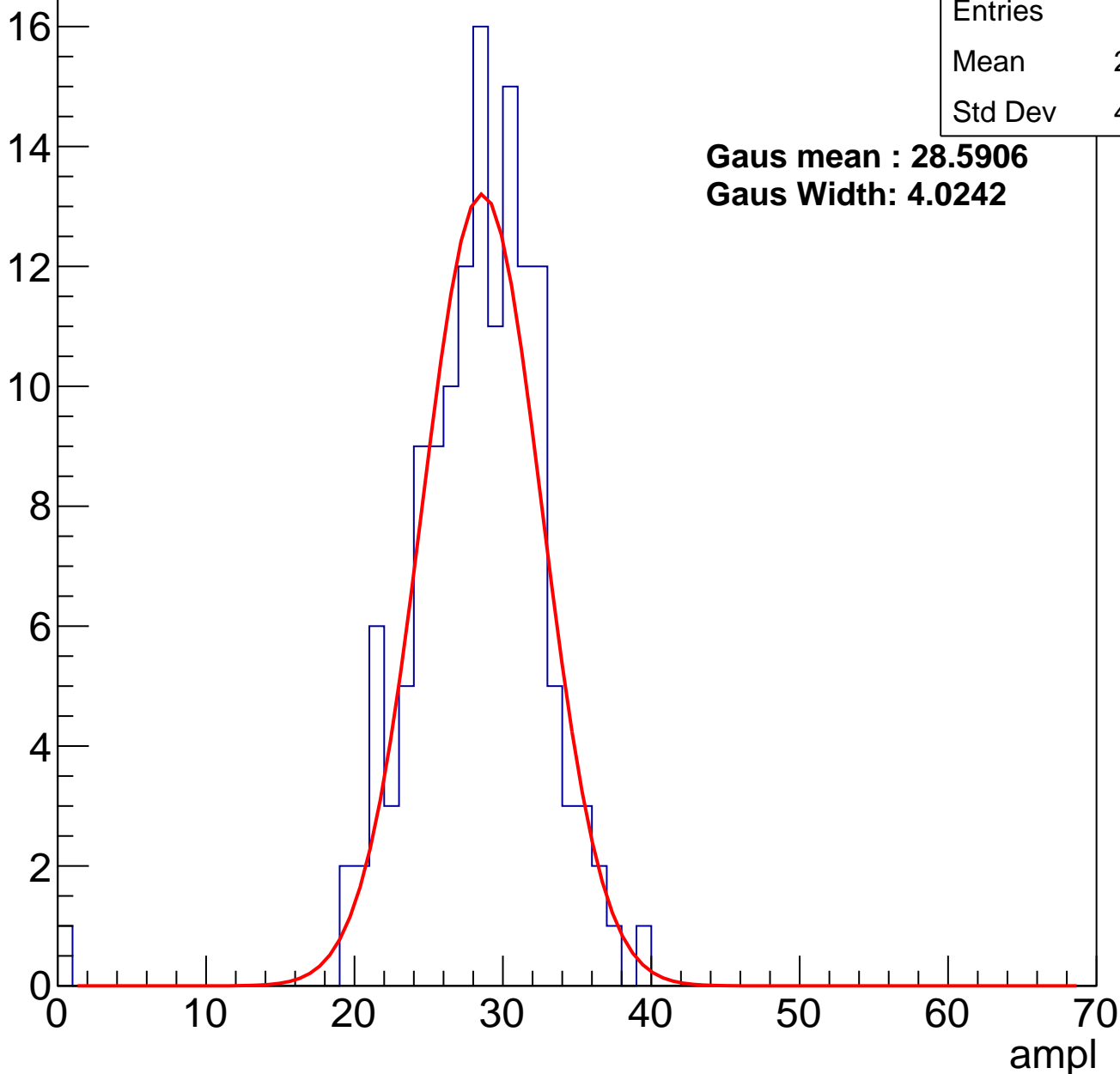
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	27.88
Std Dev	4.596

Gaus mean : 28.5906

Gaus Width: 4.0242

Entry



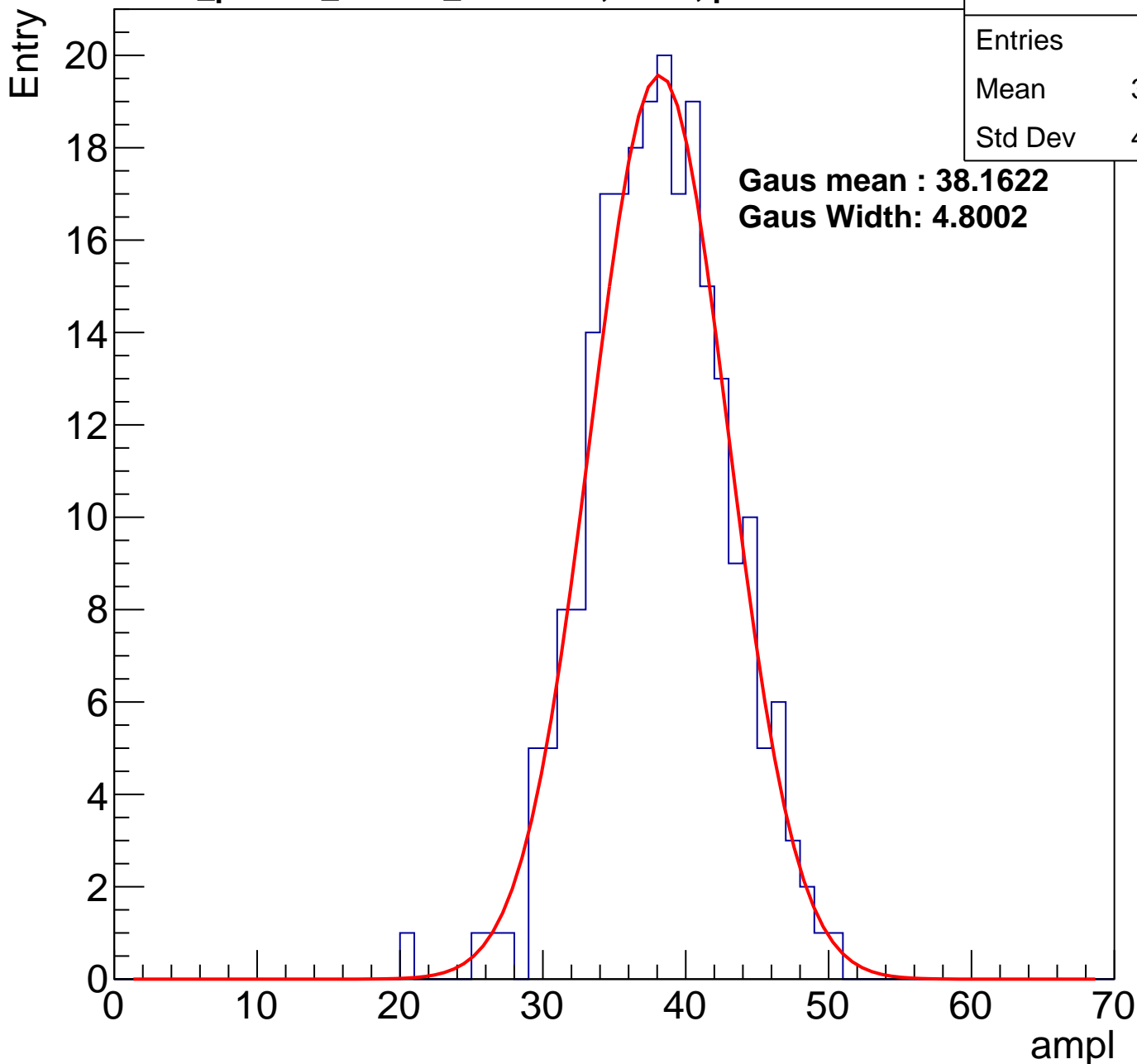
B0L101S, U14-ch120, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	236
Mean	37.67
Std Dev	4.767

Gaus mean : 38.1622

Gaus Width: 4.8002



B0L101S, U14-ch120, adc2

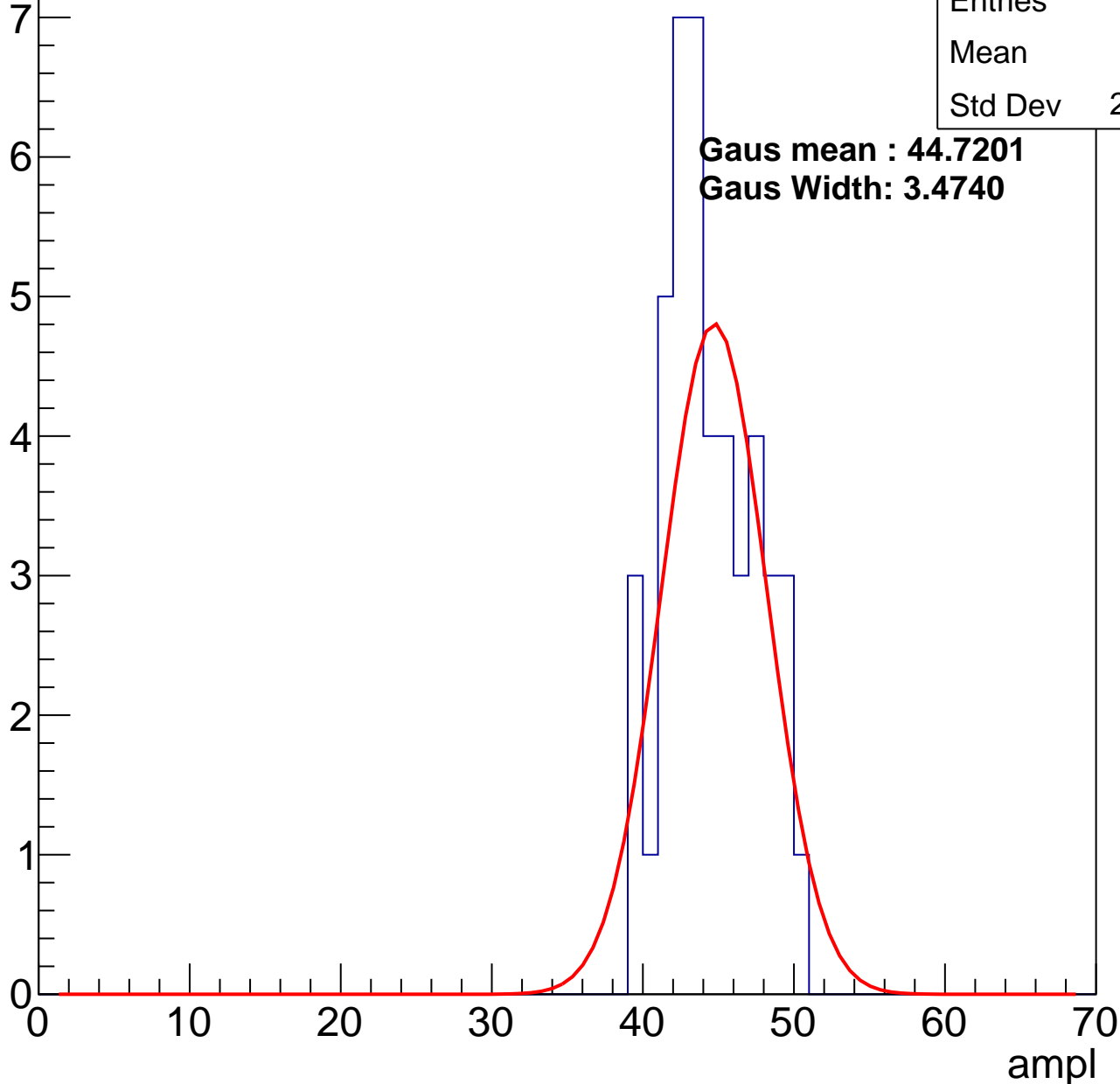
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	45
Mean	44
Std Dev	2.914

Gaus mean : 44.7201

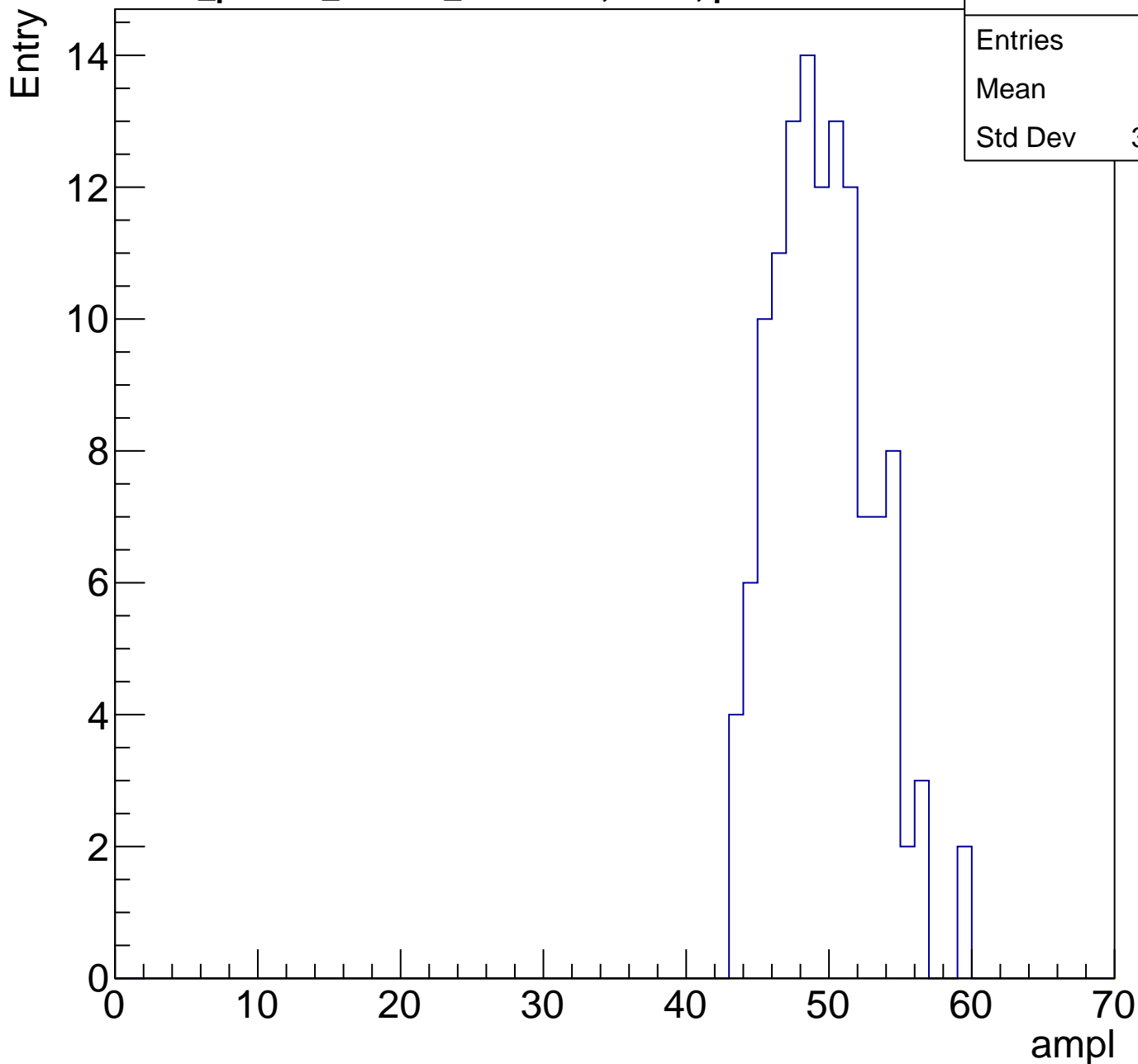
Gaus Width: 3.4740



B0L101S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

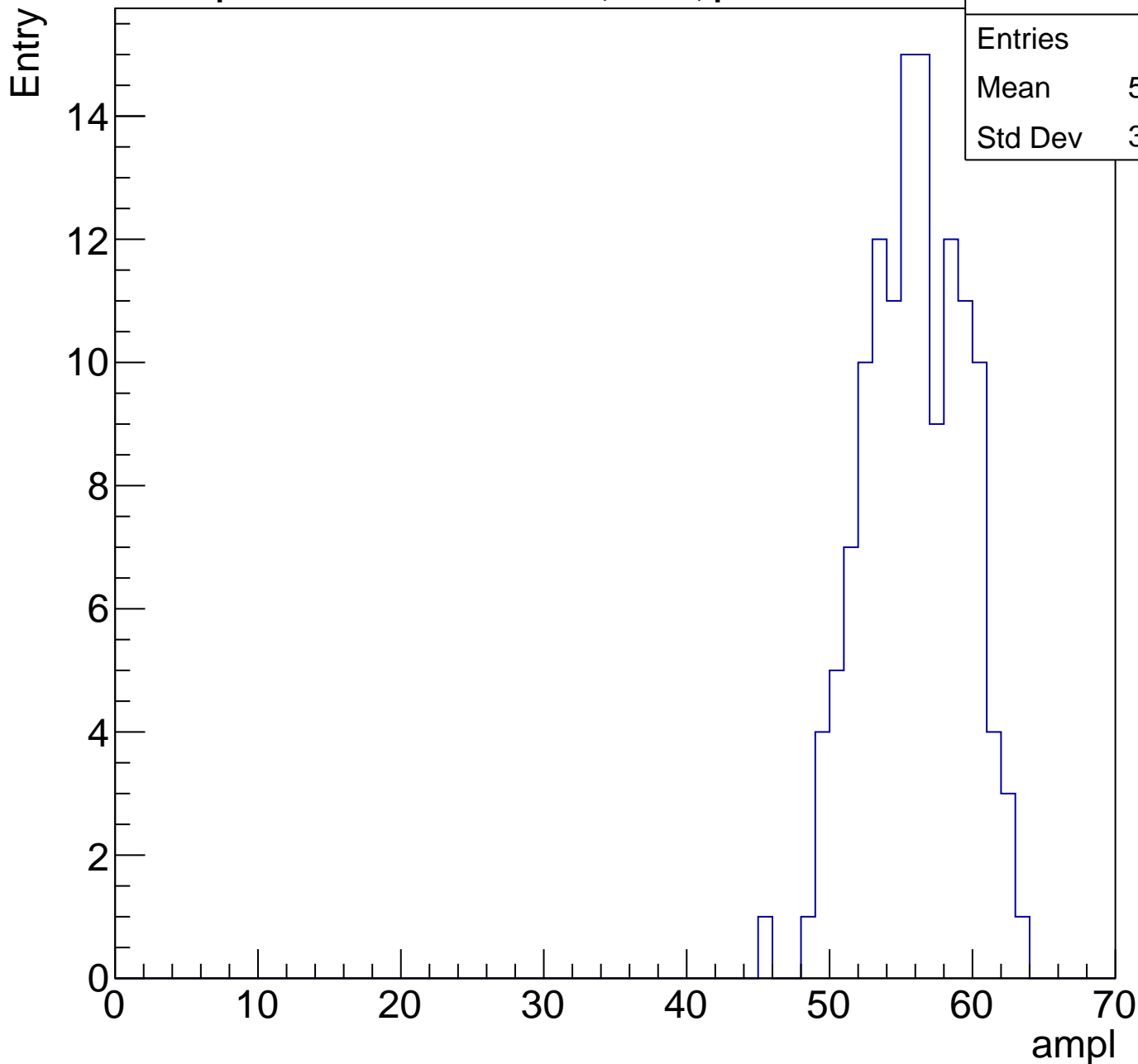
Entries	124
Mean	49.1
Std Dev	3.446



B0L101S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#1, port C1

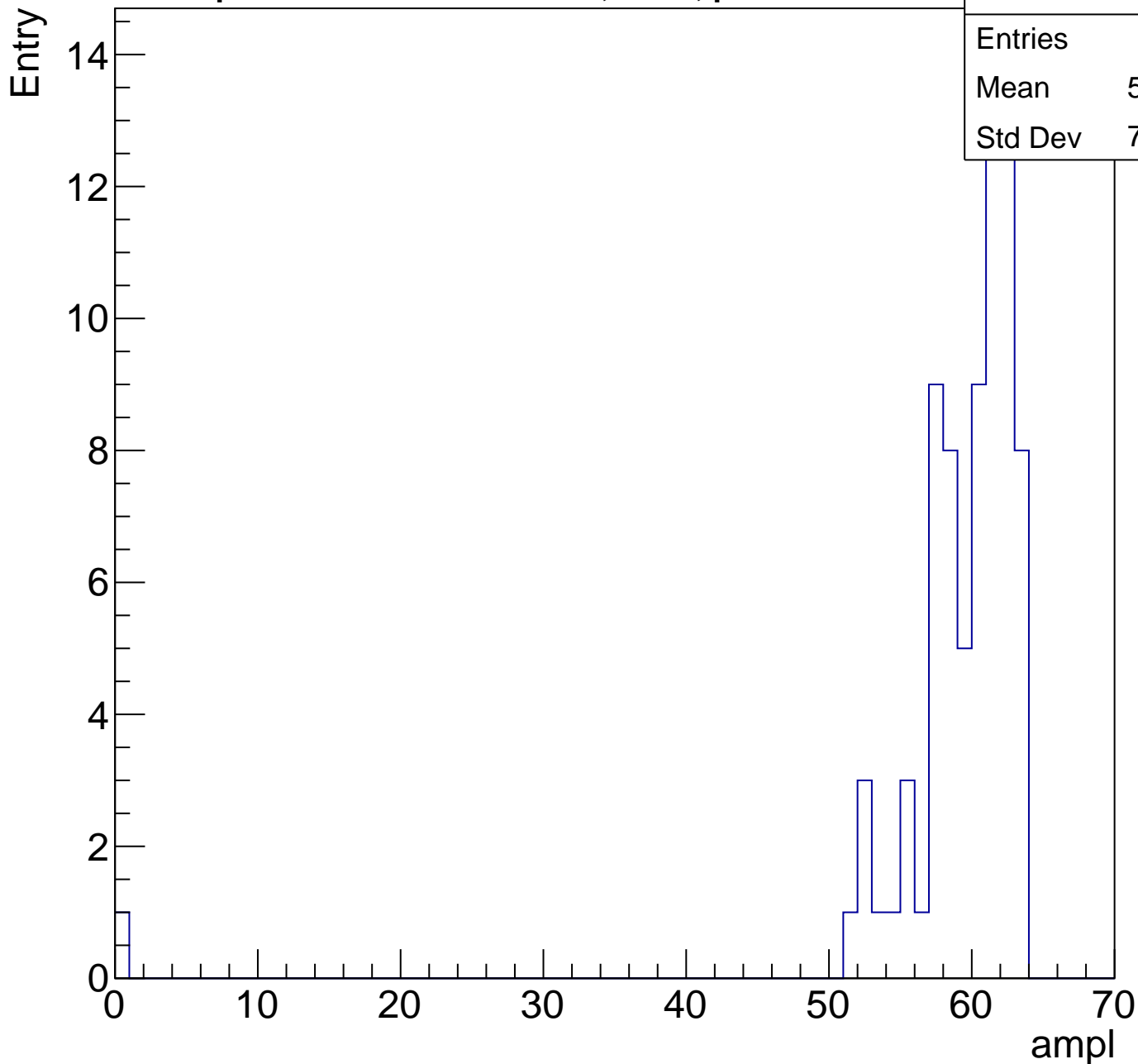
Entries	131
Mean	55.44
Std Dev	3.488



B0L101S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	78
Mean	58.63
Std Dev	7.305

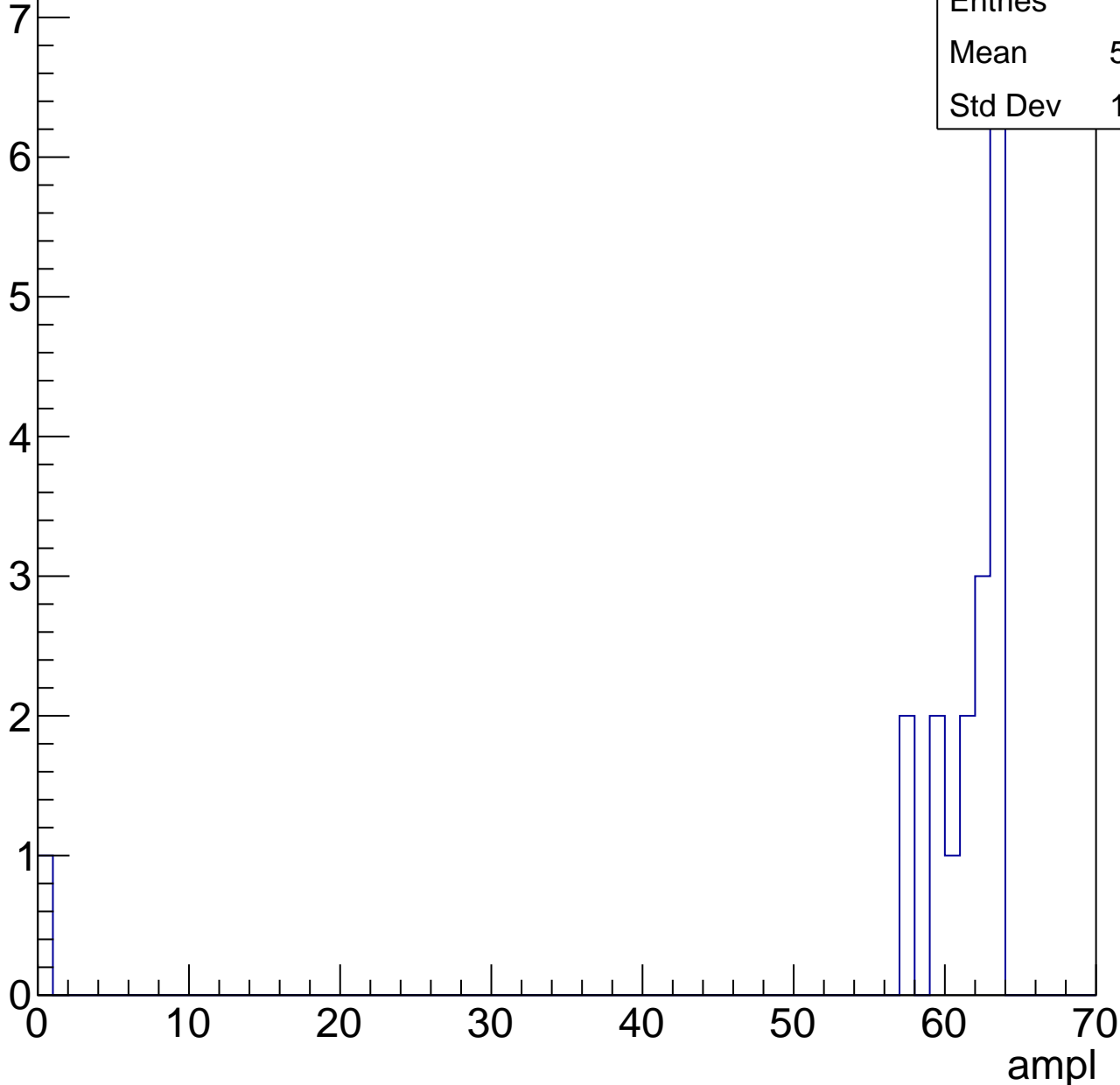


B0L101S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

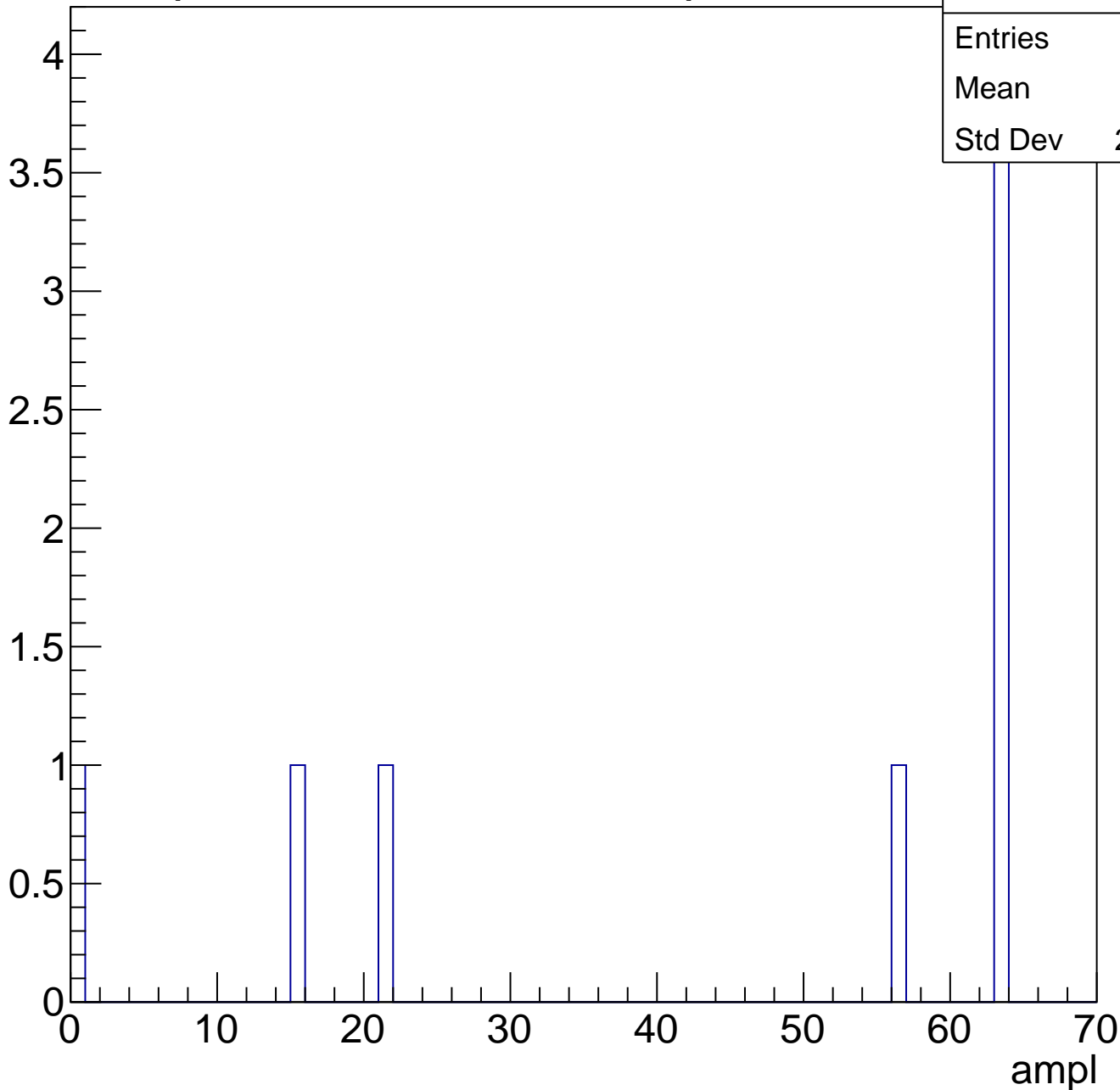
Entries	18
Mean	57.83
Std Dev	14.17



B0L101S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	8
Mean	43
Std Dev	24.71

B0L101S, U14-ch121, adc0

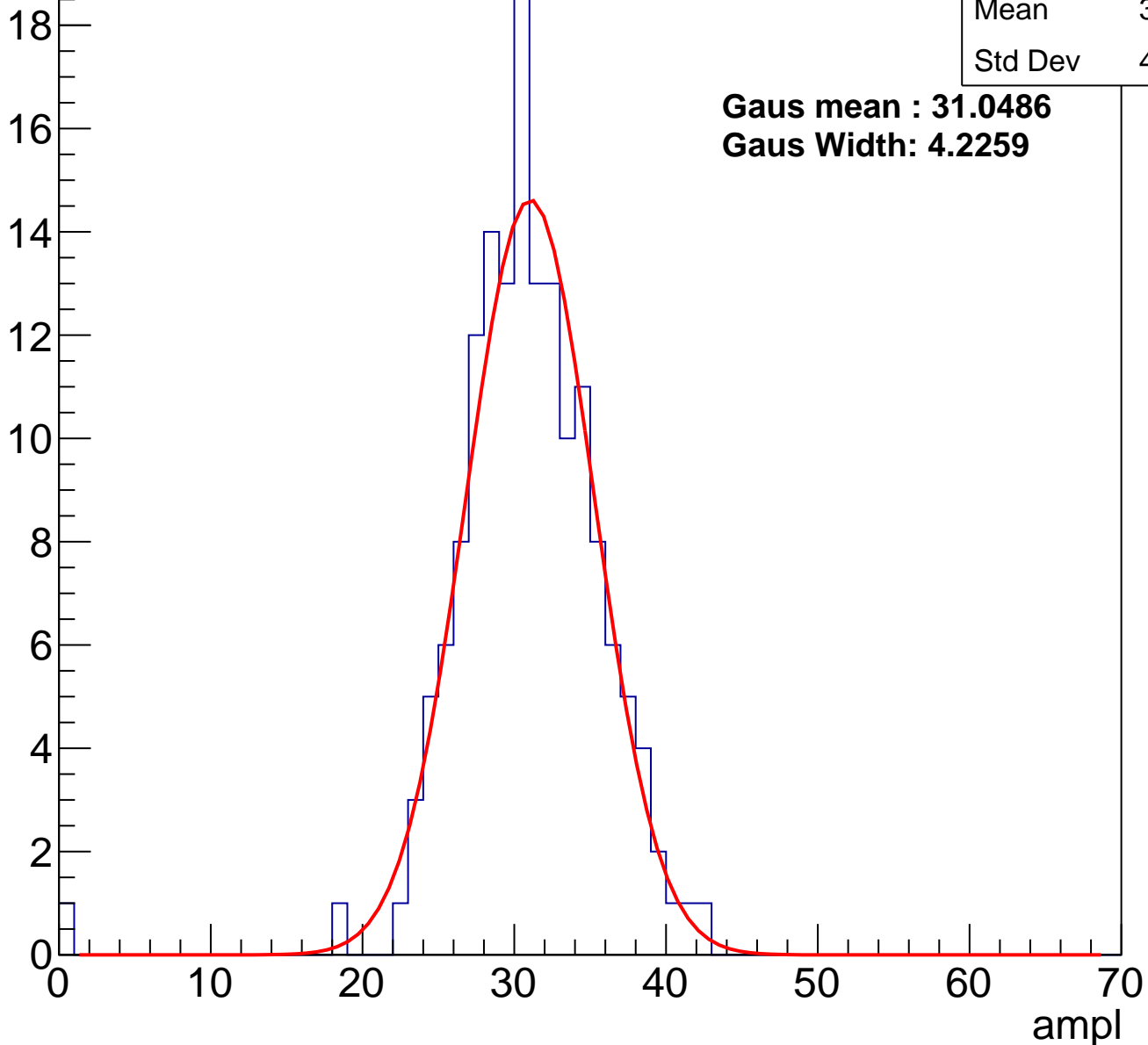
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	30.42
Std Dev	4.798

Gaus mean : 31.0486

Gaus Width: 4.2259

Entry



B0L101S, U14-ch121, adc1

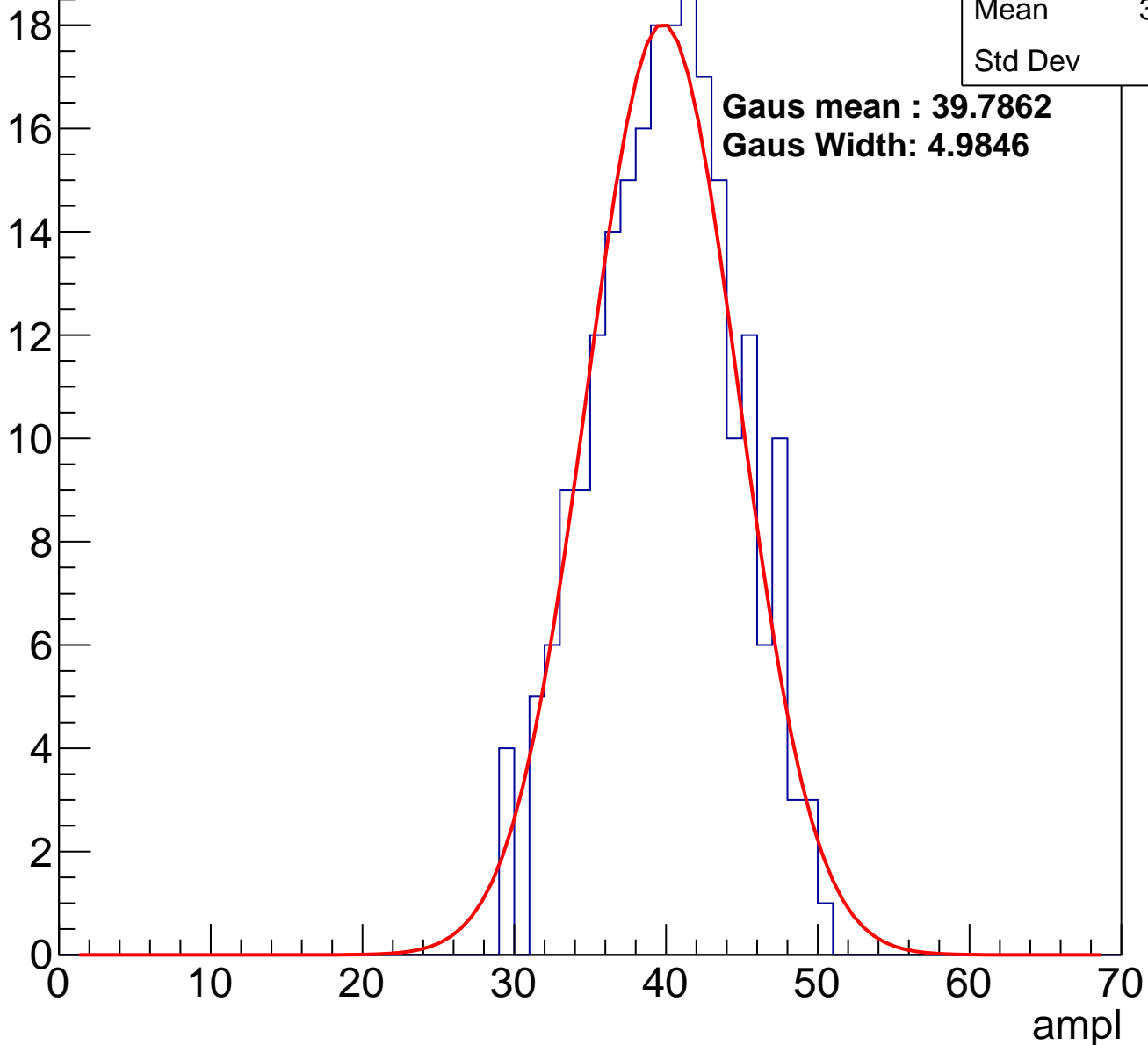
calib_packv5_042523_0143.root, FC#1, port C1

Entries	222
Mean	39.55
Std Dev	4.58

Gaus mean : 39.7862

Gaus Width: 4.9846

Entry



B0L101S, U14-ch121, adc2

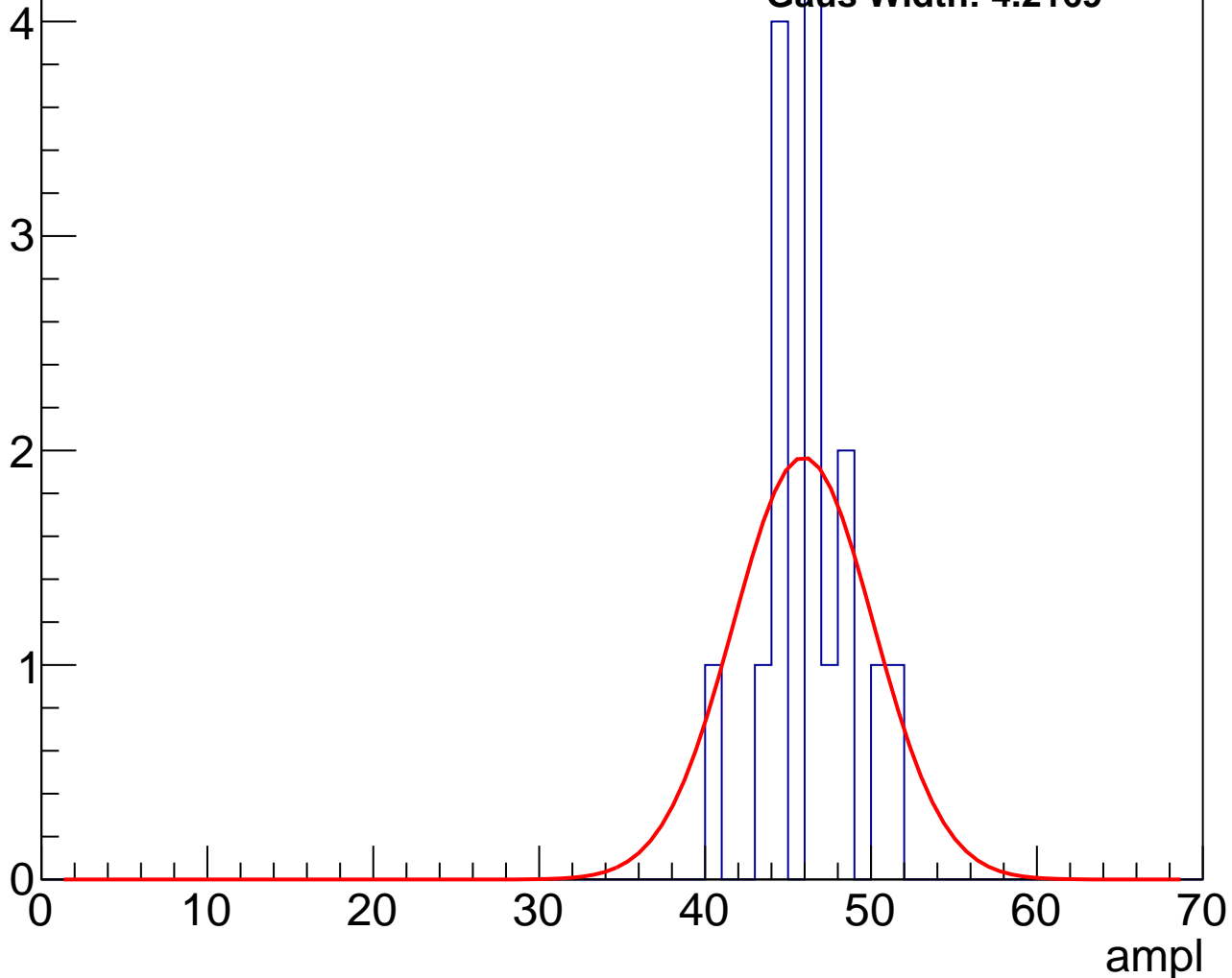
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	45.81
Std Dev	2.627

Gaus mean : 45.9298

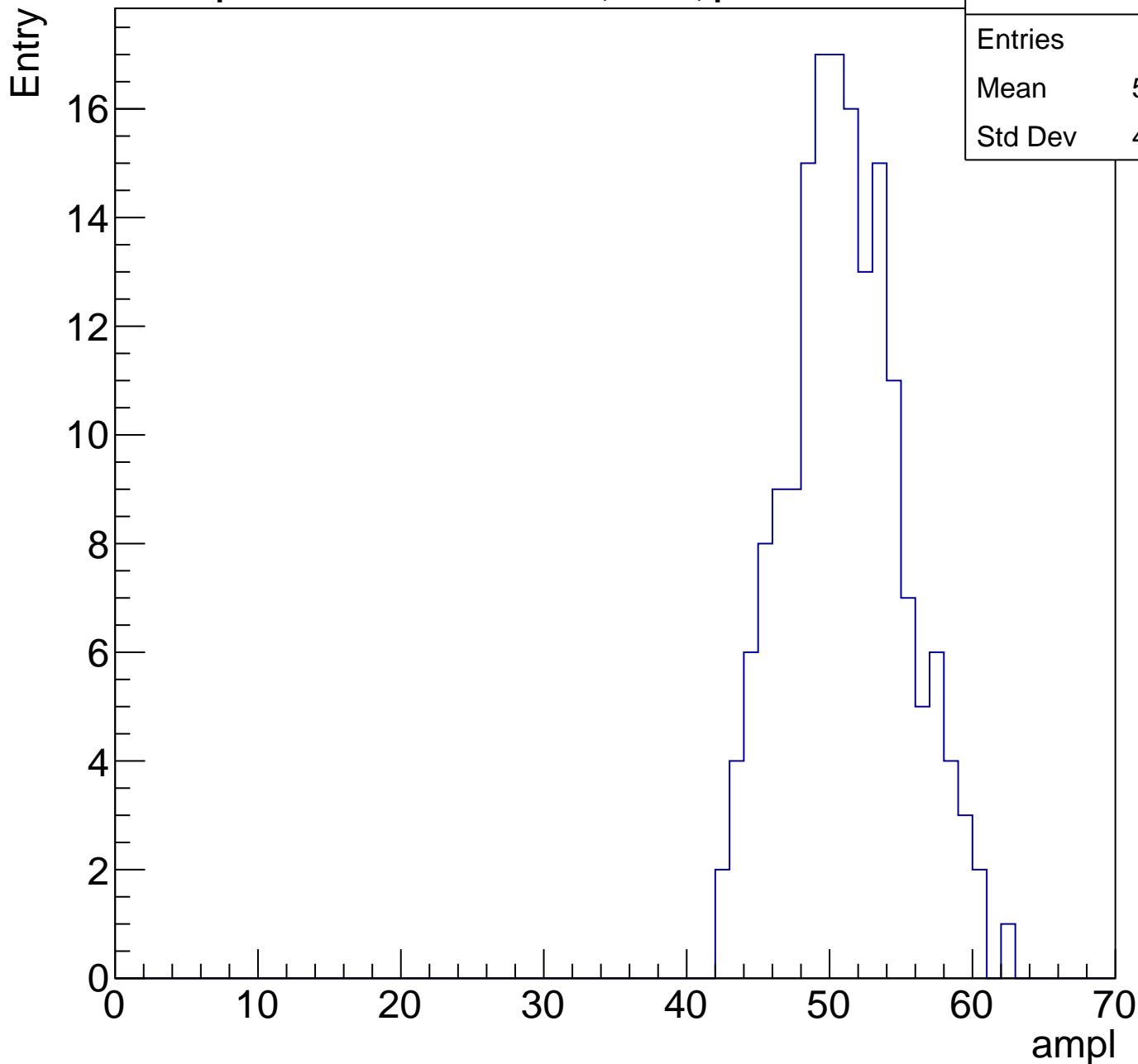
Gaus Width: 4.2169



B0L101S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	50.58
Std Dev	4.098



B0L101S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

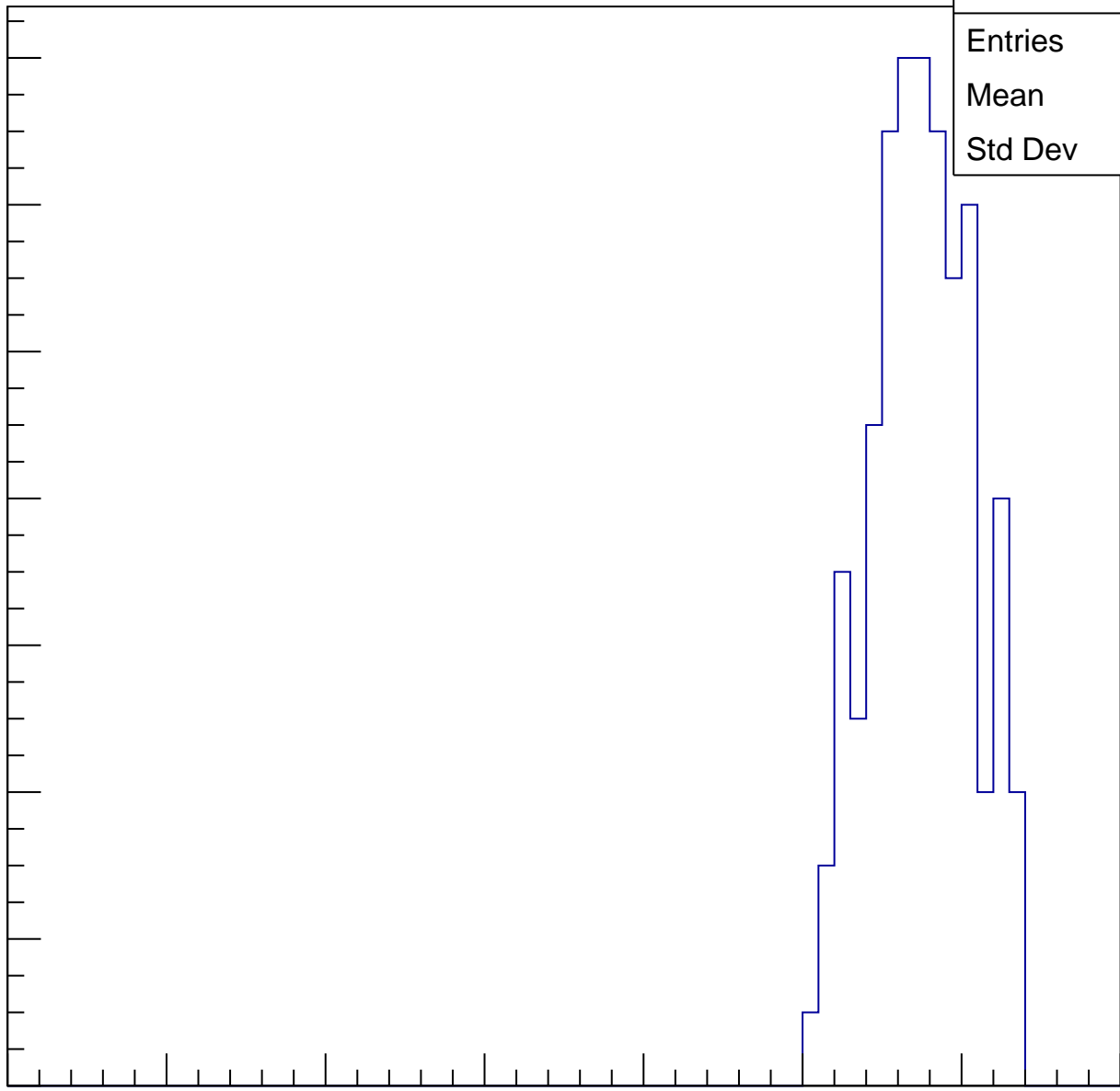
Entries	118
Mean	57.03
Std Dev	3.097

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	47
Mean	61
Std Dev	1.676

ampl

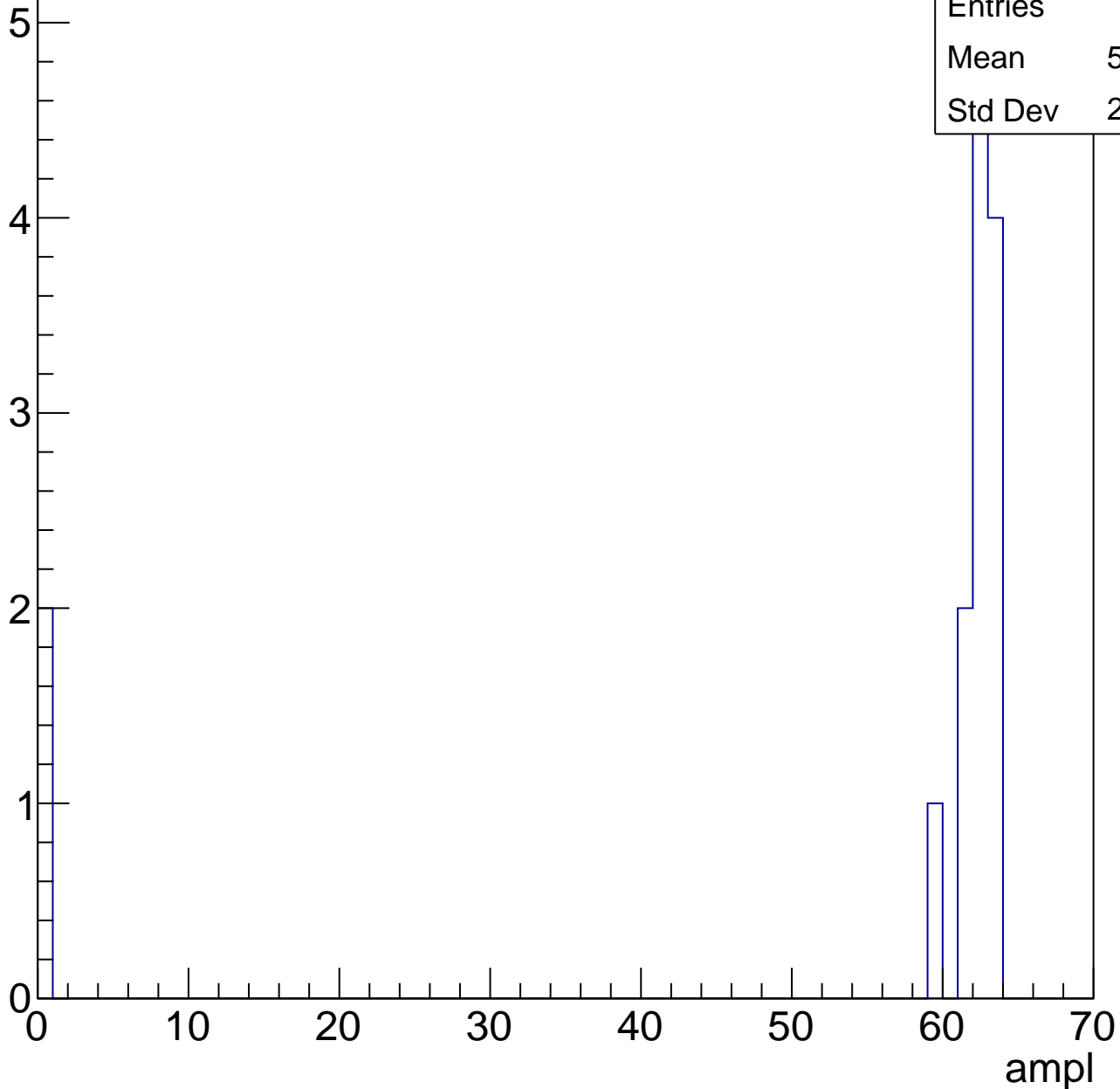
0 10 20 30 40 50 60 70

B0L101S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	53.07
Std Dev	21.69



B0L101S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

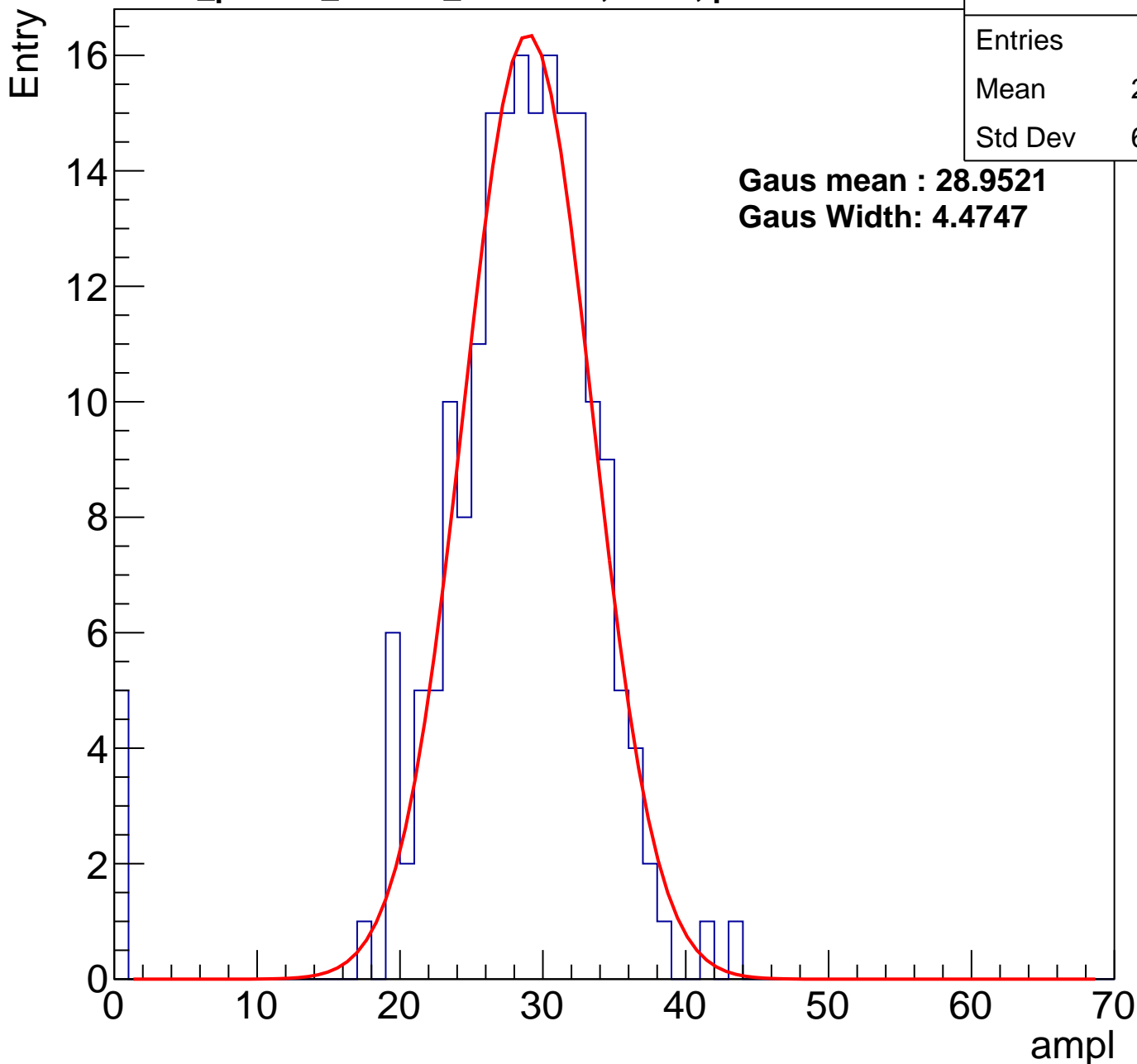
B0L101S, U14-ch122, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	193
Mean	27.65
Std Dev	6.355

Gaus mean : 28.9521

Gaus Width: 4.4747



B0L101S, U14-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

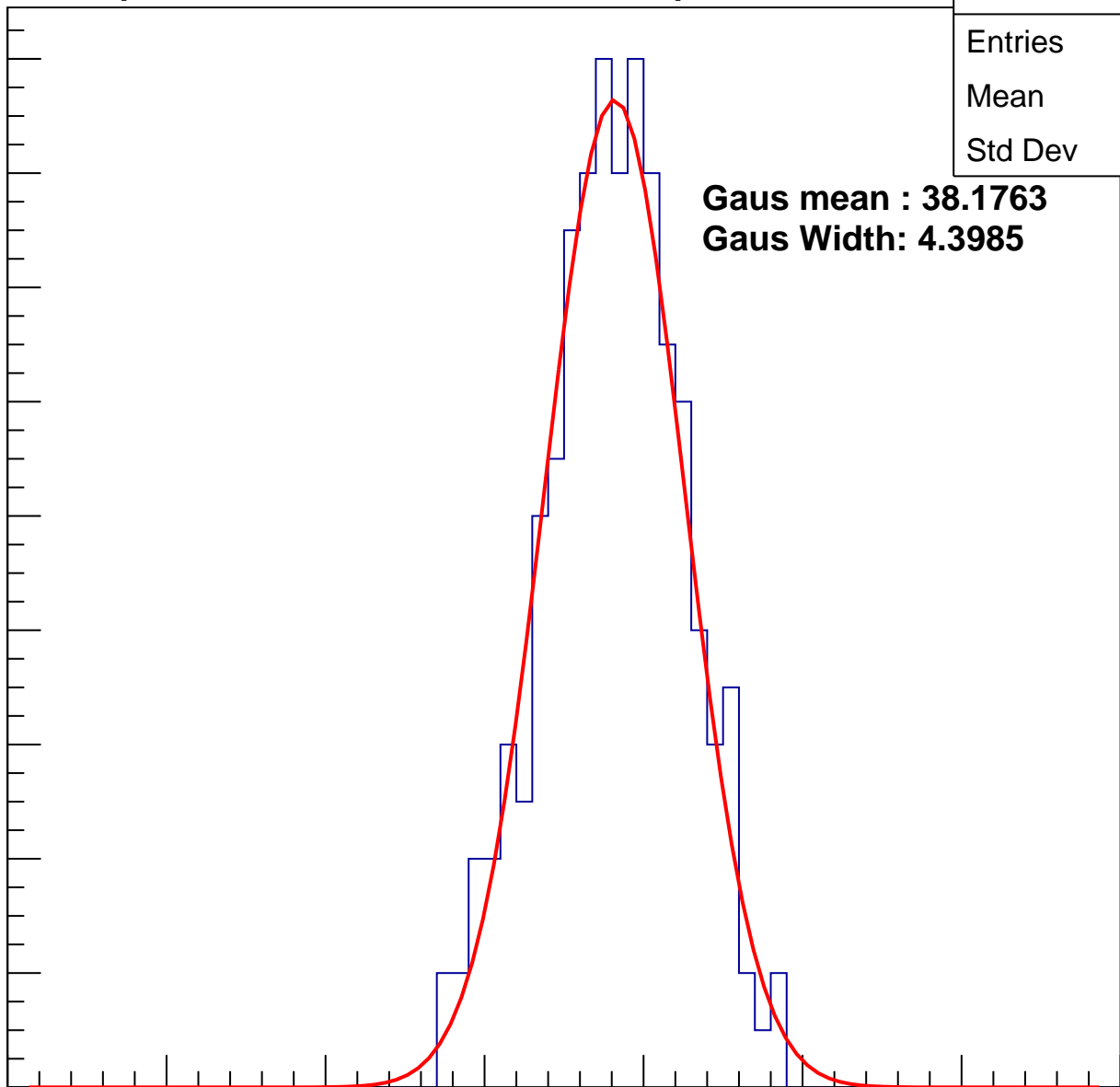
Entries	194
Mean	37.67
Std Dev	4.354

Gaus mean : 38.1763

Gaus Width: 4.3985

0 10 20 30 40 50 60 70

ampl



B0L101S, U14-ch122, adc2

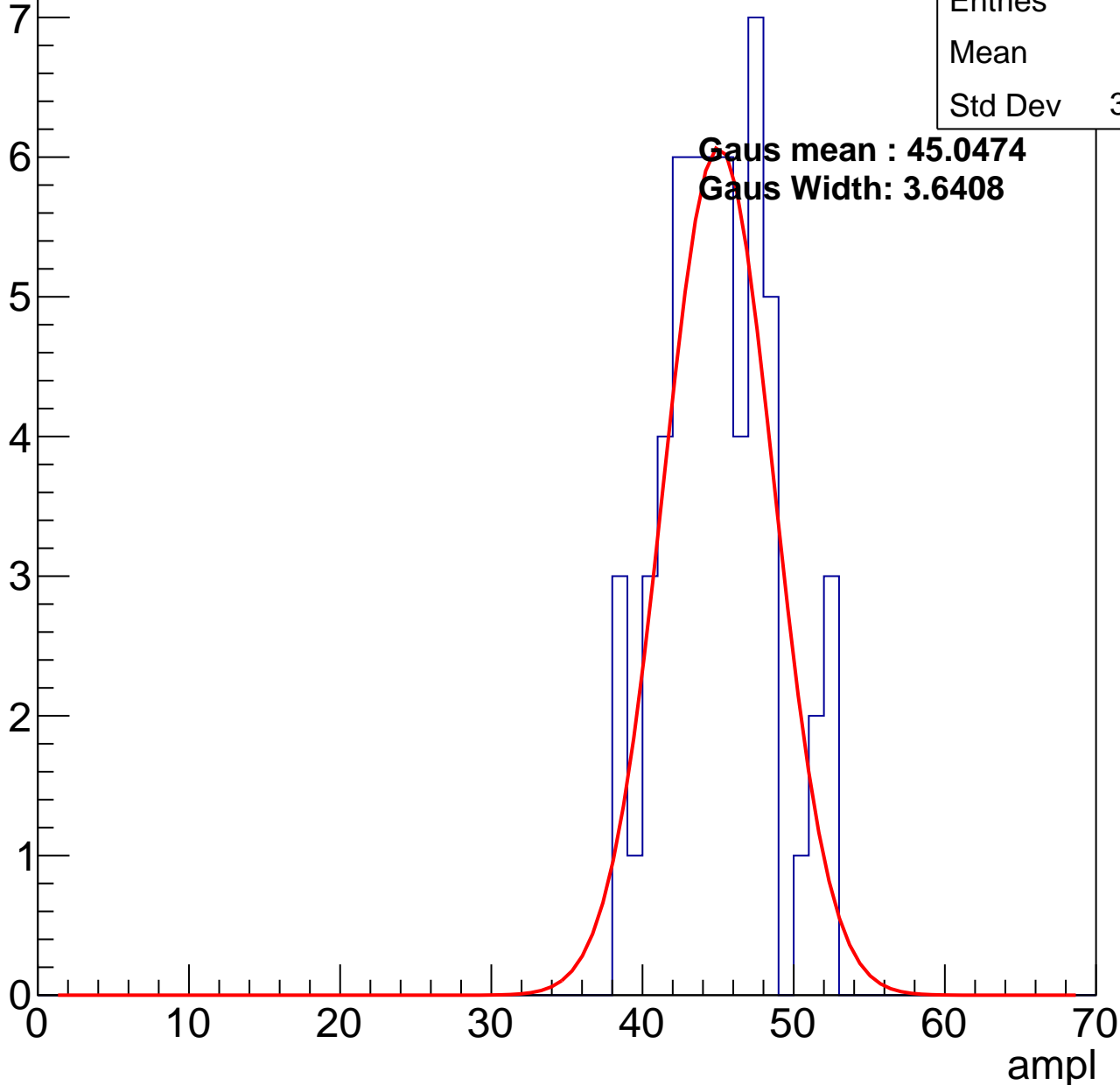
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	57
Mean	44.6
Std Dev	3.548

Gaus mean : 45.0474

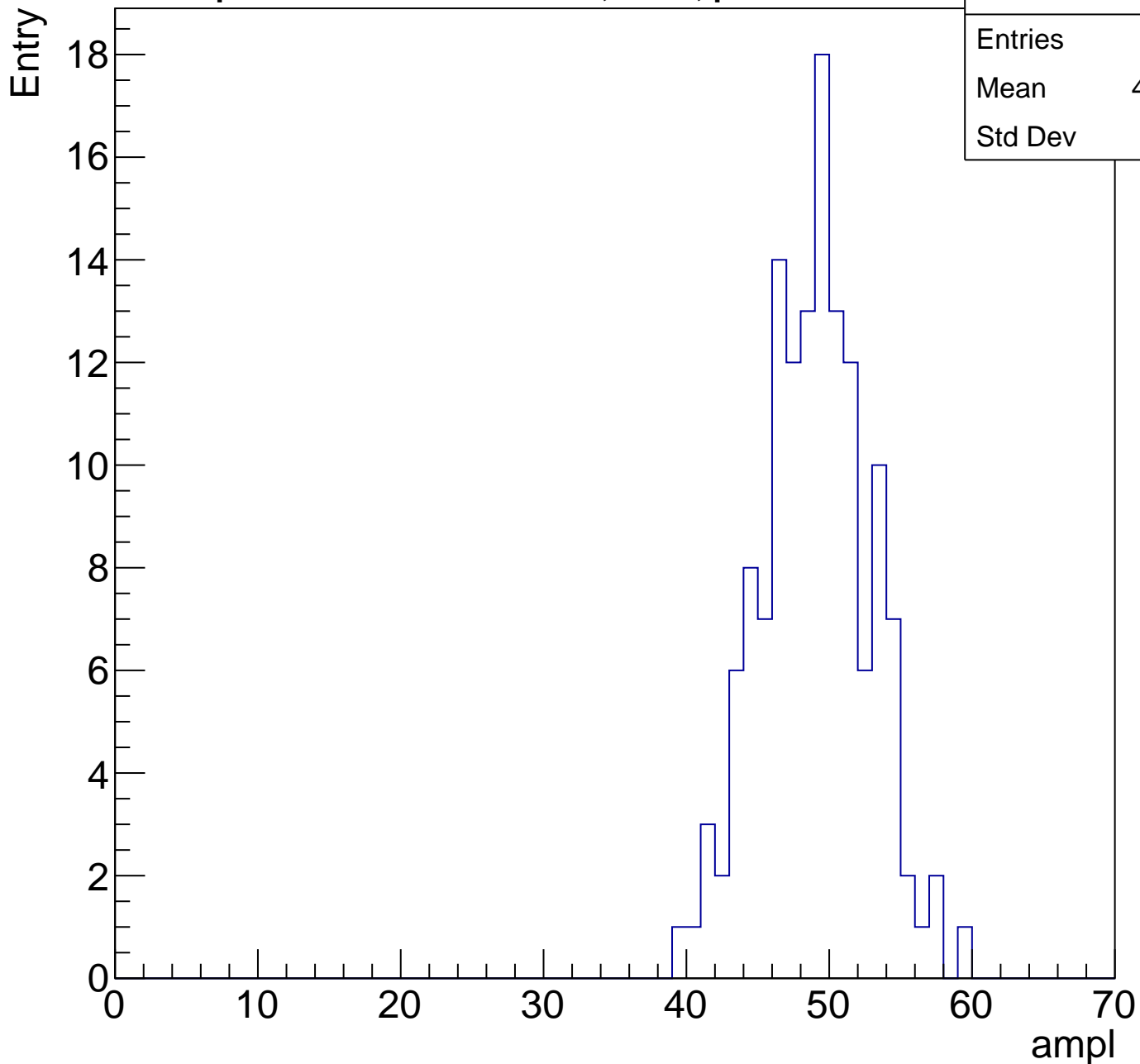
Gaus Width: 3.6408



B0L101S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

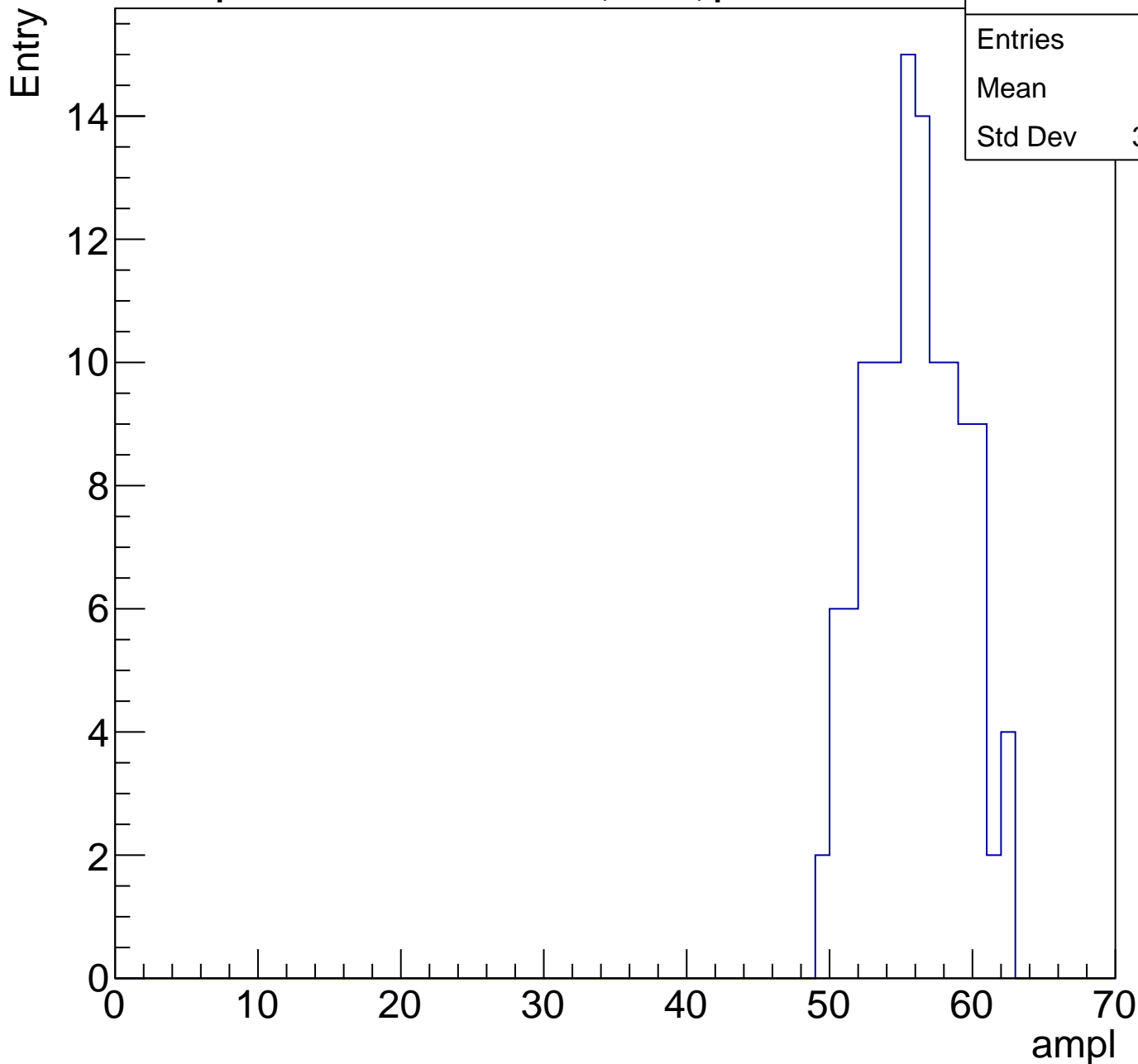
Entries	139
Mean	48.53
Std Dev	3.75



B0L101S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	55.5
Std Dev	3.215



B0L101S, U14-ch122, adc5

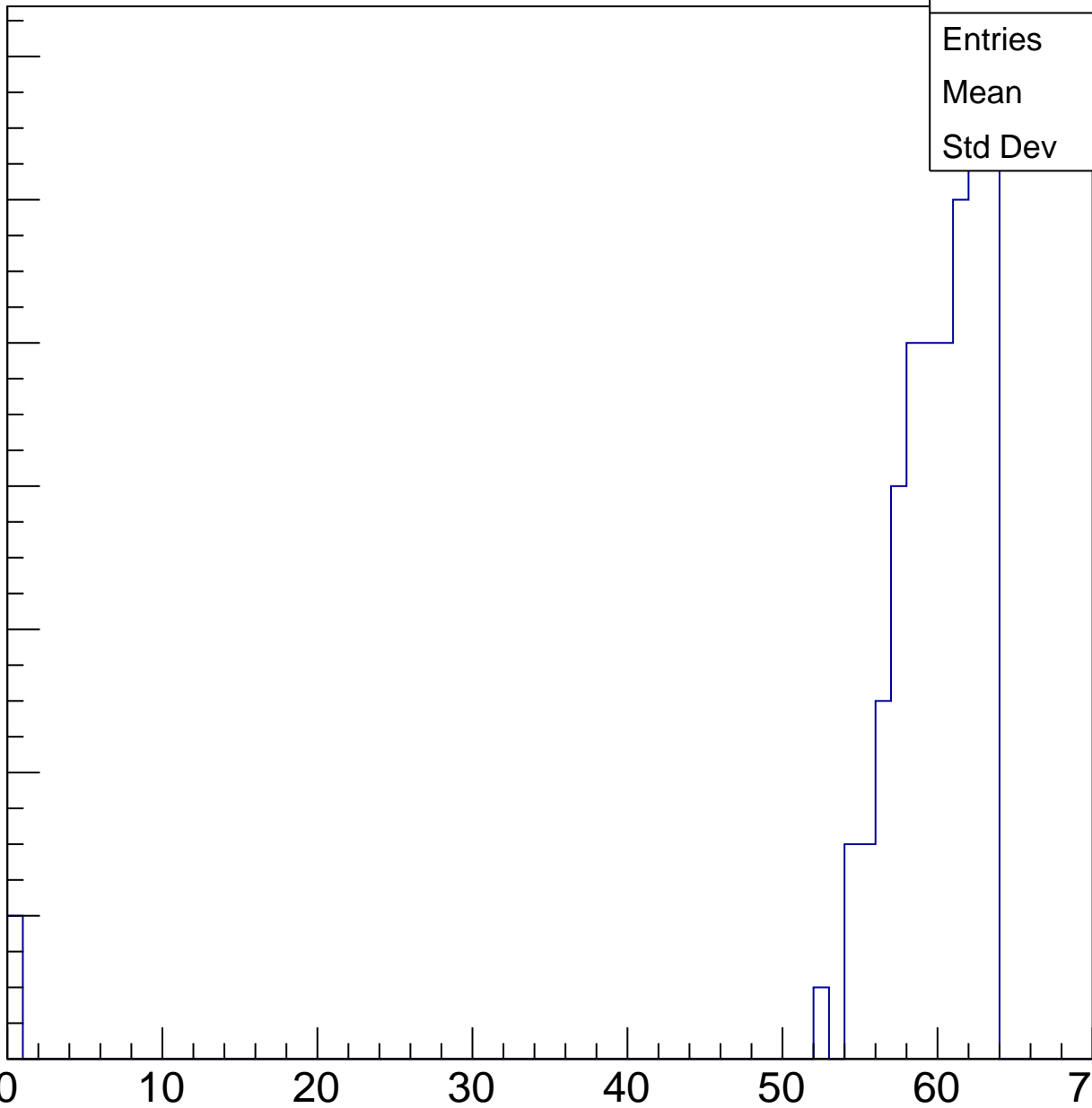
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.29
Std Dev	9.119

ampl

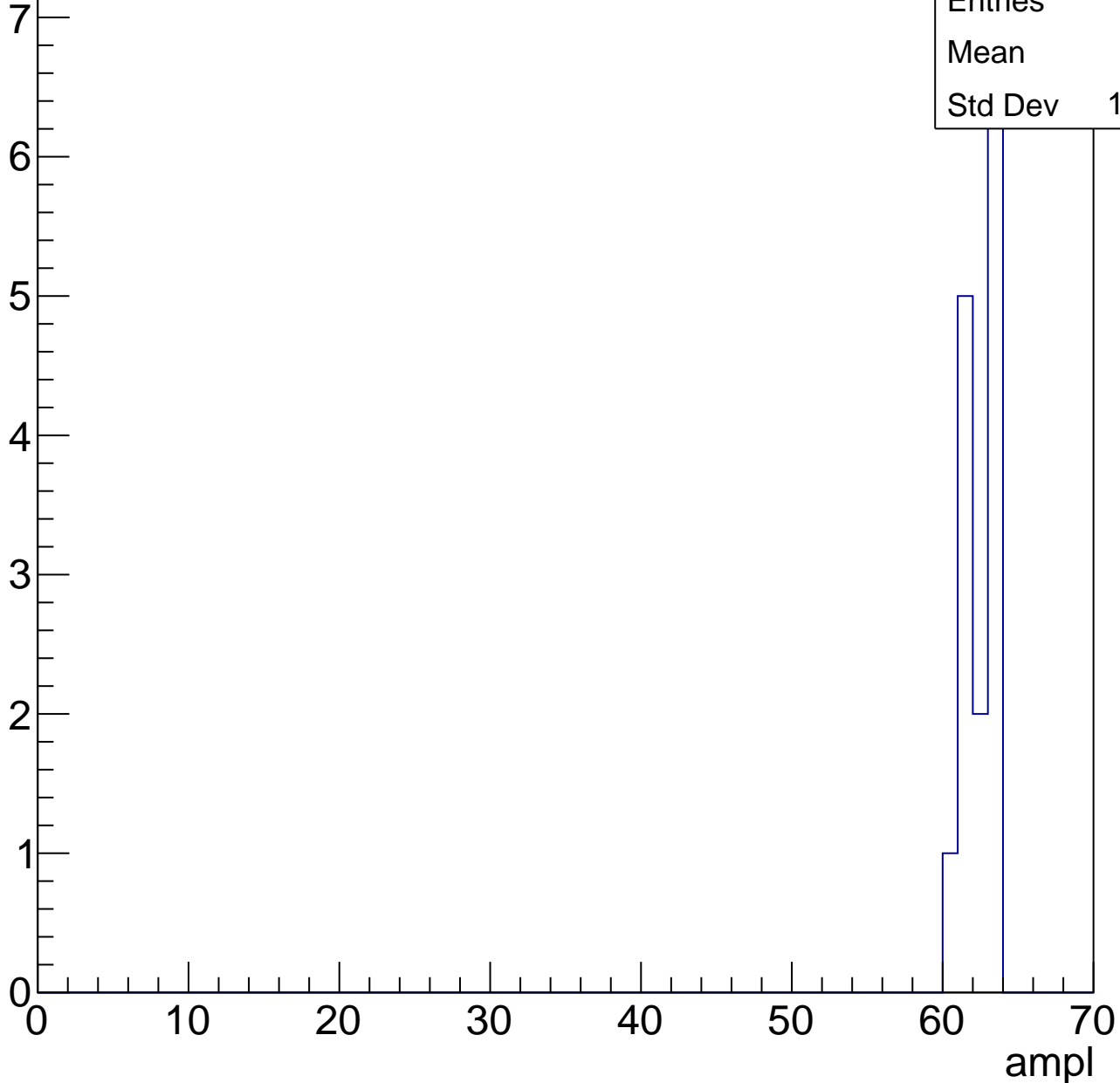


B0L101S, U14-ch122, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	62
Std Dev	1.033



B0L101S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



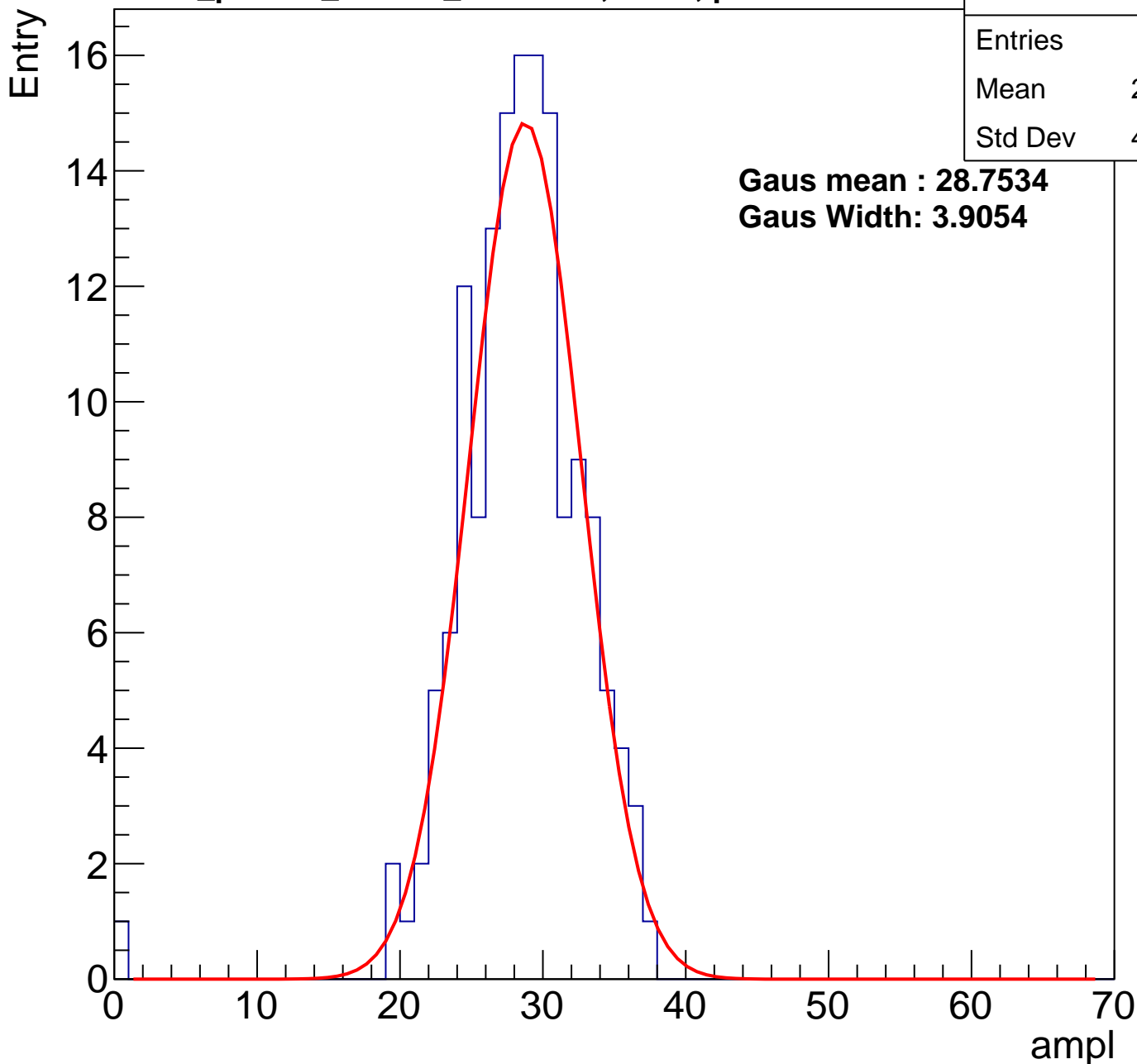
B0L101S, U14-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	27.97
Std Dev	4.404

Gaus mean : 28.7534

Gaus Width: 3.9054



B0L101S, U14-ch123, adc1

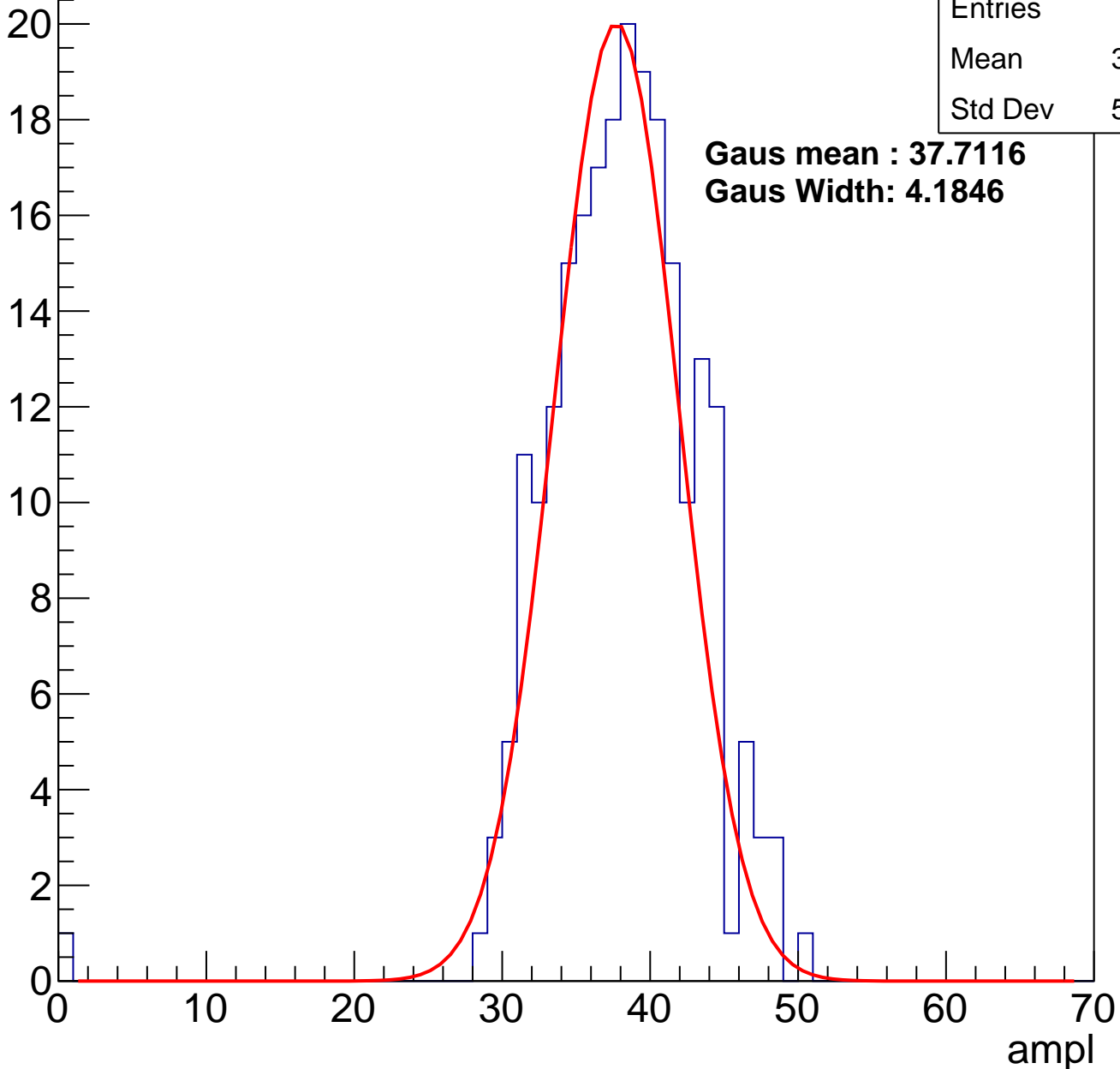
calib_packv5_042523_0143.root, FC#1, port C1

Entries	229
Mean	37.66
Std Dev	5.096

Gaus mean : 37.7116

Gaus Width: 4.1846

Entry



B0L101S, U14-ch123, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	52
Mean	44.92
Std Dev	2.901

Gaus mean : 45.3381

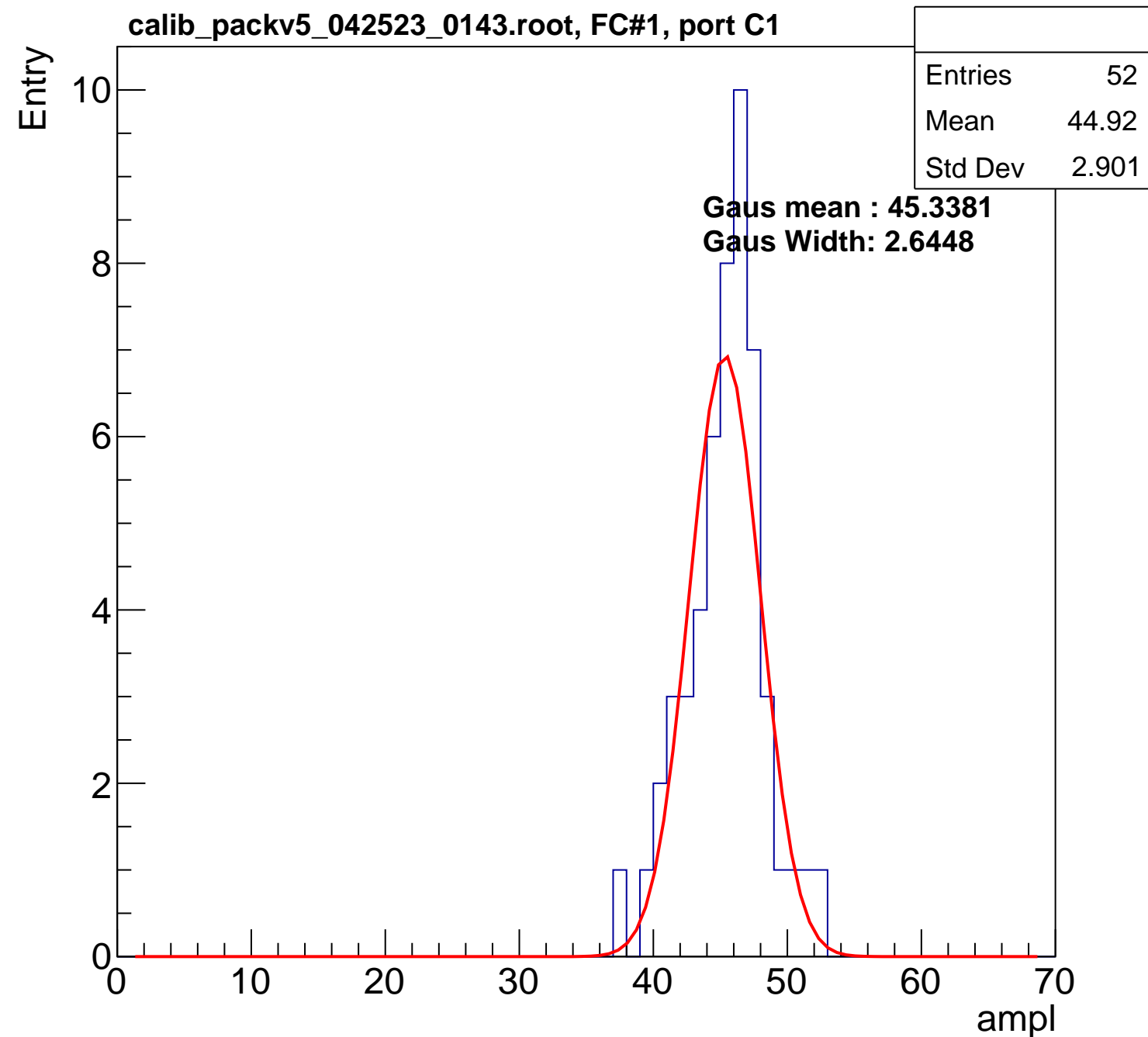
Gaus Width: 2.6448

Entry

10
8
6
4
2
0

ampl

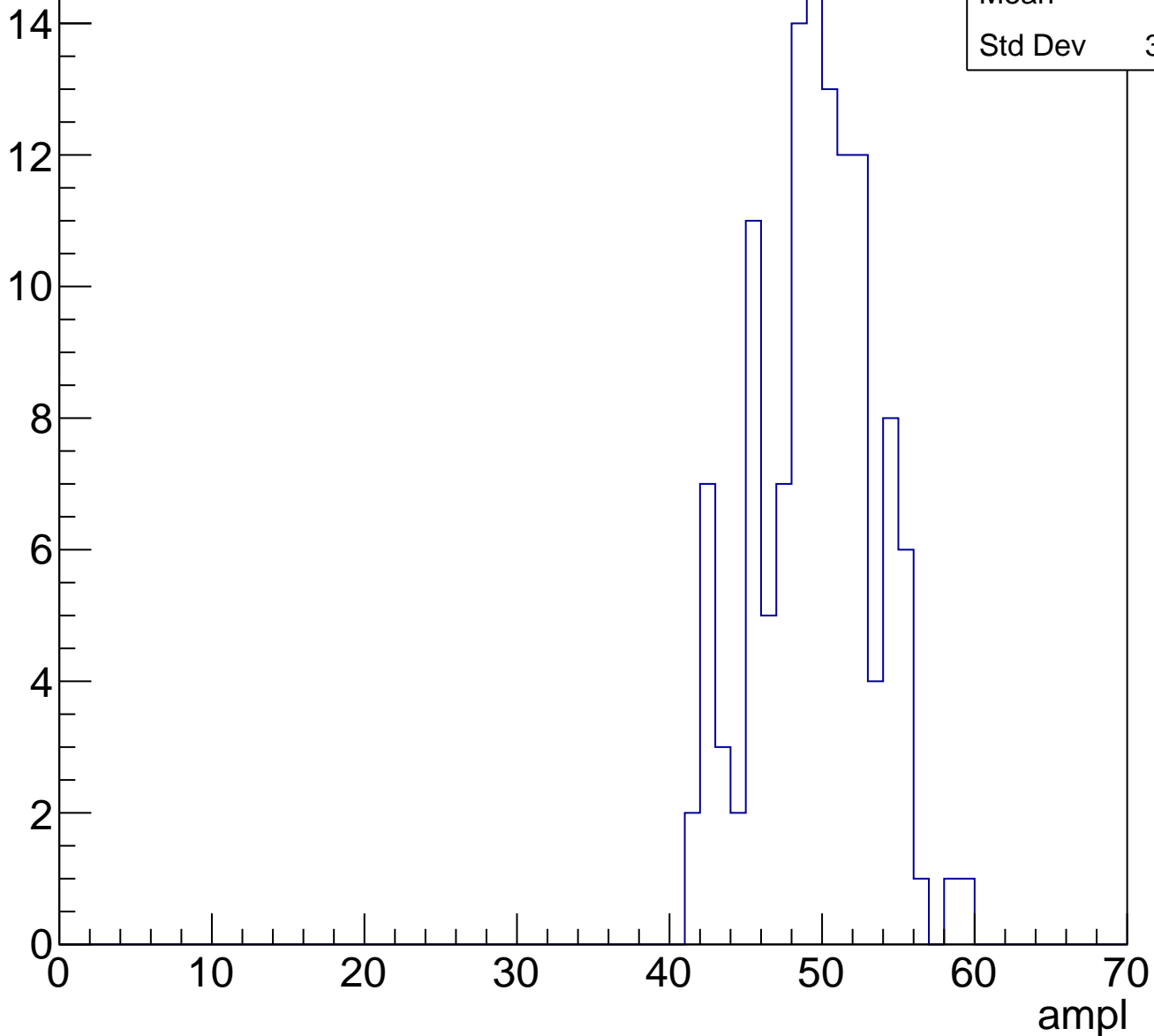
0 10 20 30 40 50 60 70



B0L101S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

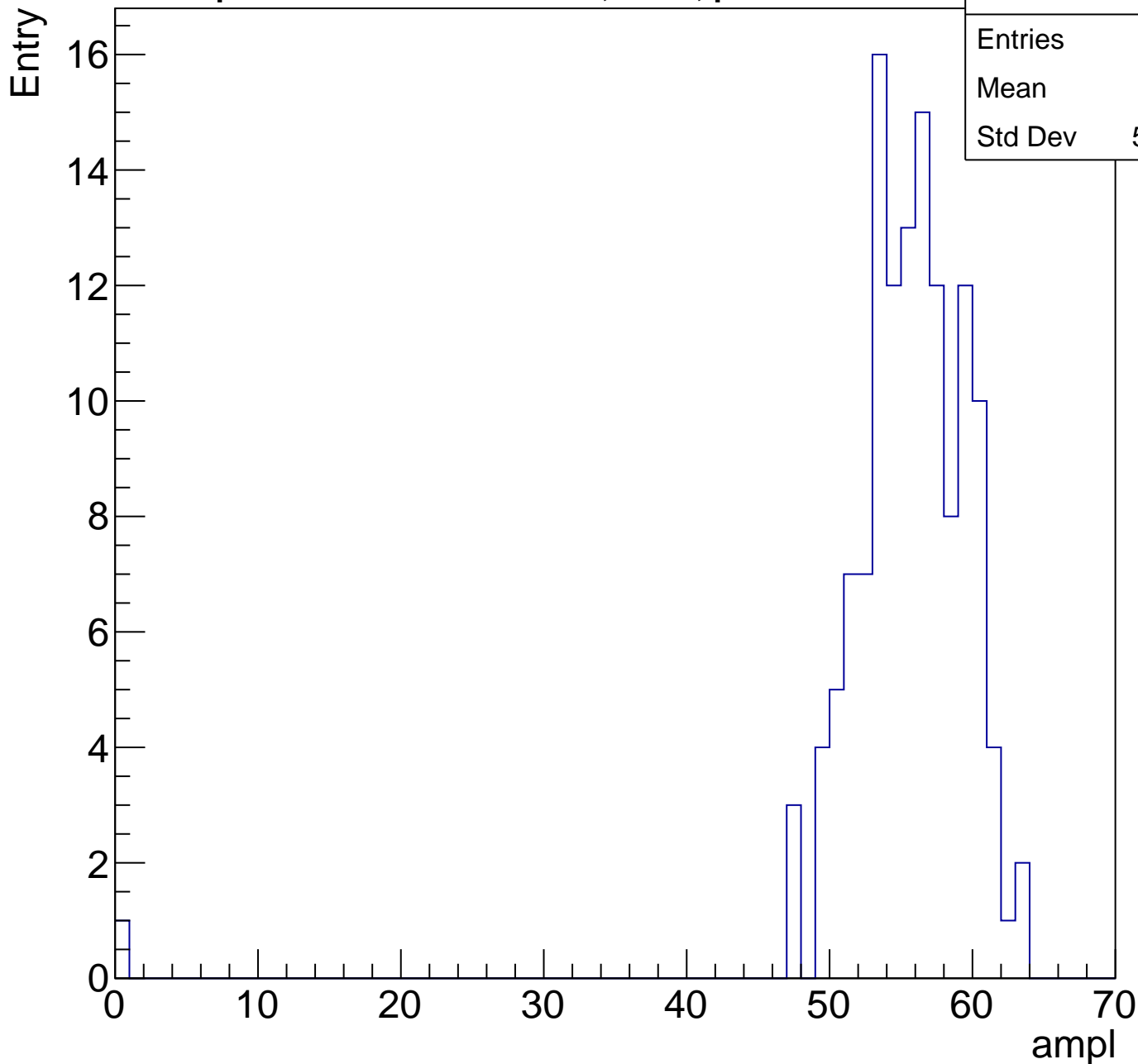
Entry



B0L101S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	54.9
Std Dev	5.925



B0L101S, U14-ch123, adc5

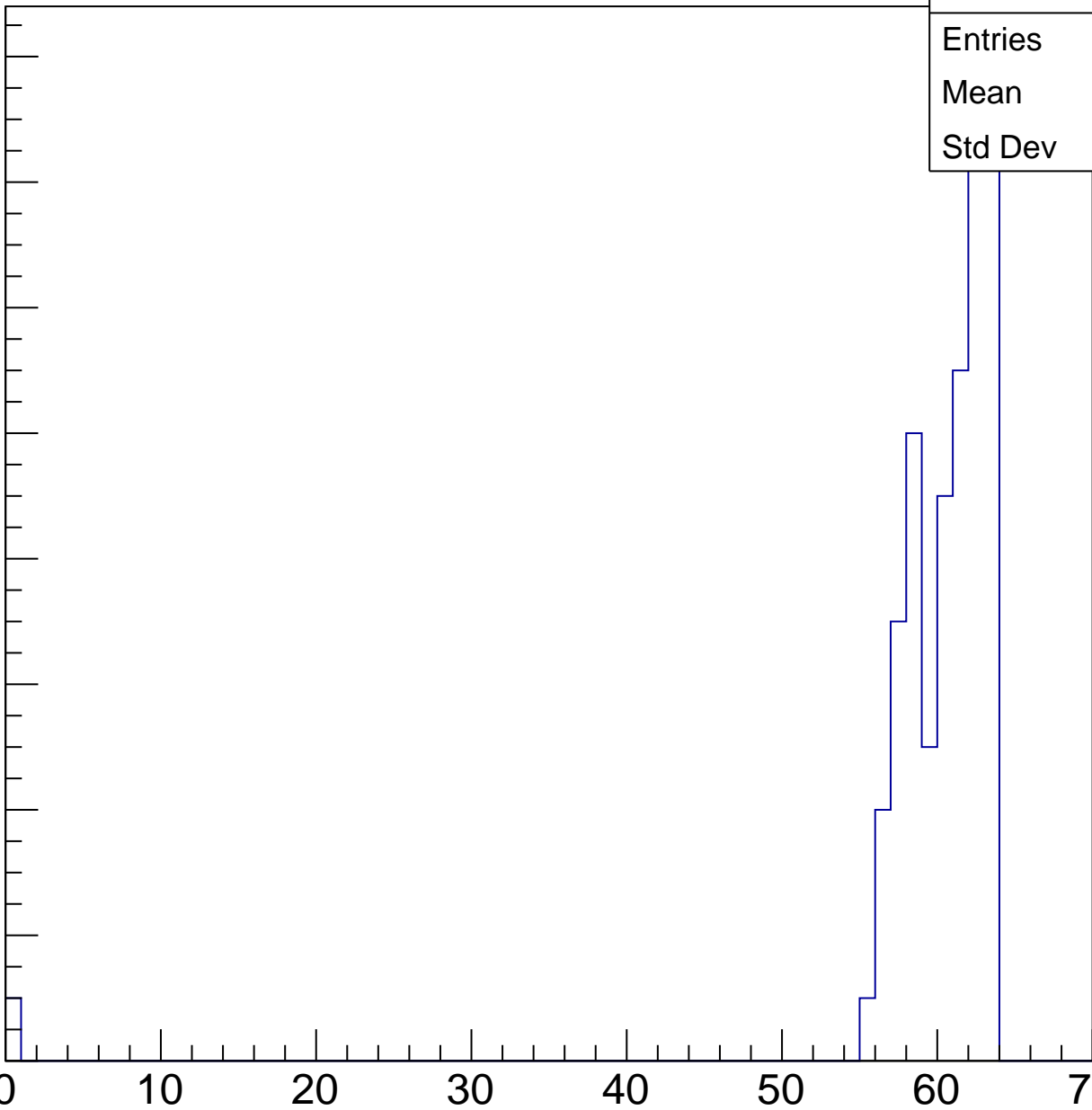
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.55
Std Dev	7.071

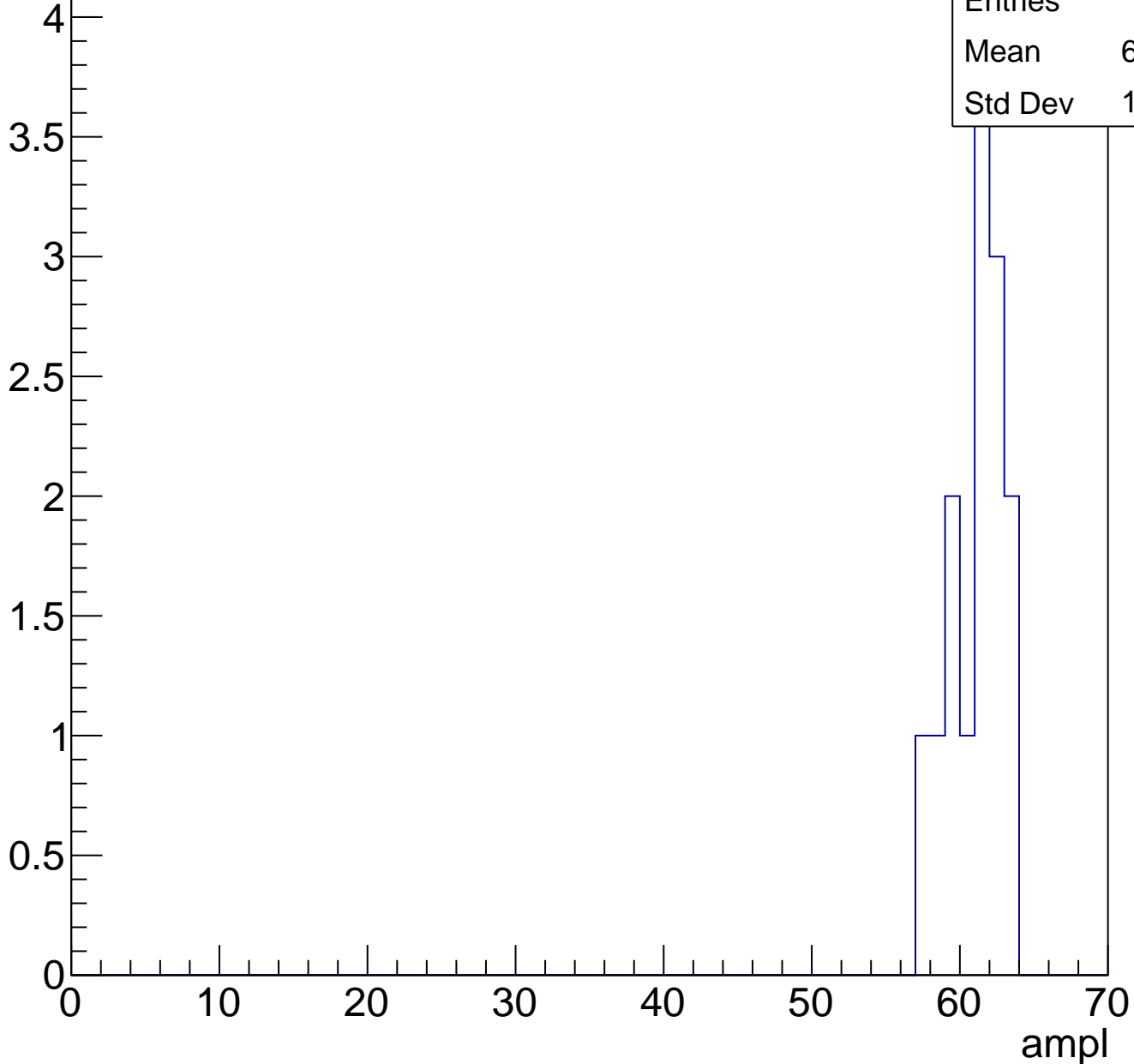
ampl



B0L101S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

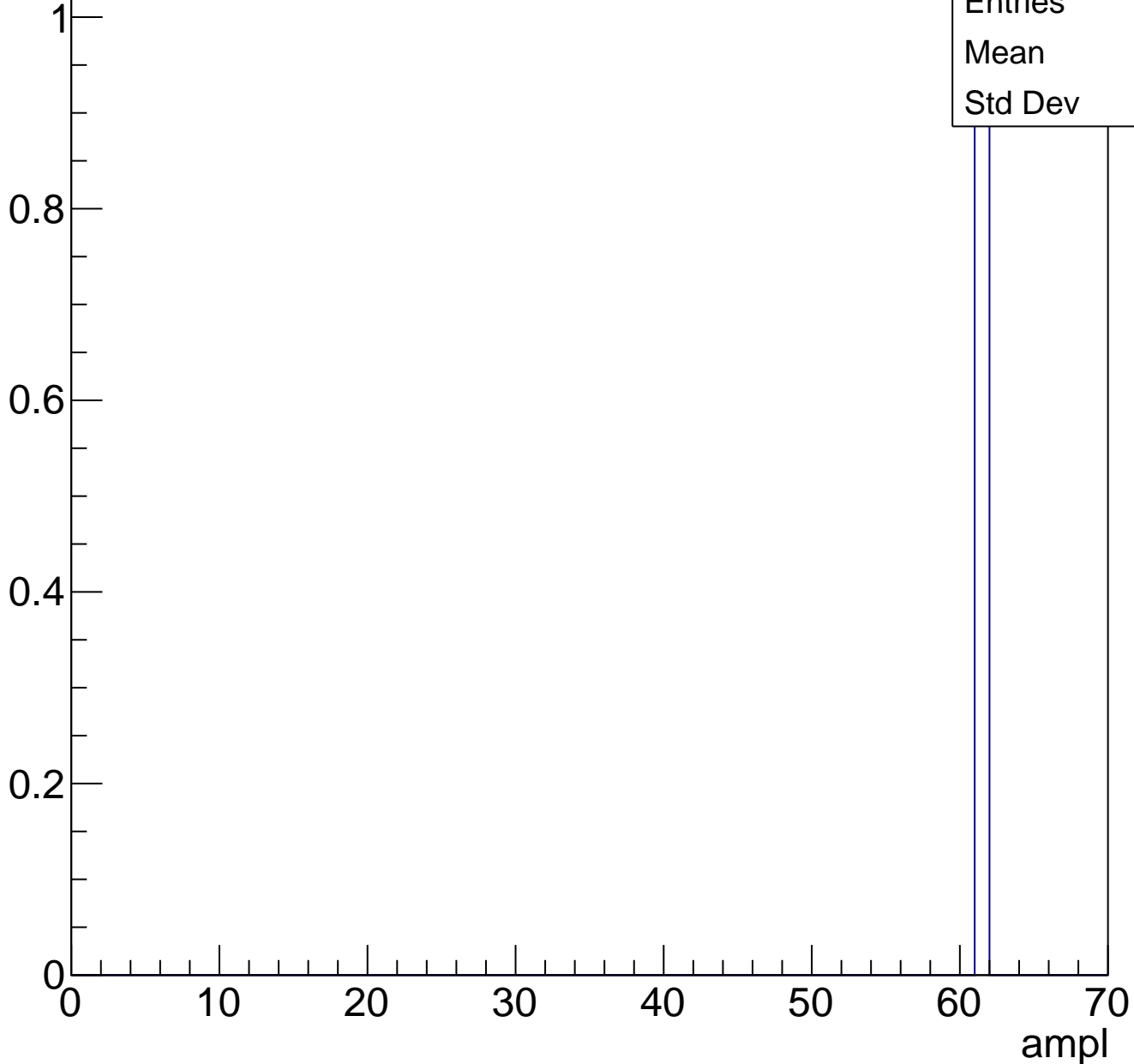
Entry



B0L101S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch124, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	26.5
Std Dev	7.308

Gaus mean : 27.7503

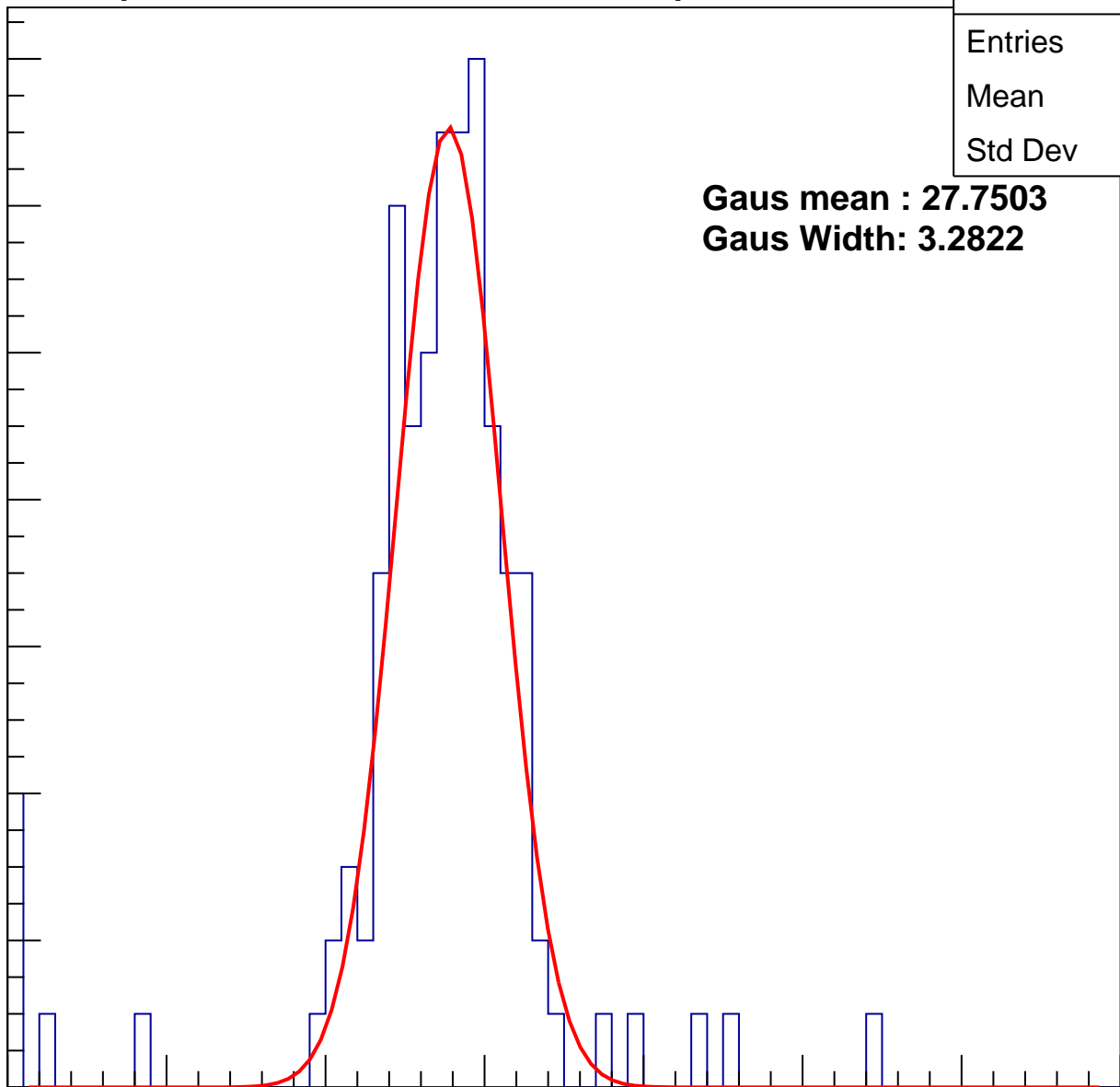
Gaus Width: 3.2822

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



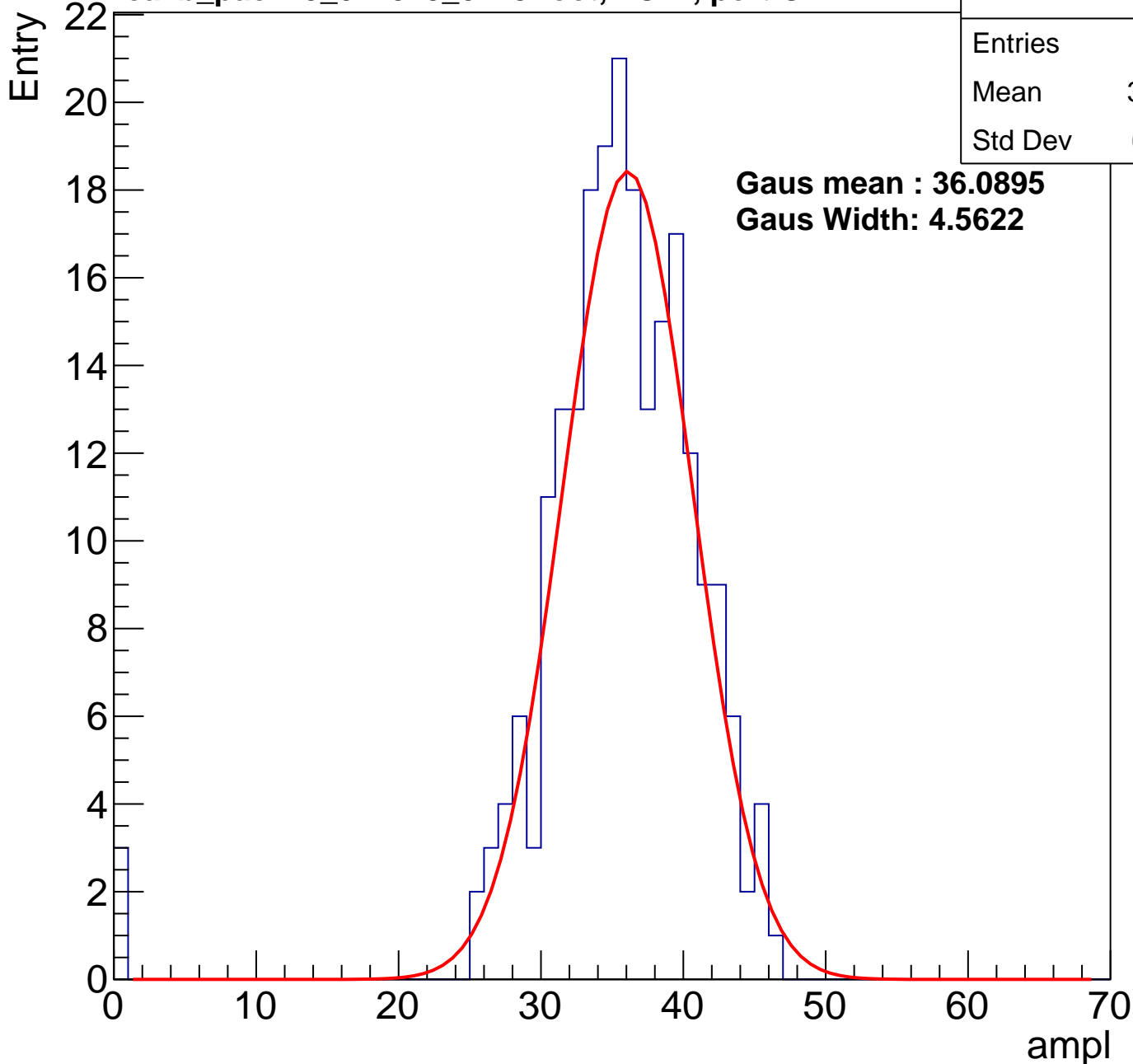
B0L101S, U14-ch124, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	222
Mean	35.03
Std Dev	6.031

Gaus mean : 36.0895

Gaus Width: 4.5622



B0L101S, U14-ch124, adc2

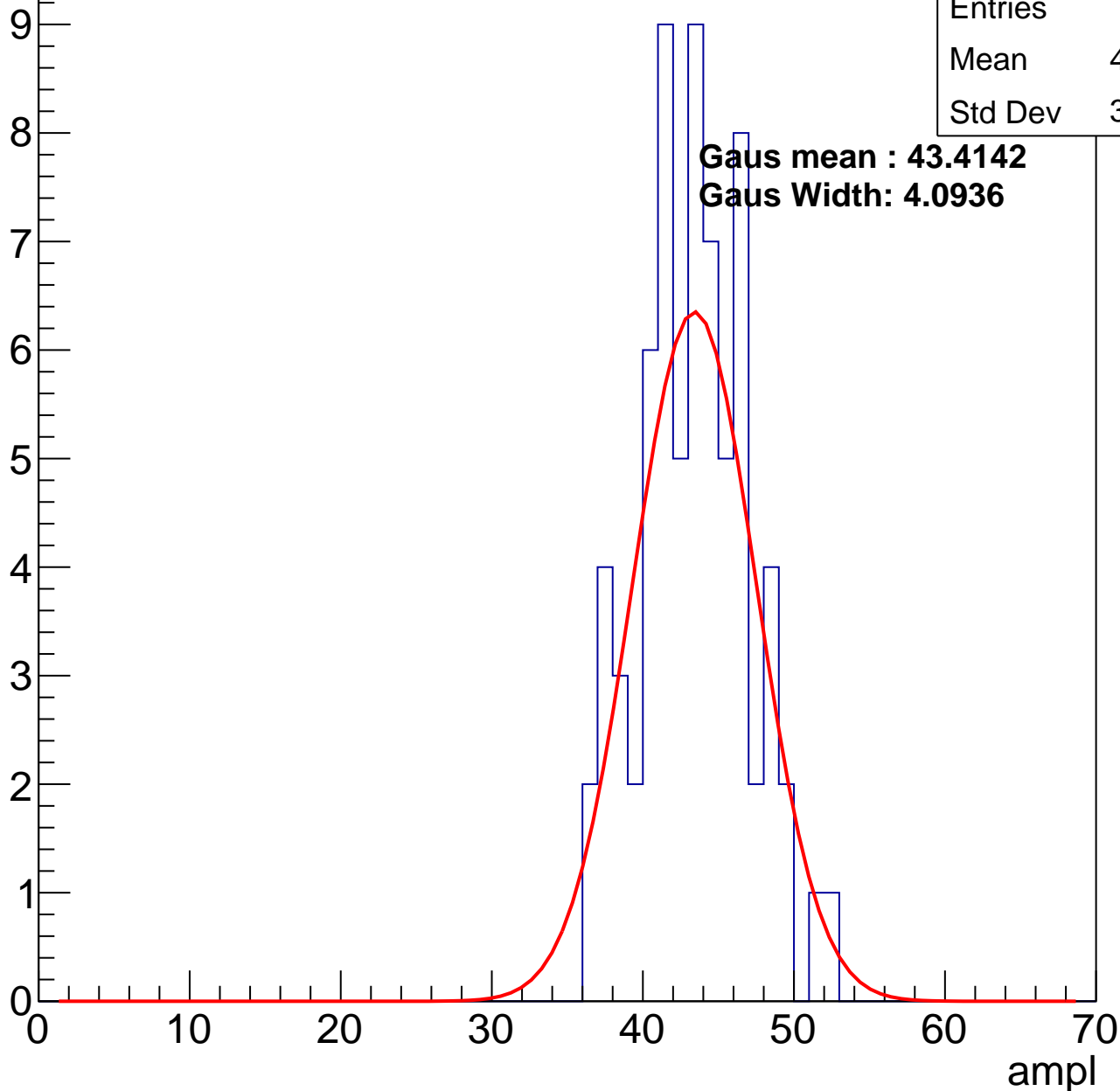
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	70
Mean	42.94
Std Dev	3.569

Gaus mean : 43.4142

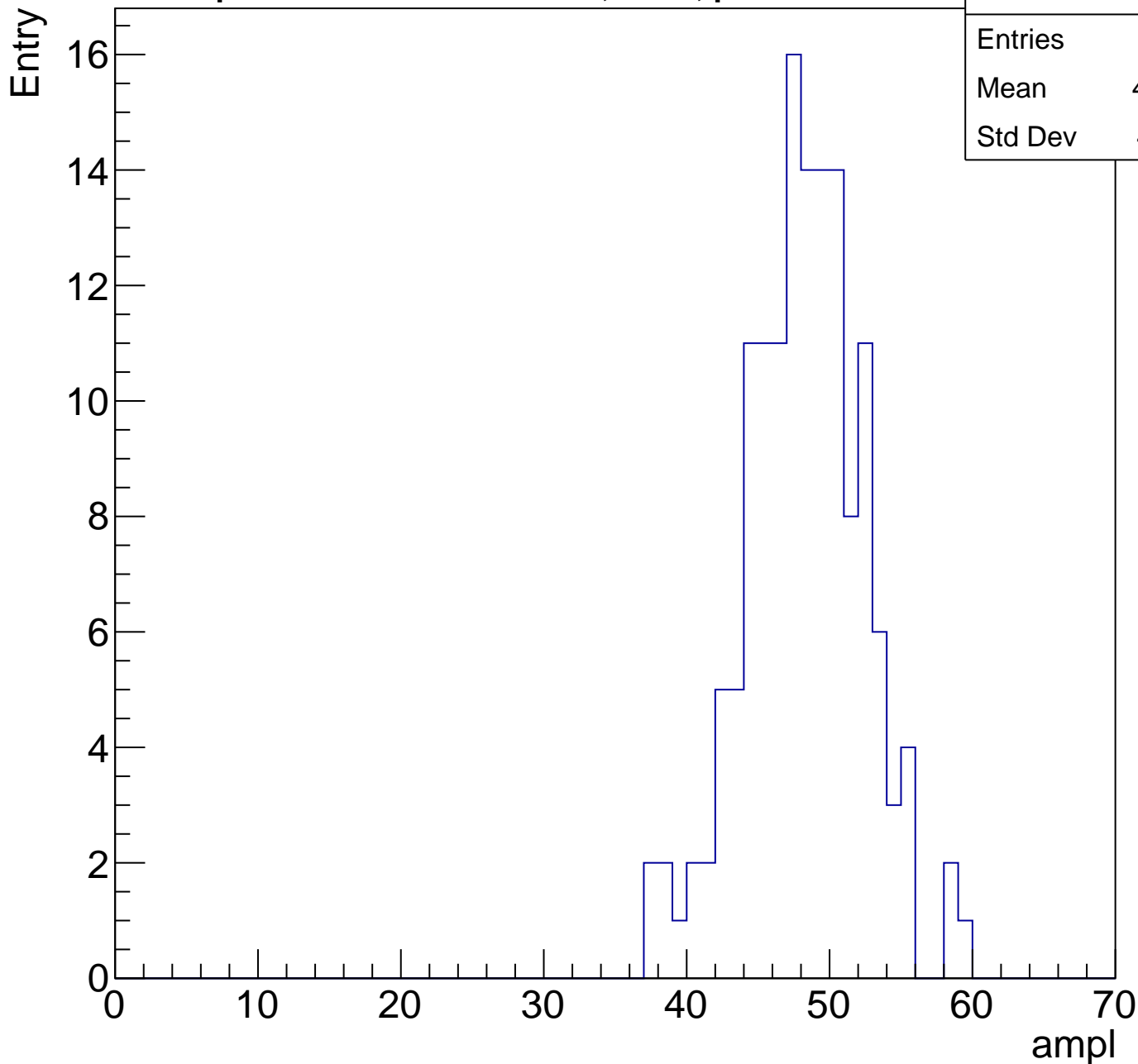
Gaus Width: 4.0936



B0L101S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

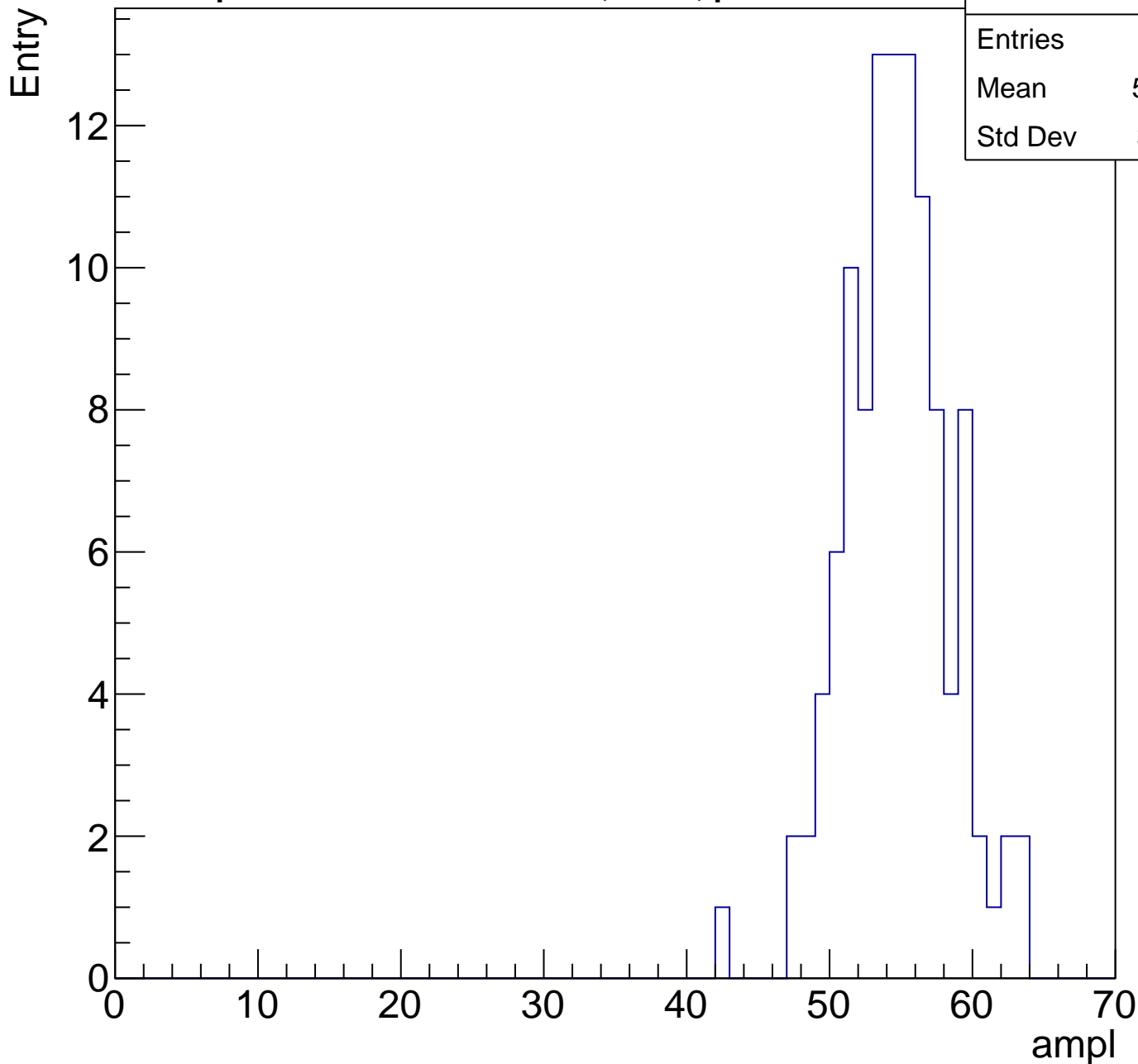
Entries	145
Mean	47.77
Std Dev	4.131



B0L101S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	54.25
Std Dev	3.621



B0L101S, U14-ch124, adc5

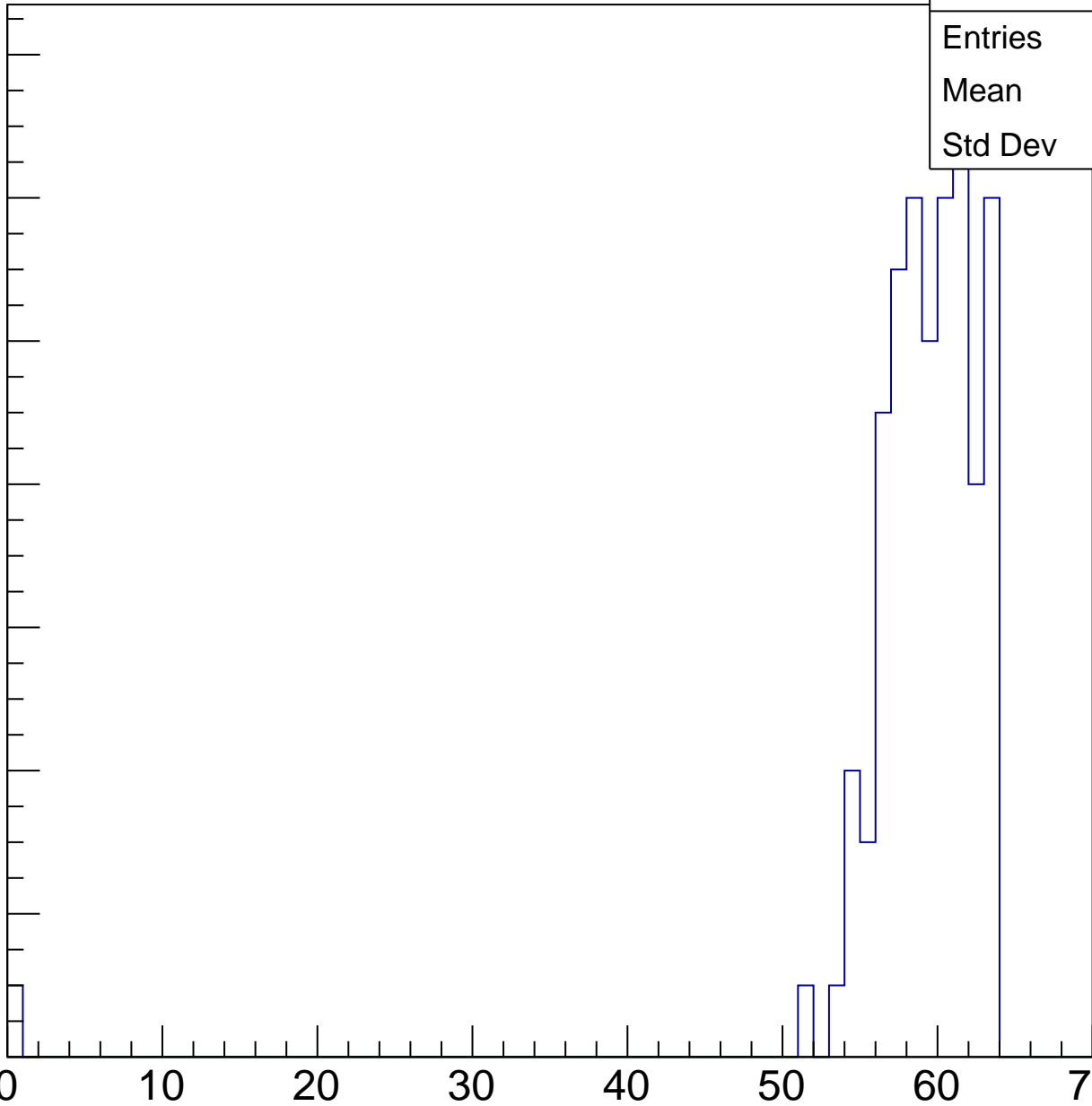
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	58.45
Std Dev	6.522

ampl

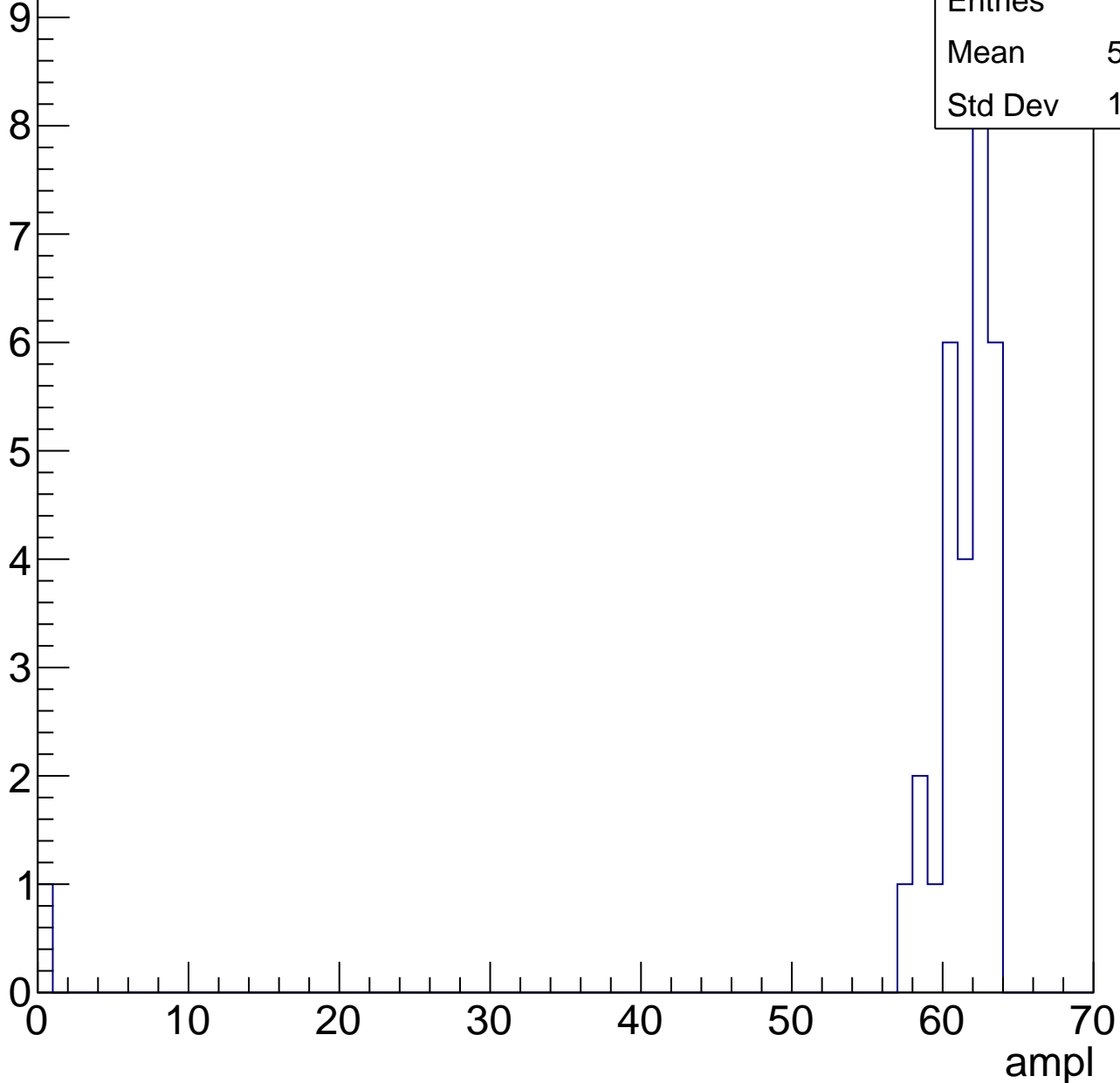


B0L101S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

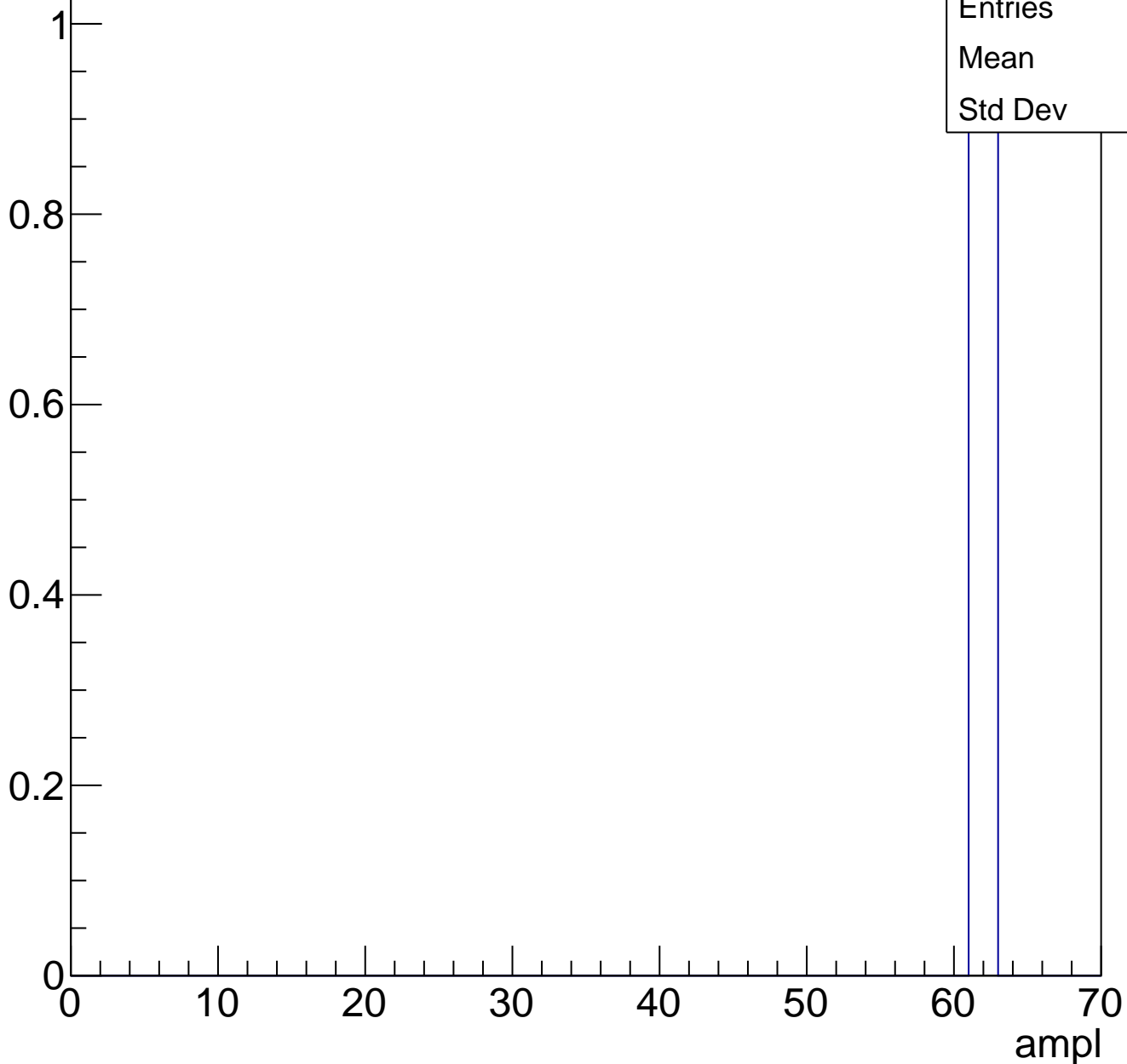
Entries	30
Mean	59.07
Std Dev	11.08



B0L101S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch125, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	27.94
Std Dev	4.665

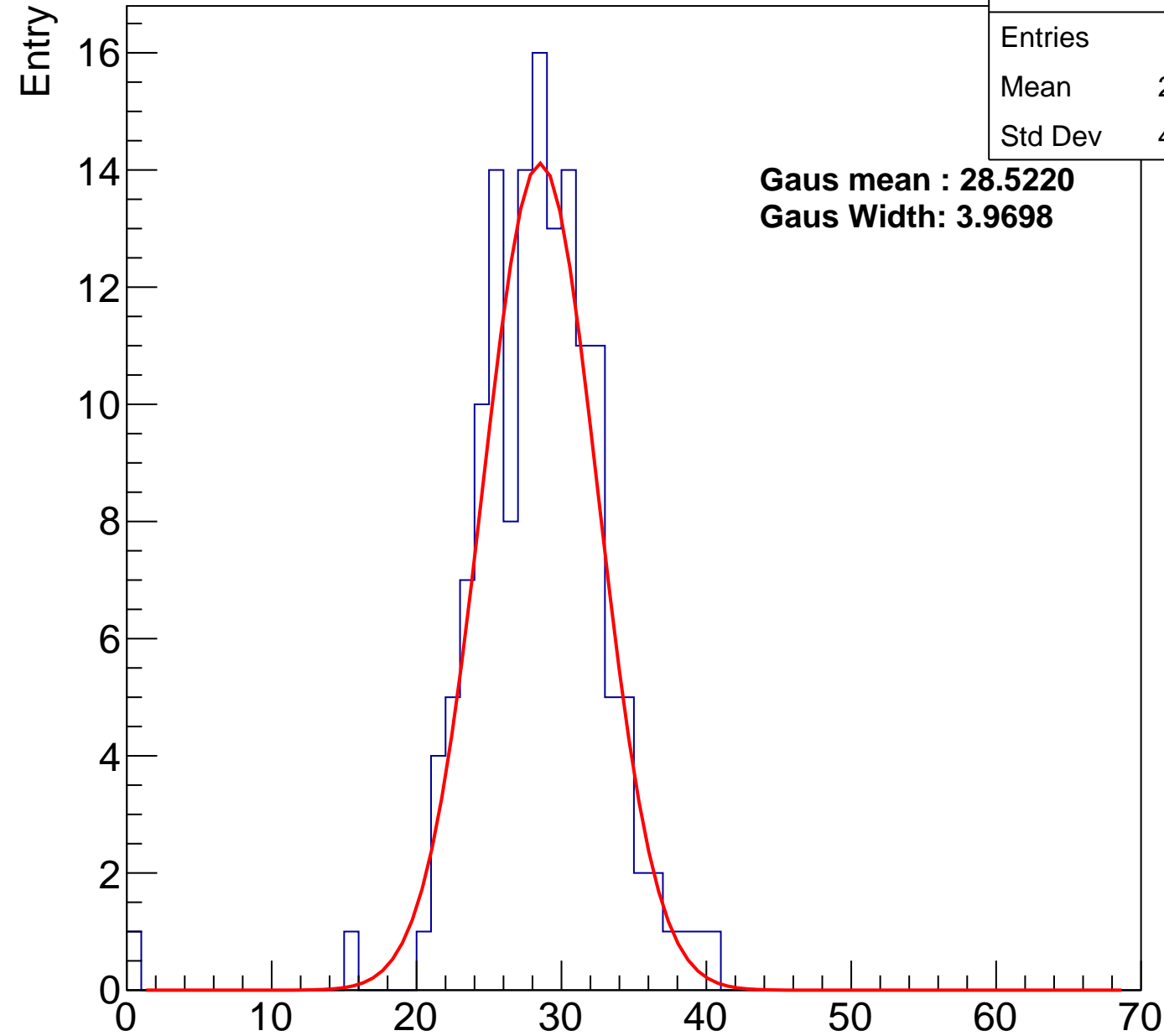
Gaus mean : 28.5220

Gaus Width: 3.9698

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch125, adc1

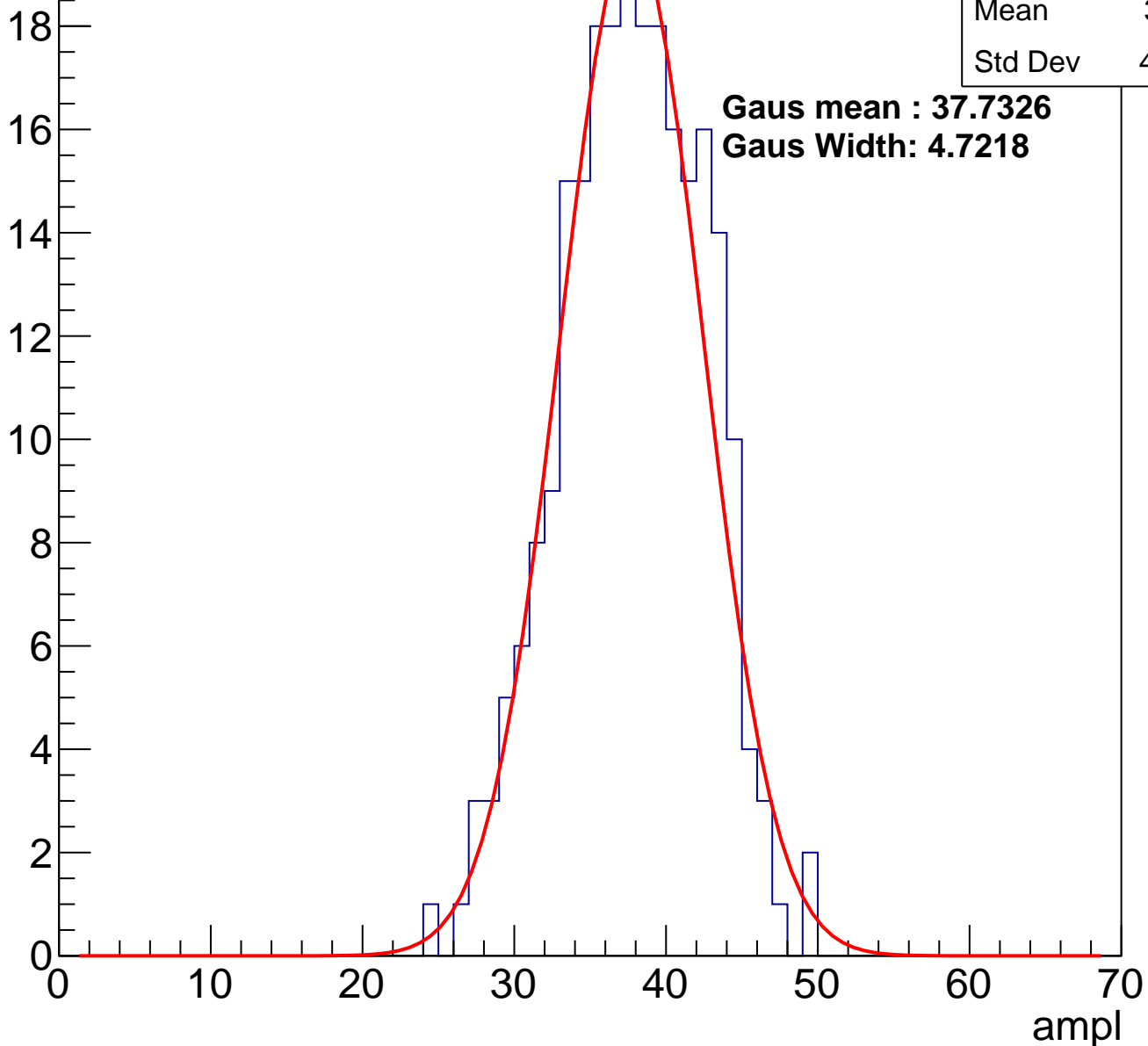
calib_packv5_042523_0143.root, FC#1, port C1

Entries	238
Mean	37.31
Std Dev	4.655

Gaus mean : 37.7326

Gaus Width: 4.7218

Entry



B0L101S, U14-ch125, adc2

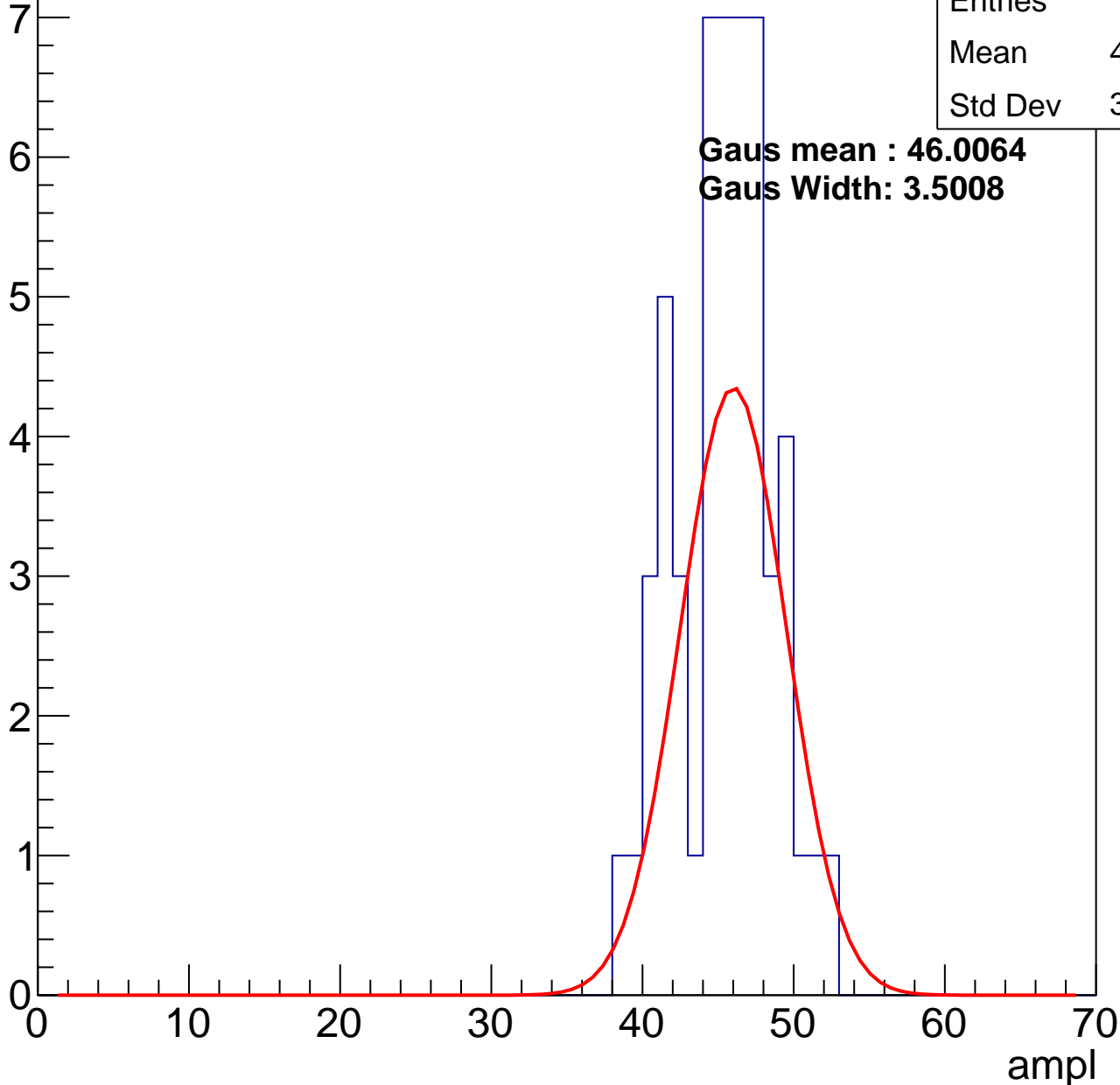
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	52
Mean	44.96
Std Dev	3.144

Gaus mean : 46.0064

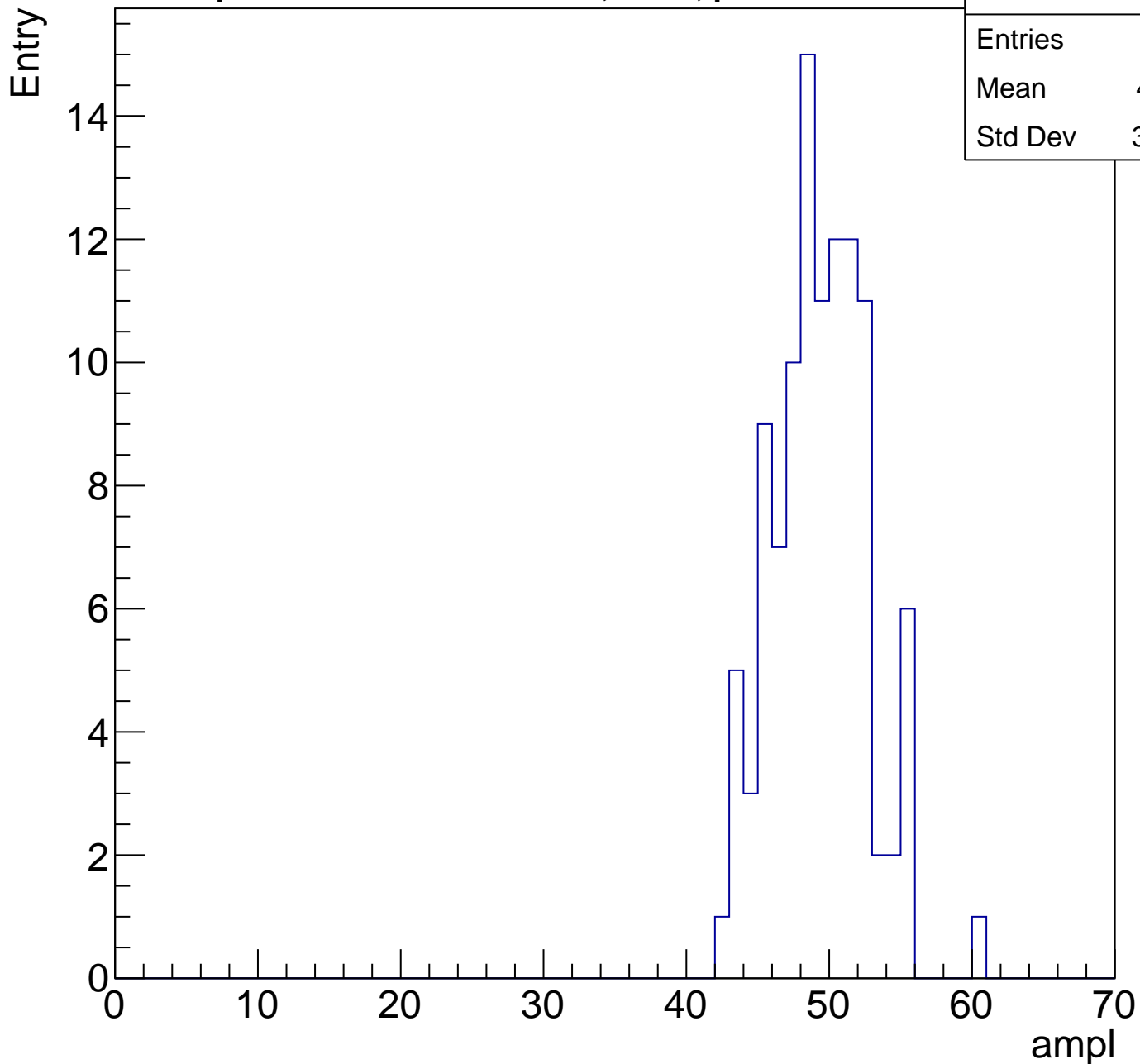
Gaus Width: 3.5008



B0L101S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	48.91
Std Dev	3.268



B0L101S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	55.19
Std Dev	3.784

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

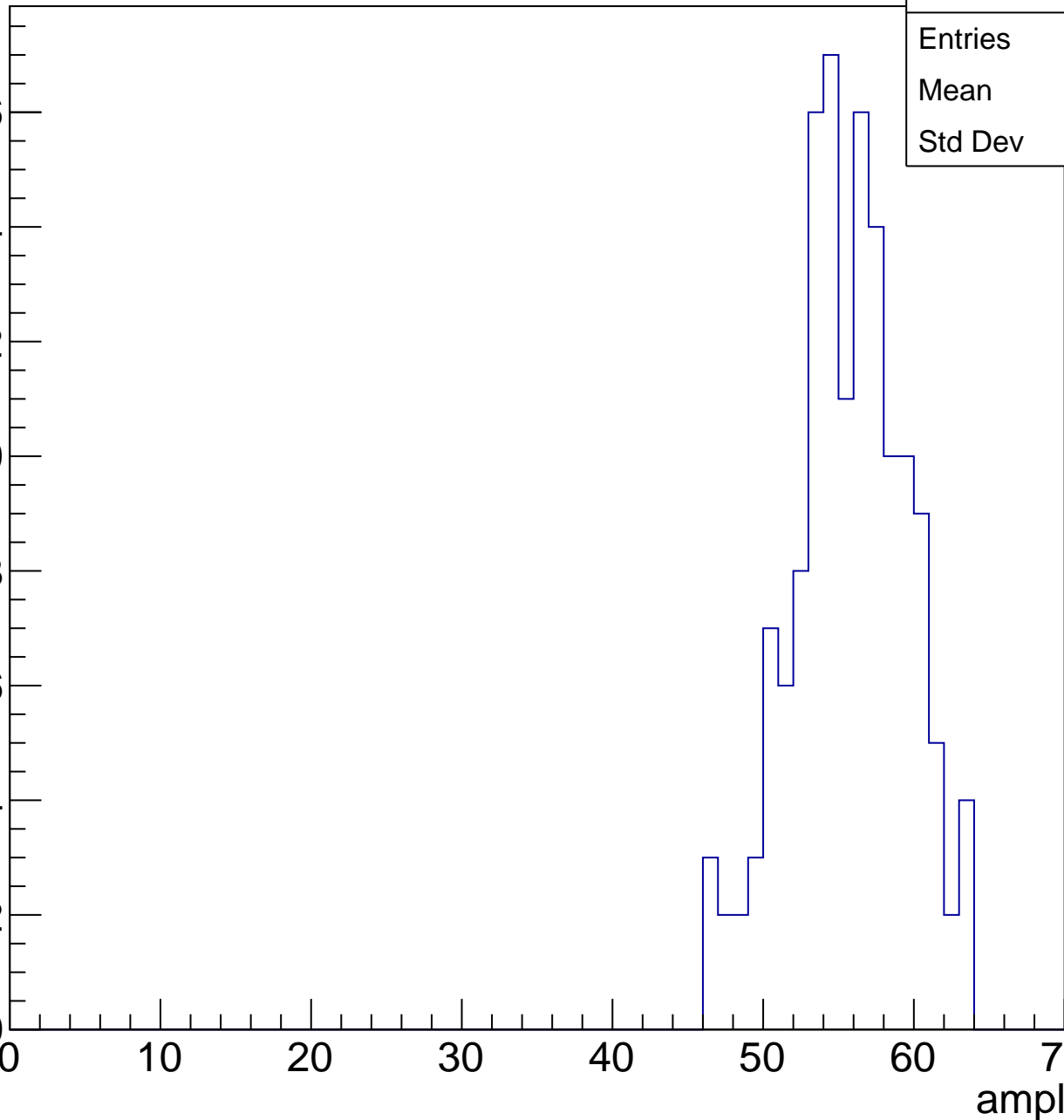
40

50

60

ampl

70



B0L101S, U14-ch125, adc5

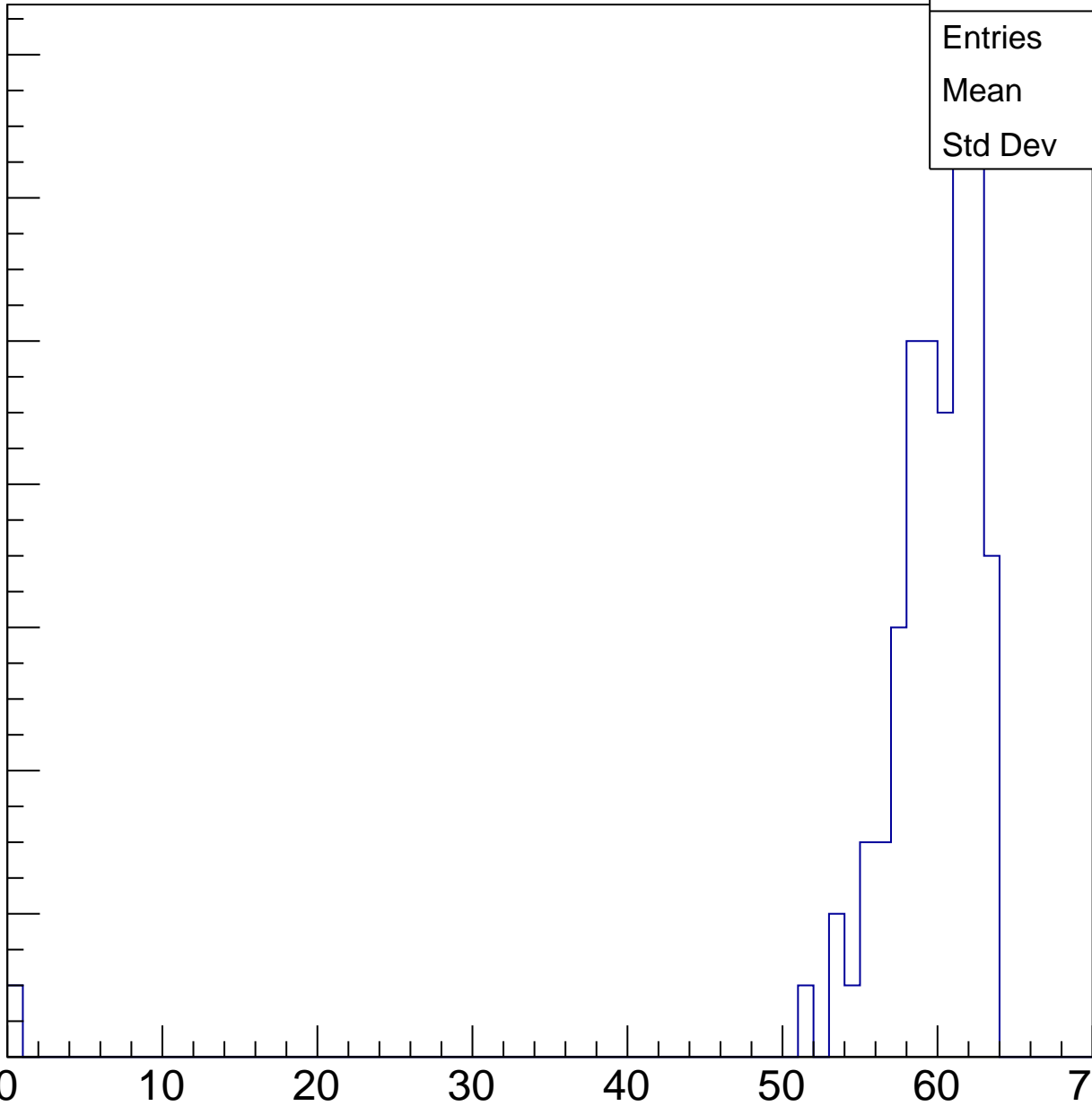
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.73
Std Dev	7.11

ampl

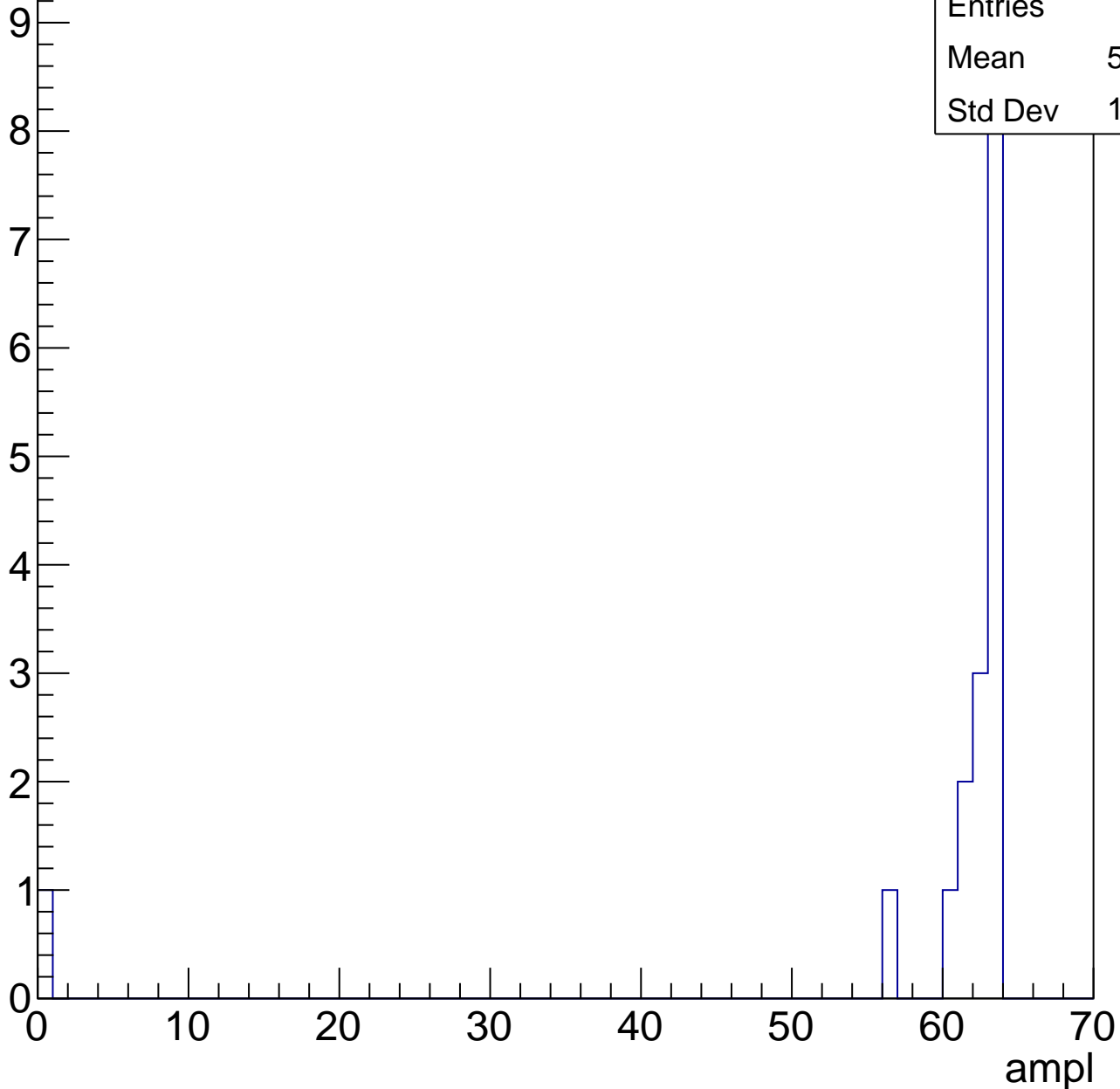


B0L101S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

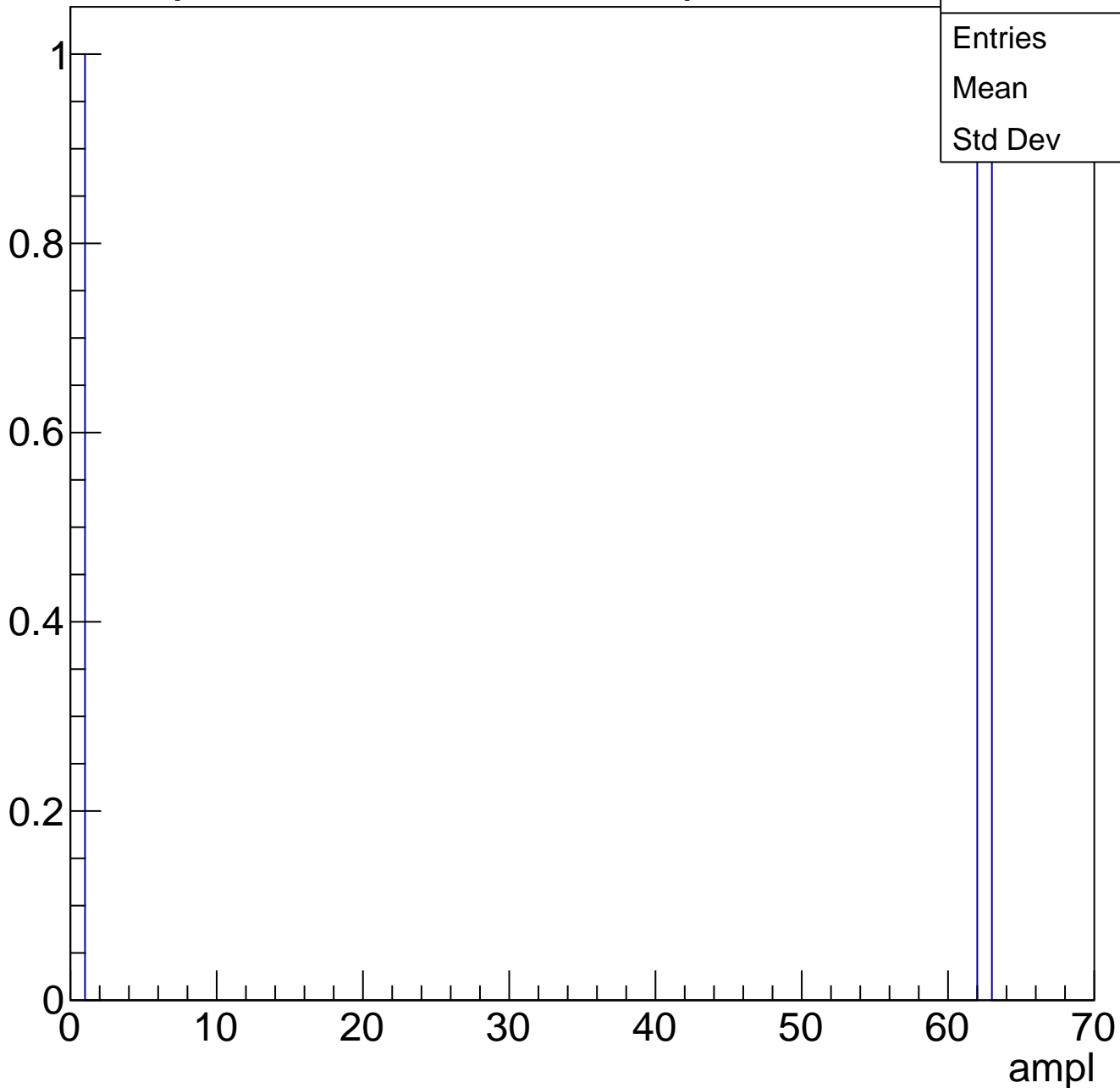
Entries	17
Mean	58.29
Std Dev	14.68



B0L101S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch126, adc0

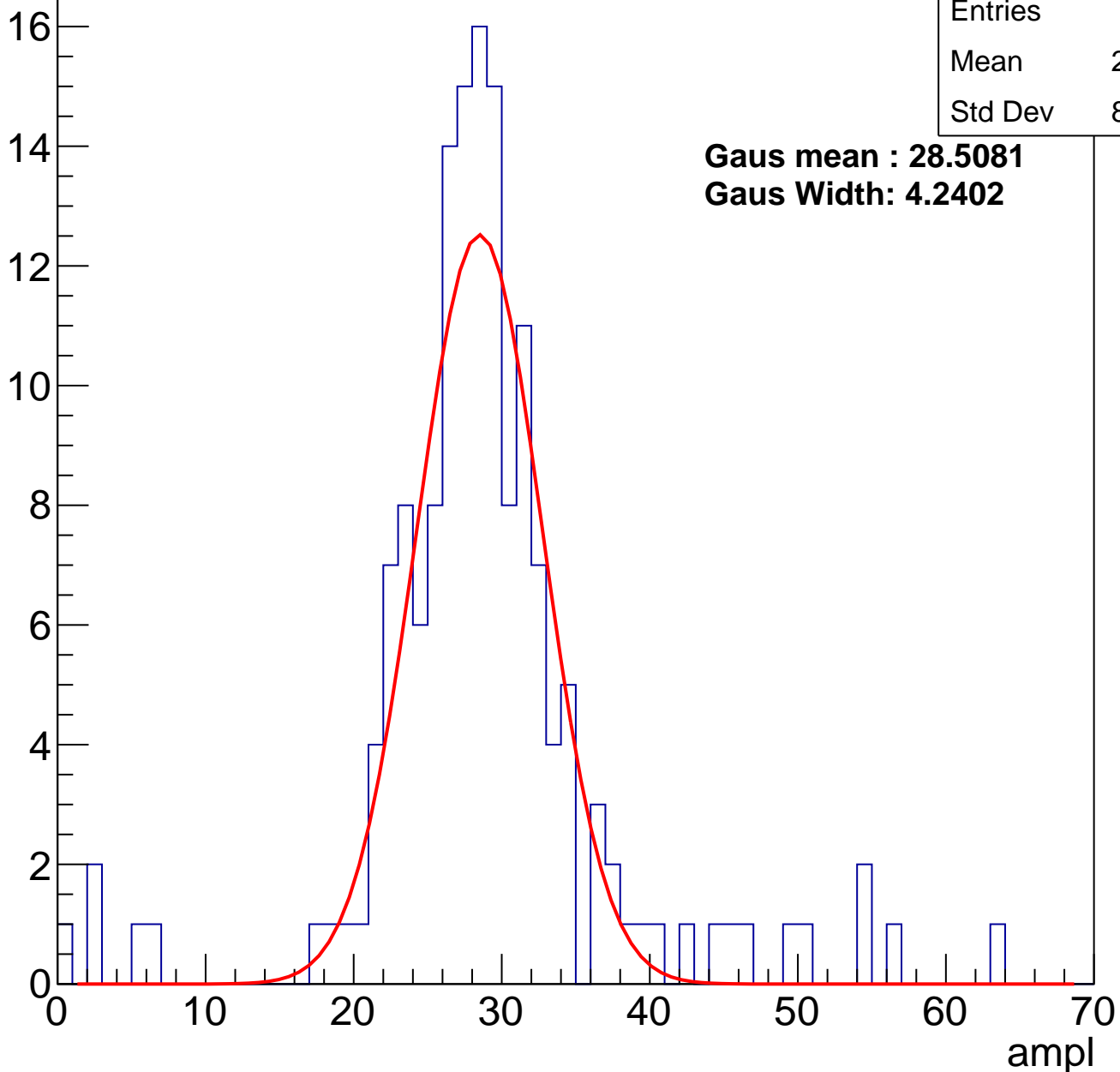
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	28.49
Std Dev	8.414

Gaus mean : 28.5081

Gaus Width: 4.2402

Entry



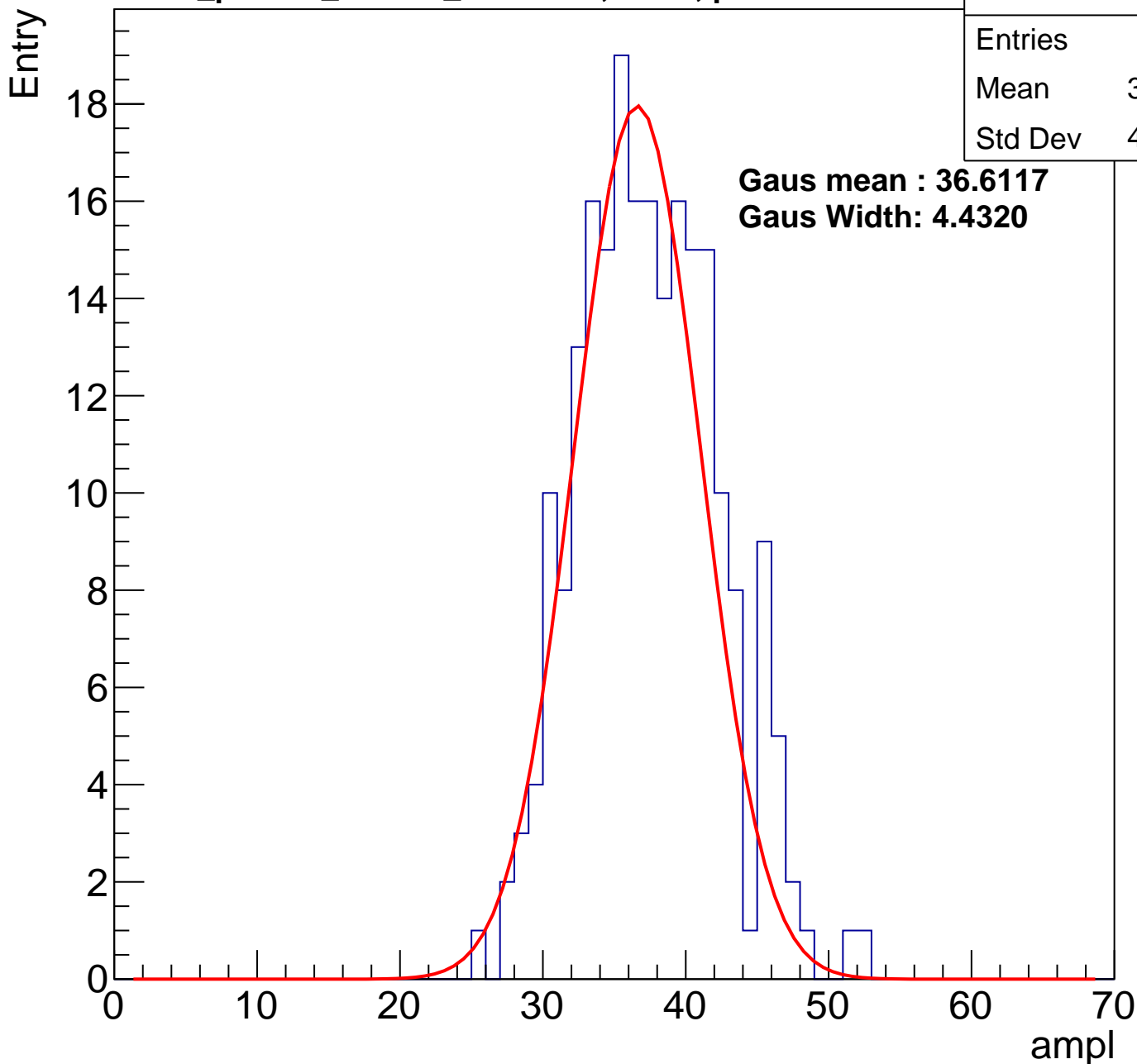
B0L101S, U14-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	221
Mean	36.98
Std Dev	4.847

Gaus mean : 36.6117

Gaus Width: 4.4320



B0L101S, U14-ch126, adc2

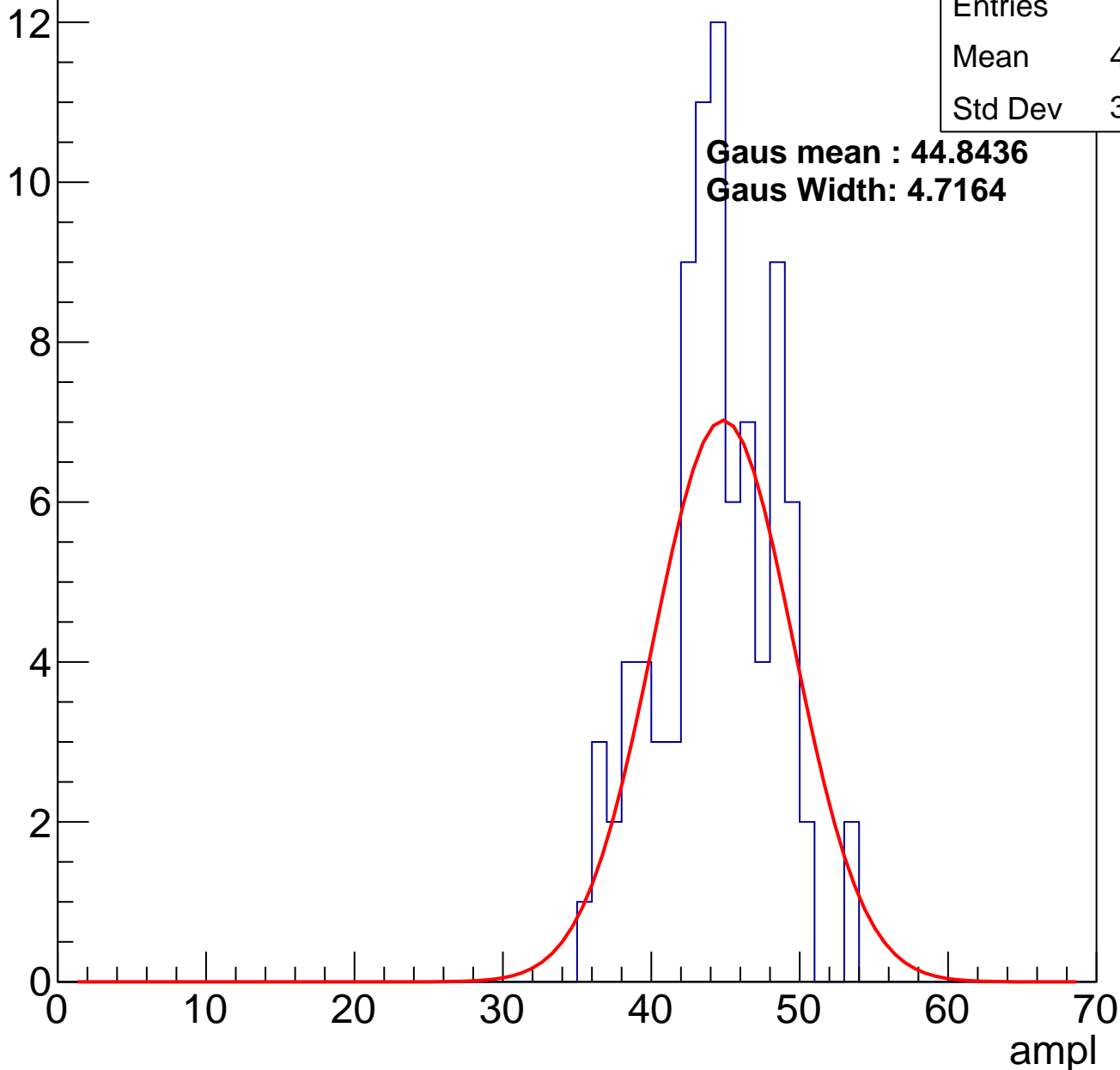
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	88
Mean	43.85
Std Dev	3.892

Gaus mean : 44.8436

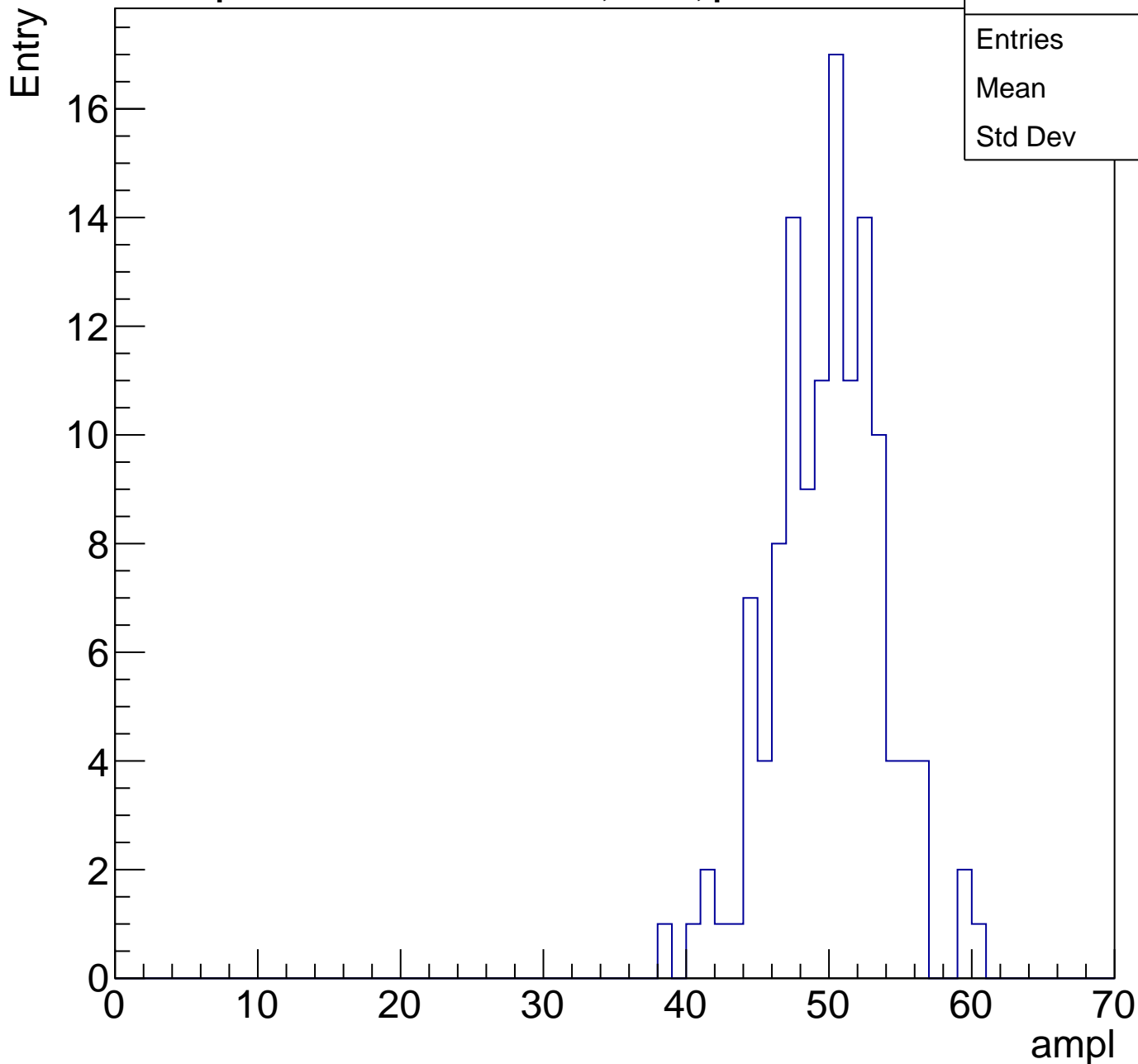
Gaus Width: 4.7164



B0L101S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

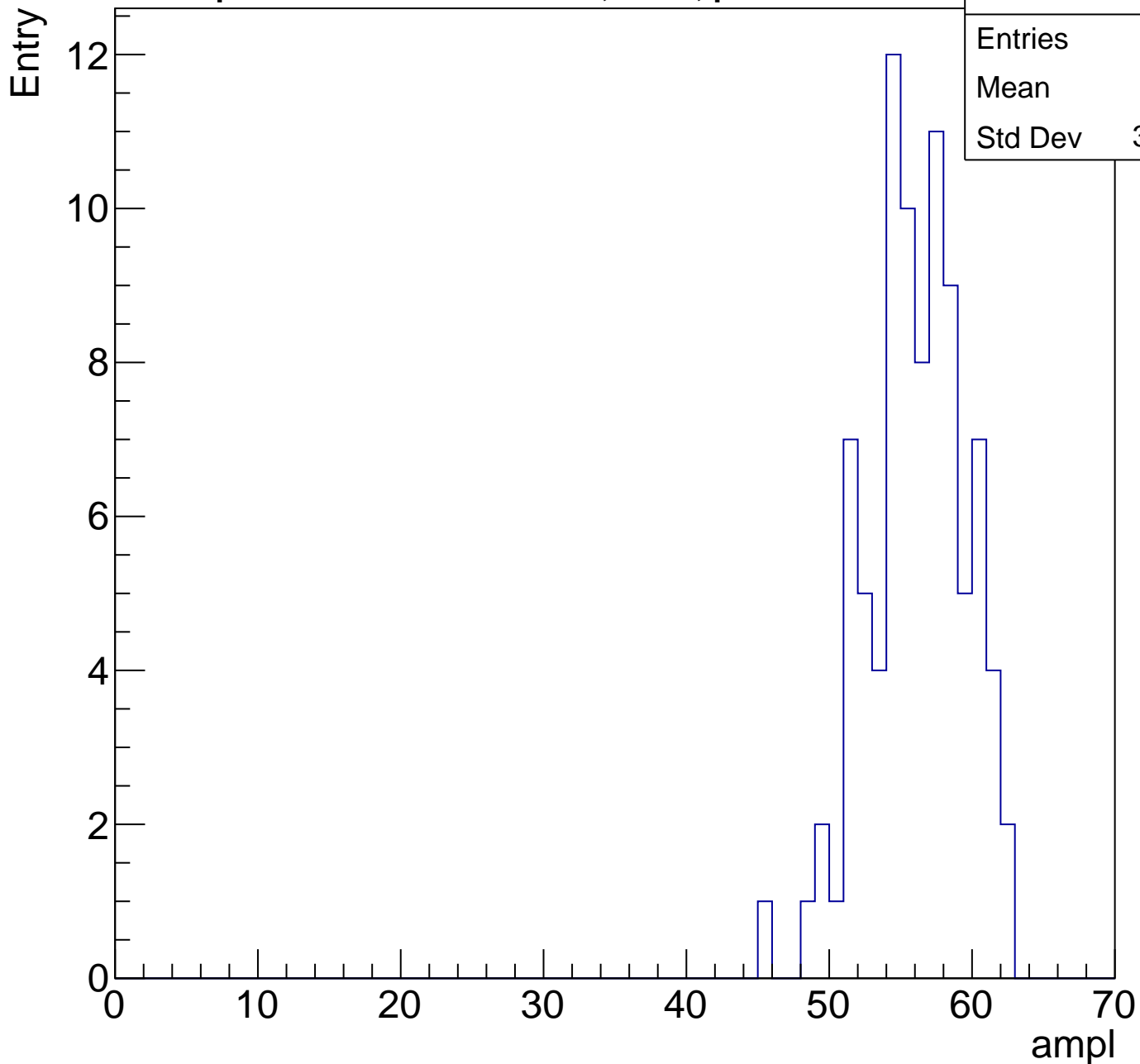
Entries	126
Mean	49.5
Std Dev	3.85



B0L101S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	55.6
Std Dev	3.401



B0L101S, U14-ch126, adc5

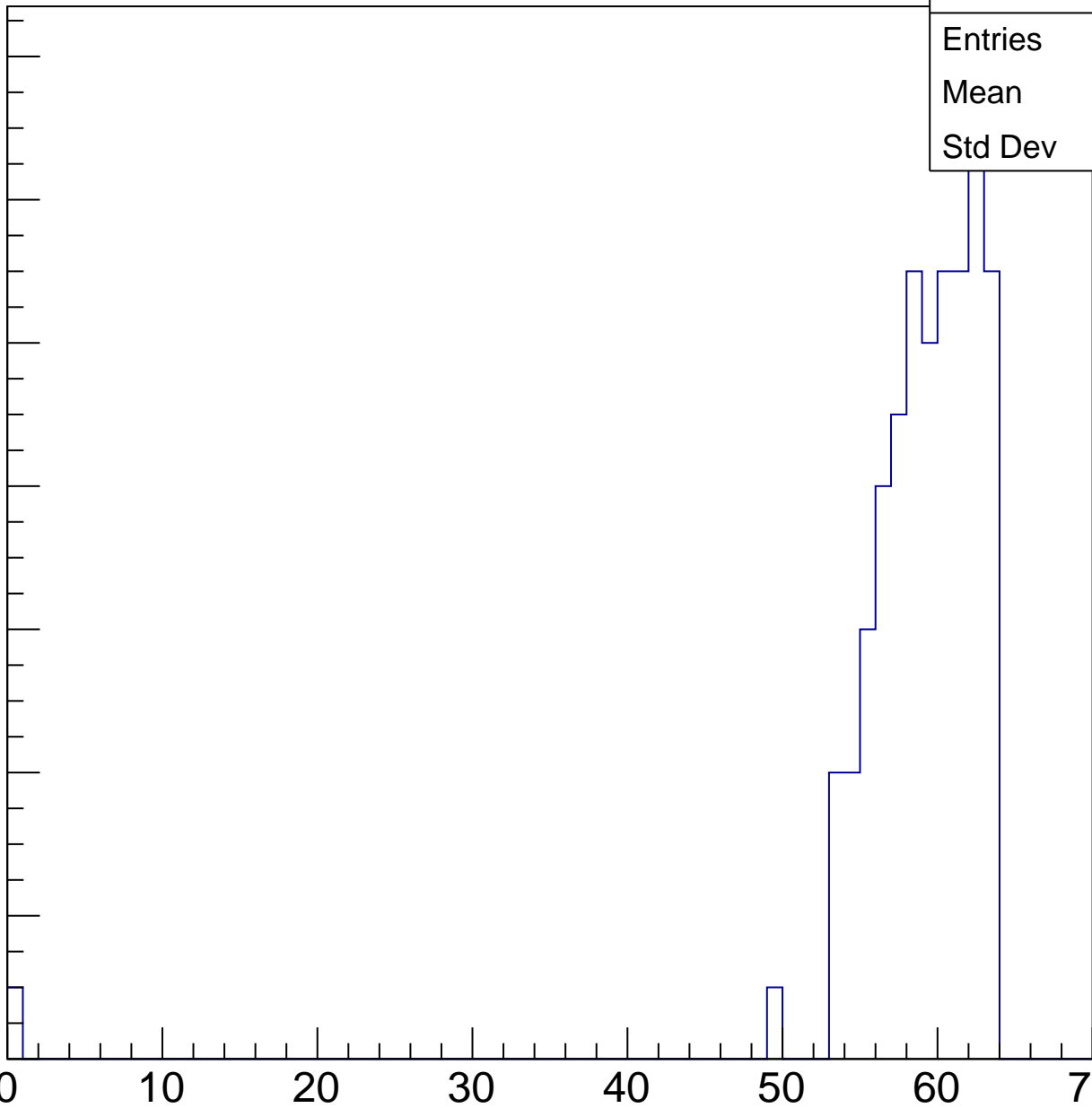
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	101
Mean	58.3
Std Dev	6.561

ampl

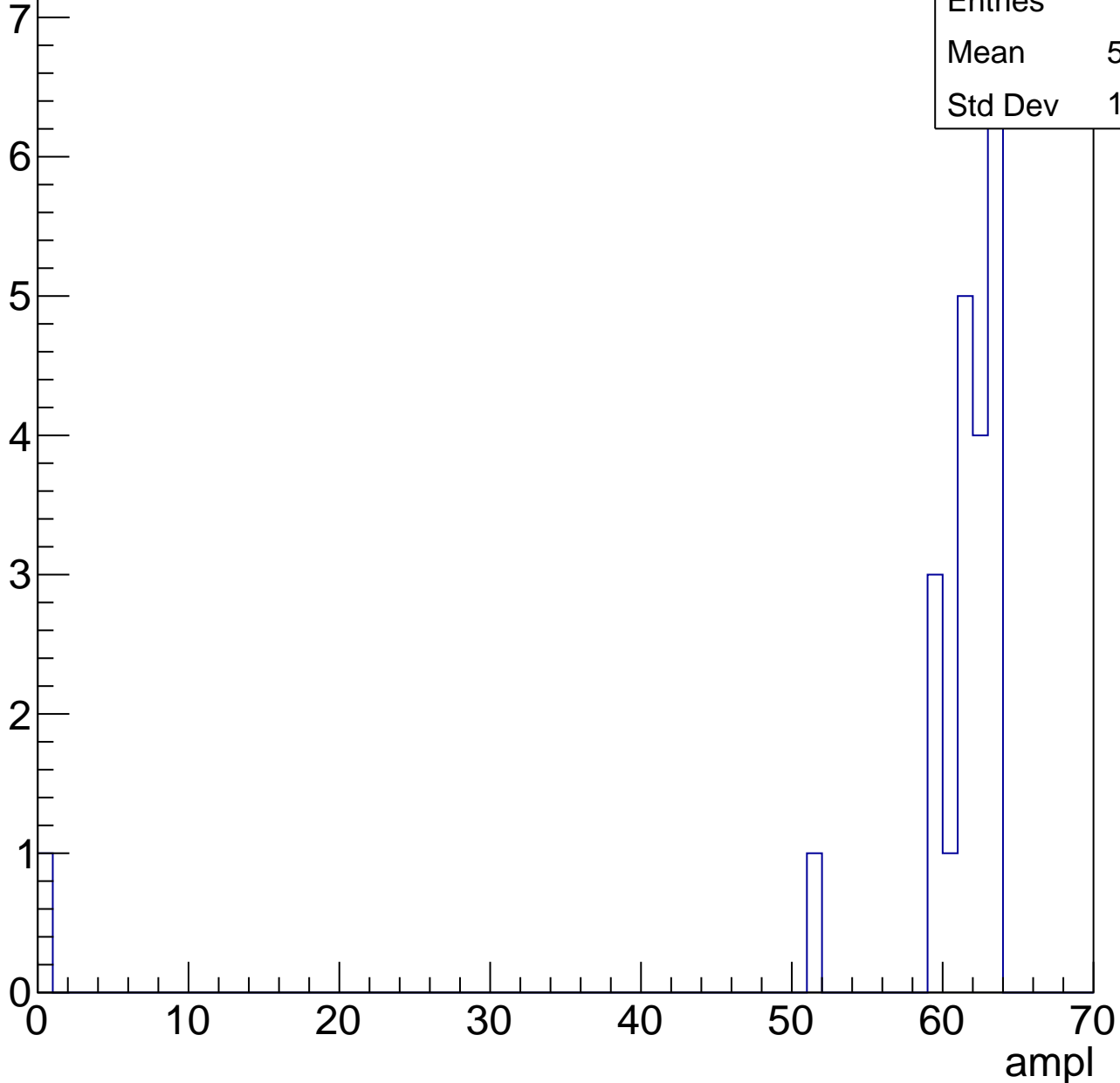


B0L101S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	58.27
Std Dev	12.97



B0L101S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch127, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	28.65
Std Dev	5.689

Gaus mean : 28.9403

Gaus Width: 4.1241

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

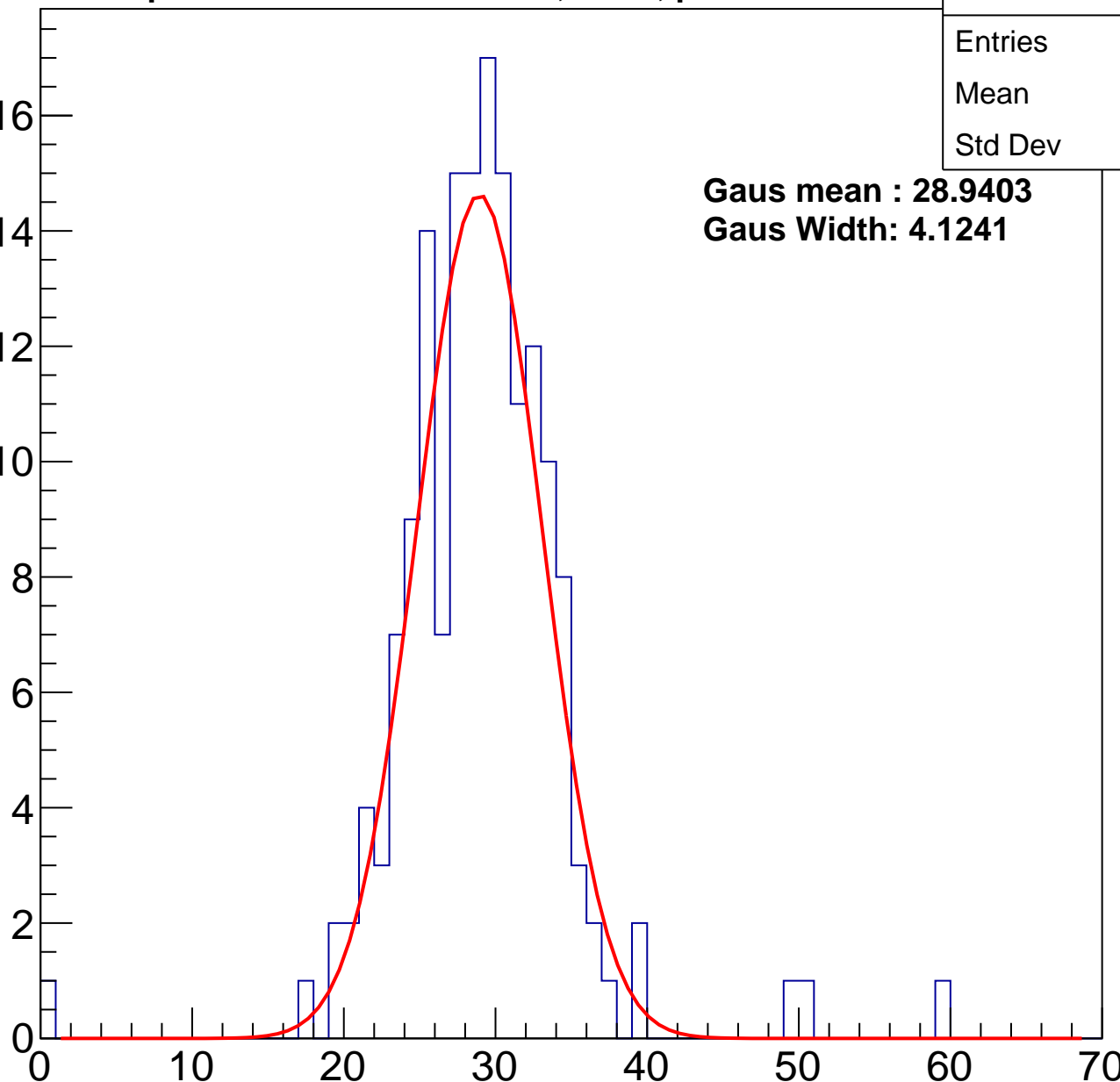
40

50

60

70

ampl



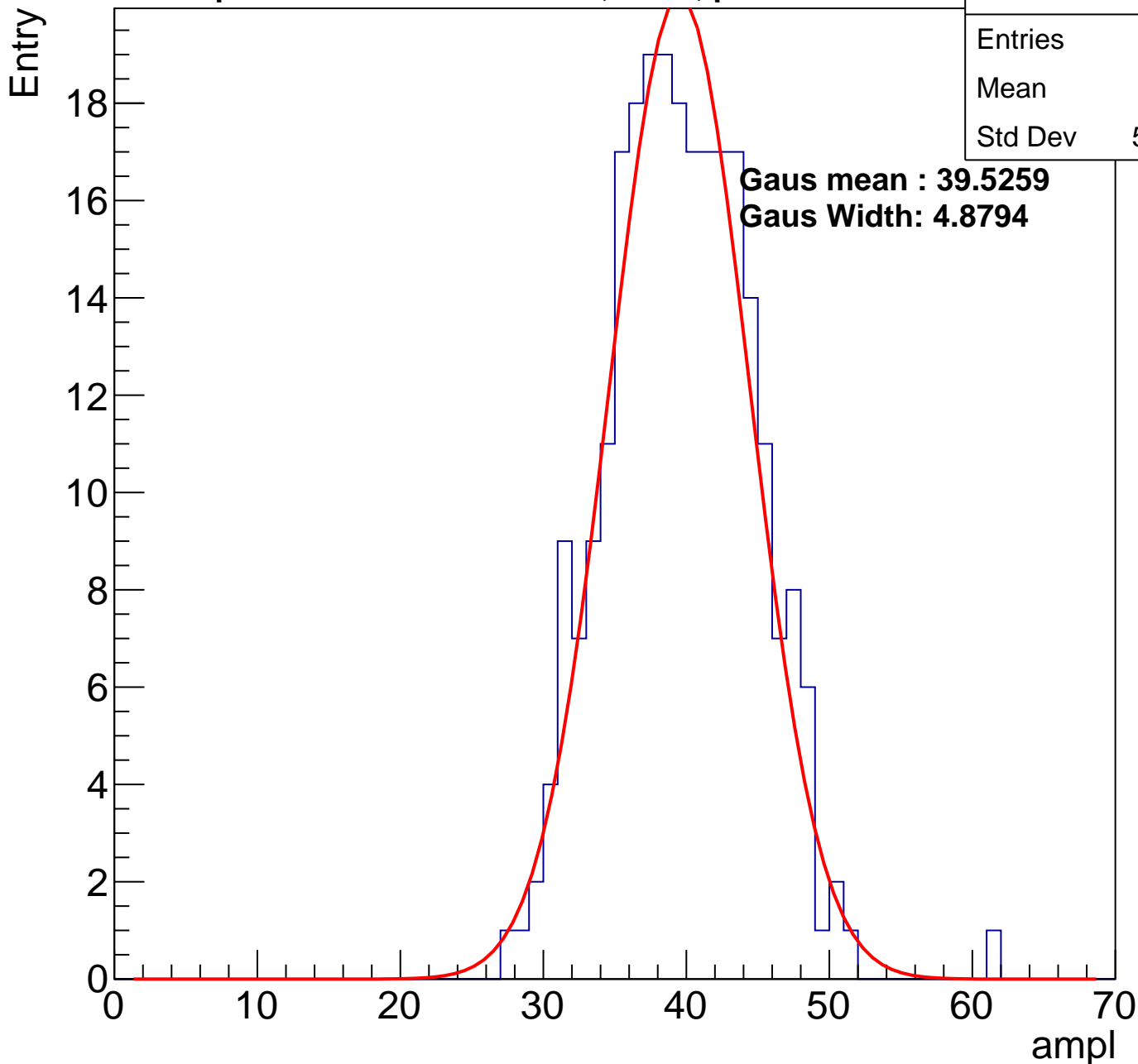
B0L101S, U14-ch127, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	254
Mean	39.2
Std Dev	5.017

Gaus mean : 39.5259

Gaus Width: 4.8794



B0L101S, U14-ch127, adc2

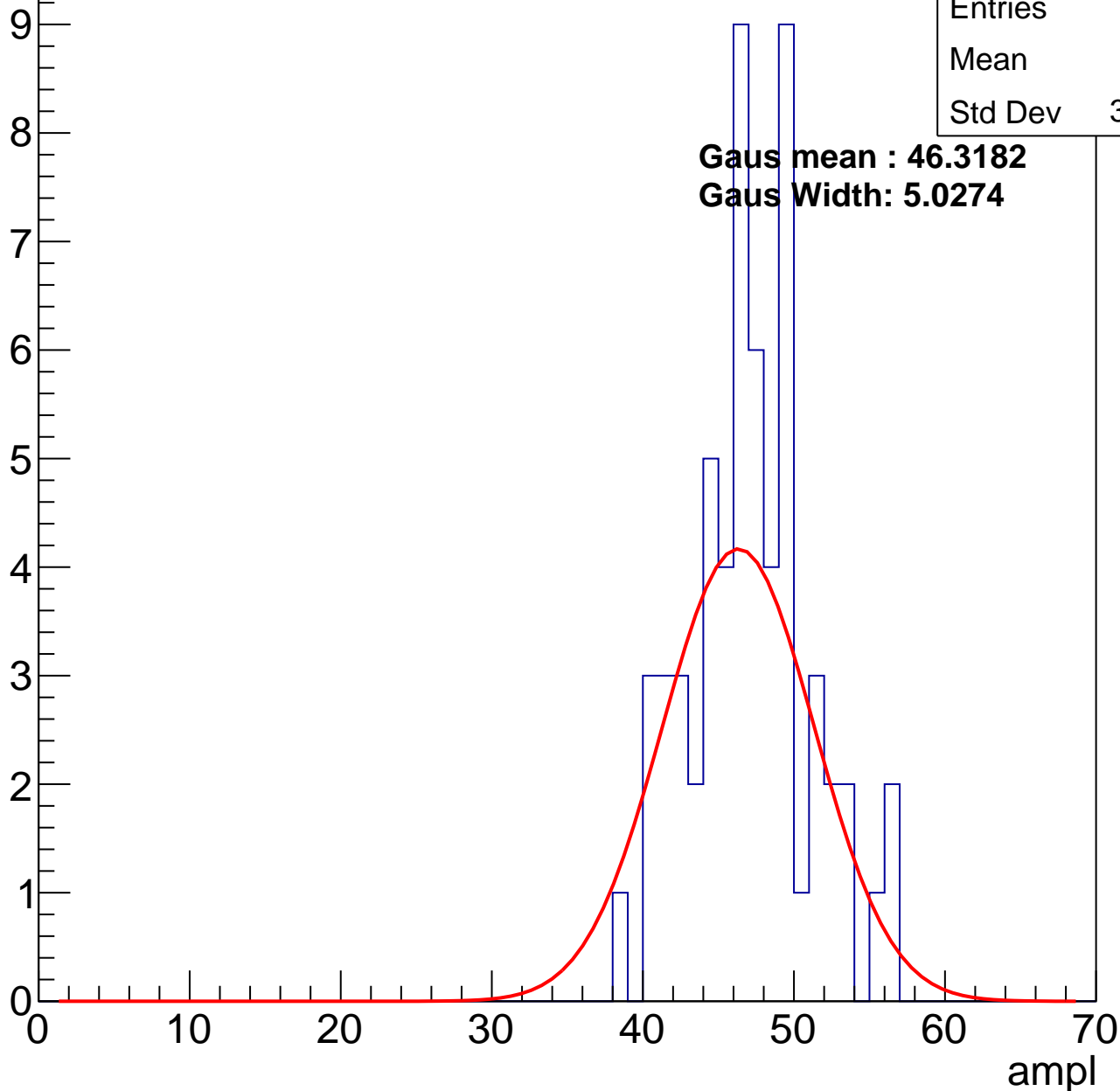
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	60
Mean	46.7
Std Dev	3.985

Gaus mean : 46.3182

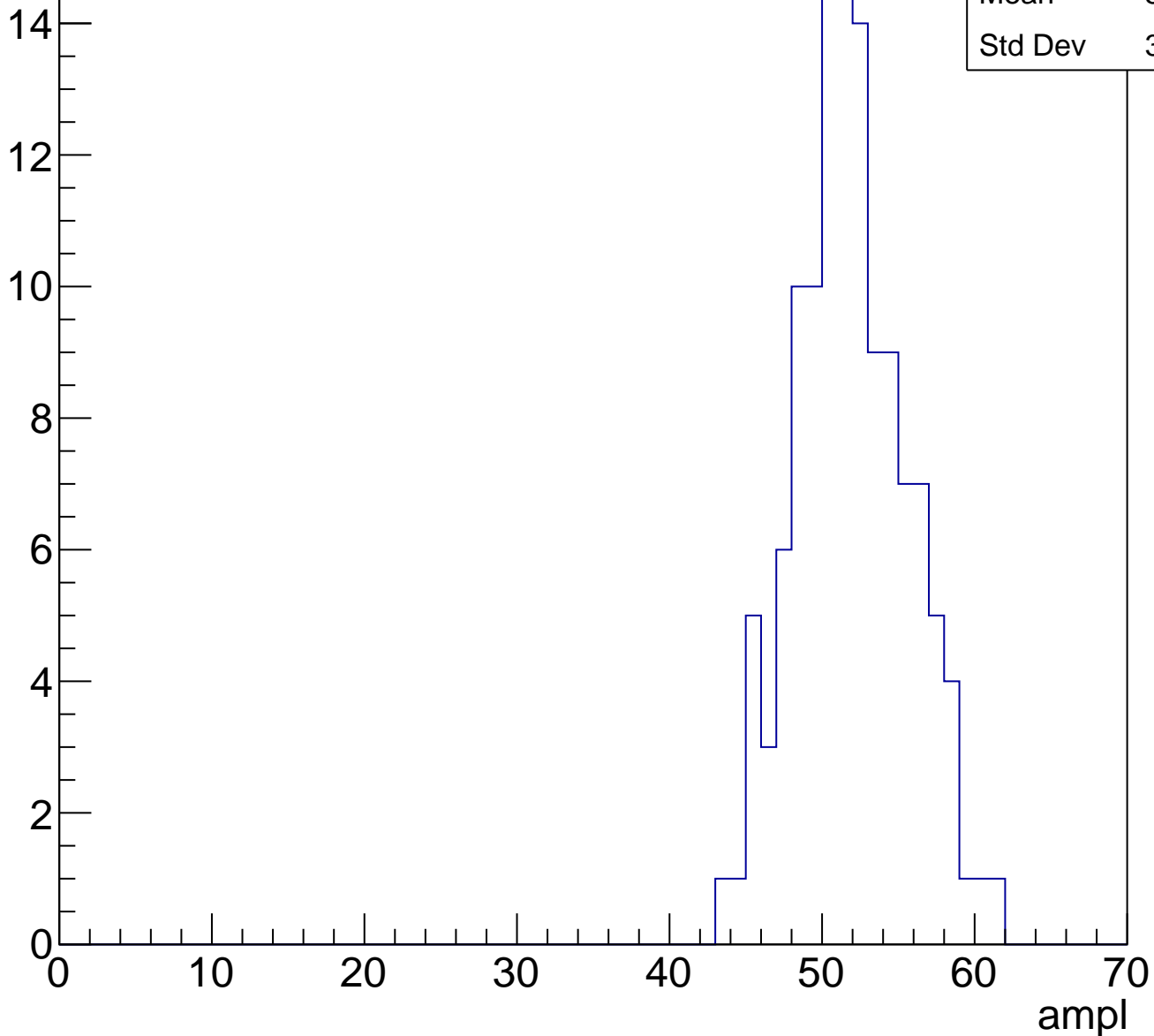
Gaus Width: 5.0274



B0L101S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch127, adc4

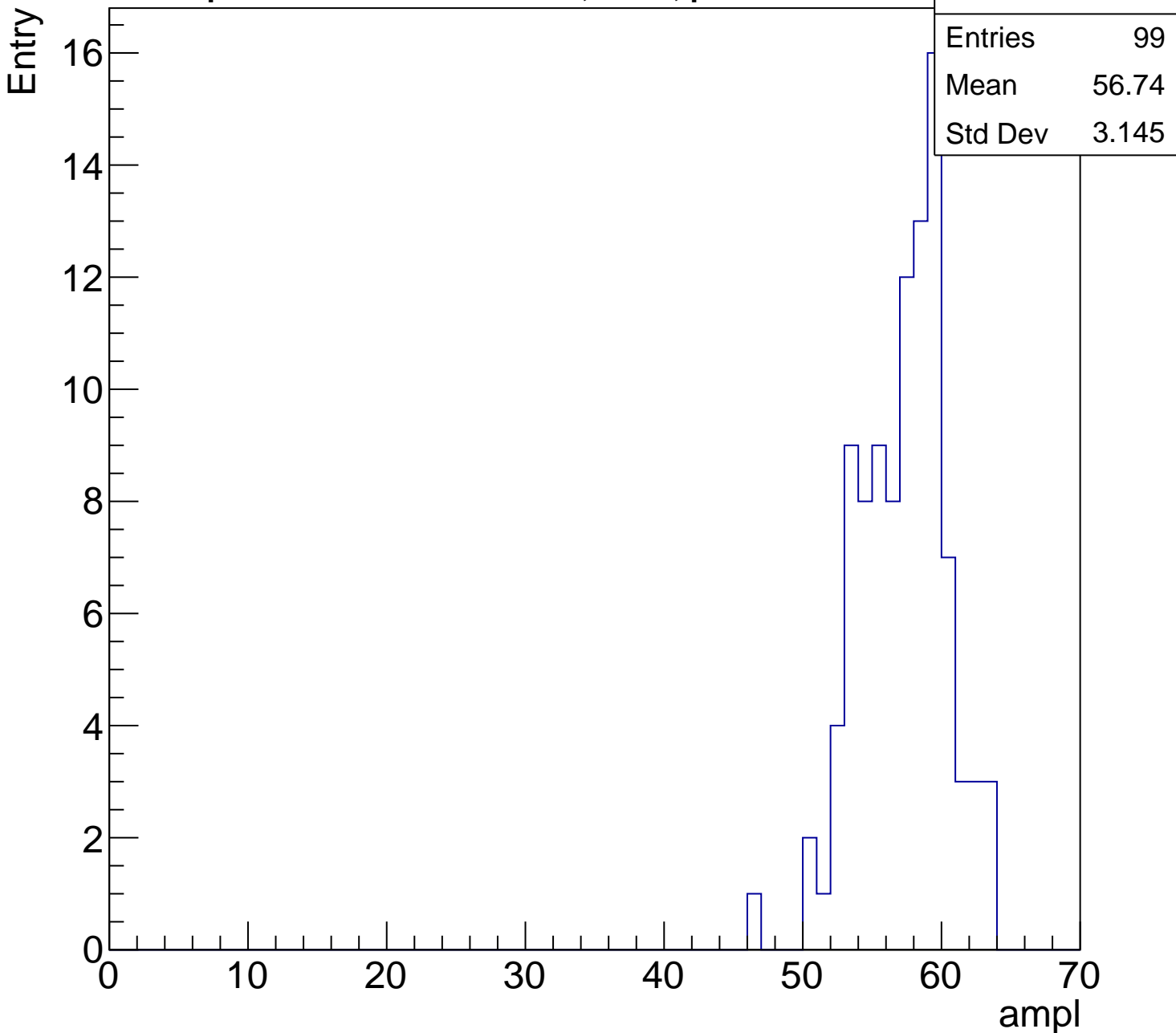
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	56.74
Std Dev	3.145

ampl



B0L101S, U14-ch127, adc5

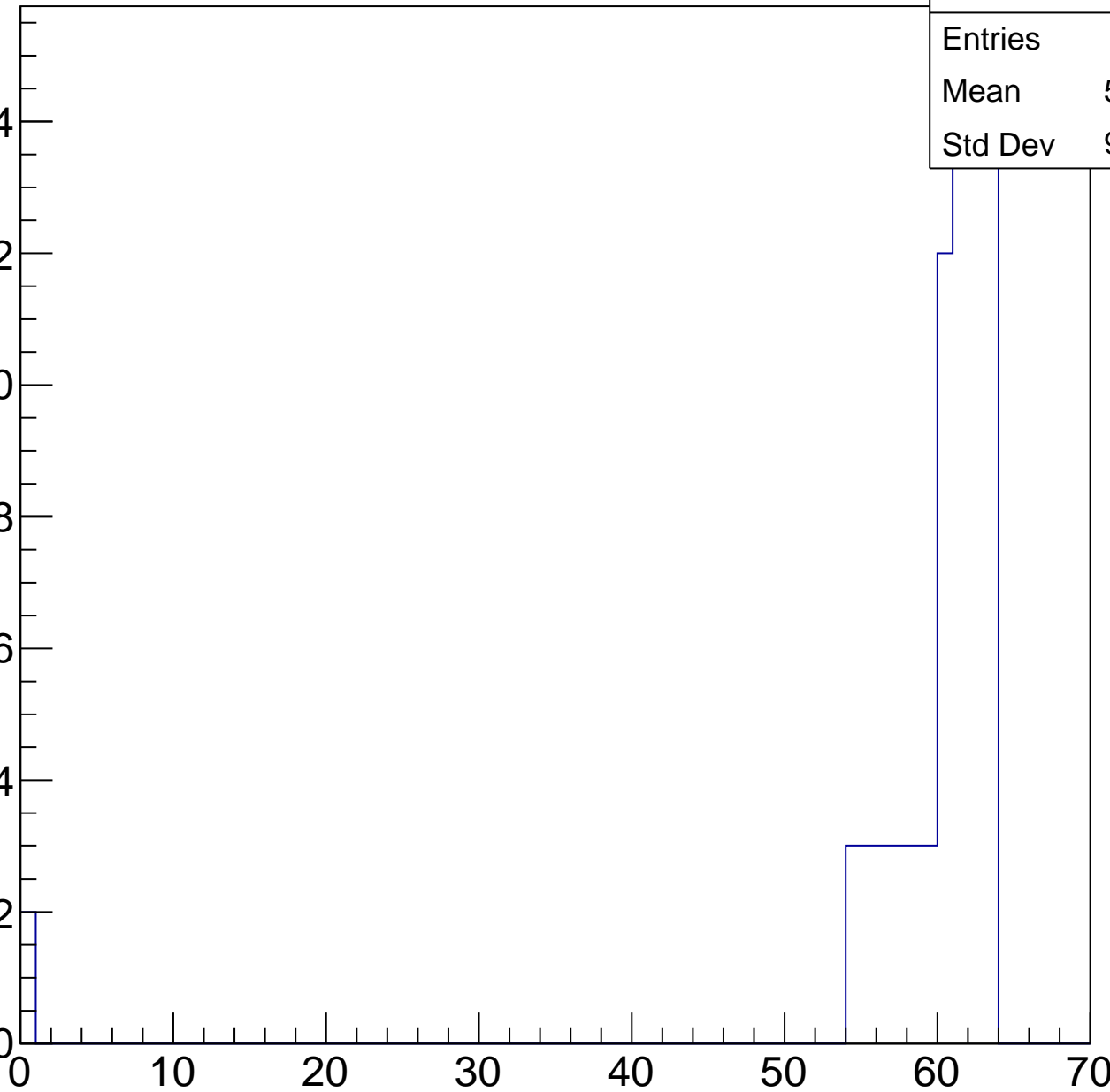
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	58.74
Std Dev	9.969

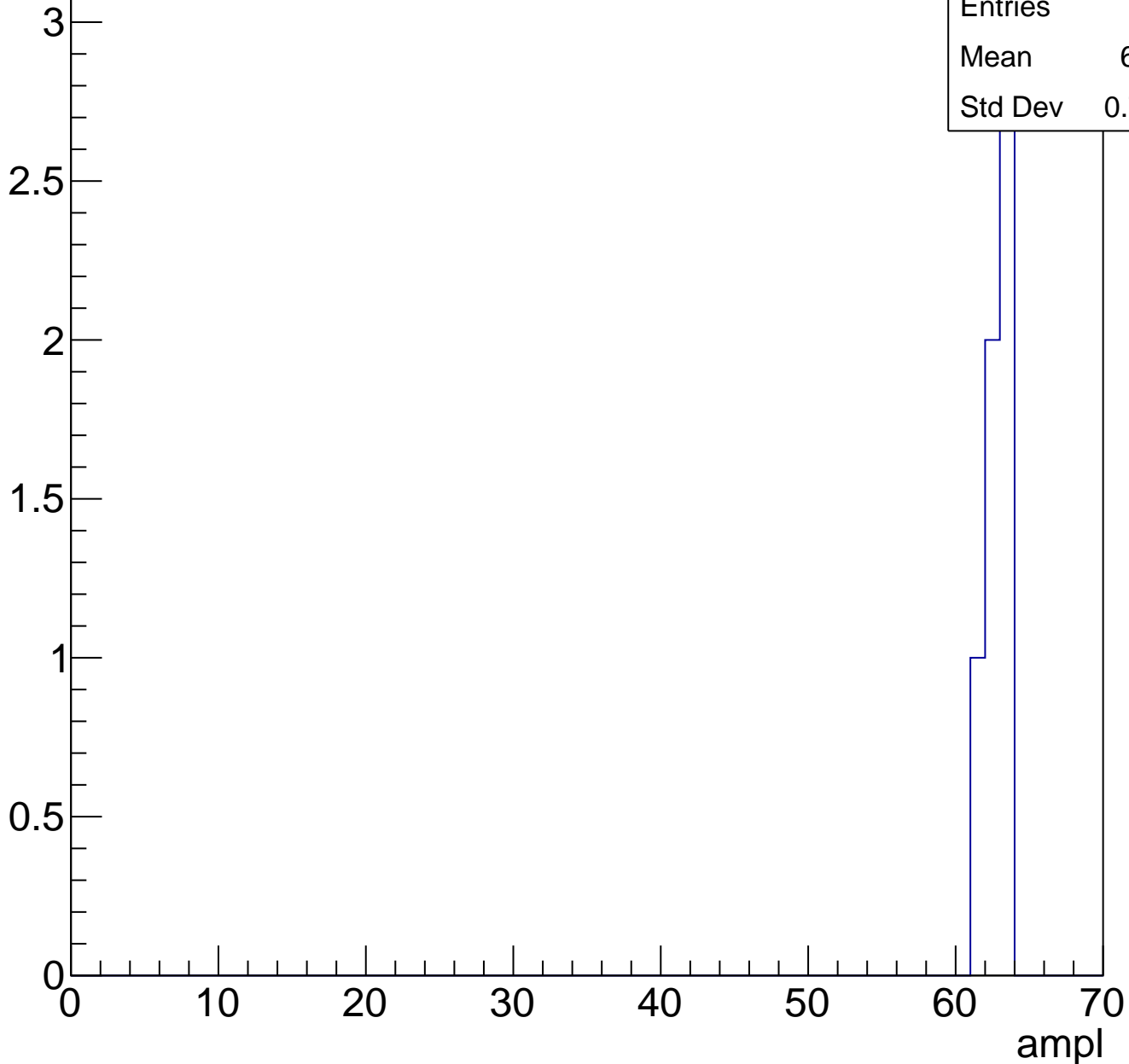
ampl



B0L101S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	6
Mean	62.33
Std Dev	0.7454

B0L101S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

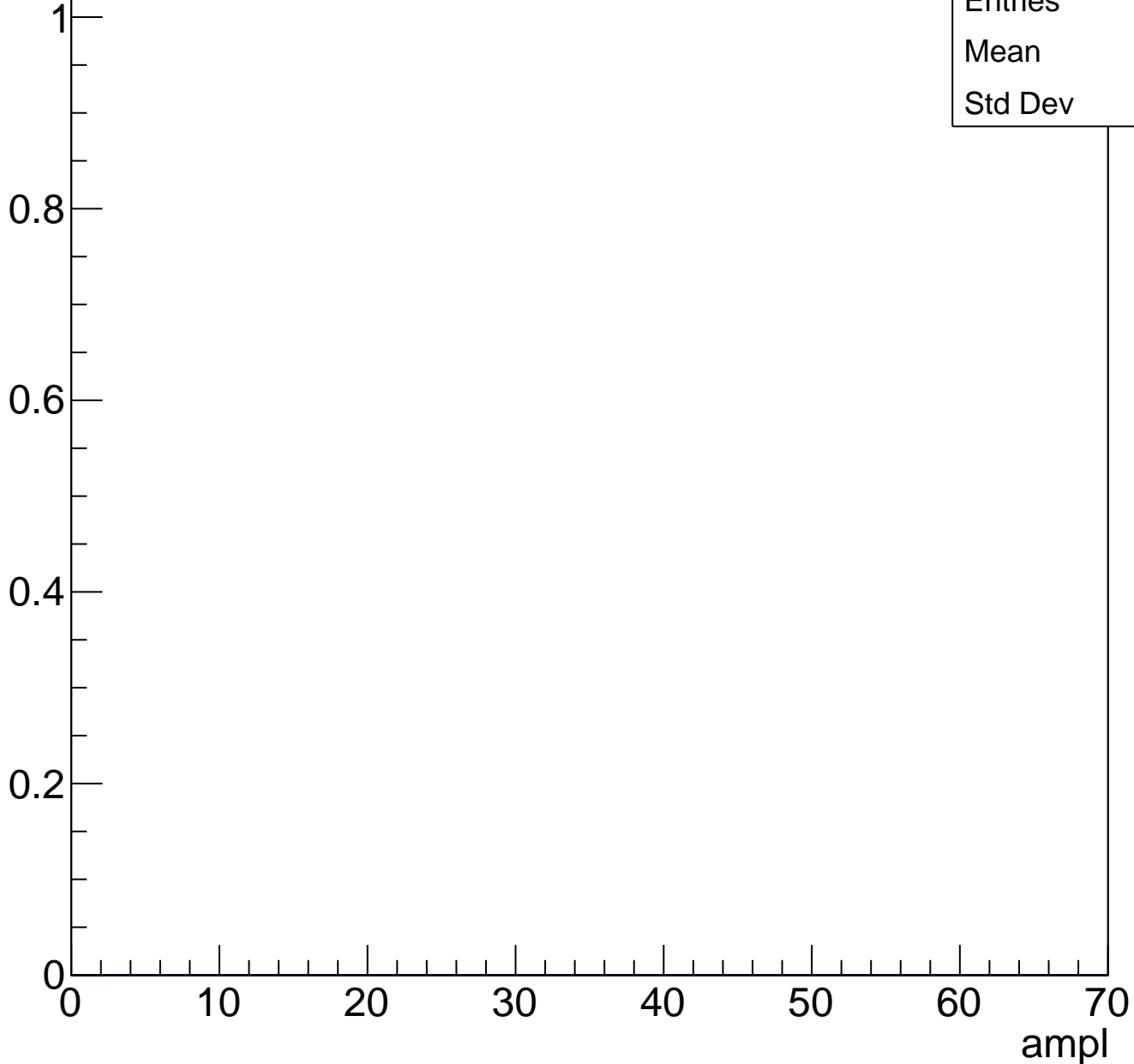


Entries	0
Mean	0
Std Dev	0

B0L101S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0