

B1L001S, U21-ch0, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.33
Std Dev	3.257

Gaus mean : 29.8356

Gaus Width: 3.4850

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

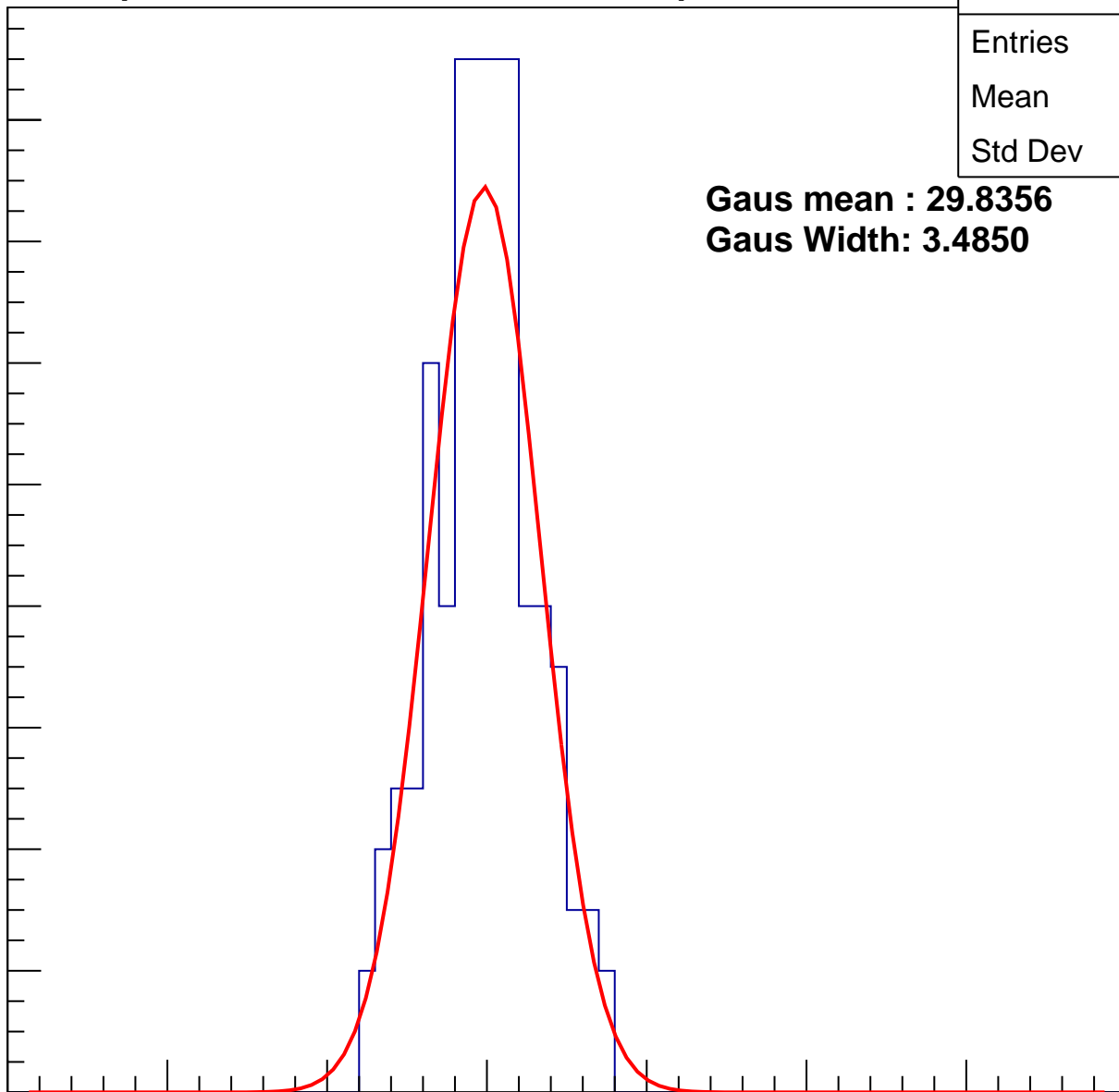
40

50

60

70

ampl



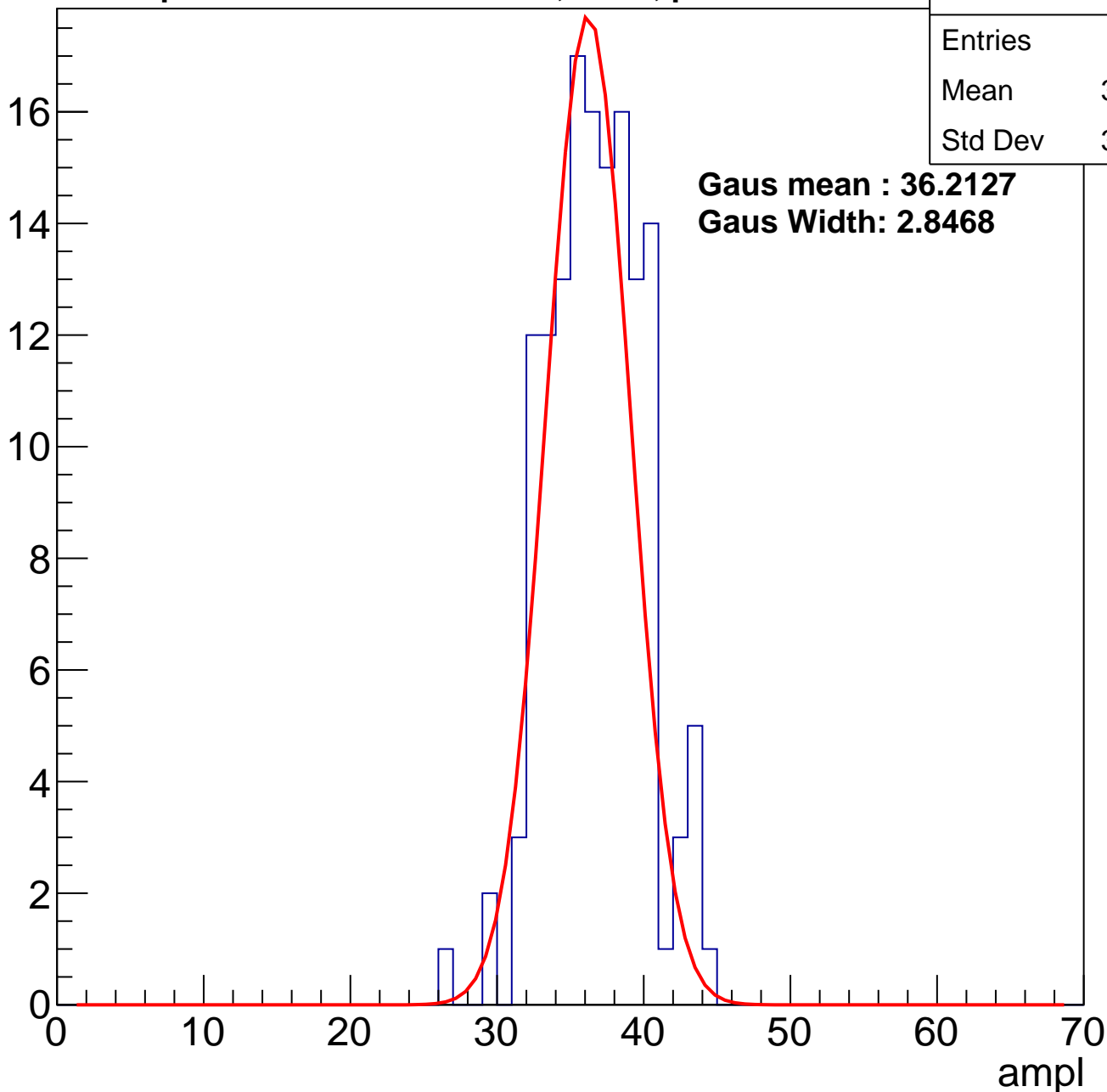
B1L001S, U21-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	36.29
Std Dev	3.219

Gaus mean : 36.2127
Gaus Width: 2.8468

Entry



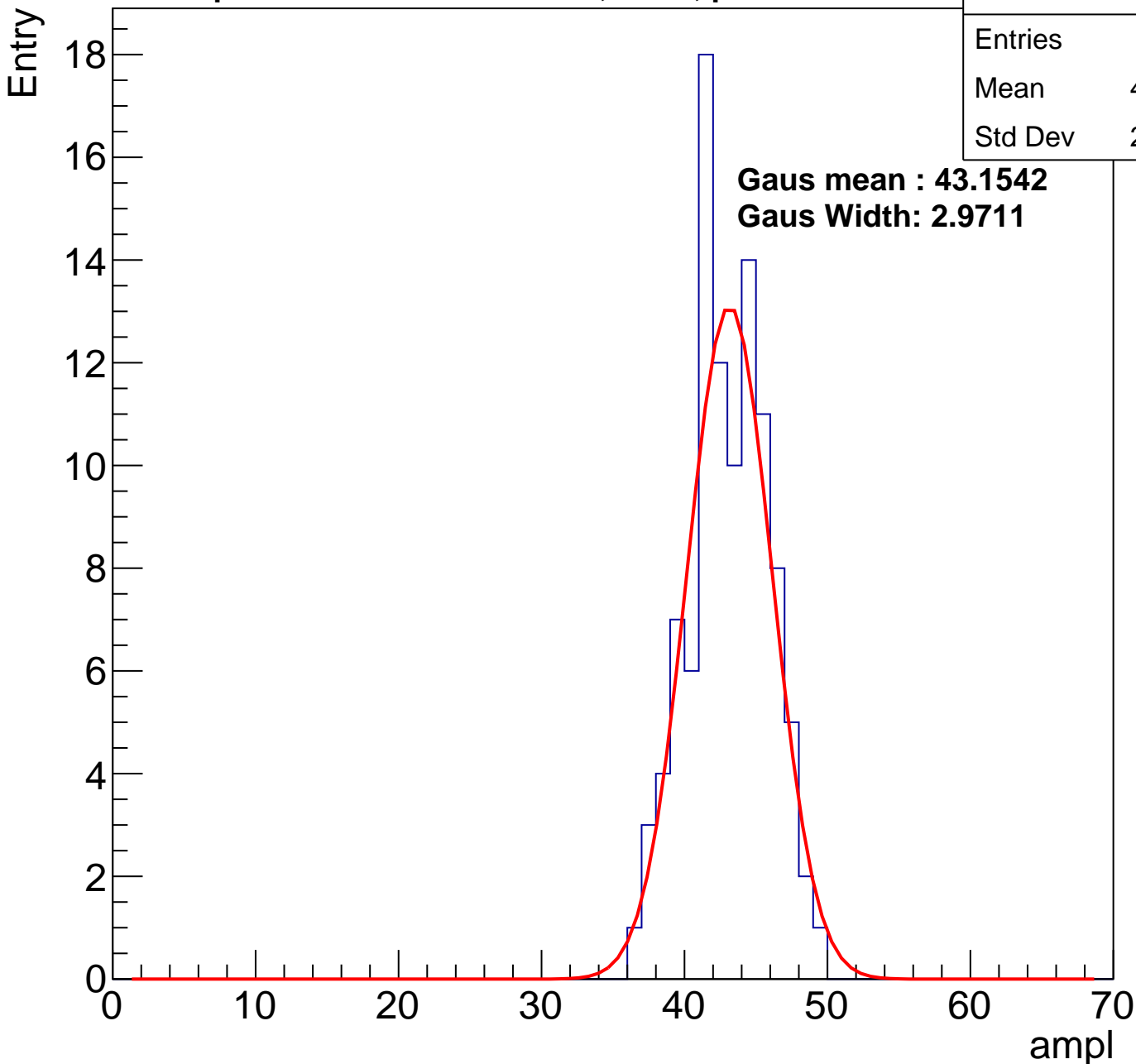
B1L001S, U21-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	42.58
Std Dev	2.774

Gaus mean : 43.1542

Gaus Width: 2.9711



B1L001S, U21-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

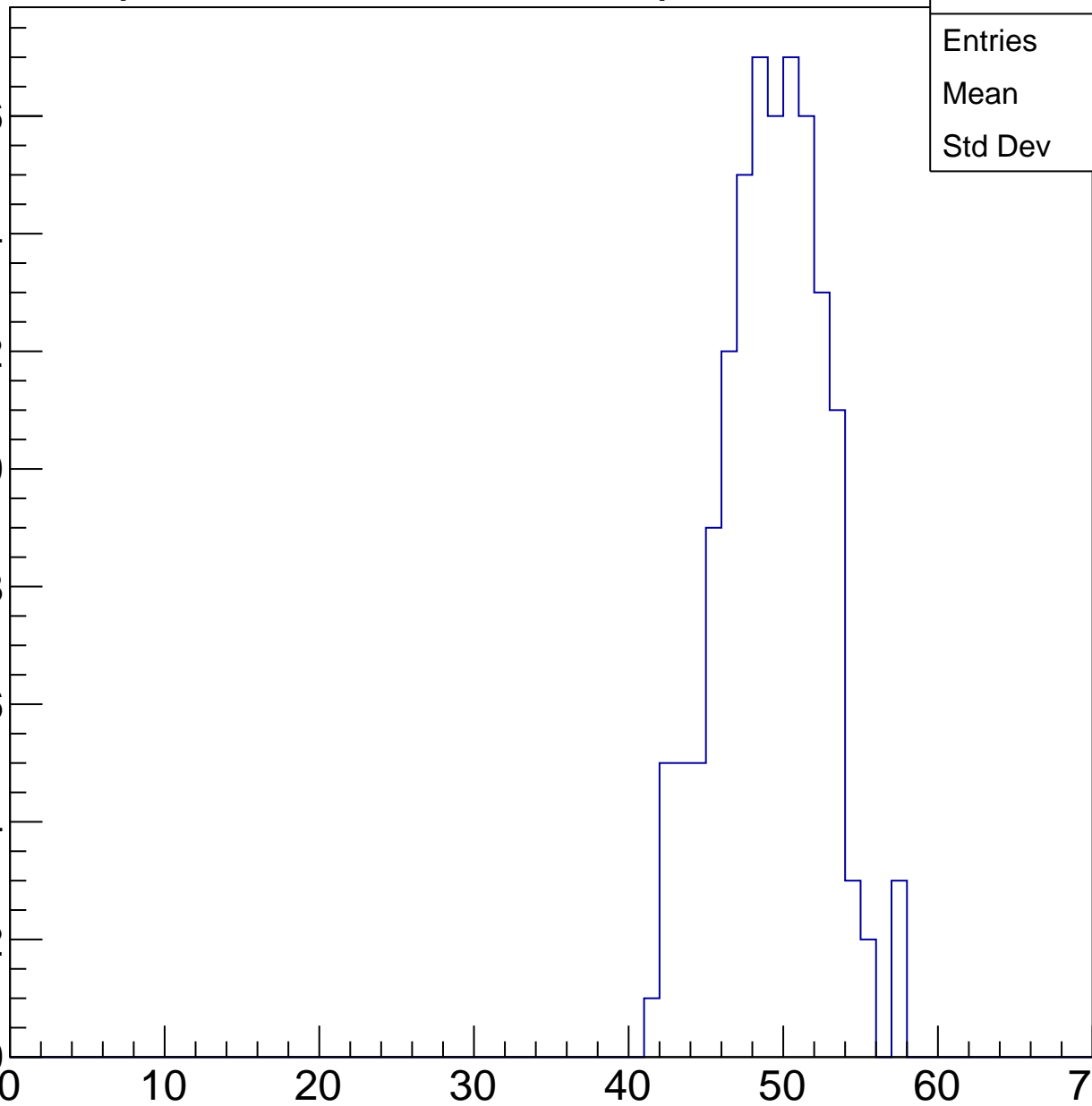
40

50

60

ampl

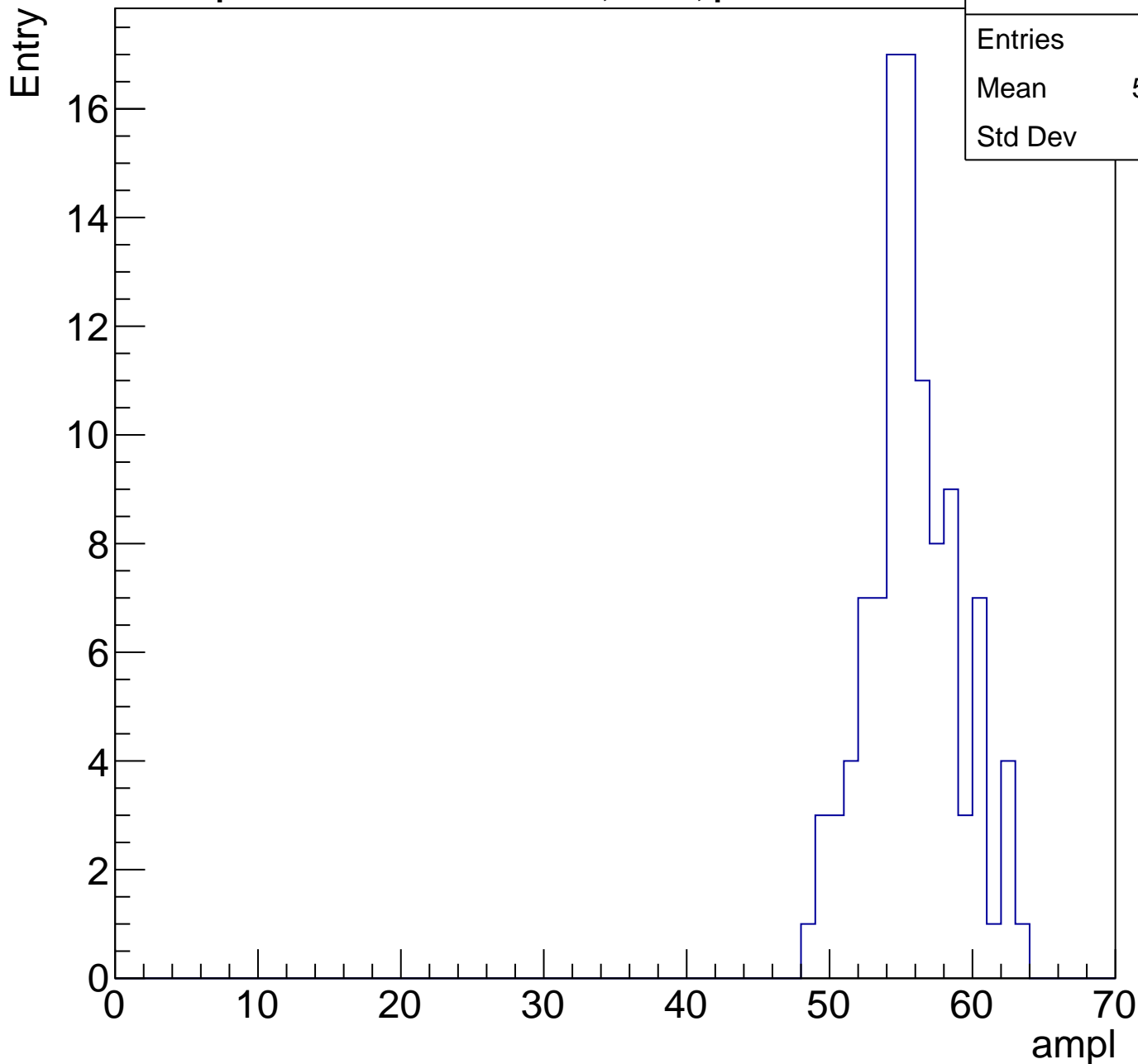
Entries	150
Mean	48.77
Std Dev	3.315



B1L001S, U21-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	55.34
Std Dev	3.17



B1L001S, U21-ch0, adc5

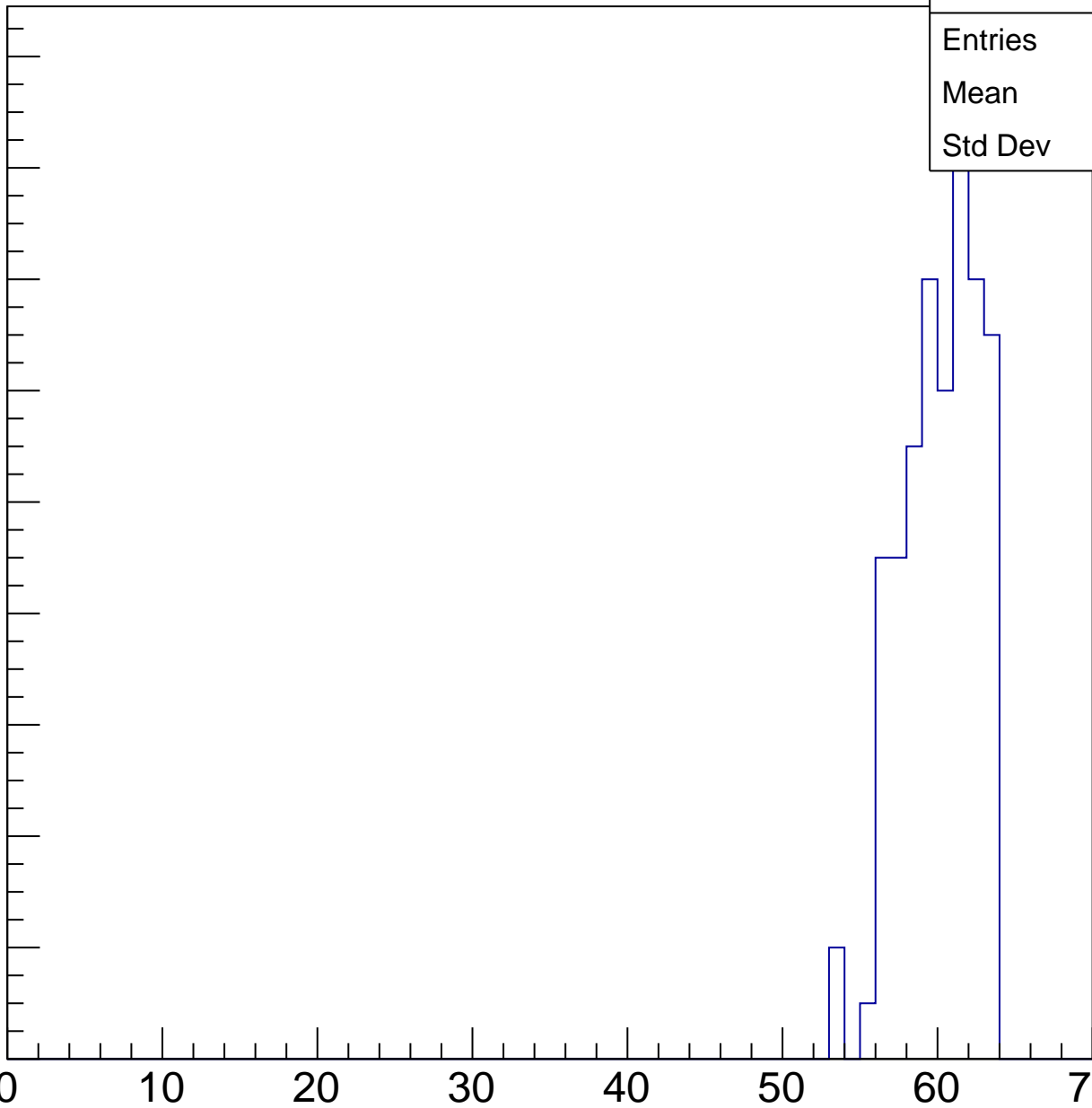
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	59.68
Std Dev	2.386

ampl

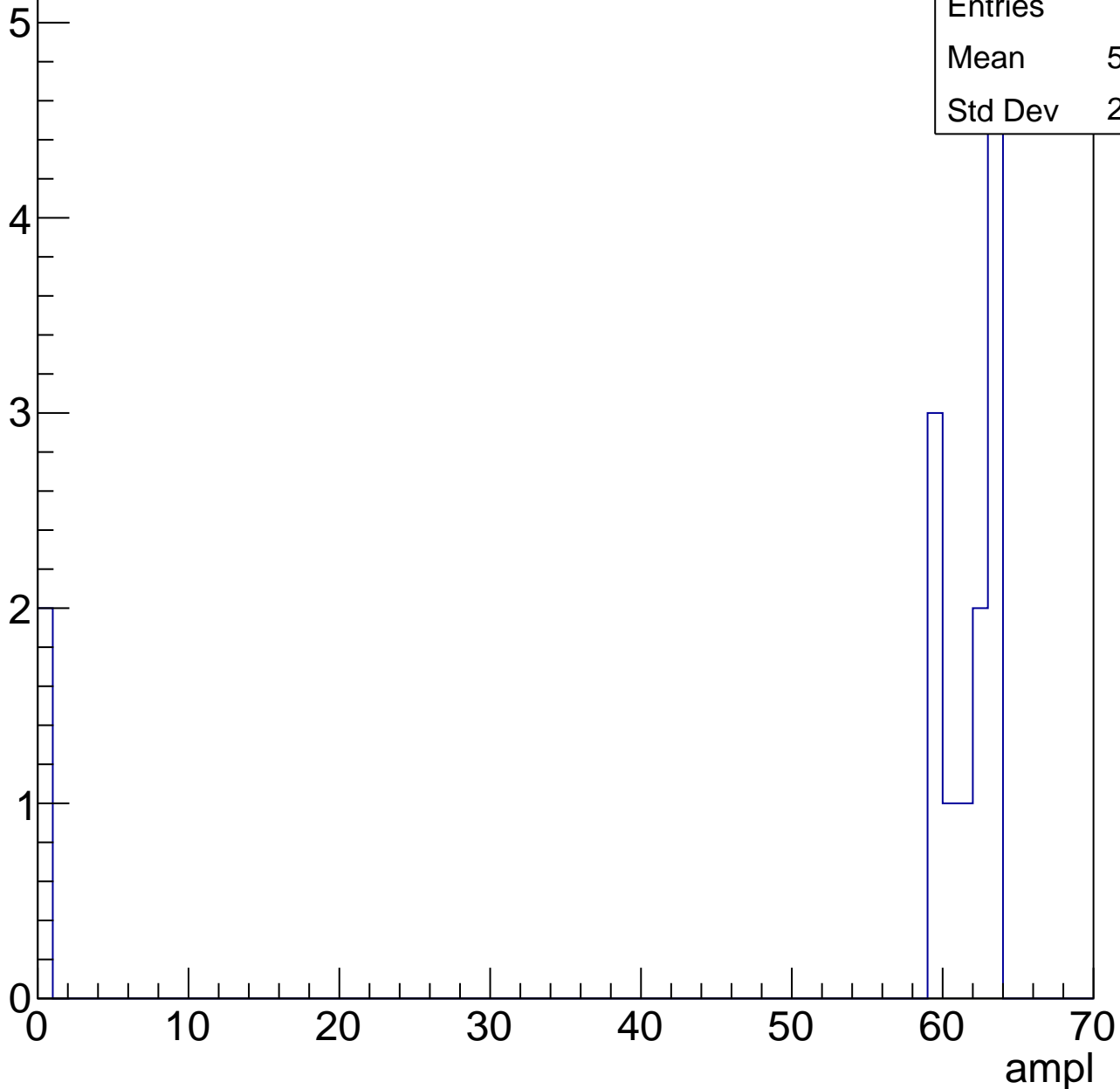


B1L001S, U21-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

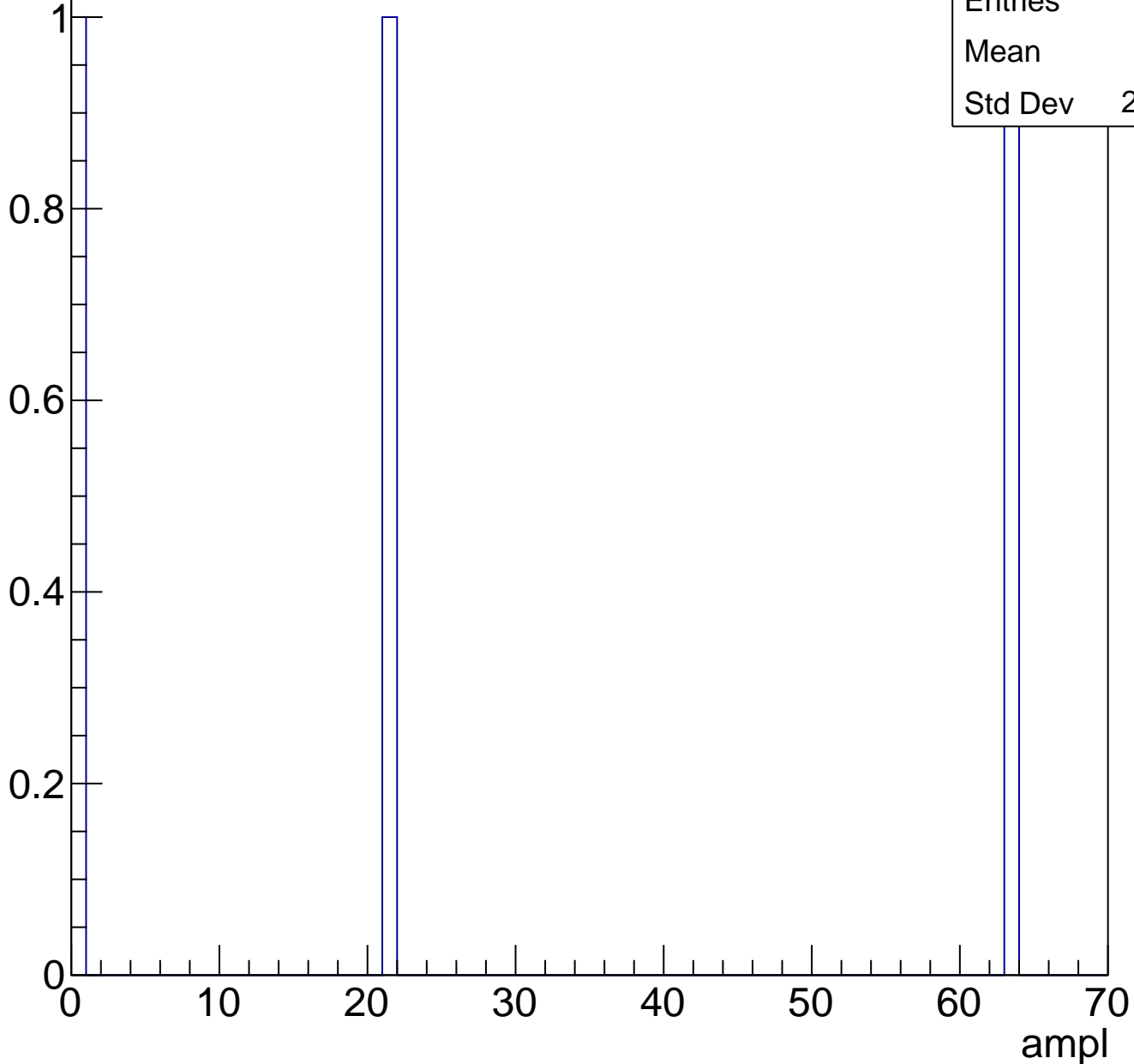
Entries	14
Mean	52.64
Std Dev	21.55



B1L001S, U21-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L001S, U21-ch1, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	31.13
Std Dev	3.231

Gaus mean : 31.3744

Gaus Width: 3.4842

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

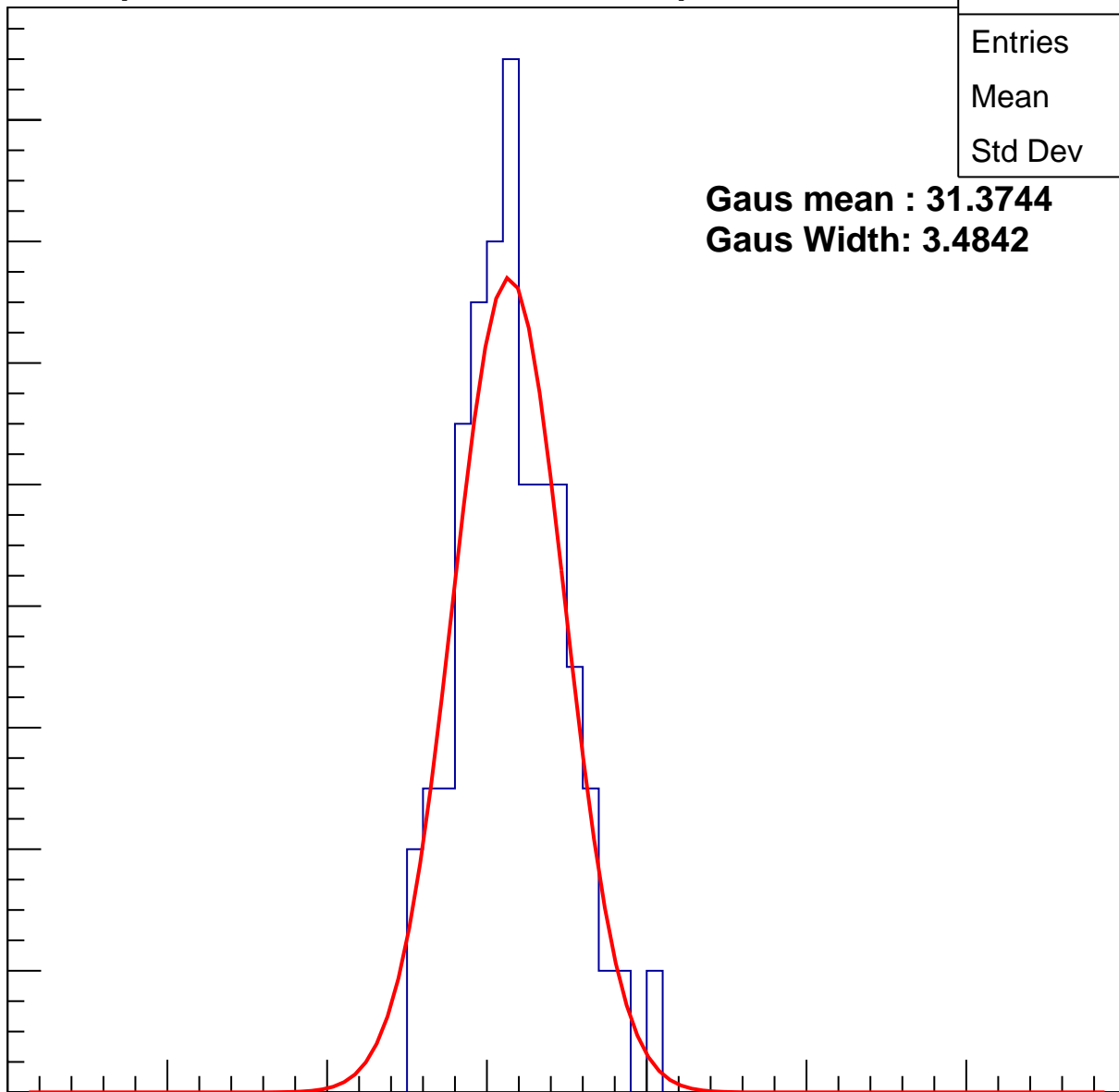
40

50

60

70

ampl



B1L001S, U21-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.21
Std Dev	3.793

Gaus mean : 37.7259

Gaus Width: 3.9567

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

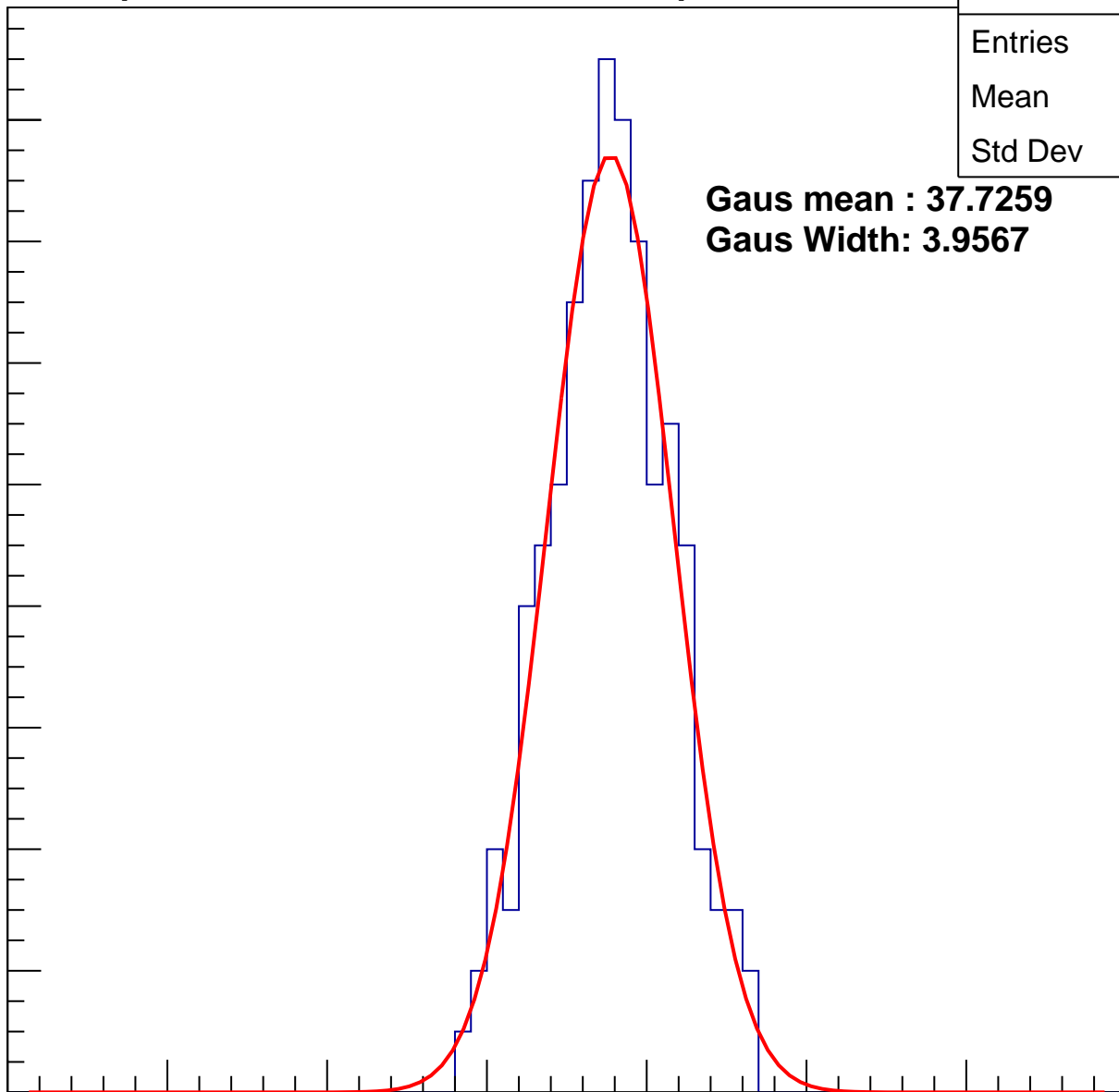
40

50

60

70

ampl



B1L001S, U21-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	44.8
Std Dev	3.508

Gaus mean : 45.1272

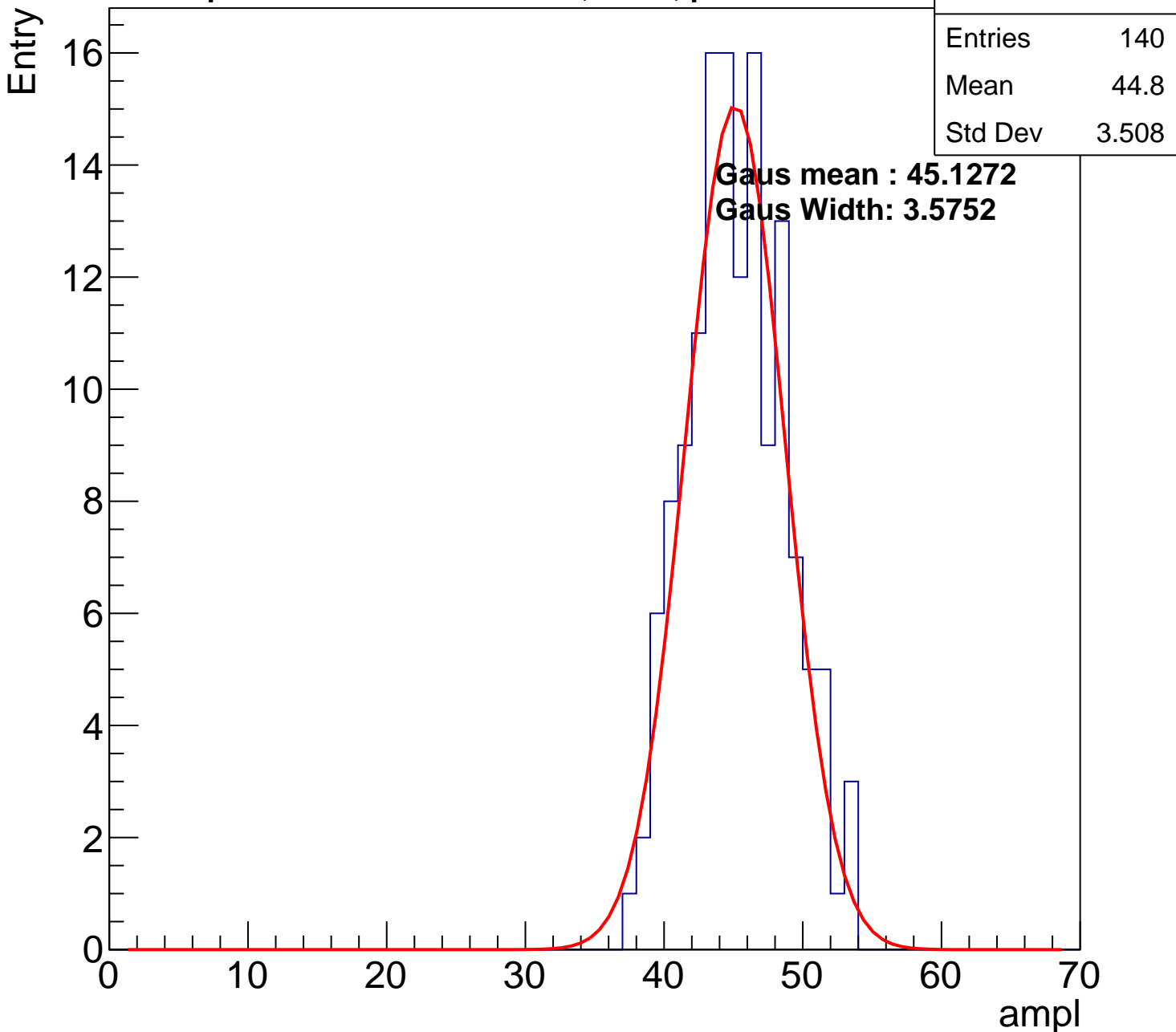
Gaus Width: 3.5752

Entry

16
14
12
10
8
6
4
2
0

ampl

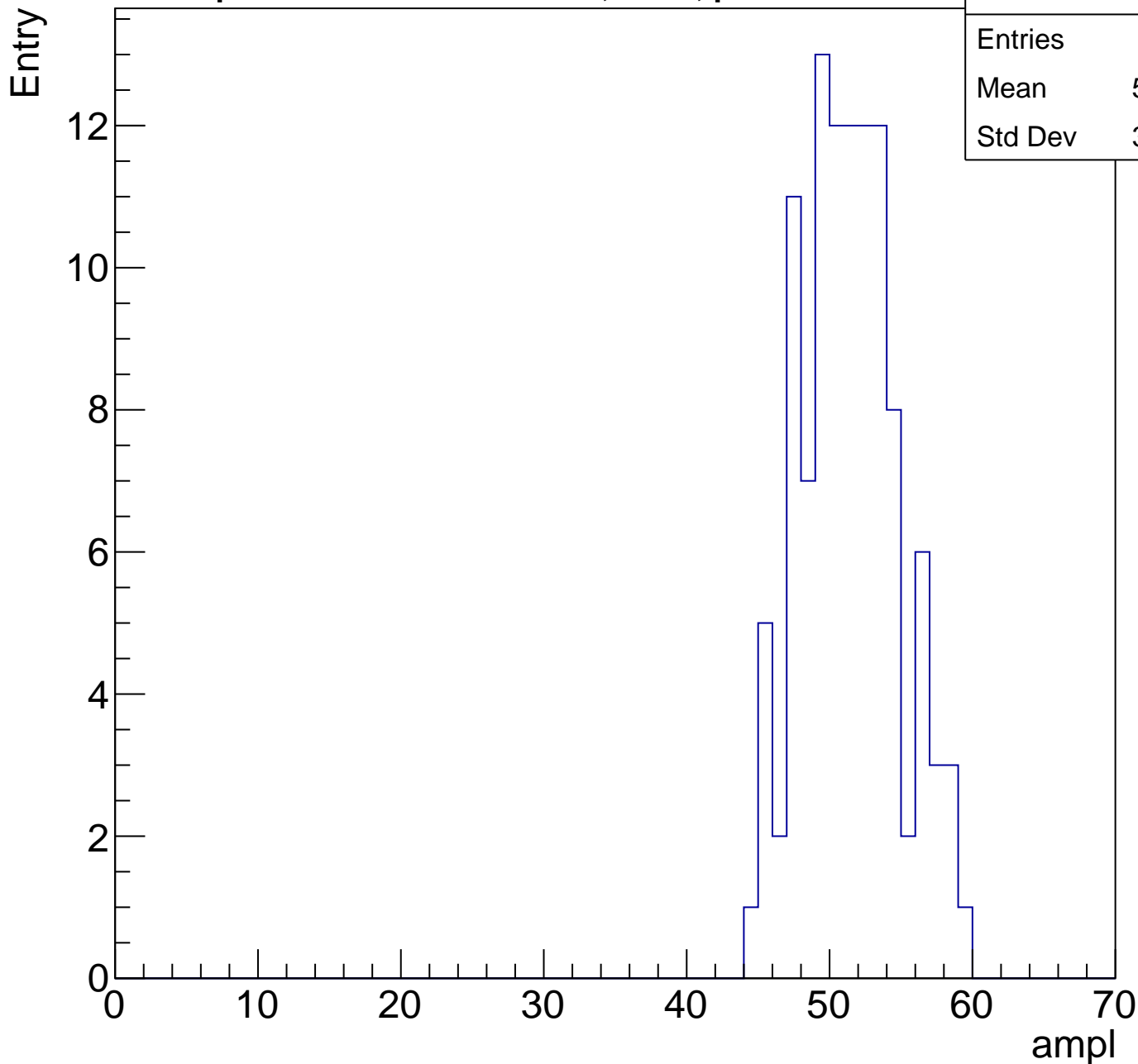
0 10 20 30 40 50 60 70



B1L001S, U21-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

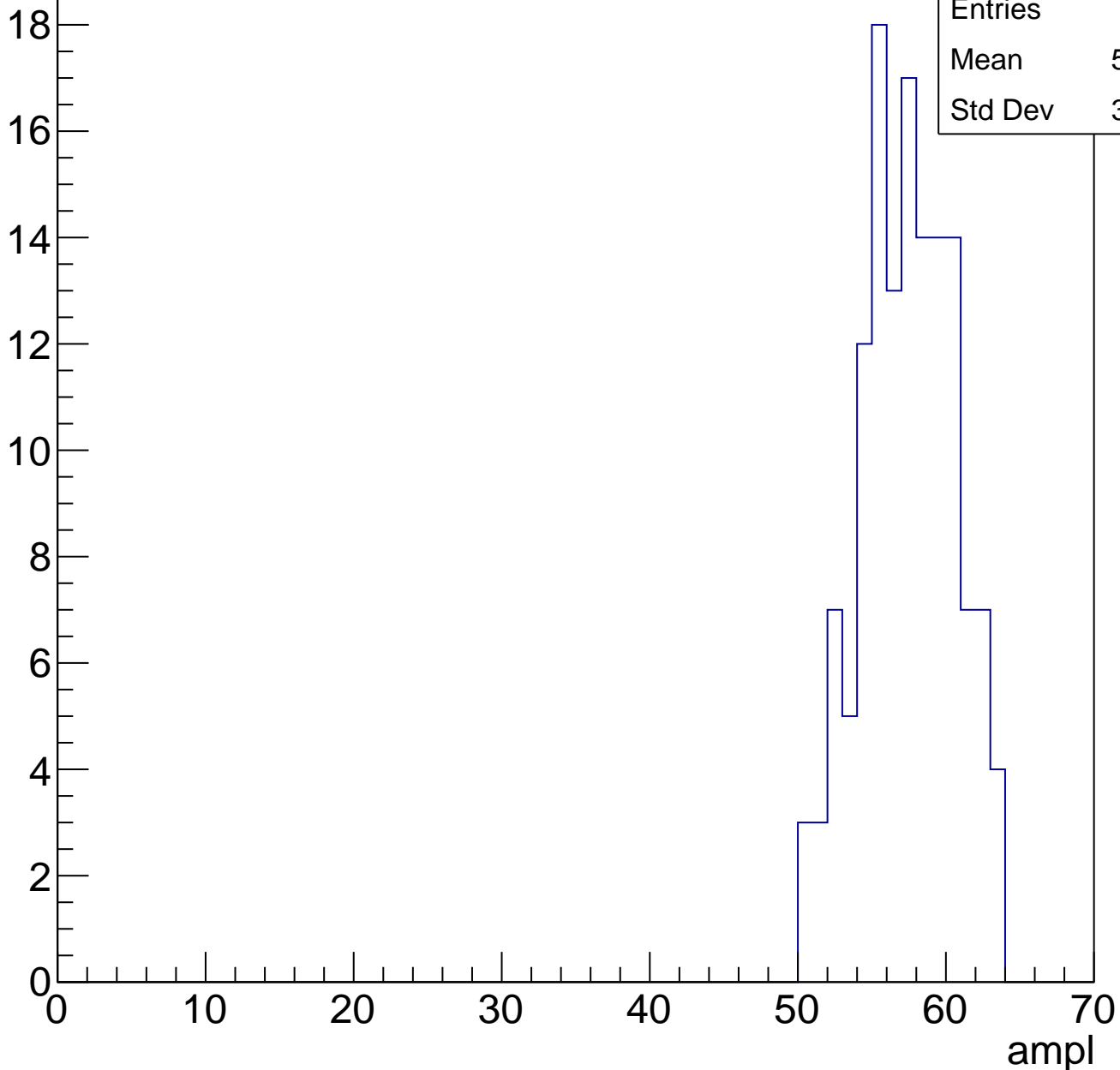
Entries	110
Mean	50.95
Std Dev	3.323



B1L001S, U21-ch1, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	138
Mean	56.94
Std Dev	3.104

B1L001S, U21-ch1, adc5

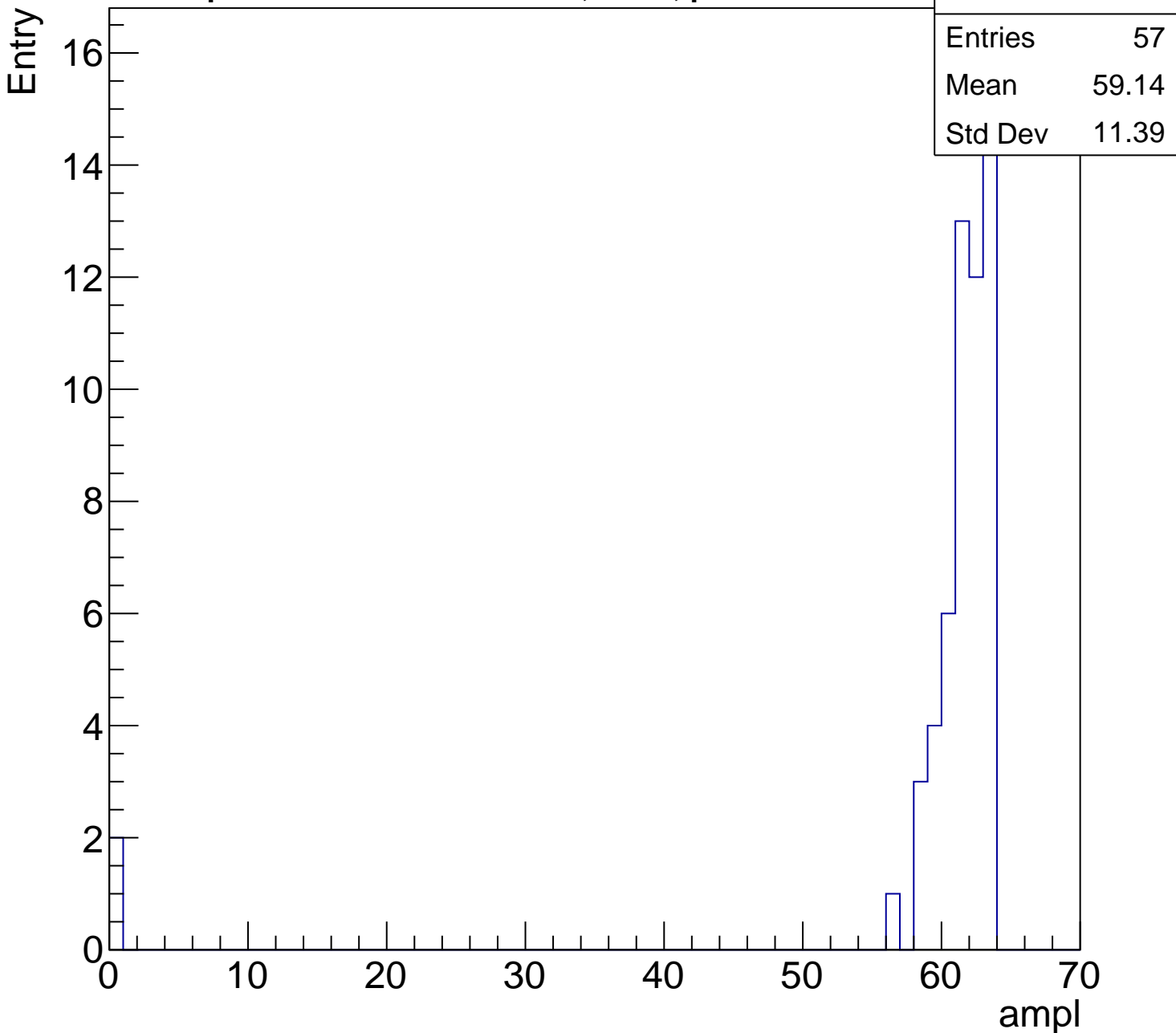
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	57
Mean	59.14
Std Dev	11.39

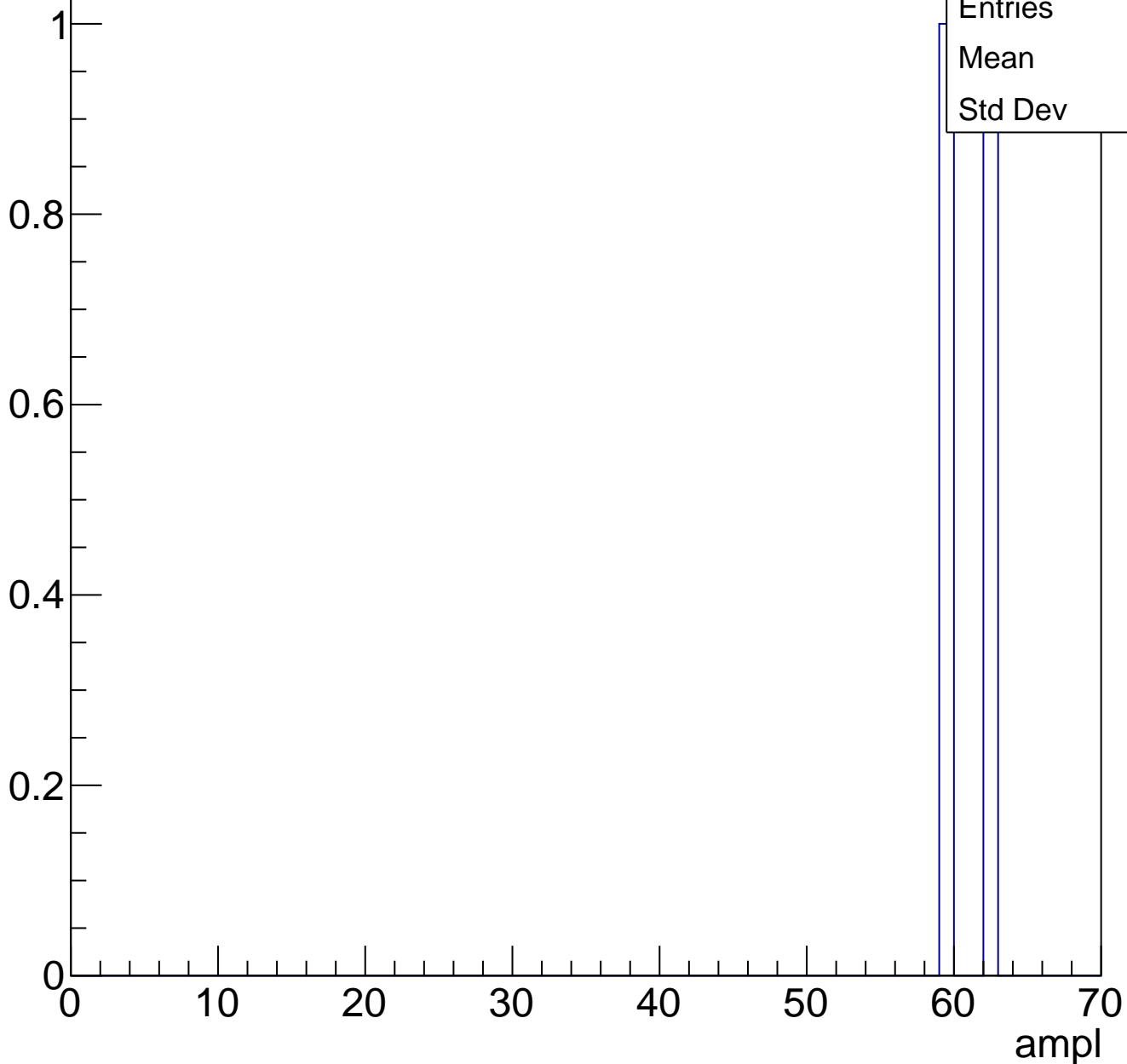
ampl



B1L001S, U21-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

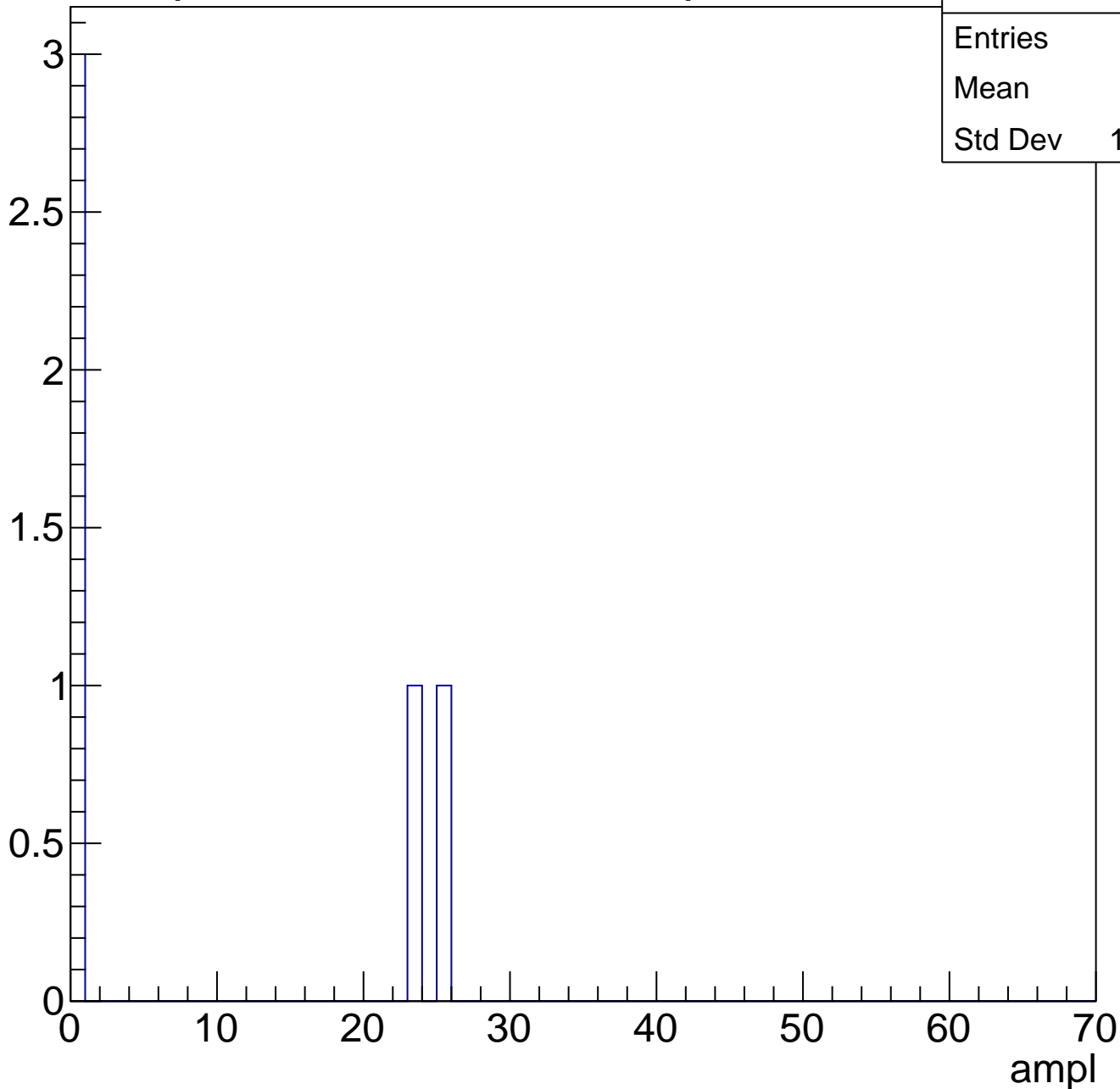
Entry



B1L001S, U21-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	9.6
Std Dev	11.77

B1L001S, U21-ch2, adc0

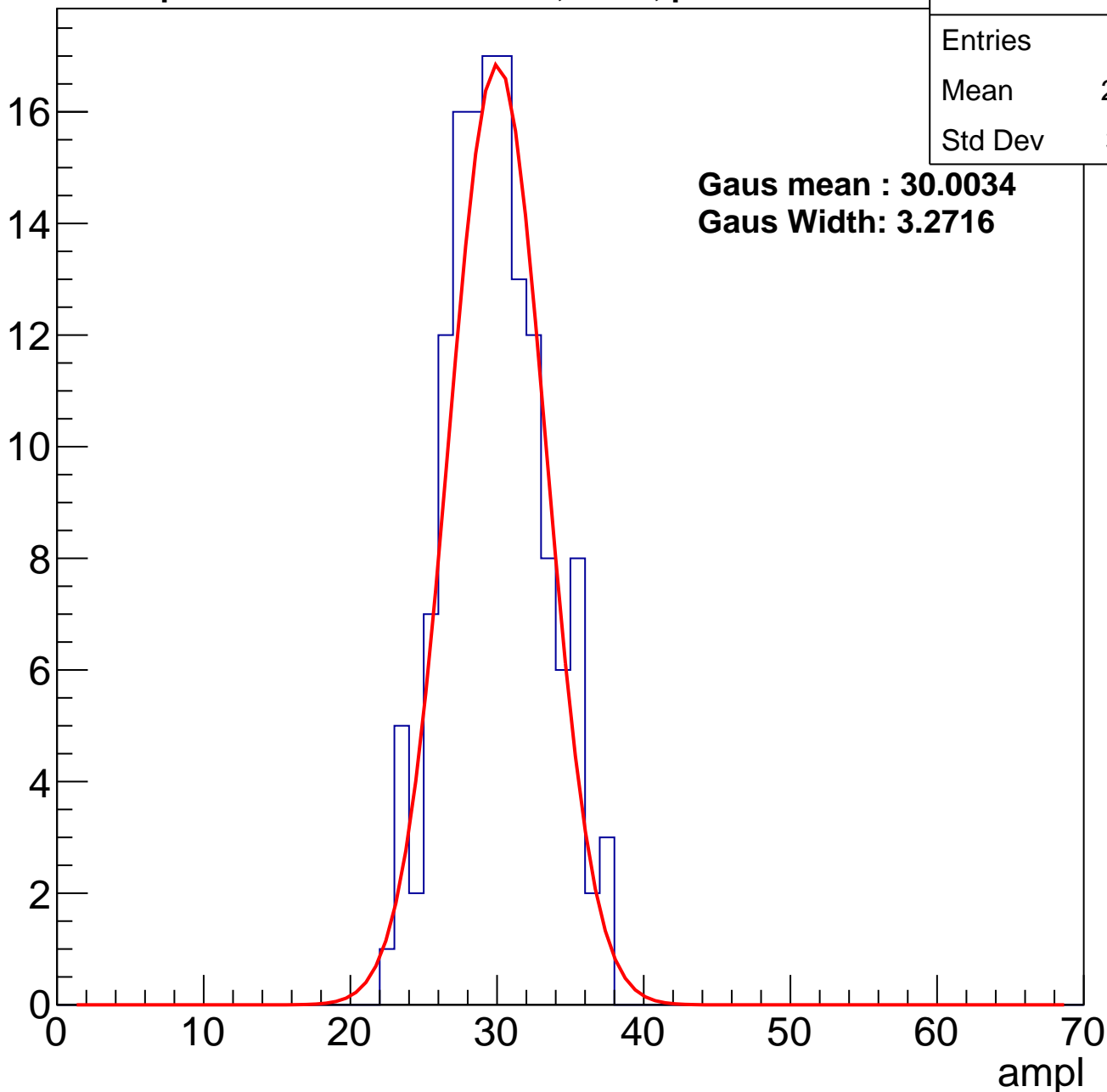
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.47
Std Dev	3.301

Gaus mean : 30.0034

Gaus Width: 3.2716

Entry



B1L001S, U21-ch2, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	35.95
Std Dev	3.223

Gaus mean : 36.2917
Gaus Width: 3.0784

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

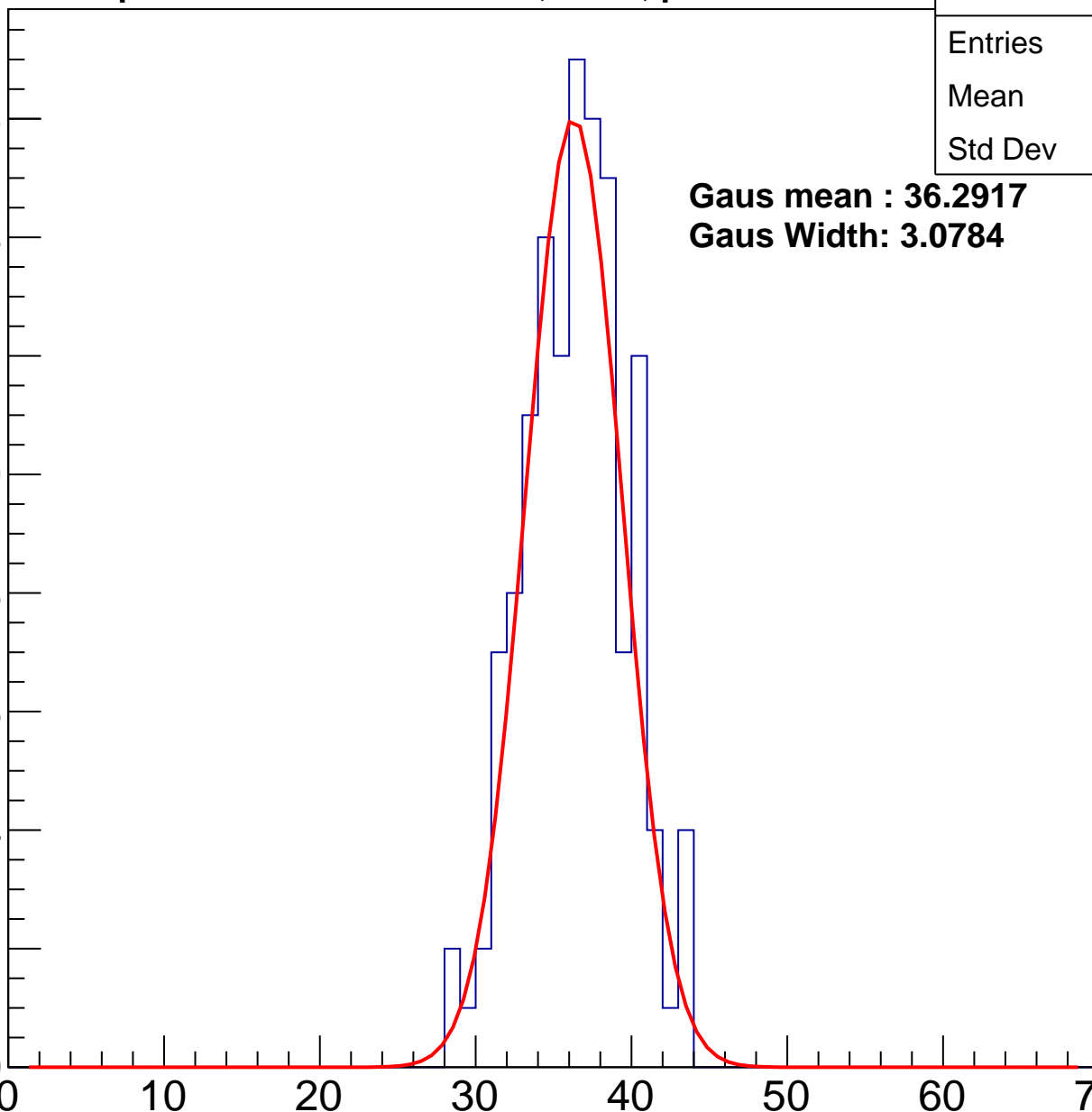
40

50

60

70

ampl



B1L001S, U21-ch2, adc2

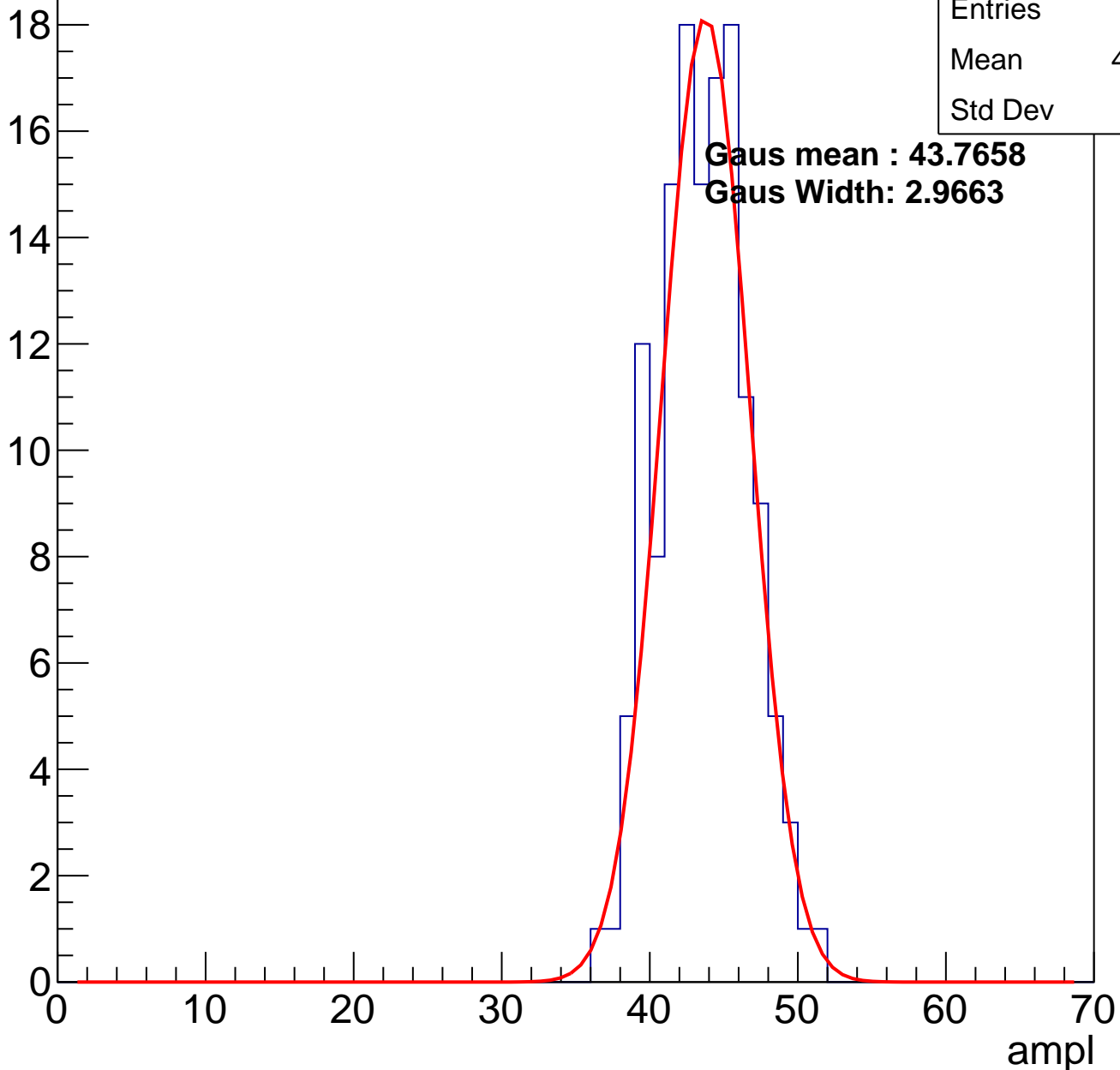
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	43.16
Std Dev	2.96

Gaus mean : 43.7658

Gaus Width: 2.9663

Entry



B1L001S, U21-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	89
Mean	48.97
Std Dev	3.213

Entry

12

10

8

6

4

2

0

0

10

20

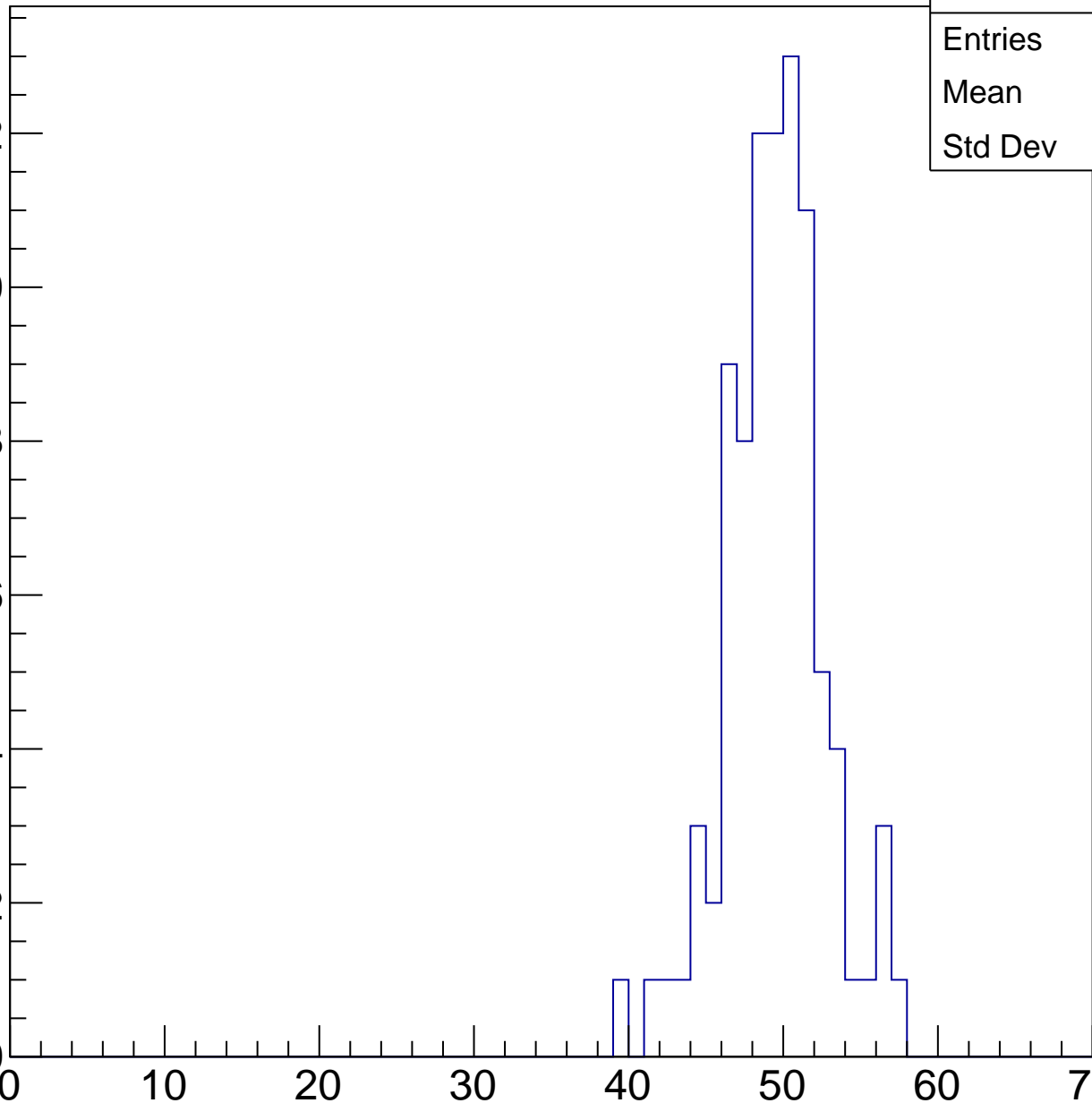
30

40

50

60

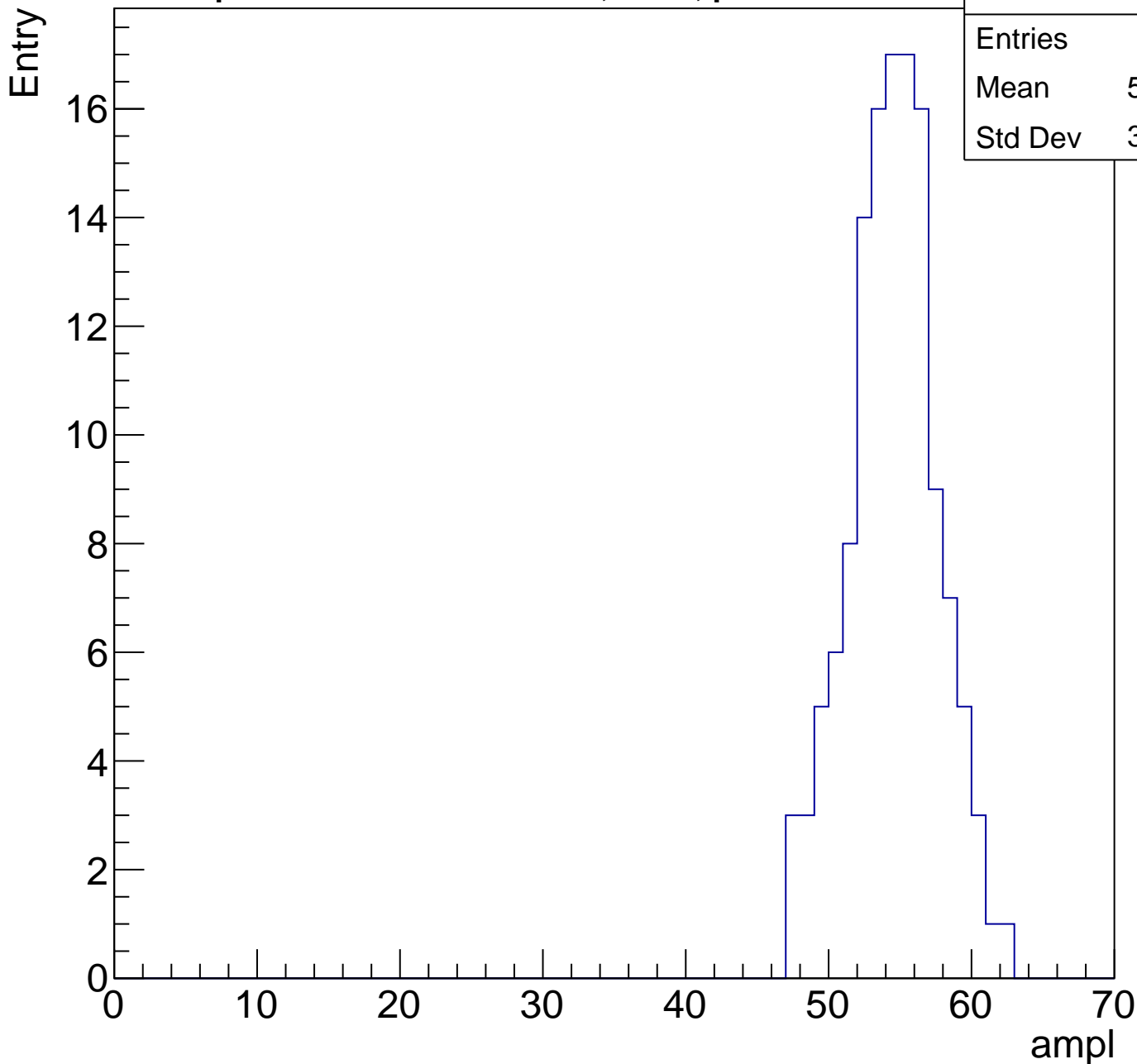
ampl



B1L001S, U21-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	54.05
Std Dev	3.076



B1L001S, U21-ch2, adc5

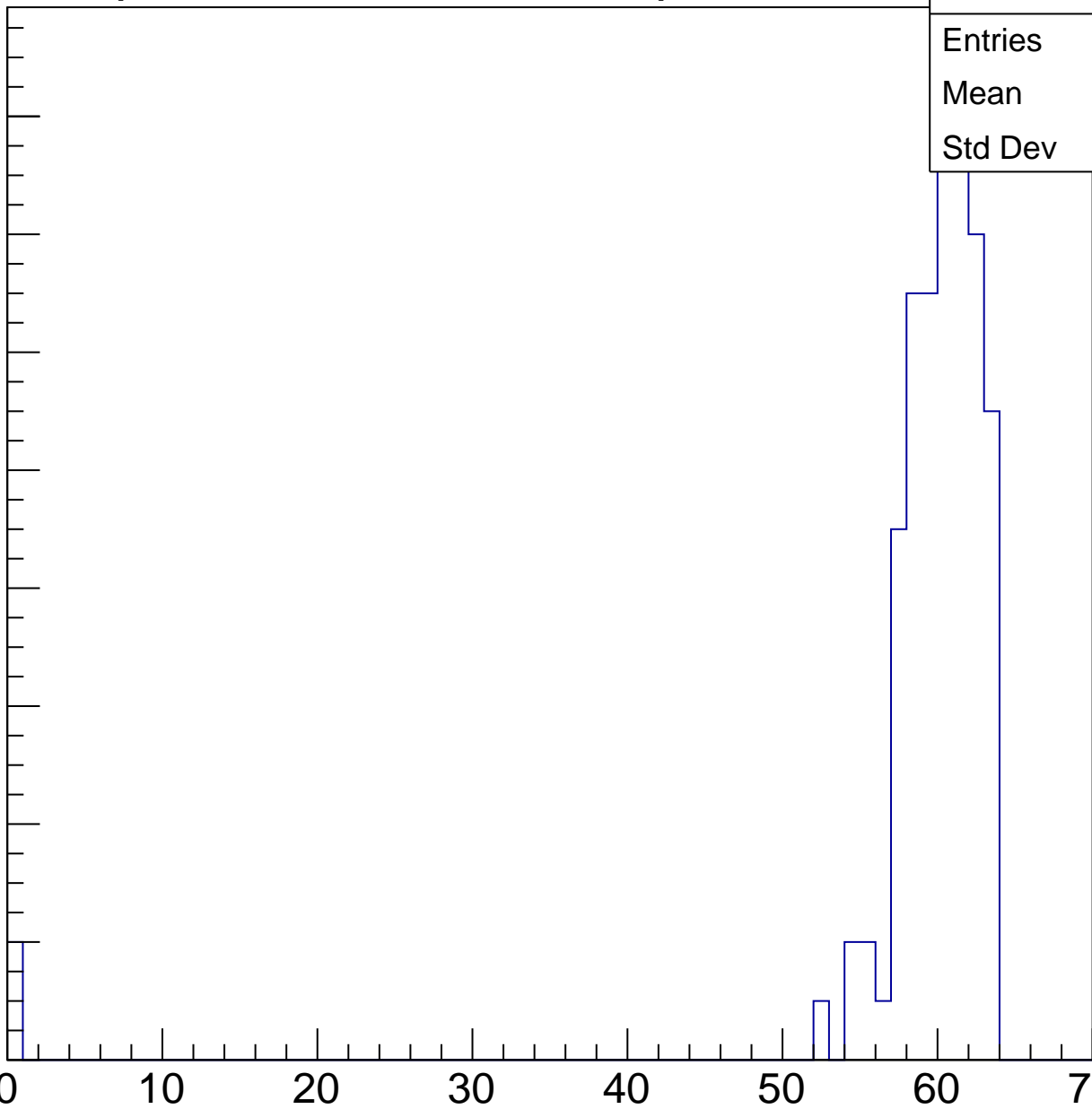
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	58.59
Std Dev	8.629

ampl

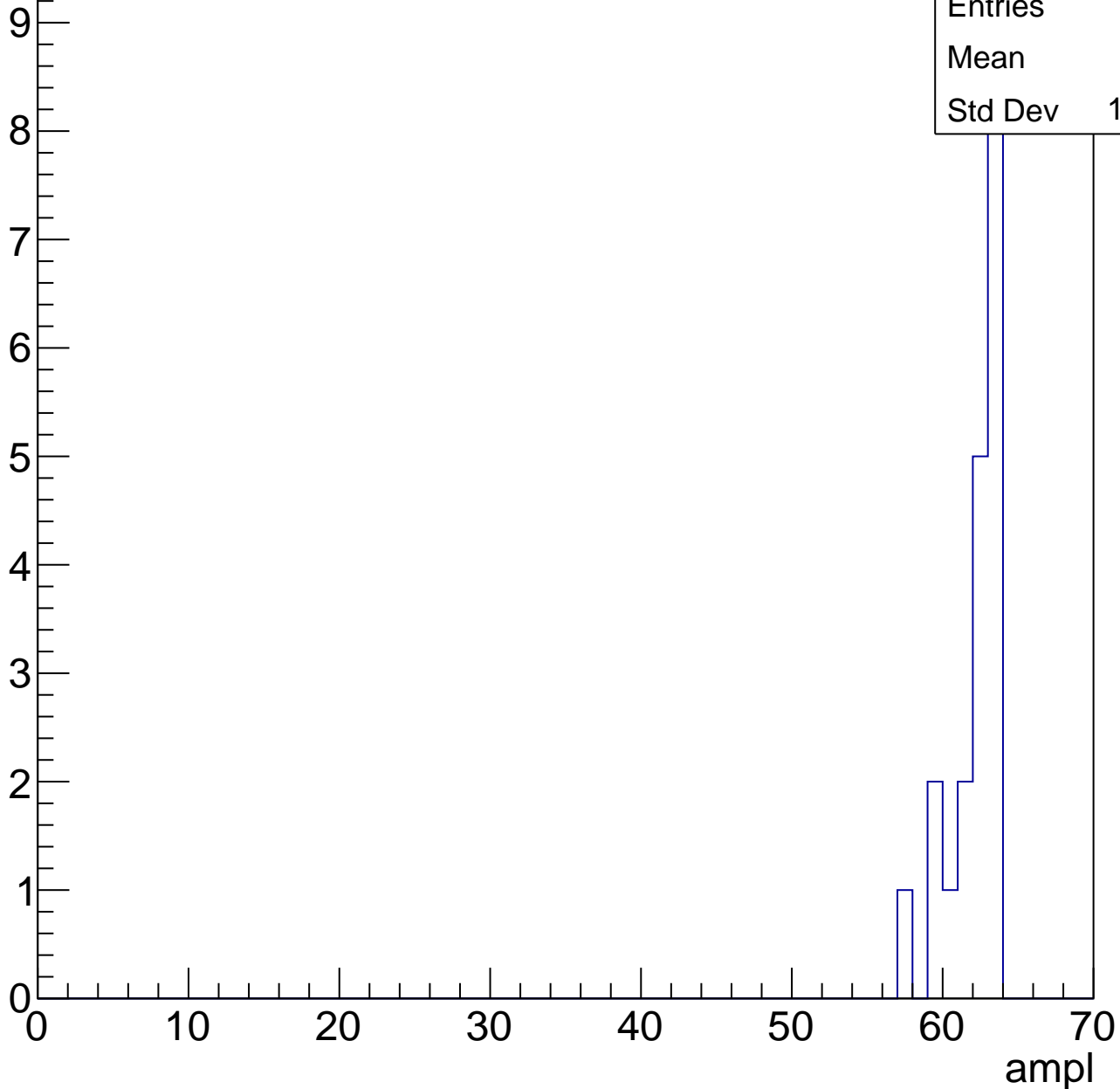


B1L001S, U21-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	61.7
Std Dev	1.676



B1L001S, U21-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L001S, U21-ch3, adc0

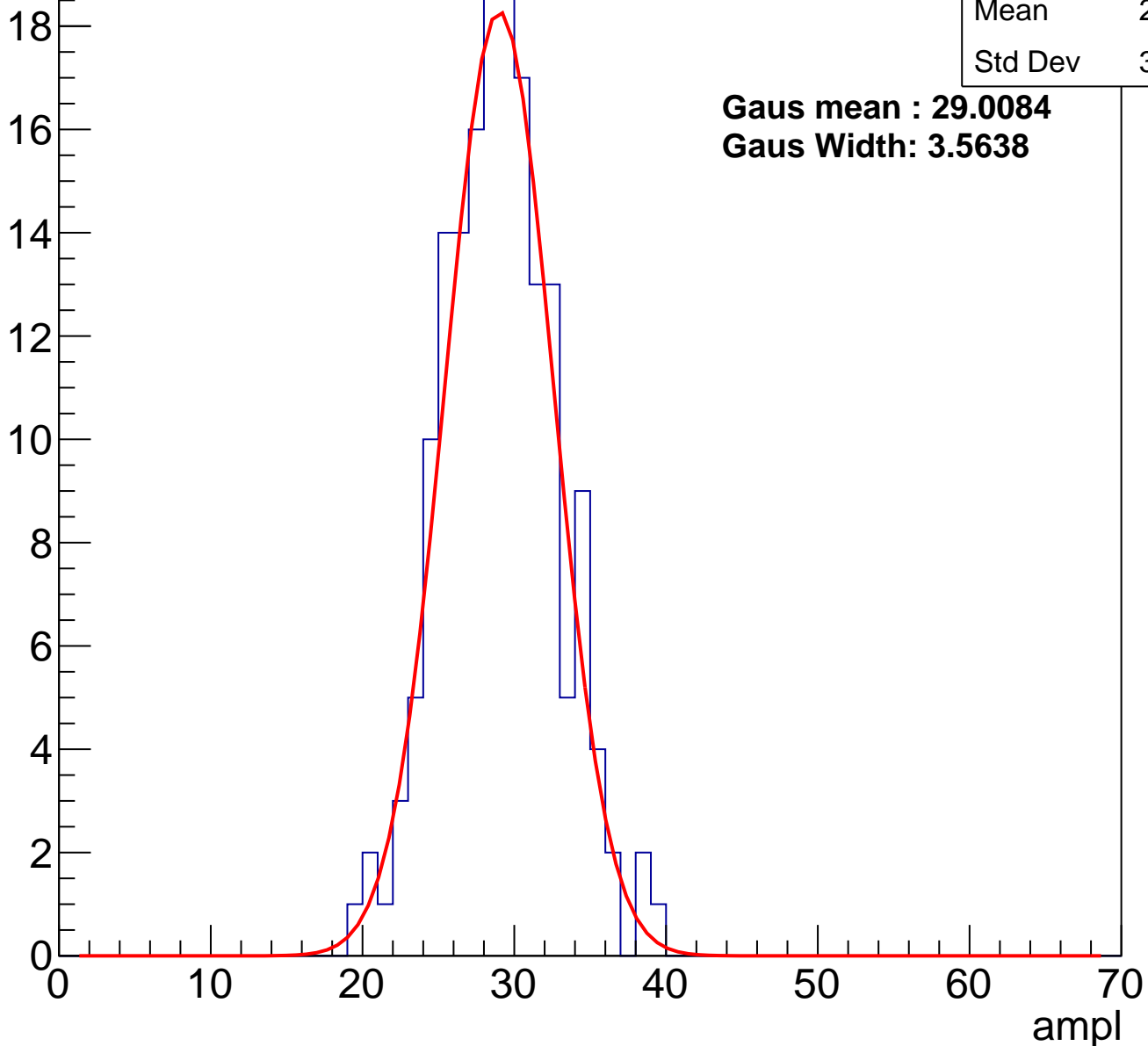
calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	28.57
Std Dev	3.678

Gaus mean : 29.0084

Gaus Width: 3.5638

Entry



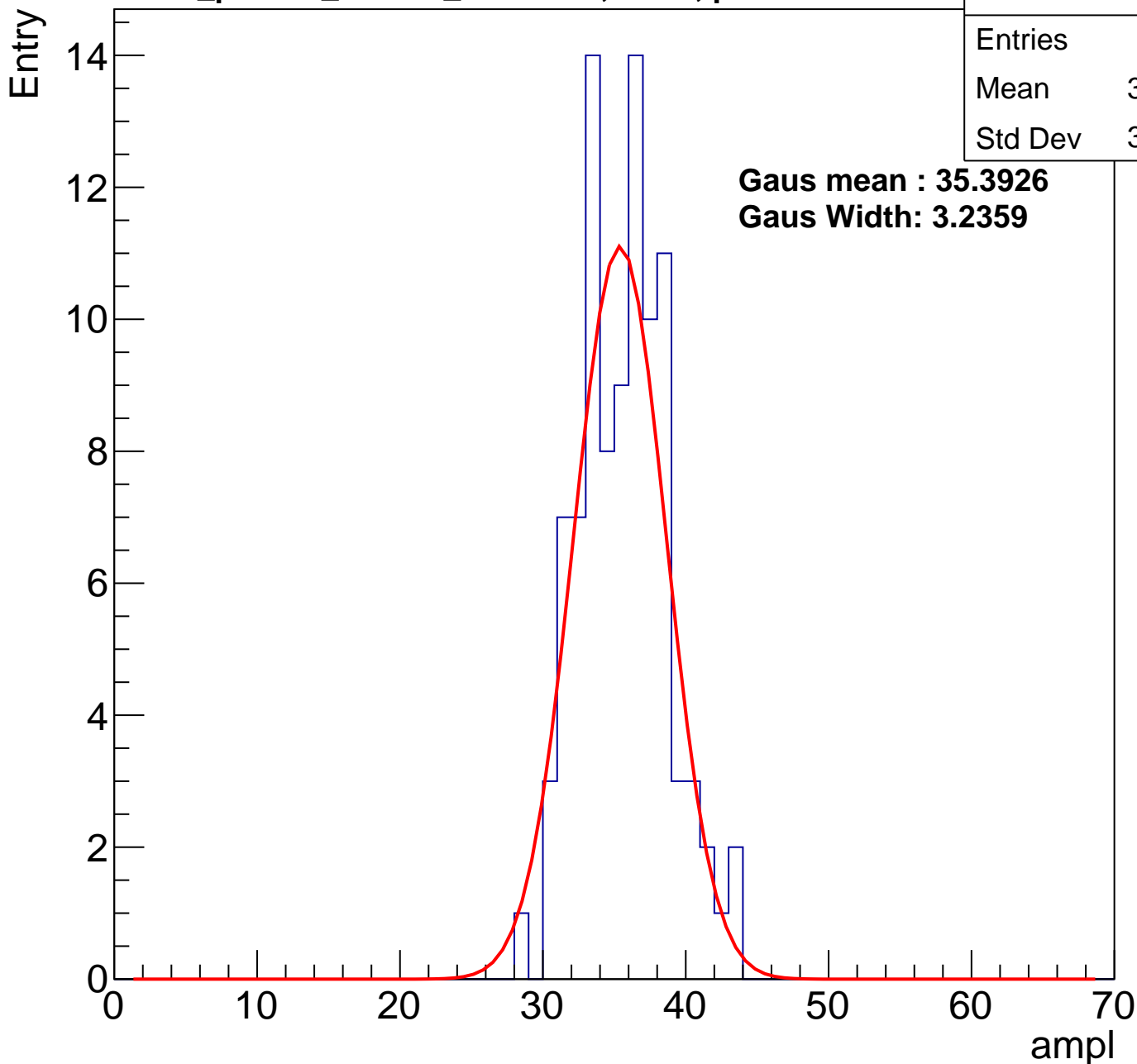
B1L001S, U21-ch3, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	95
Mean	35.23
Std Dev	3.049

Gaus mean : 35.3926

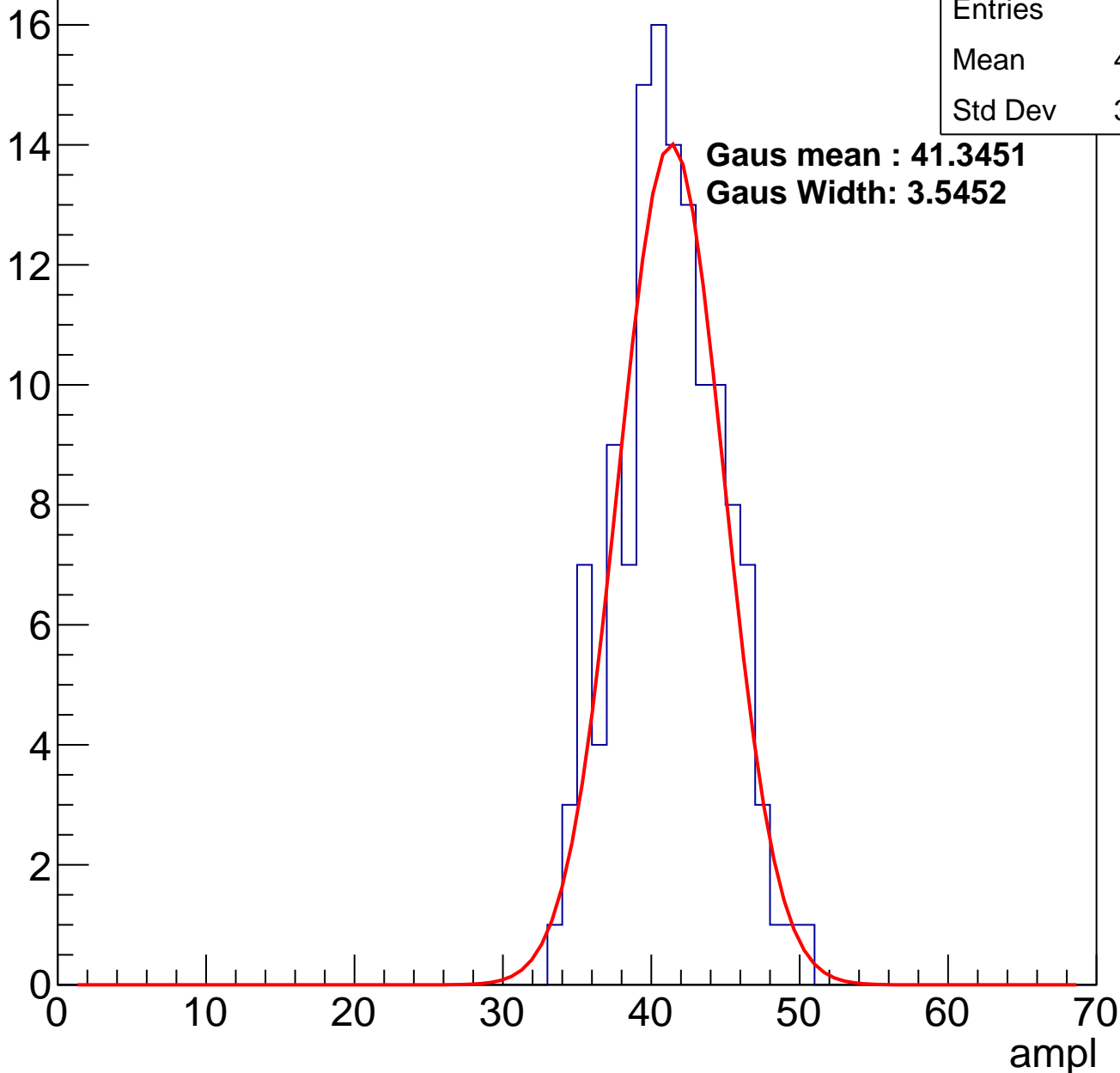
Gaus Width: 3.2359



B1L001S, U21-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

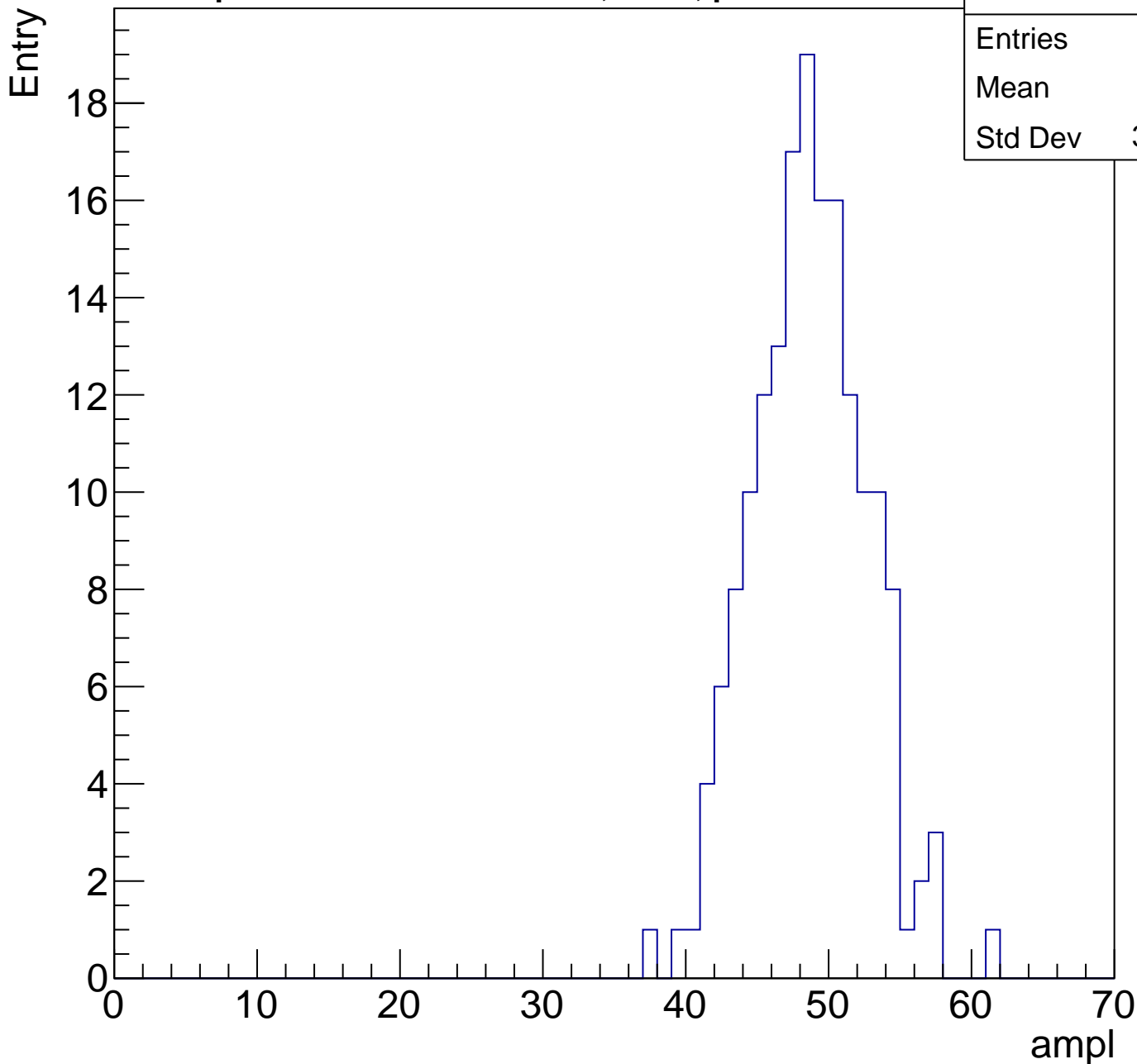
Entry



B1L001S, U21-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

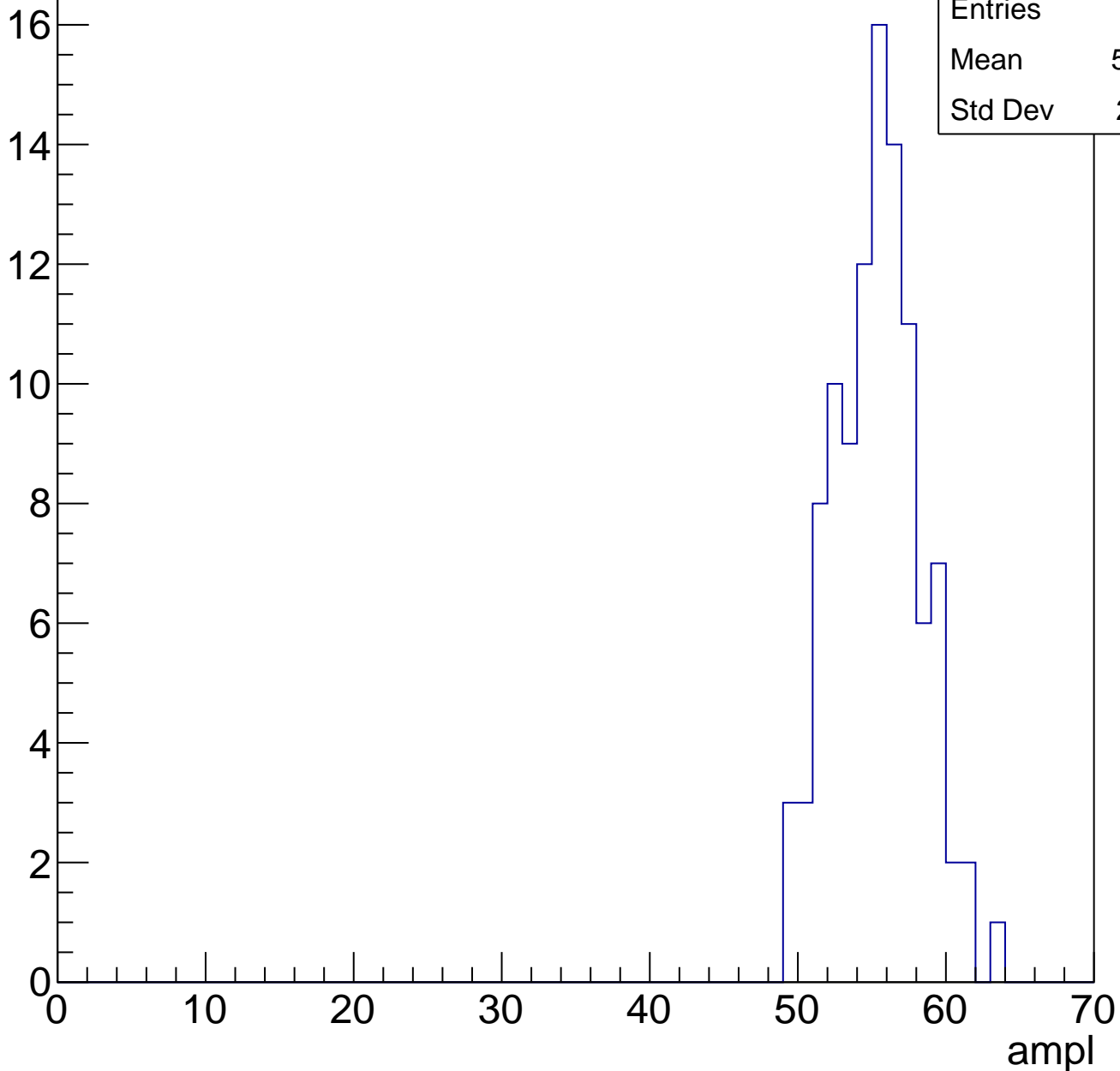
Entries	171
Mean	48.2
Std Dev	3.961



B1L001S, U21-ch3, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch3, adc5

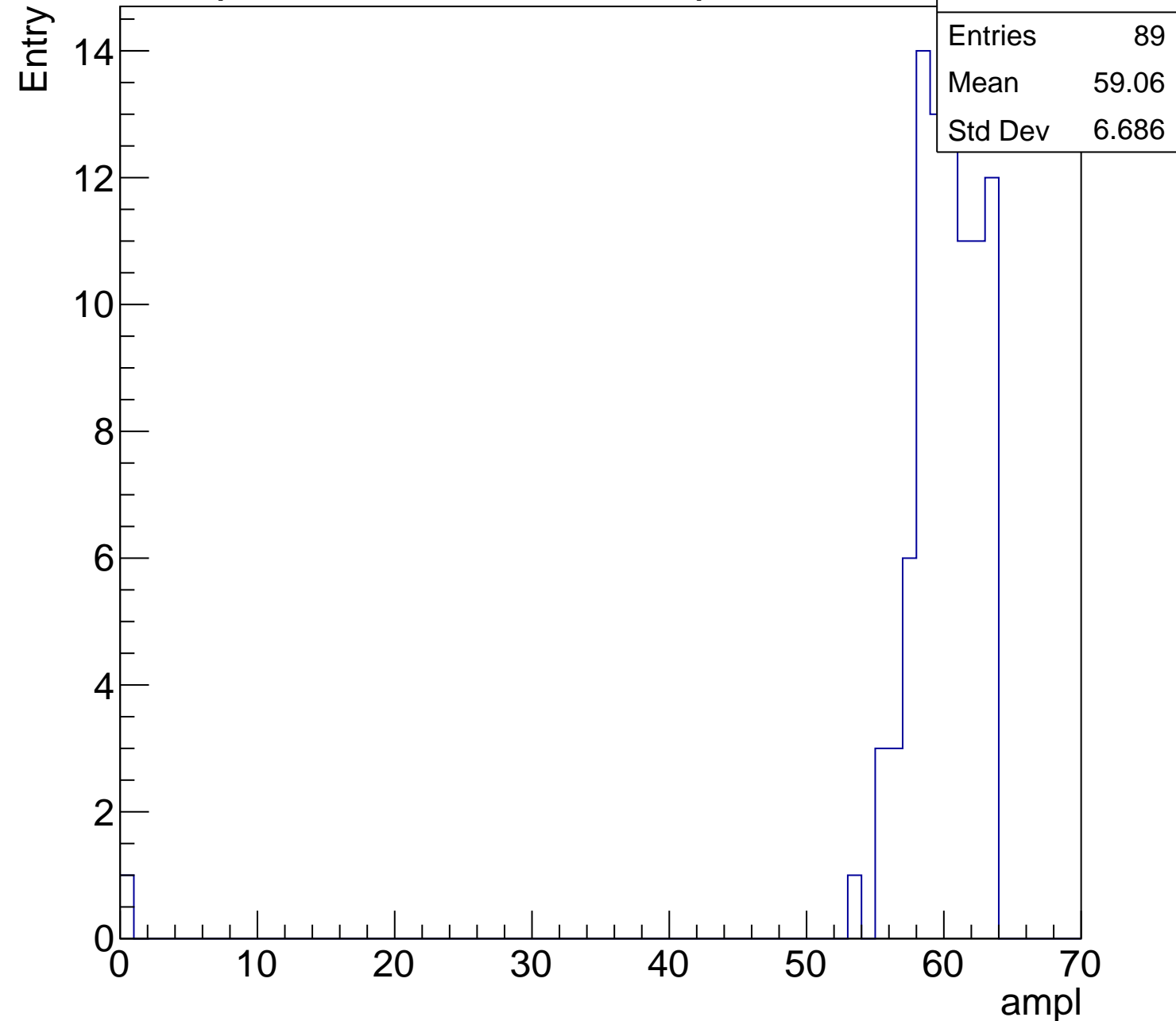
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	59.06
Std Dev	6.686

ampl

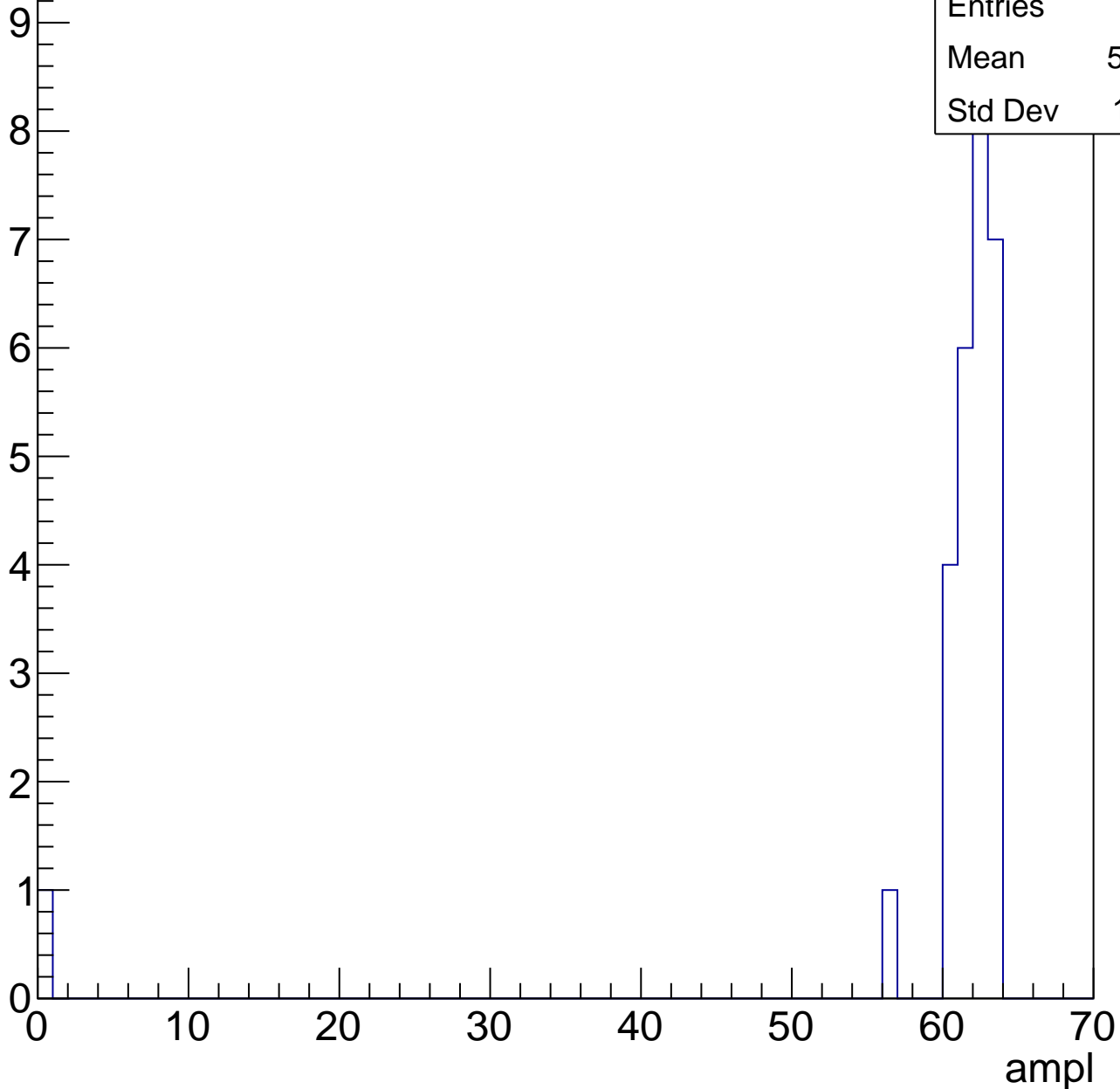


B1L001S, U21-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

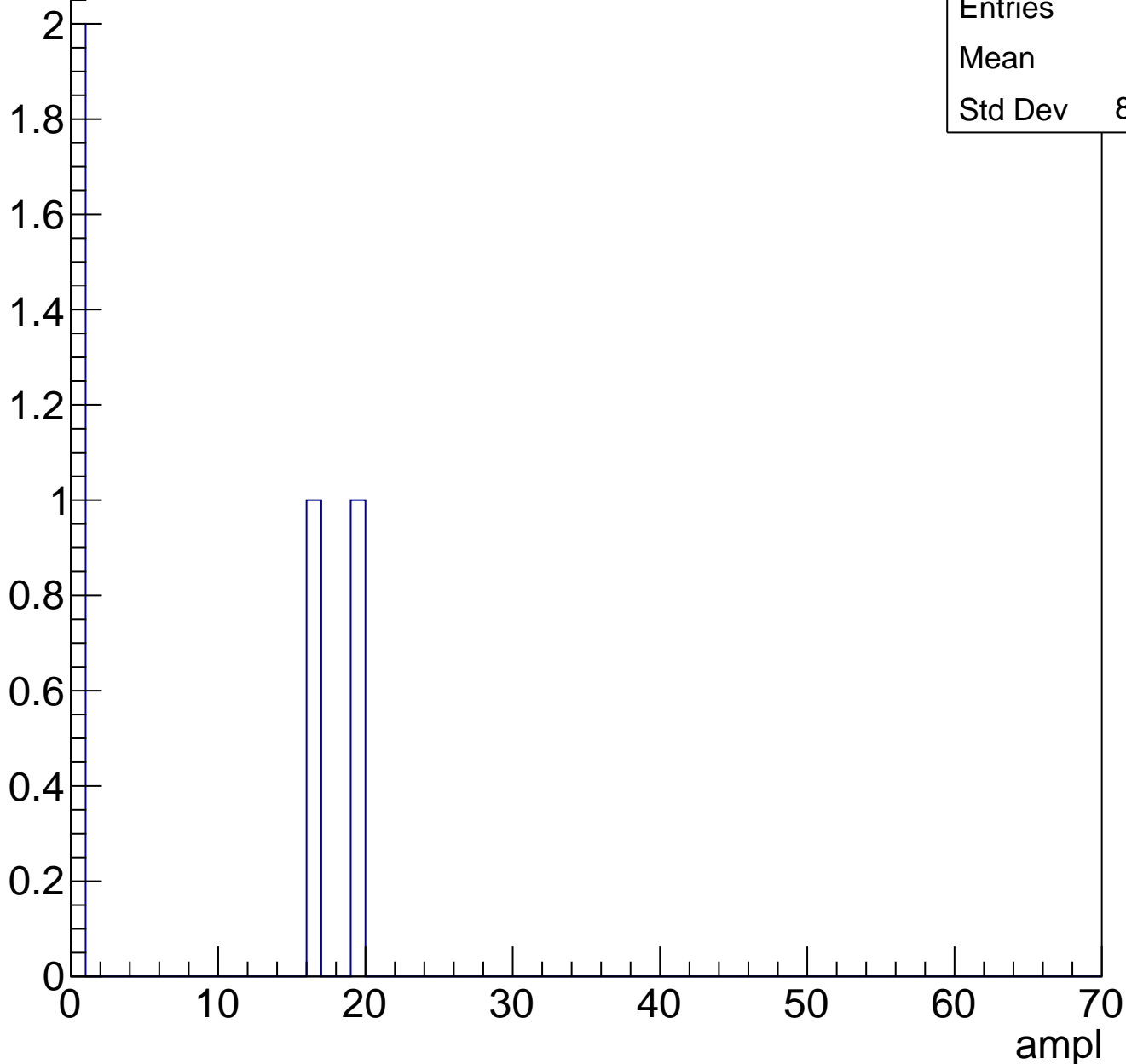
Entries	28
Mean	59.32
Std Dev	11.51



B1L001S, U21-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



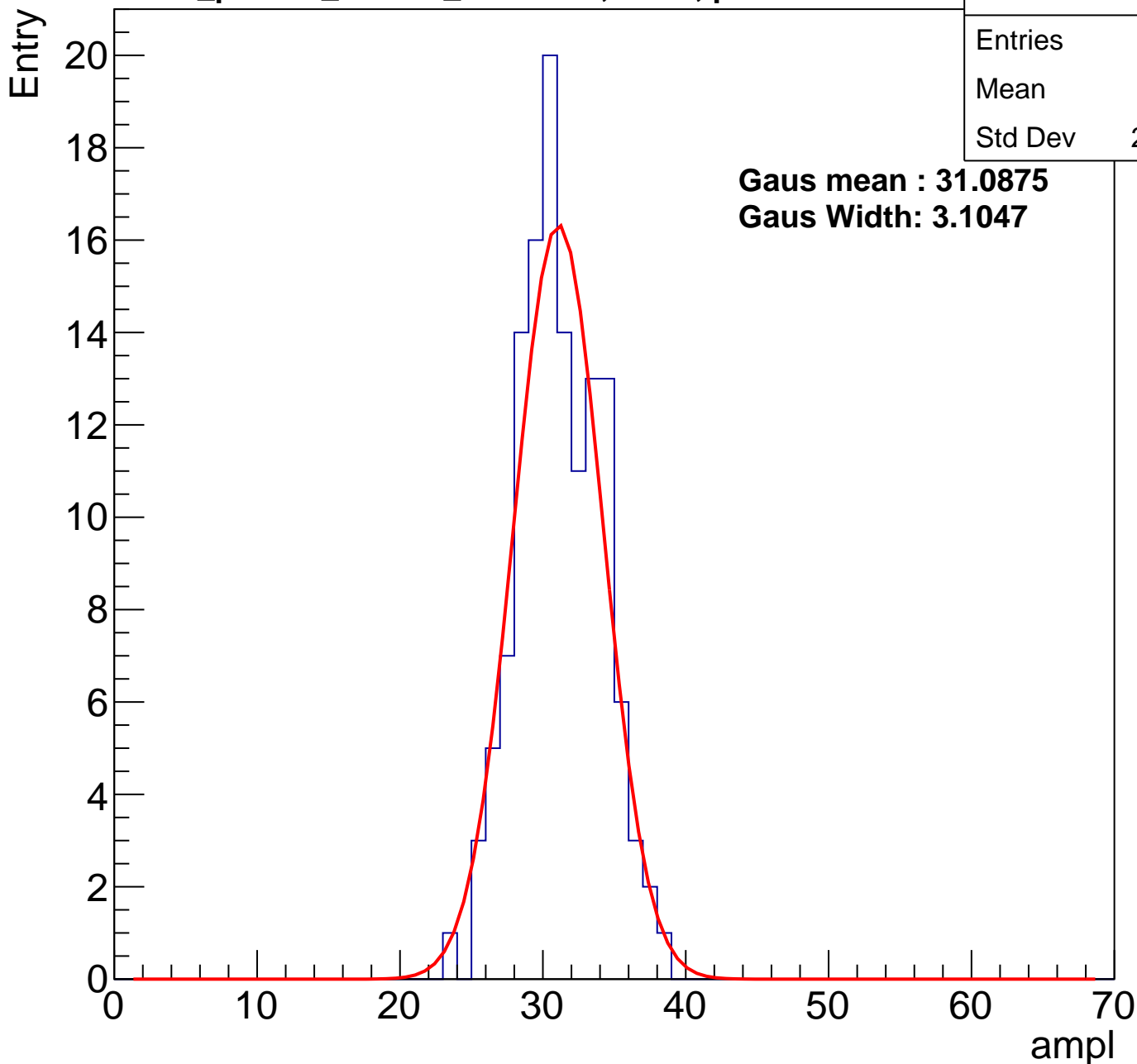
B1L001S, U21-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	30.7
Std Dev	2.892

Gaus mean : 31.0875

Gaus Width: 3.1047



B1L001S, U21-ch4, adc1

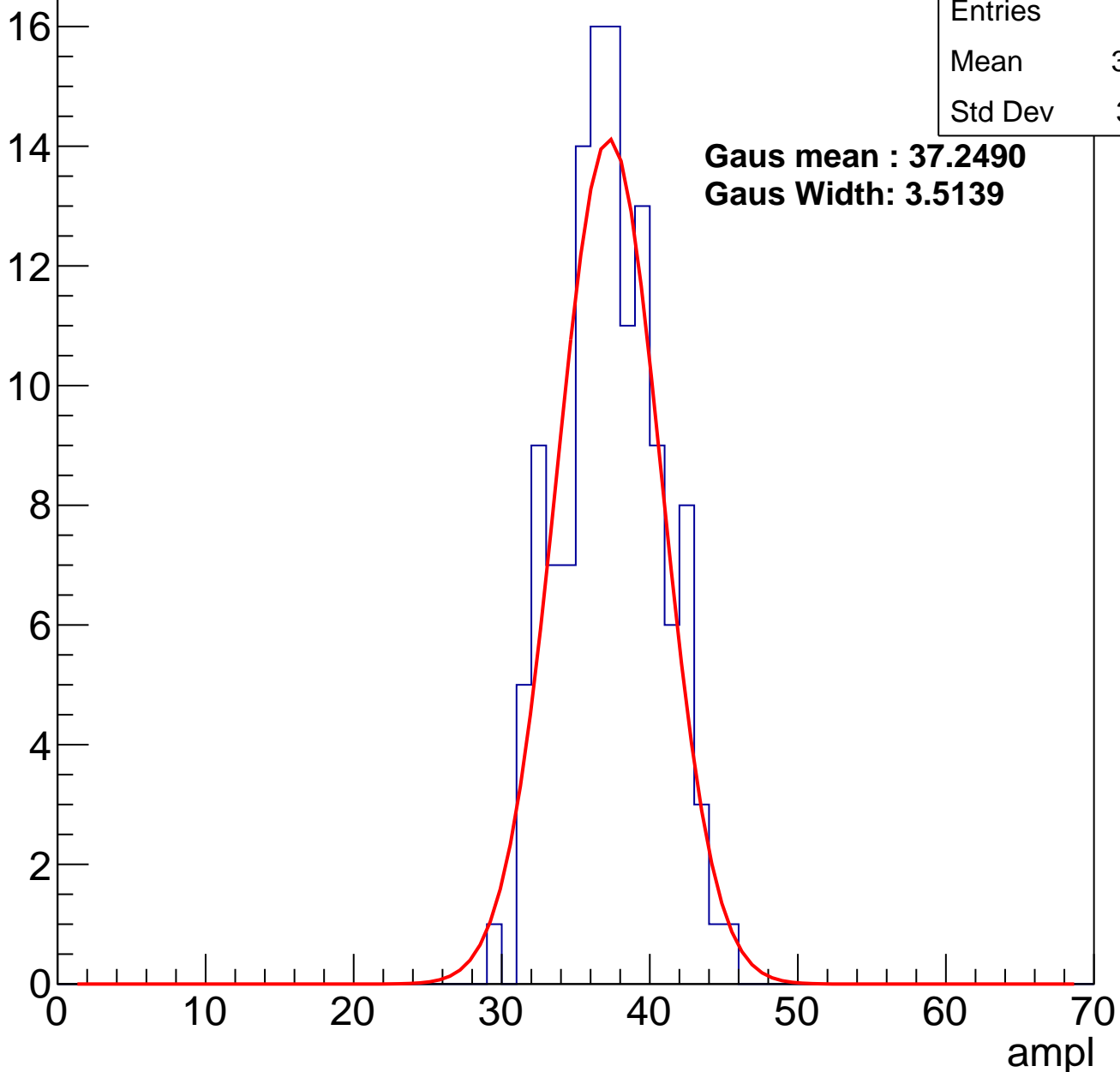
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.88
Std Dev	3.291

Gaus mean : 37.2490

Gaus Width: 3.5139

Entry



B1L001S, U21-ch4, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

133

Mean

43.41

Std Dev

3.427

Gaus mean : 43.7189

Gaus Width: 3.3208

0

10

20

30

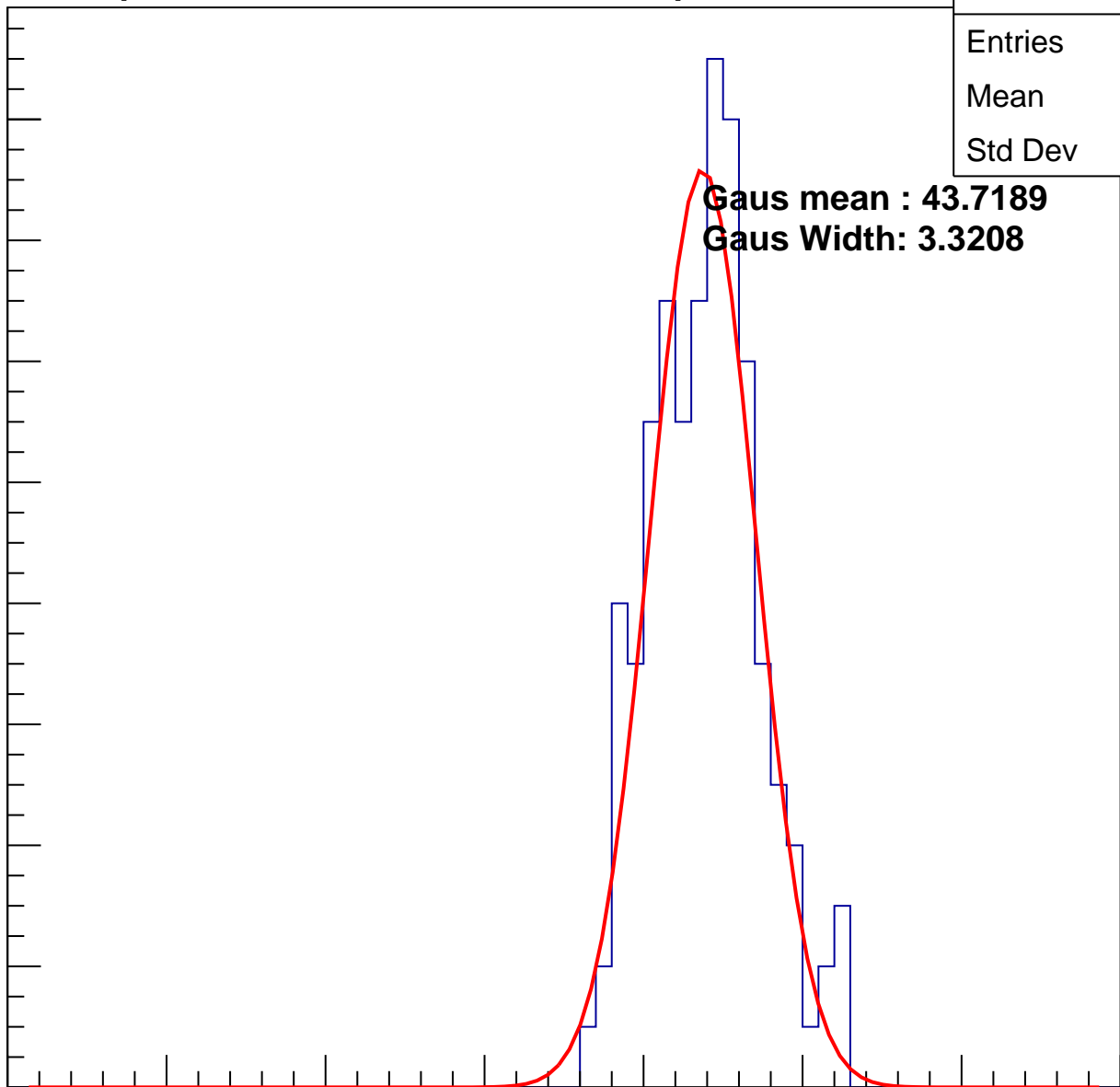
40

50

60

70

ampl



B1L001S, U21-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

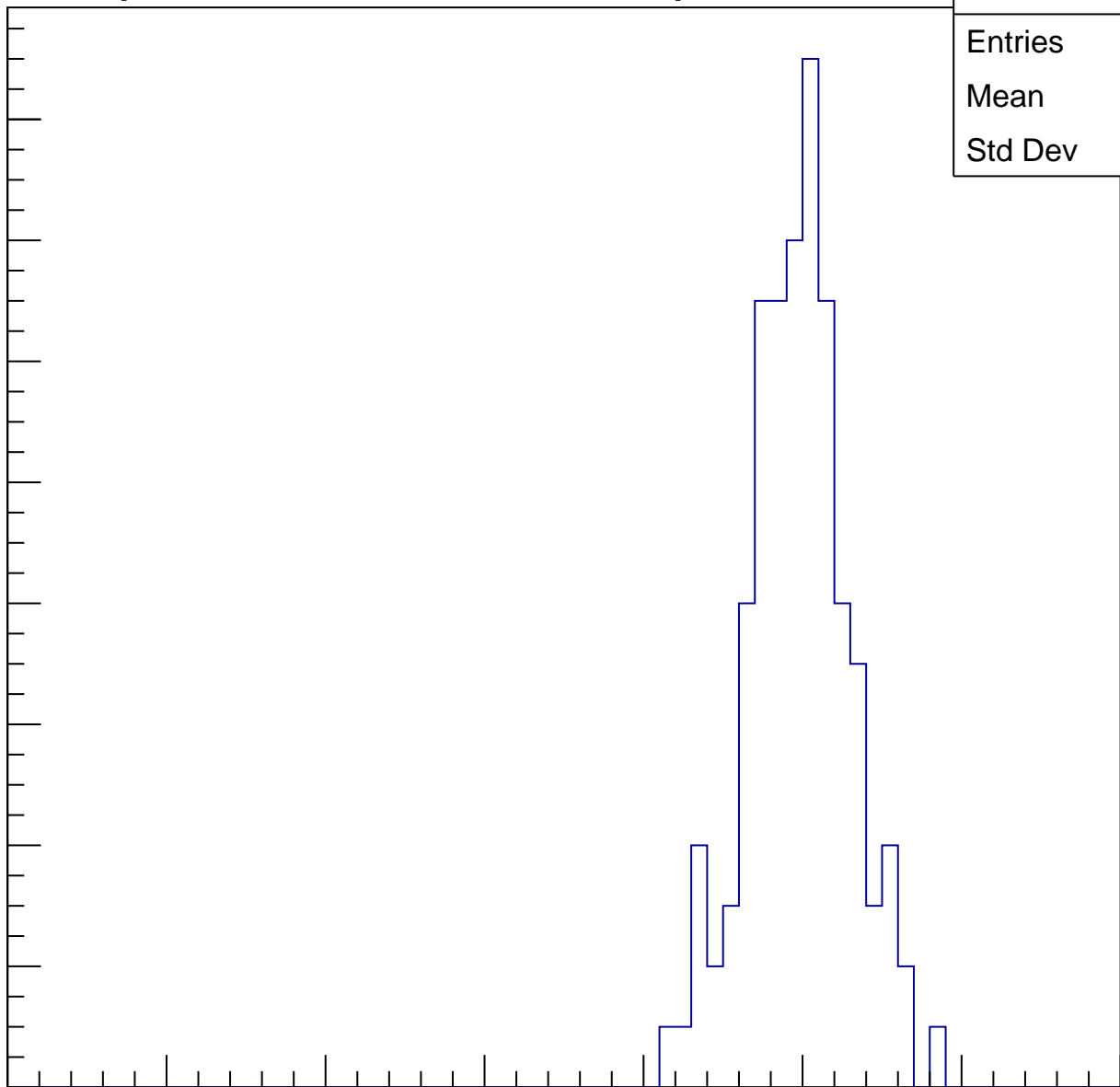
Entries	114
Mean	49.29
Std Dev	3.153

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch4, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

153

Mean

56.18

Std Dev

3.233

ampl

0

10

20

30

40

50

60

70

B1L001S, U21-ch4, adc5

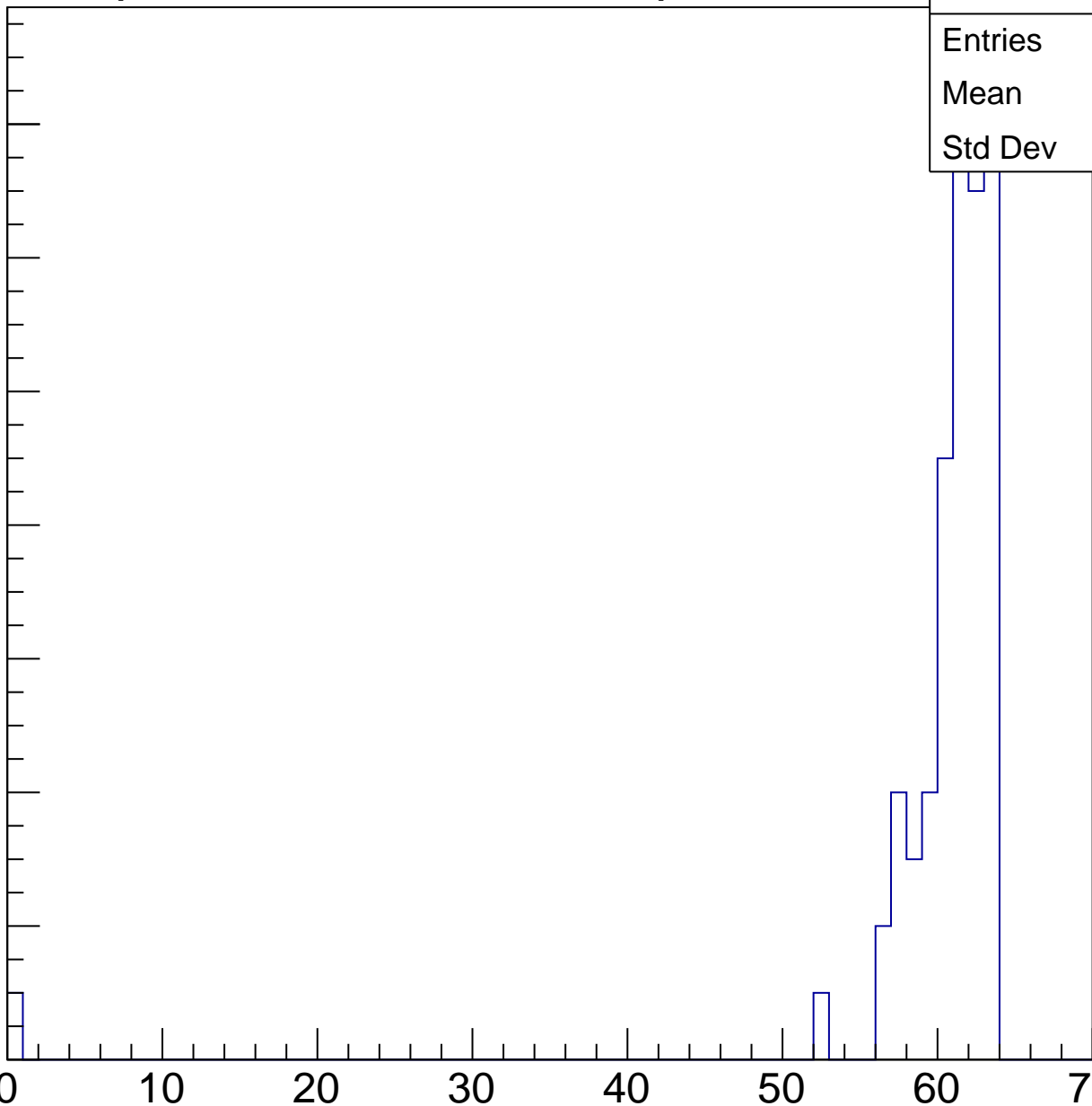
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.77
Std Dev	7.724

ampl



B1L001S, U21-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch4, adc7

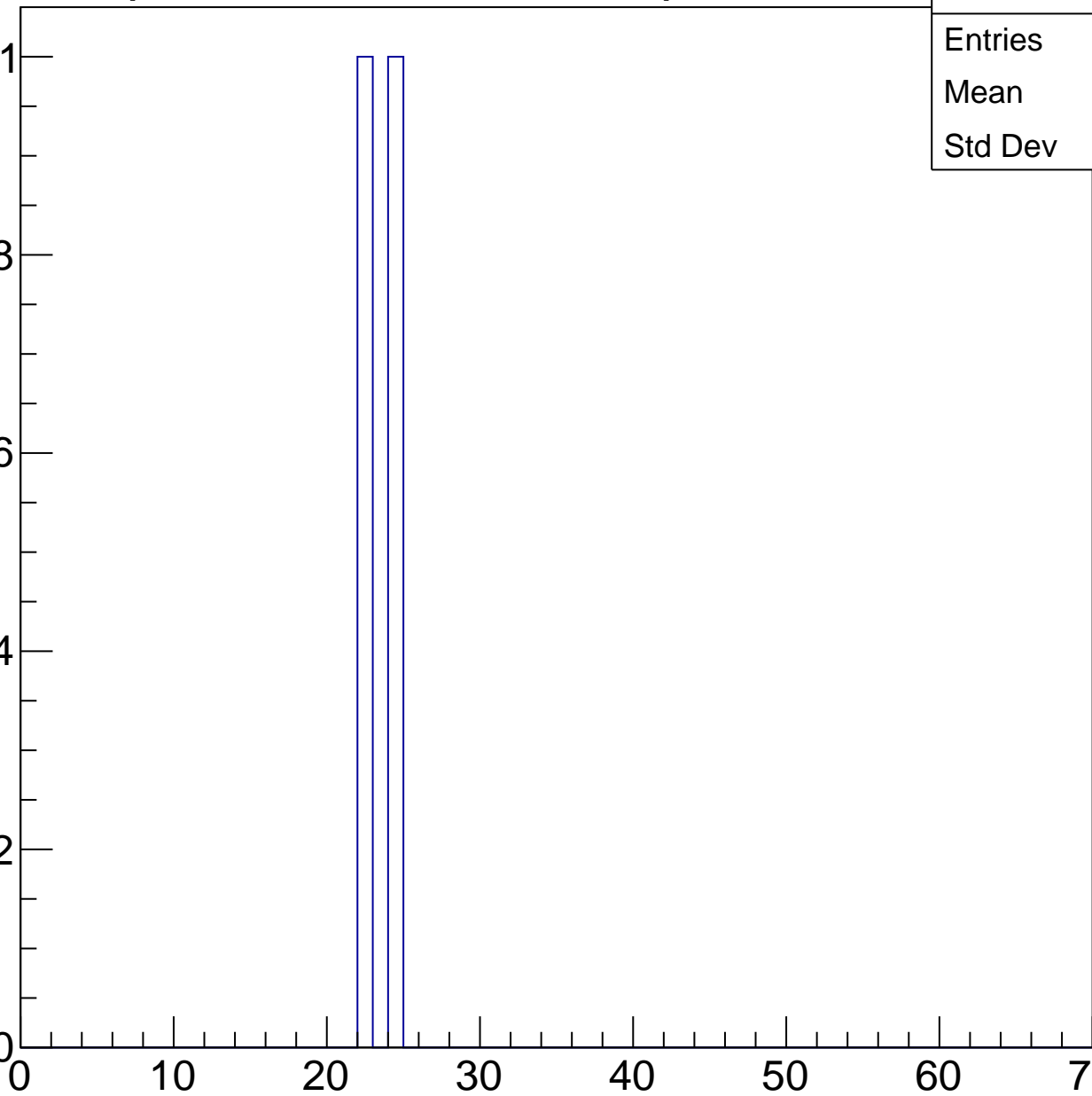
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23
Std Dev	1

ampl



B1L001S, U21-ch5, adc0

calib_packv5_042523_0143.root, FC#2, port C2

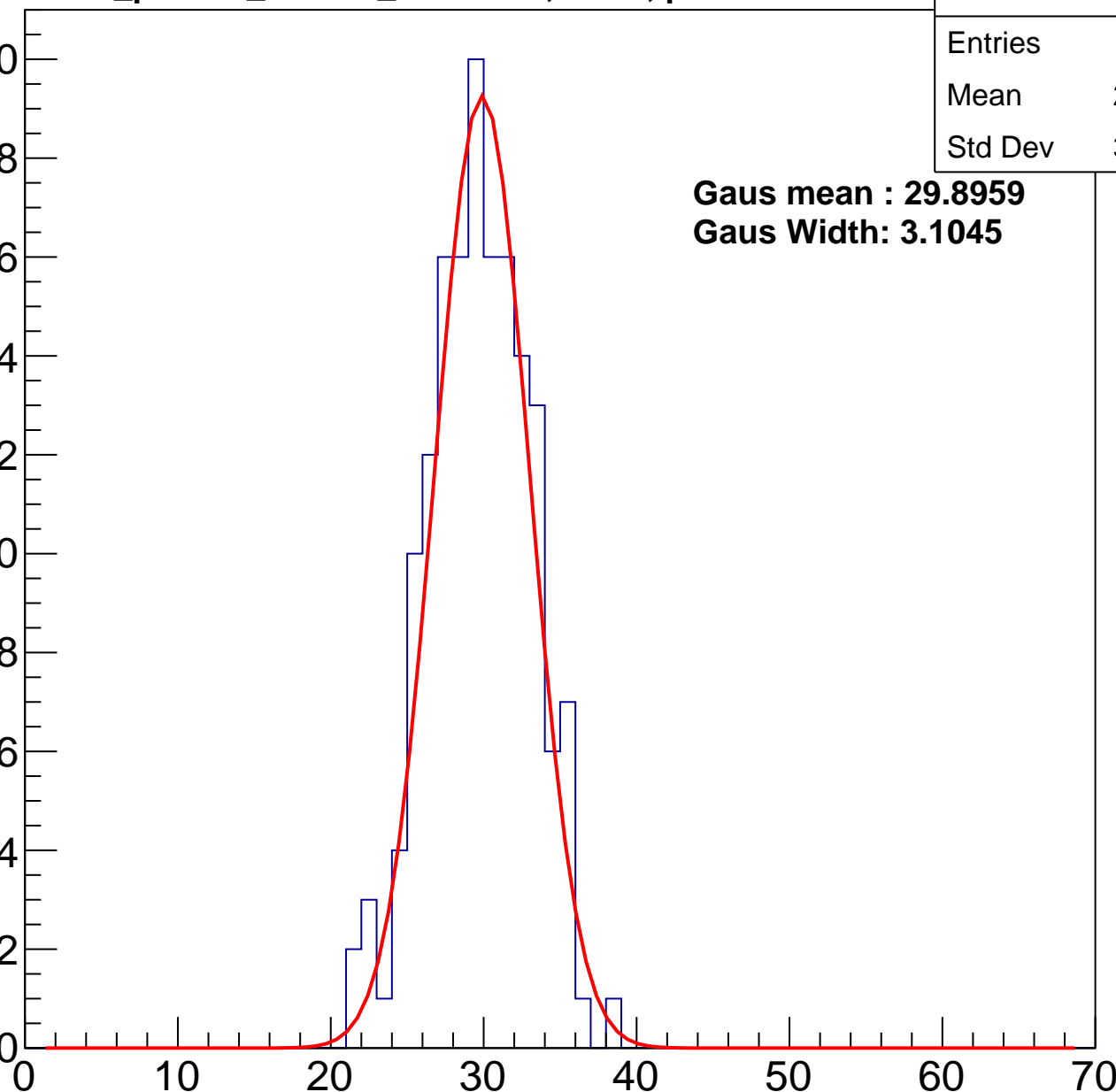
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	158
Mean	29.27
Std Dev	3.274

Gaus mean : 29.8959

Gaus Width: 3.1045



ampl

B1L001S, U21-ch5, adc1

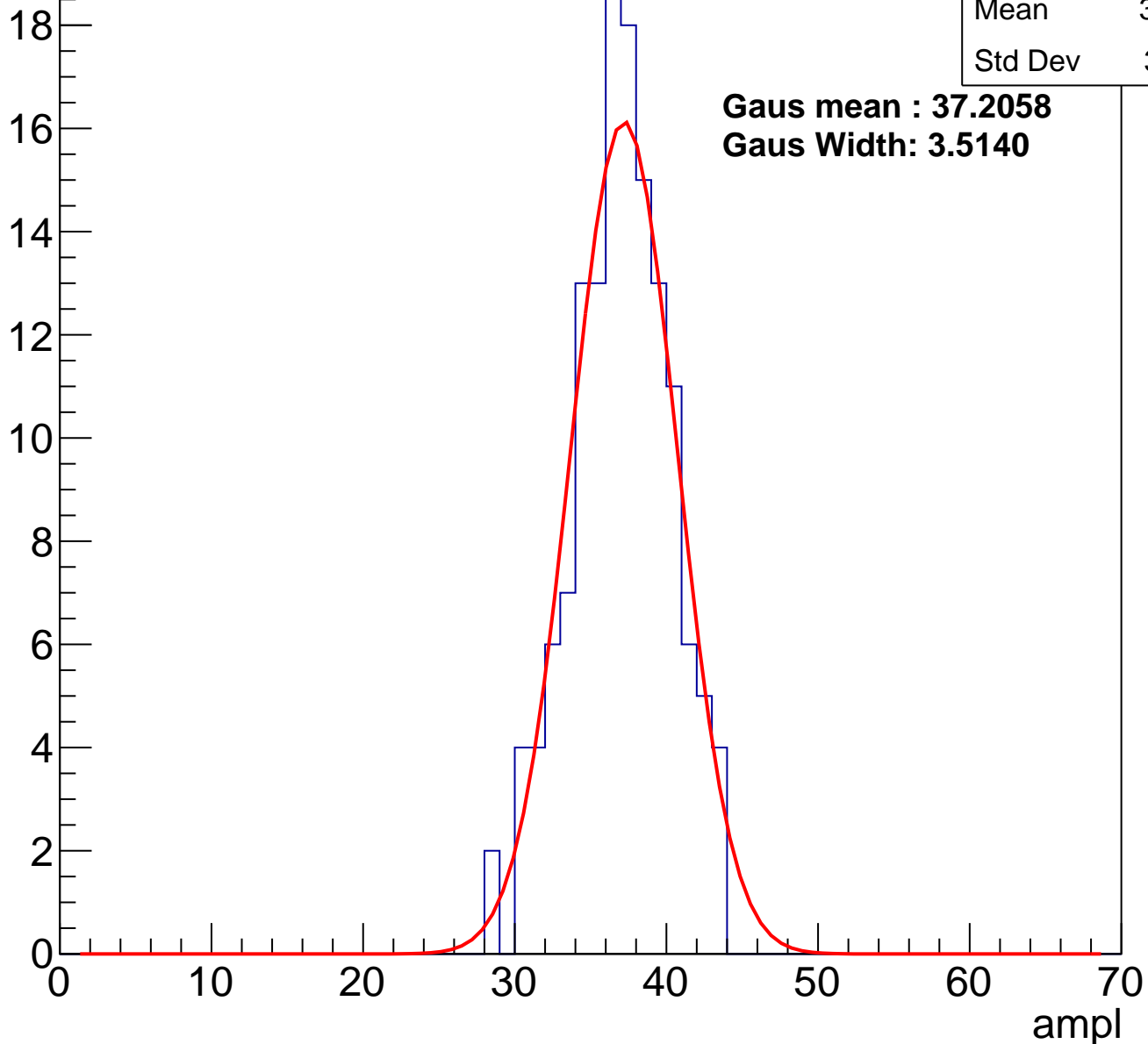
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.54
Std Dev	3.221

Gaus mean : 37.2058

Gaus Width: 3.5140

Entry



B1L001S, U21-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	43.2
Std Dev	3.094

Gaus mean : 43.7109

Gaus Width: 3.3722

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

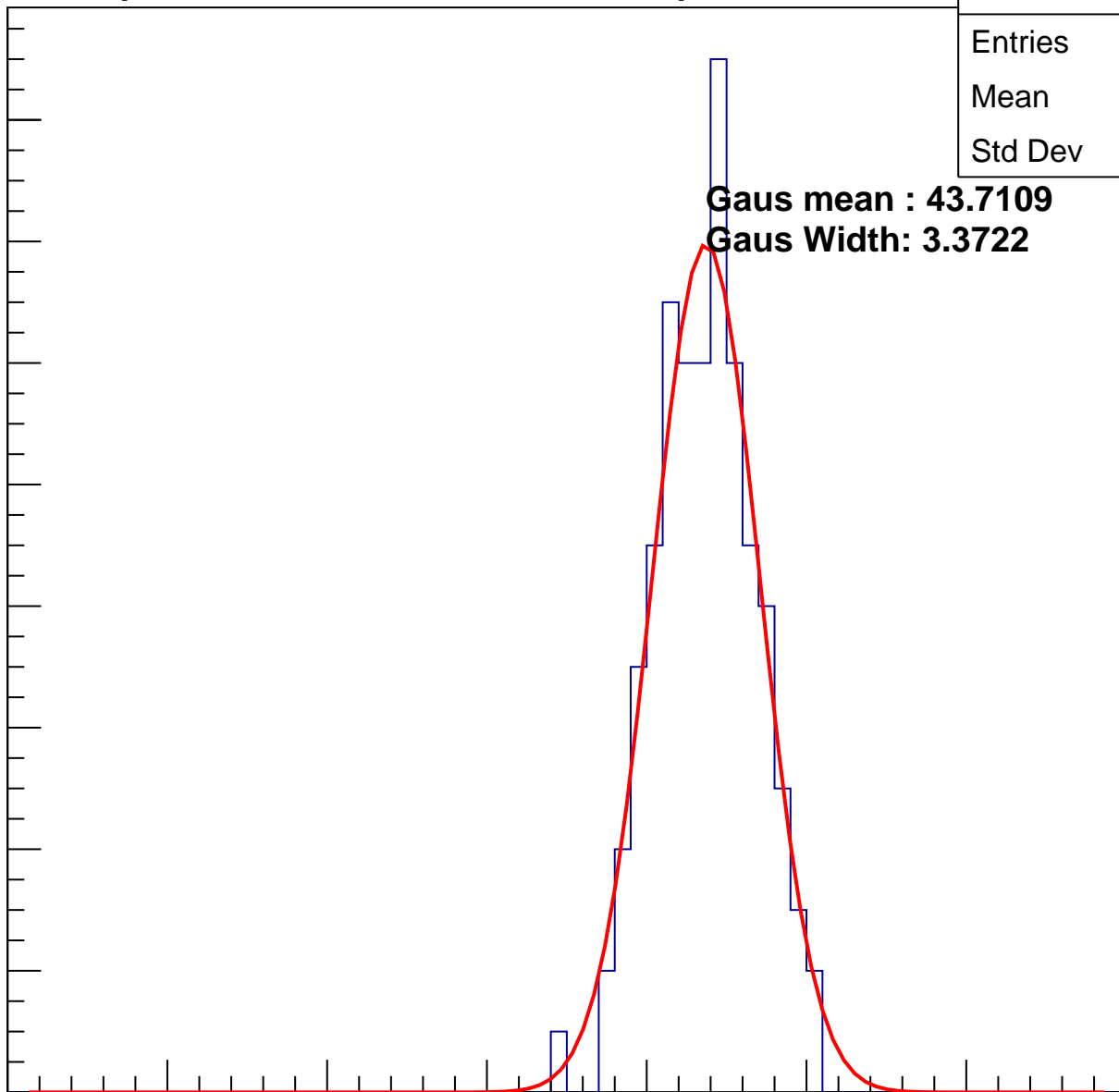
40

50

60

70

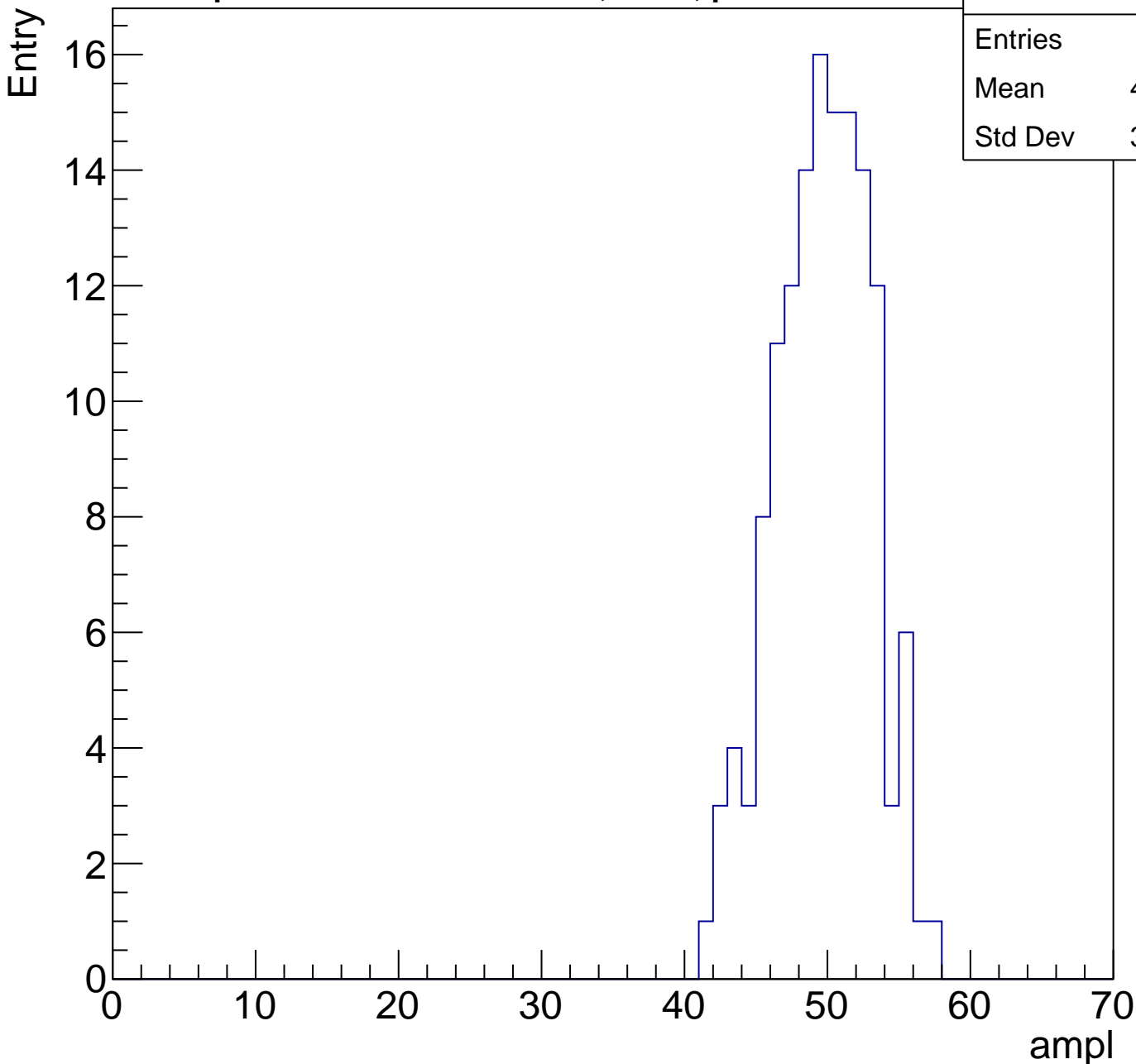
ampl



B1L001S, U21-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	49.22
Std Dev	3.295



B1L001S, U21-ch5, adc4

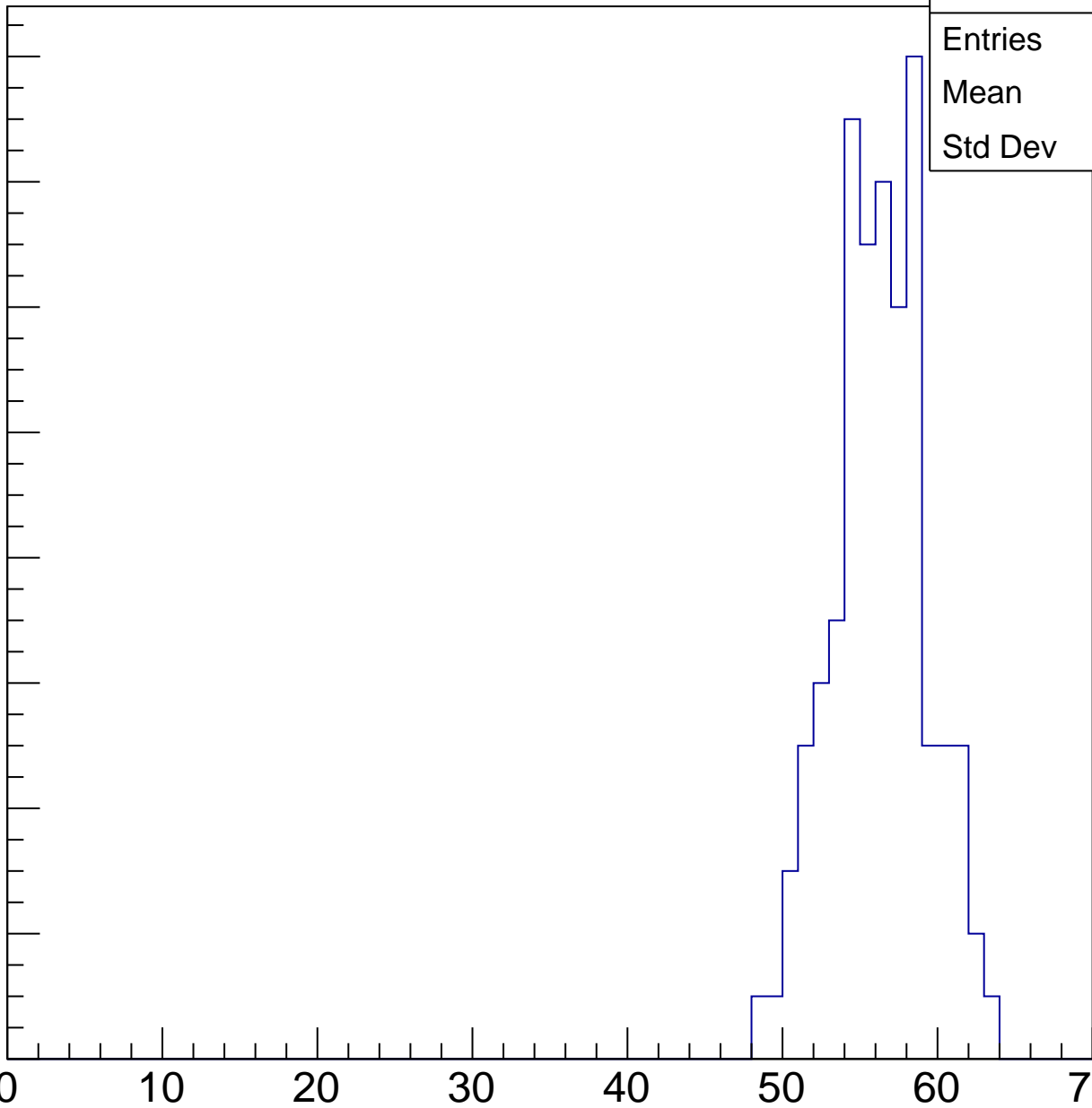
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	55.79
Std Dev	3.032

ampl



B1L001S, U21-ch5, adc5

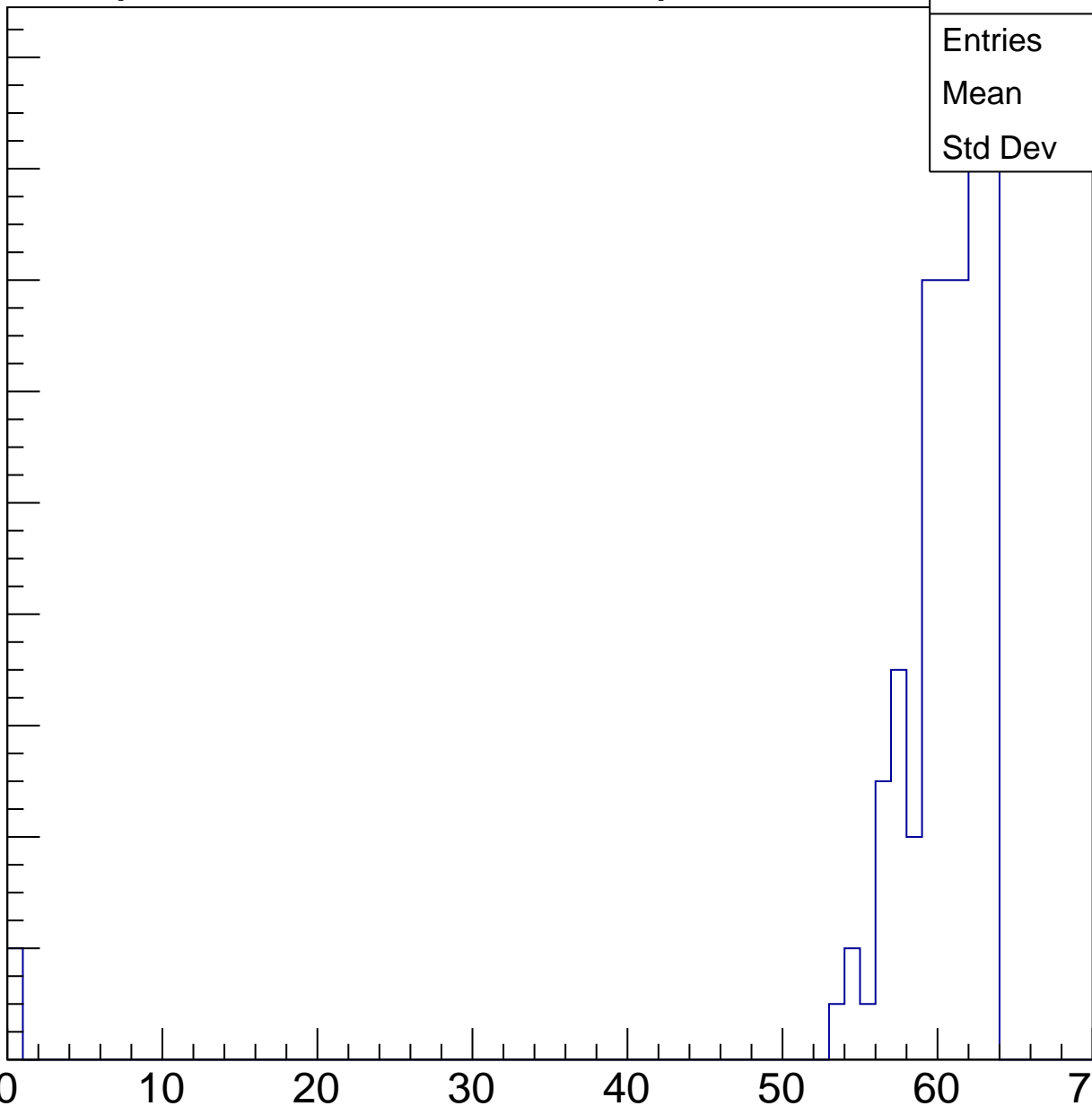
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.89
Std Dev	8.822

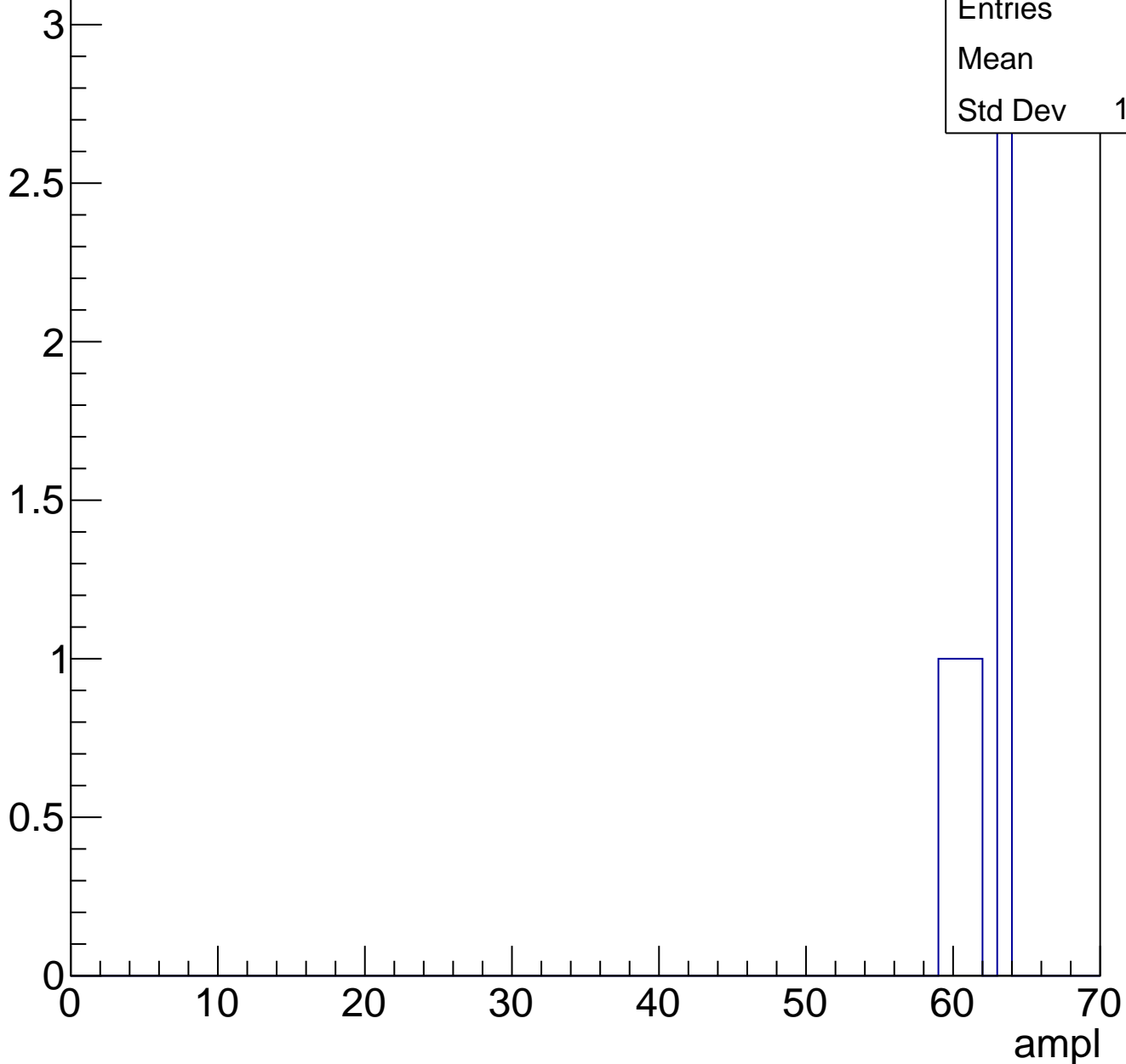
ampl



B1L001S, U21-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L001S, U21-ch6, adc0

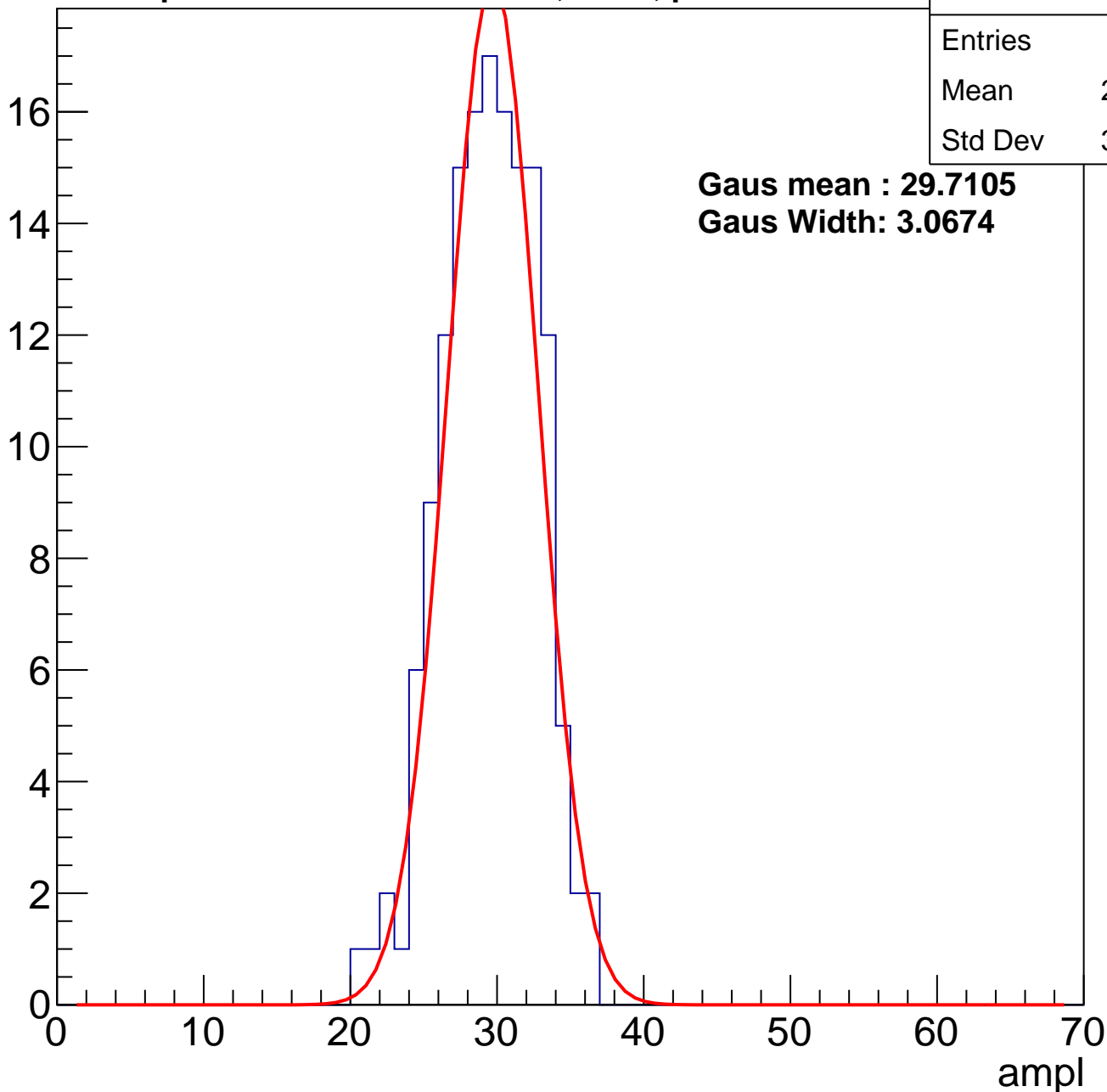
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.03
Std Dev	3.146

Gaus mean : 29.7105

Gaus Width: 3.0674

Entry



B1L001S, U21-ch6, adc1

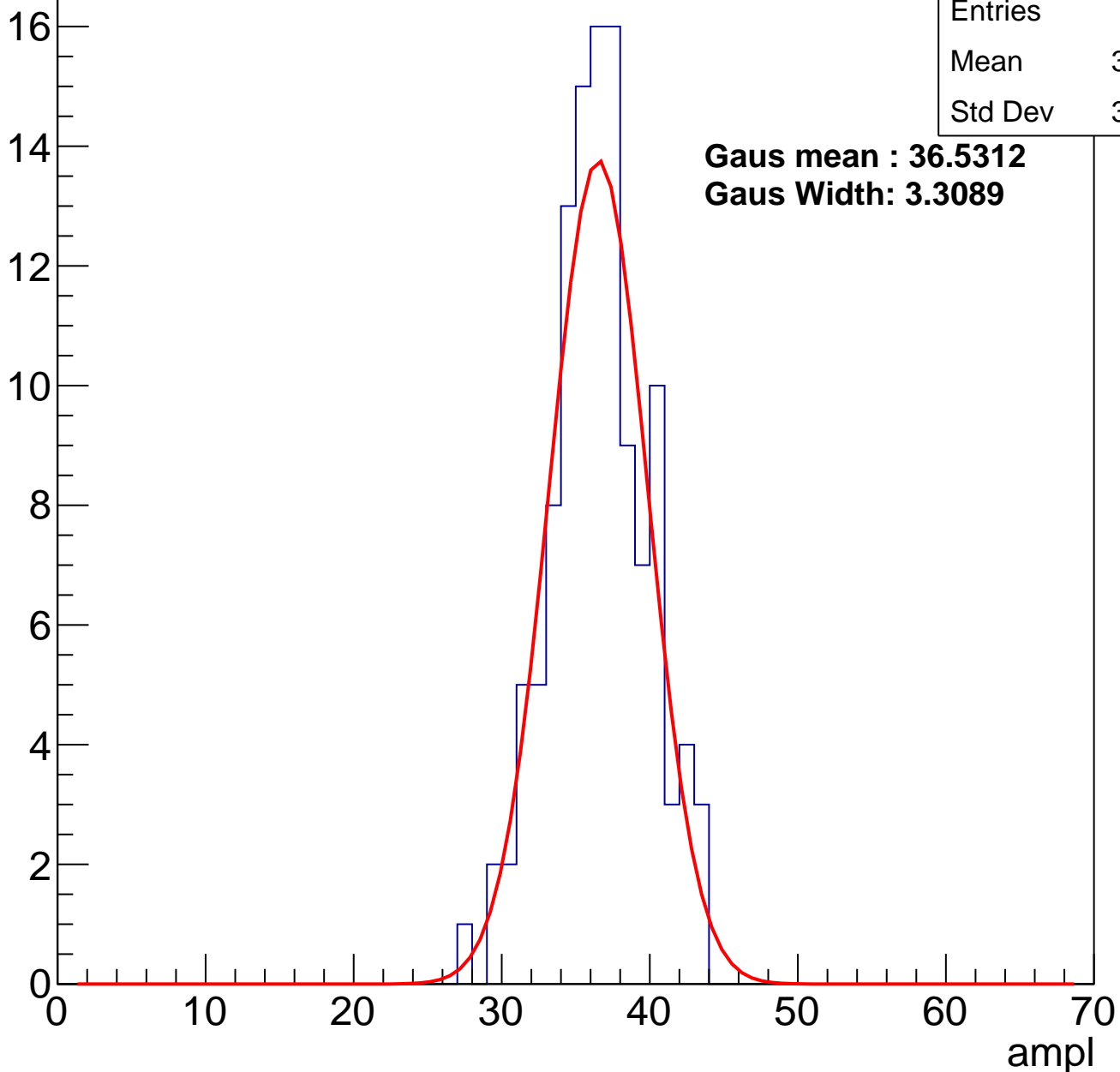
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	36.08
Std Dev	3.222

Gaus mean : 36.5312

Gaus Width: 3.3089

Entry



B1L001S, U21-ch6, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	41.47
Std Dev	3.142

Gaus mean : 42.0806

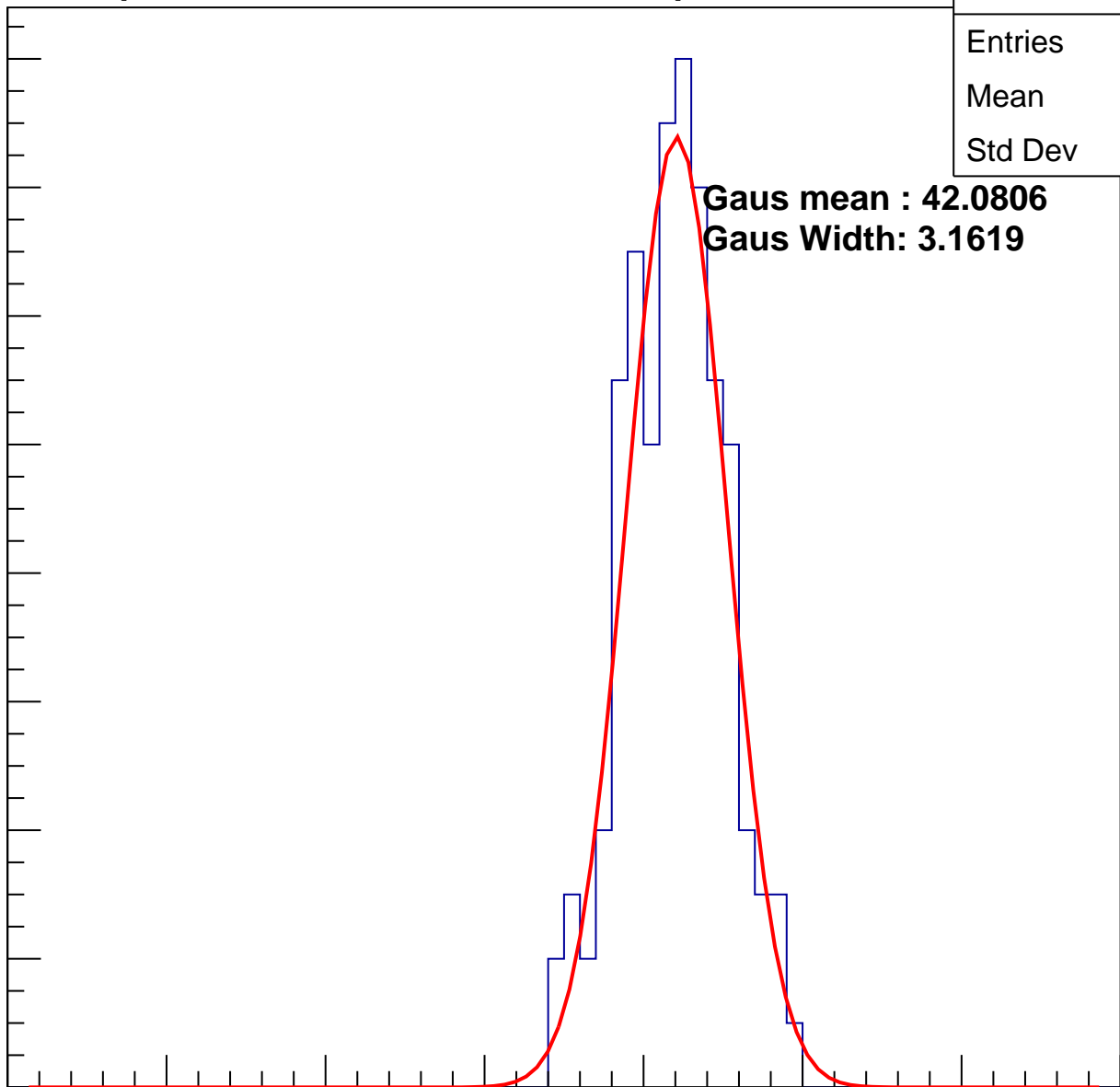
Gaus Width: 3.1619

Entry

16
14
12
10
8
6
4
2
0

ampl

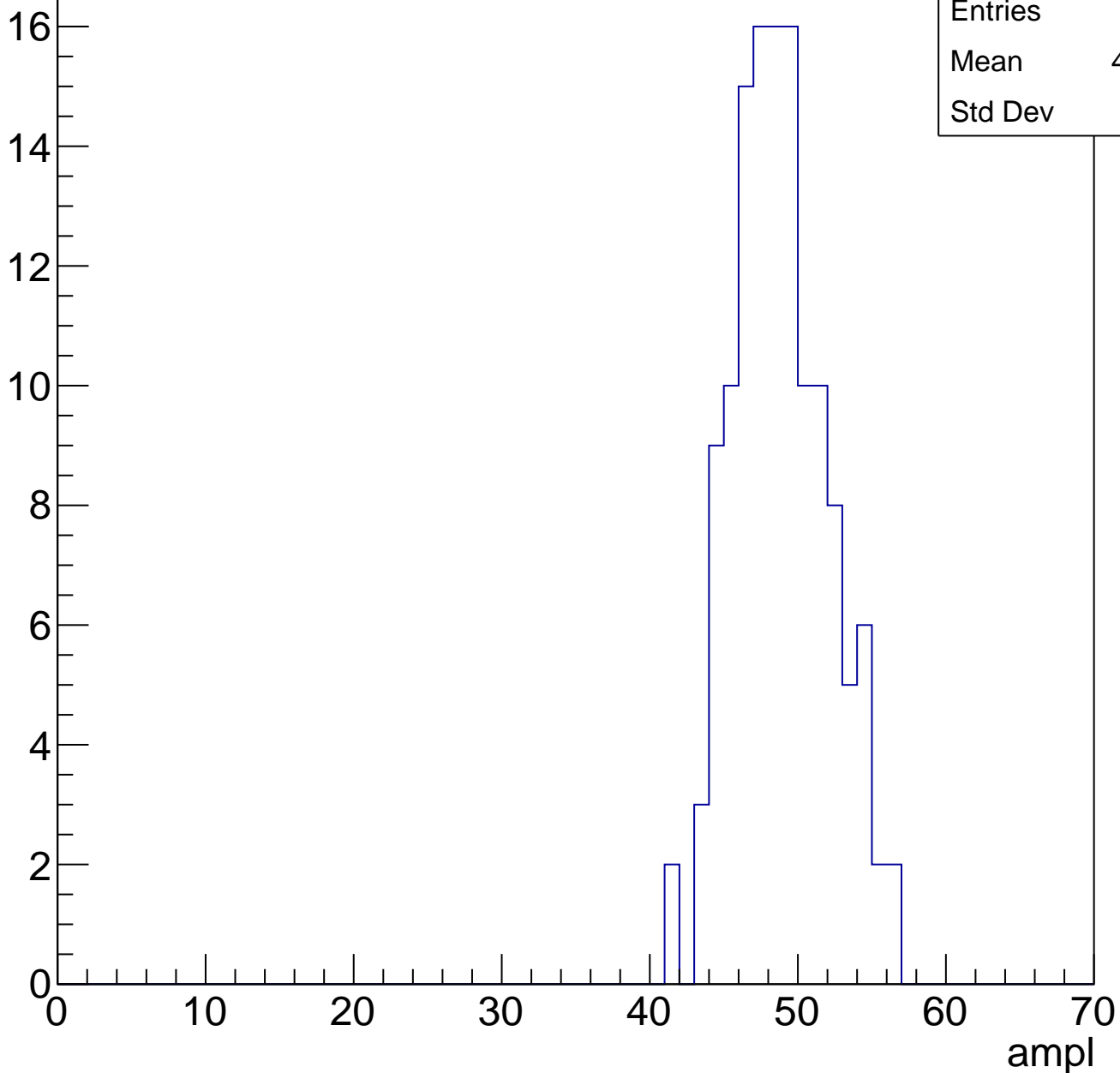
0 10 20 30 40 50 60 70



B1L001S, U21-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	130
Mean	48.37
Std Dev	3.17

B1L001S, U21-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

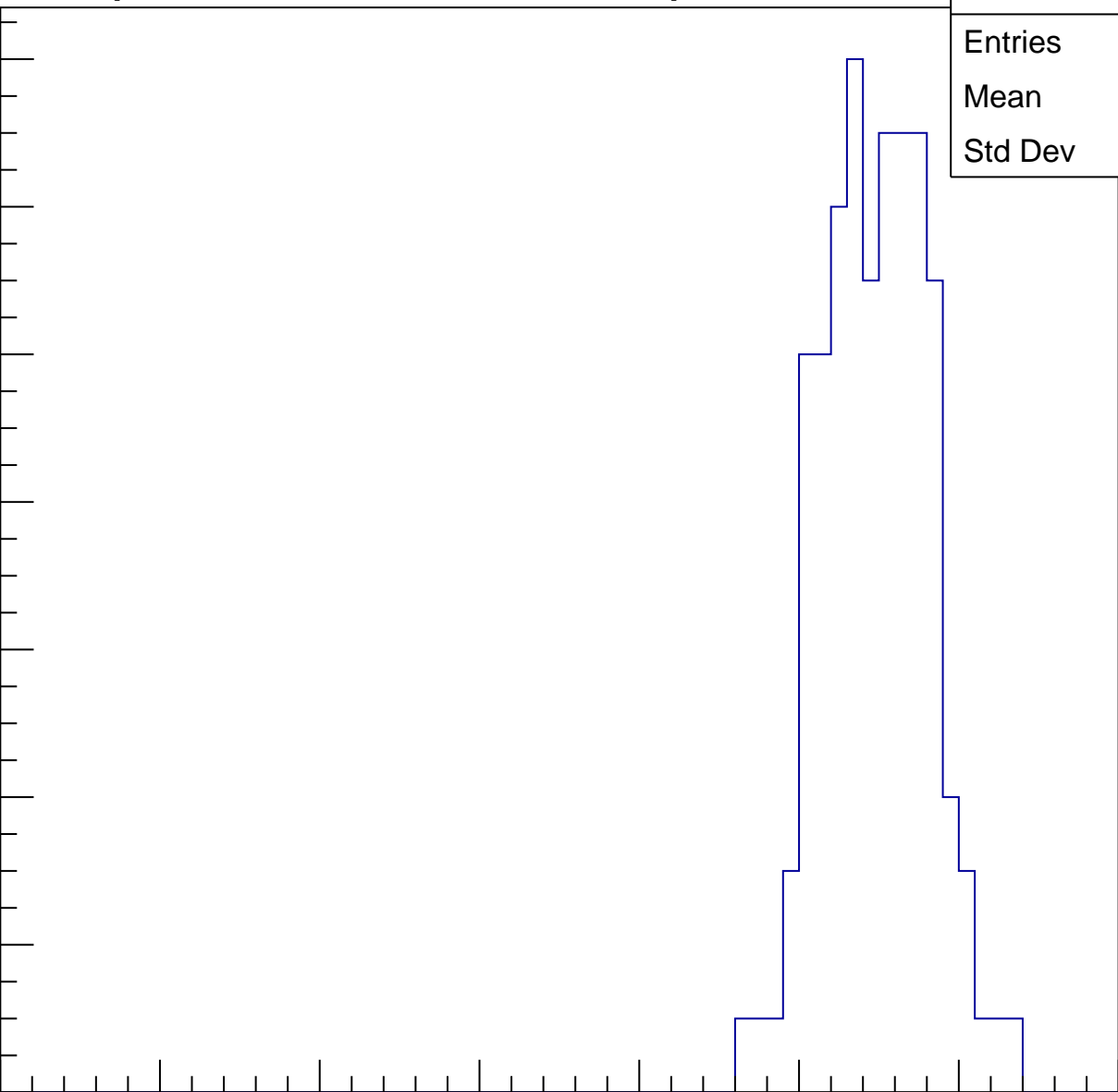
Entries	123
Mean	54.33
Std Dev	3.23

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch6, adc5

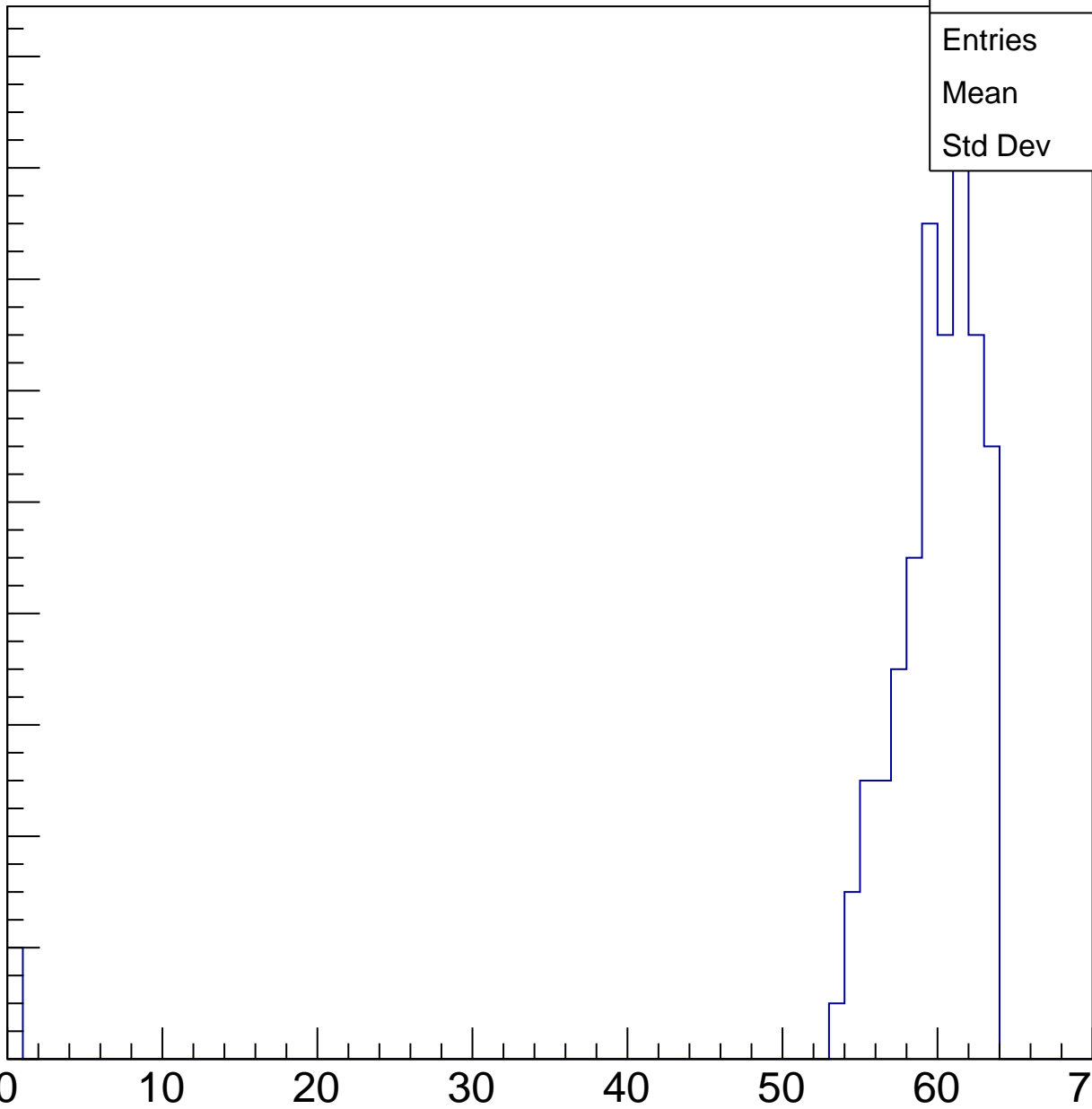
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.36
Std Dev	8.616

ampl

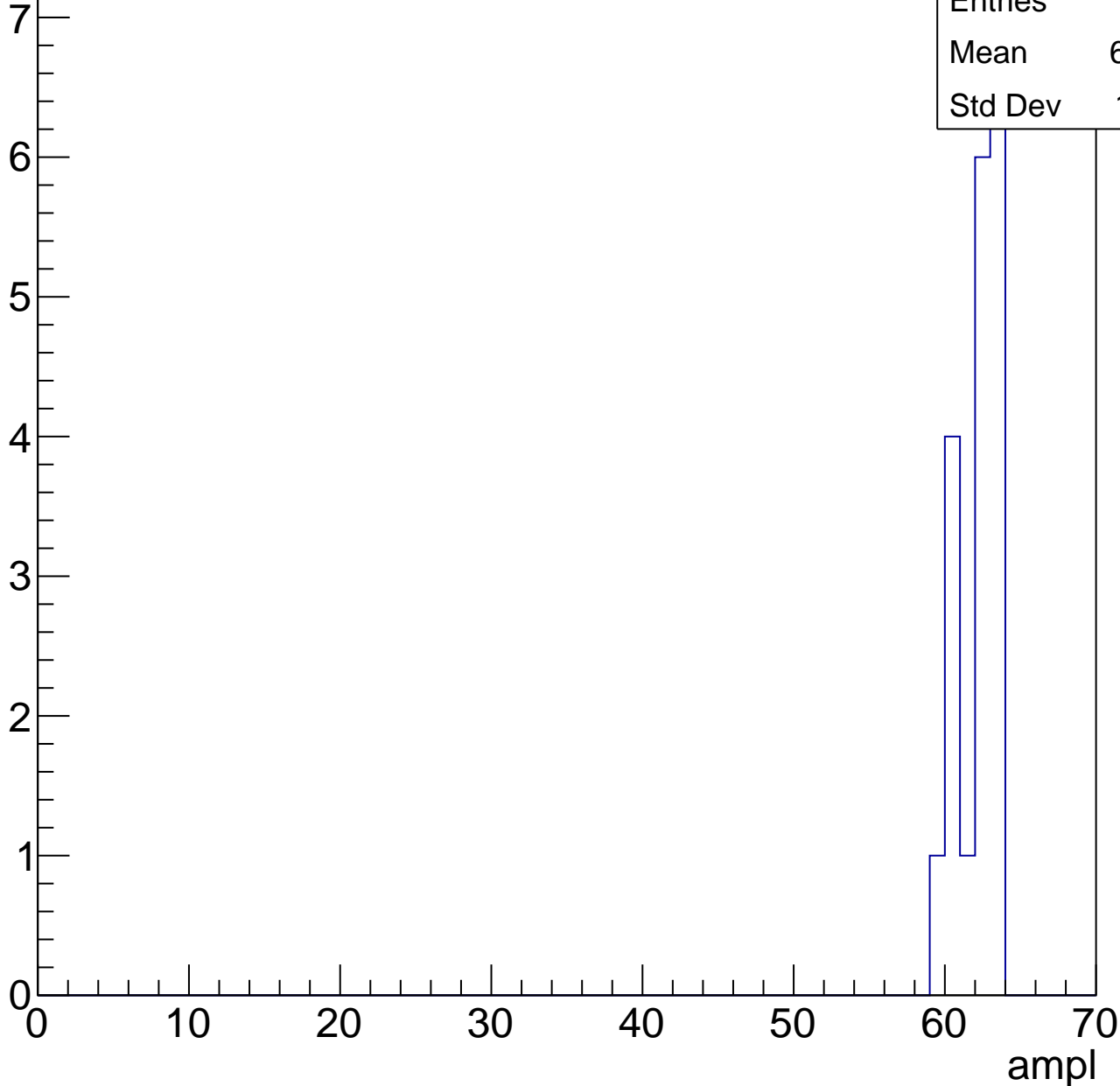


B1L001S, U21-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

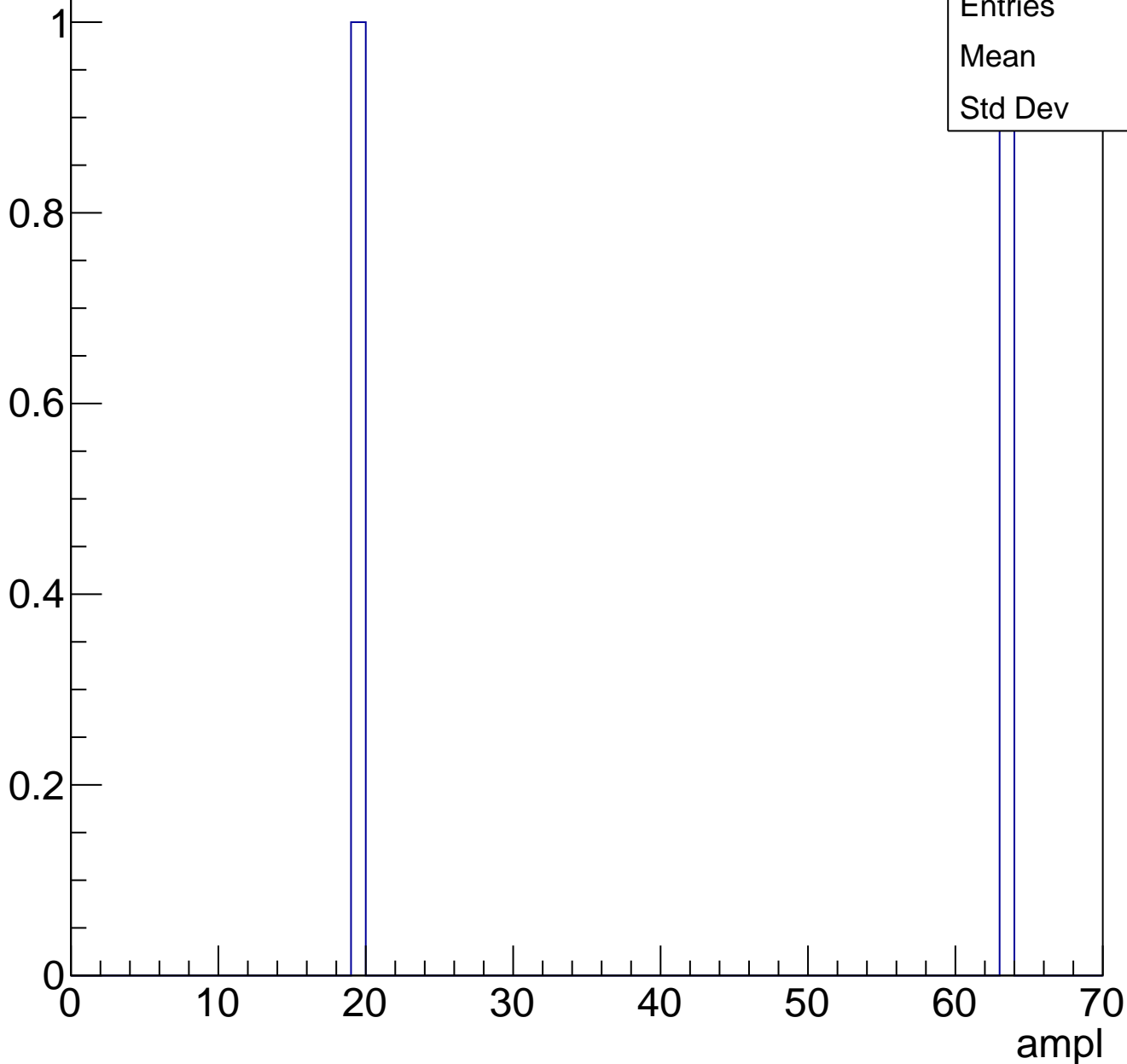
Entries	19
Mean	61.74
Std Dev	1.291



B1L001S, U21-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	41
Std Dev	22

B1L001S, U21-ch7, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	29.85
Std Dev	2.793

Gaus mean : 30.3268

Gaus Width: 2.8299

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

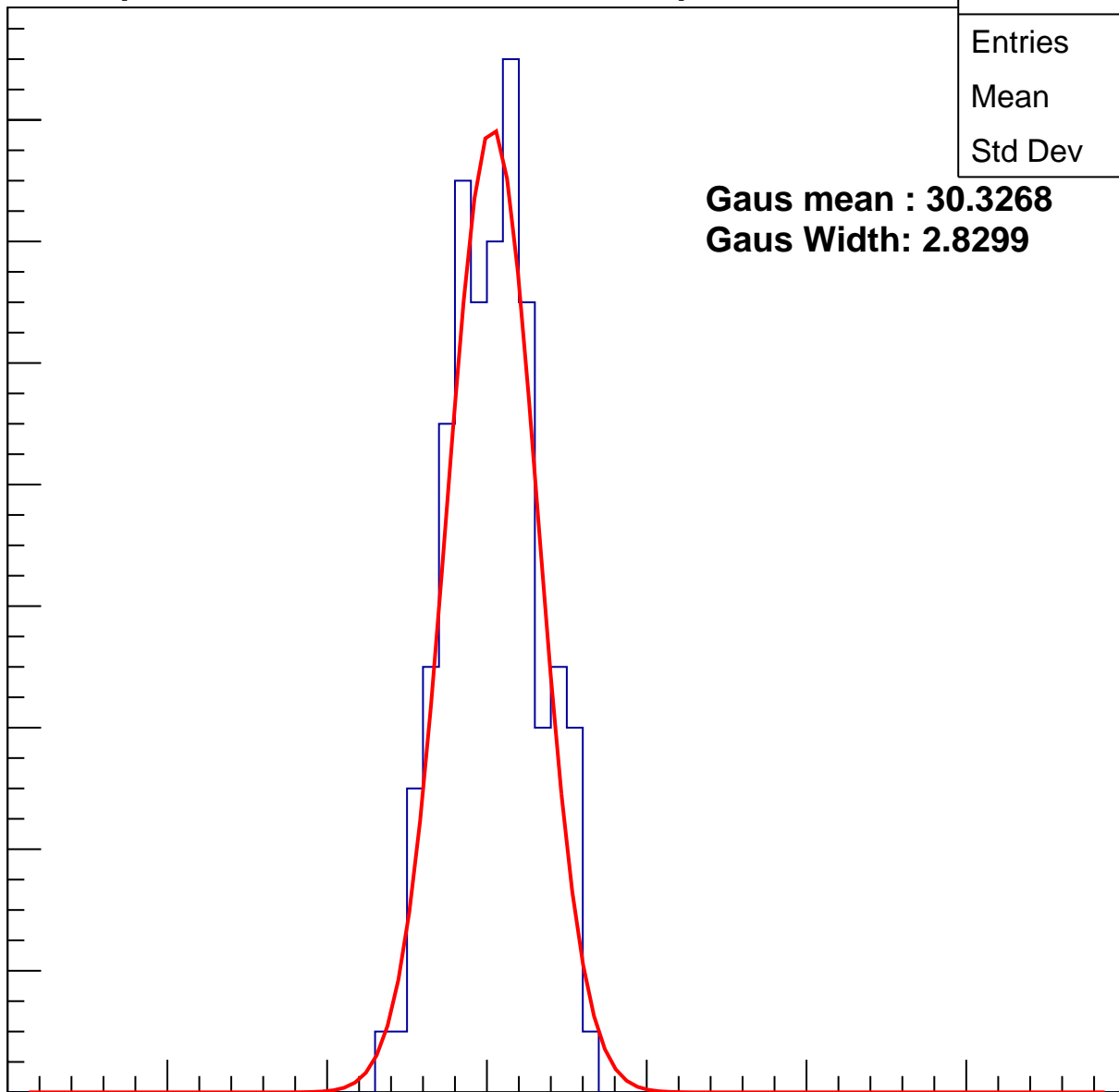
40

50

60

70

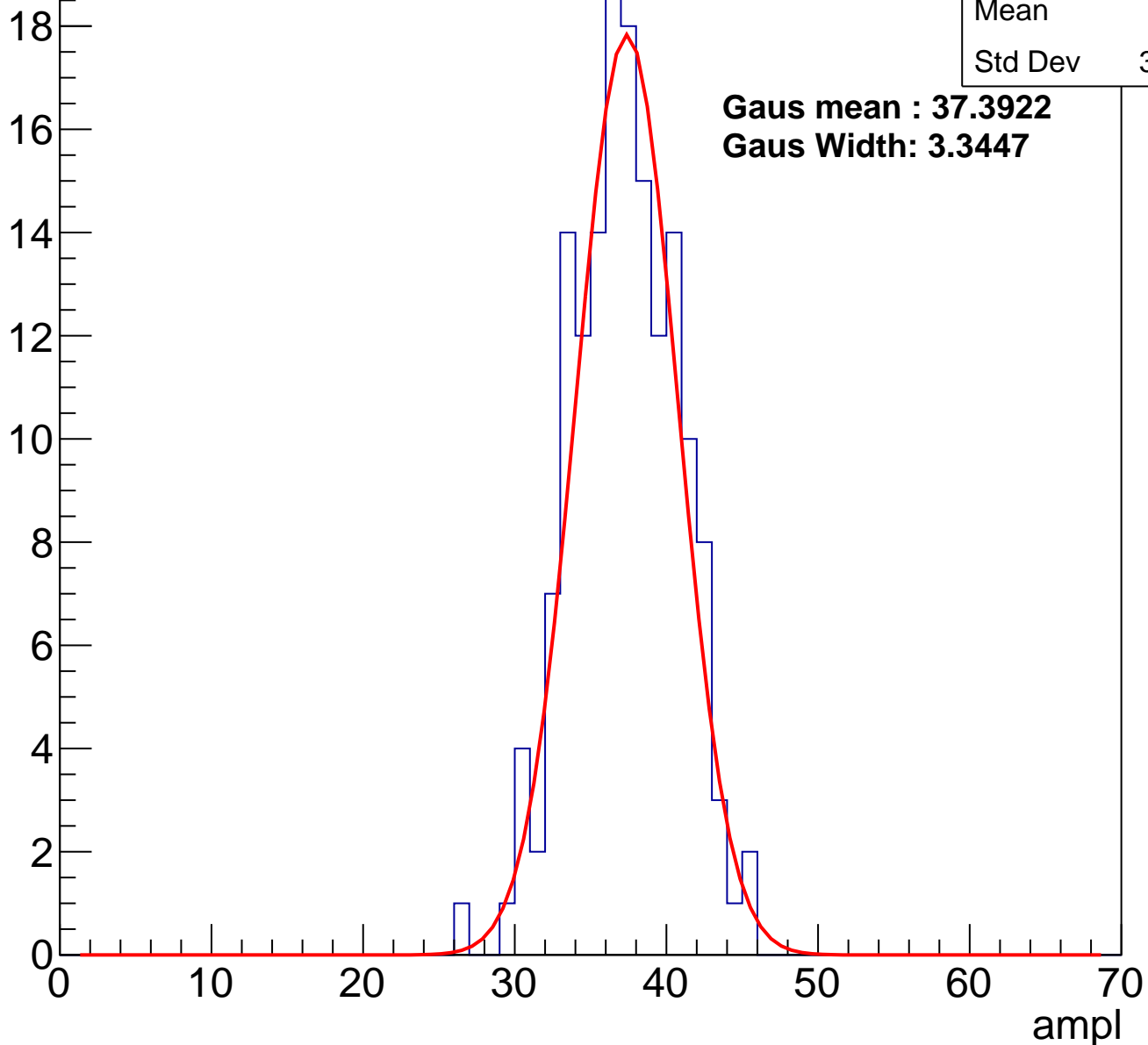
ampl



B1L001S, U21-ch7, adc1

calib_packv5_042523_0143.root, FC#2, port C2

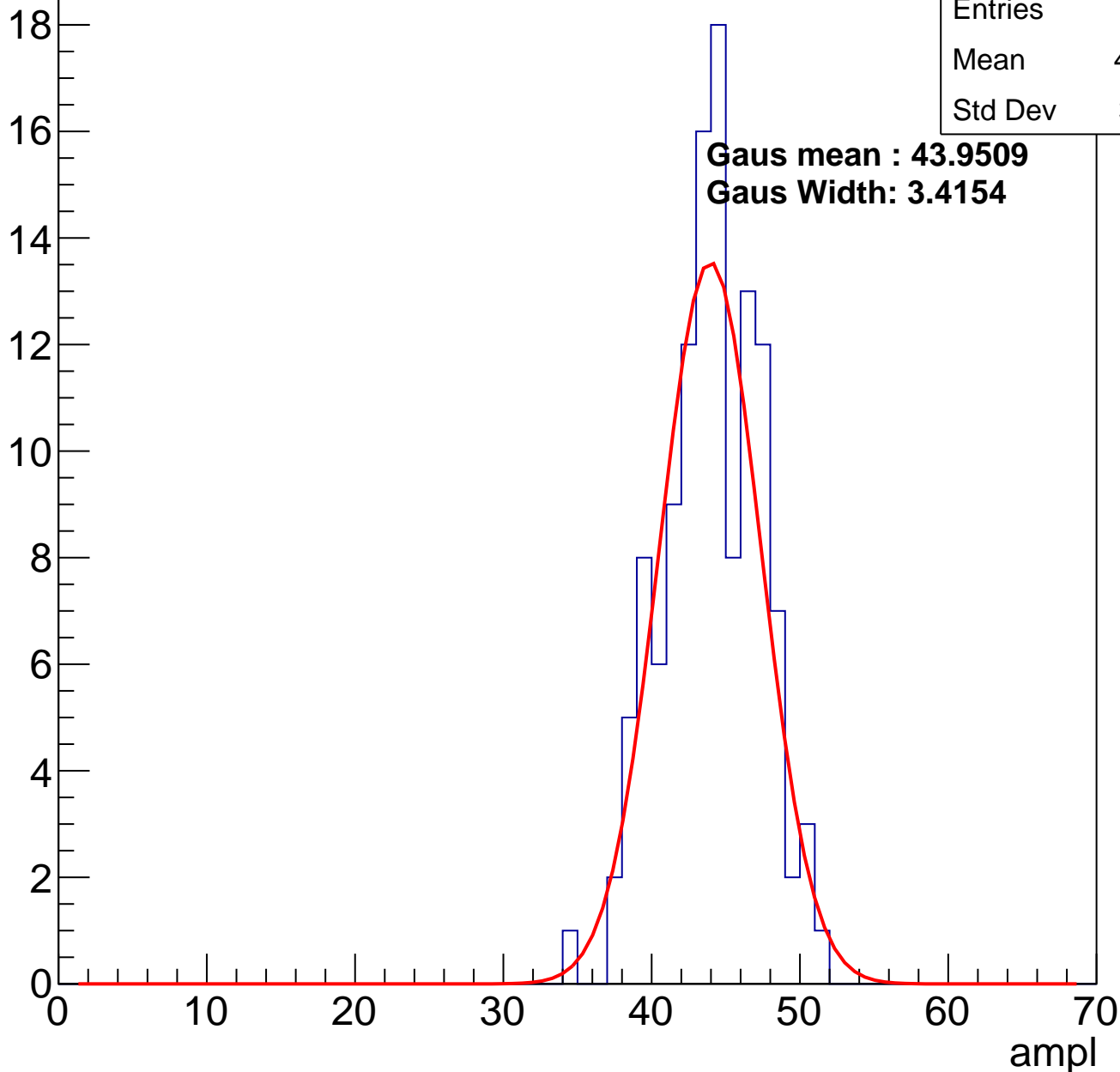
Entry



B1L001S, U21-ch7, adc2

calib_packv5_042523_0143.root, FC#2, port C2

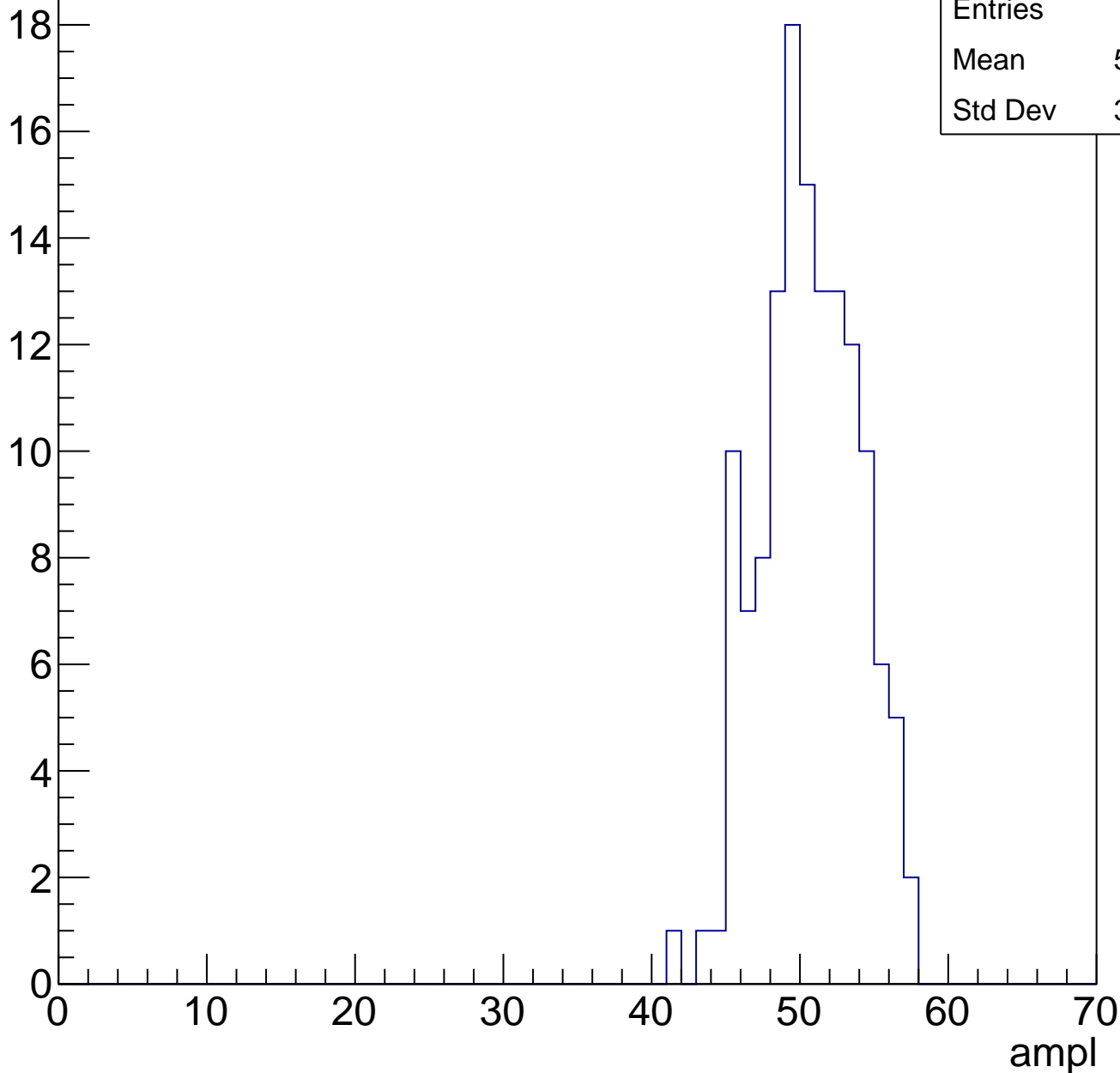
Entry



B1L001S, U21-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	135
Mean	50.16
Std Dev	3.252

B1L001S, U21-ch7, adc4

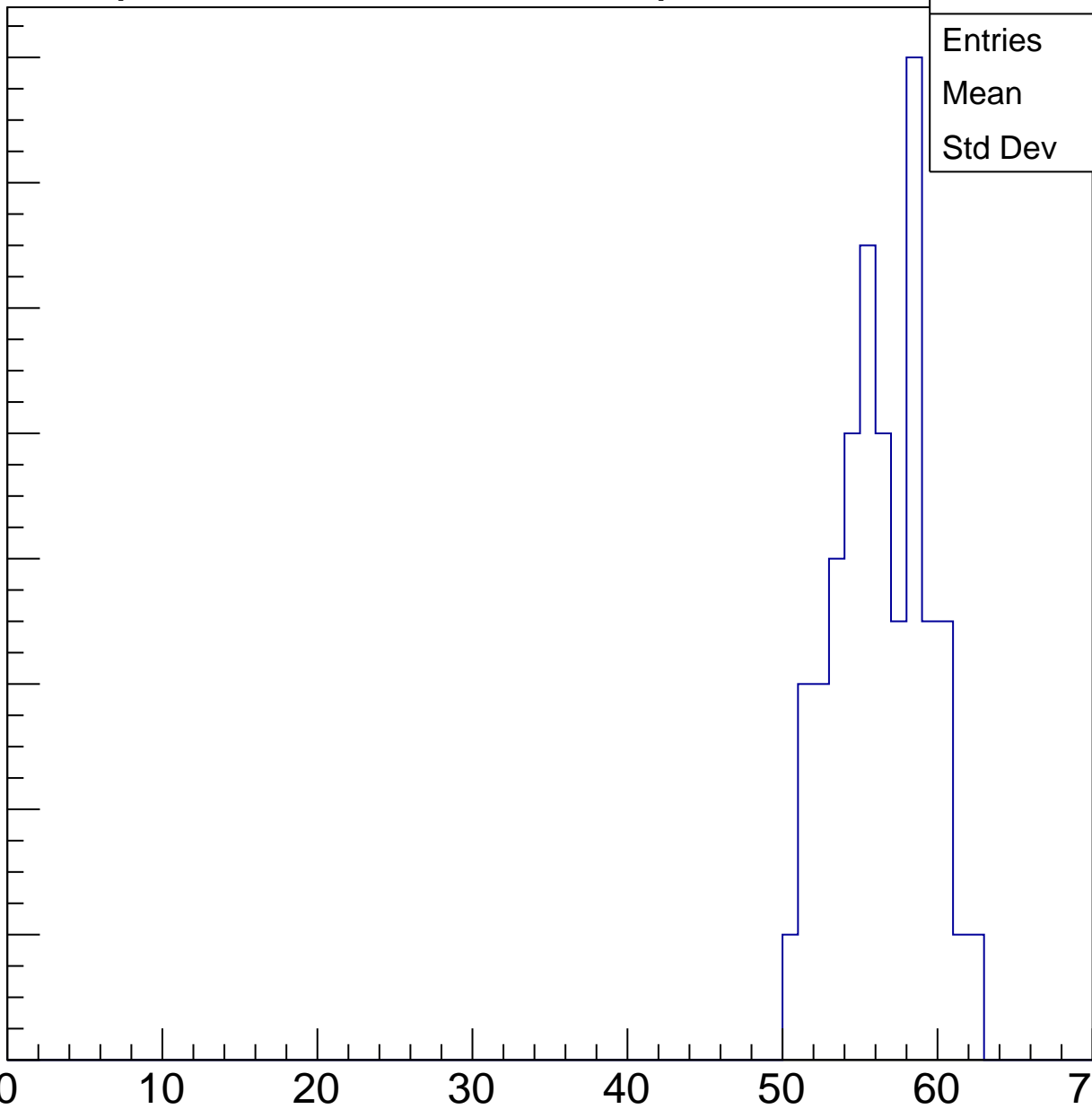
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	55.86
Std Dev	2.903

ampl



B1L001S, U21-ch7, adc5

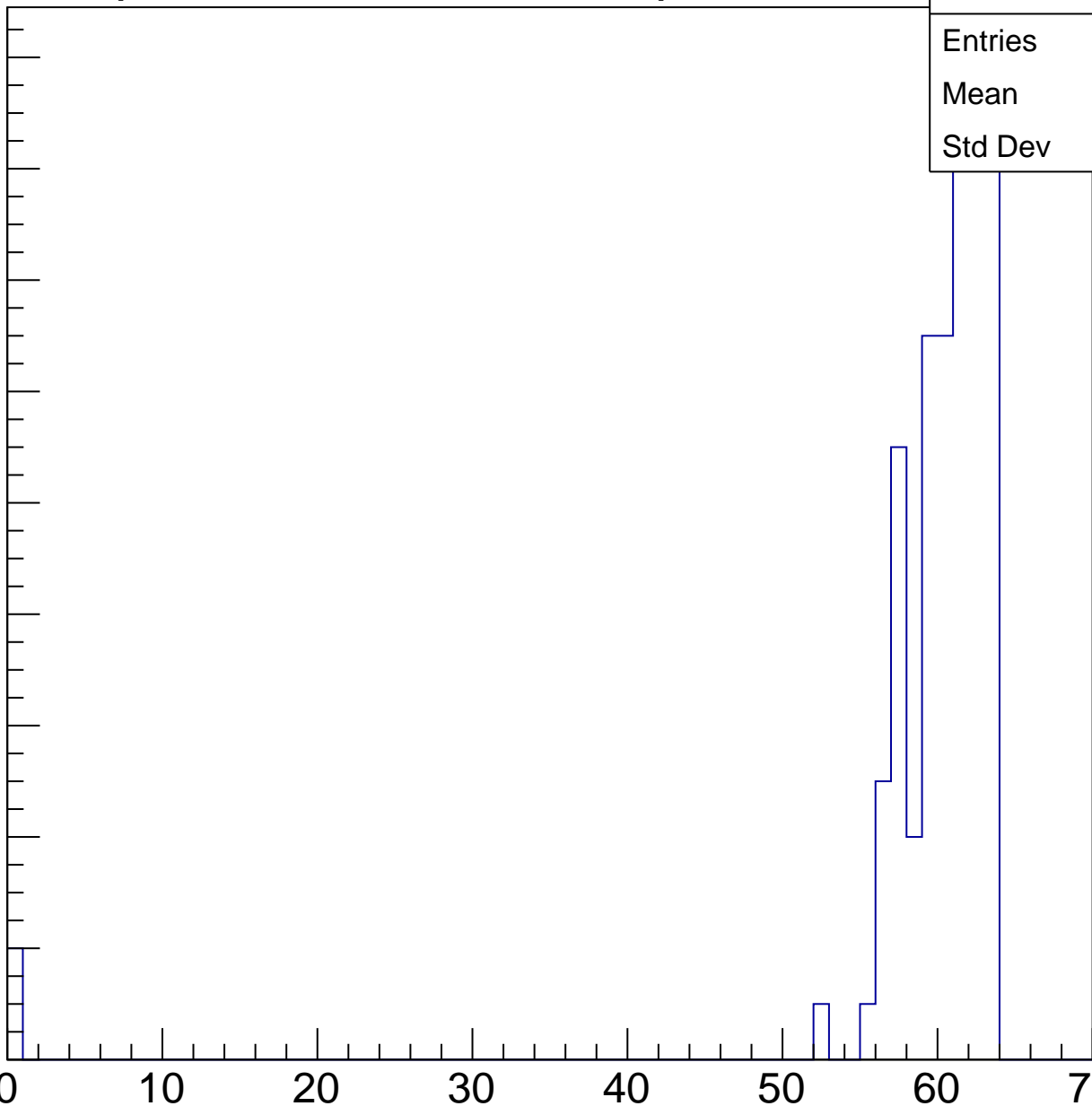
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.99
Std Dev	8.653

ampl



B1L001S, U21-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch7, adc7

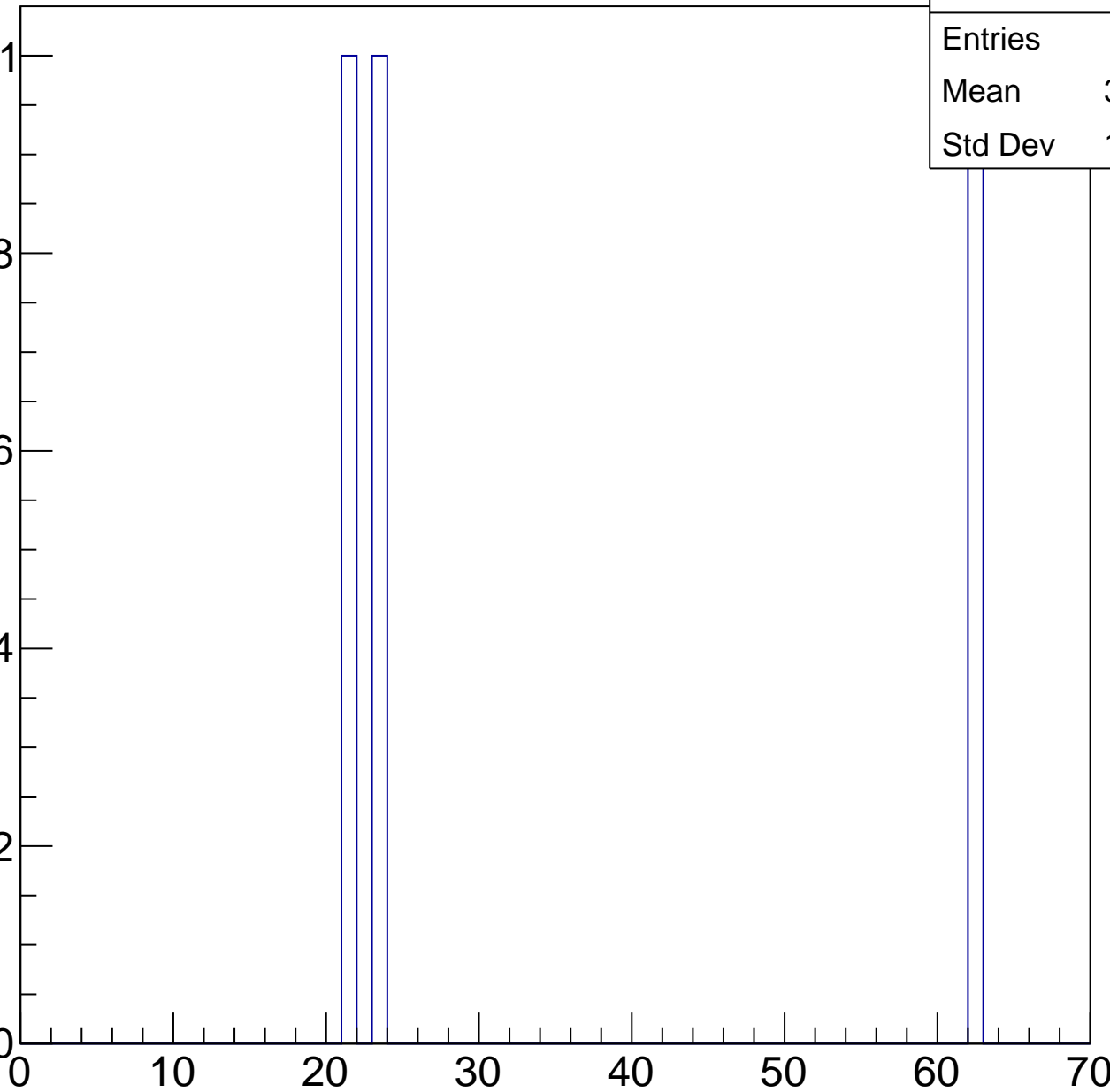
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	35.33
Std Dev	18.87

ampl



B1L001S, U21-ch8, adc0

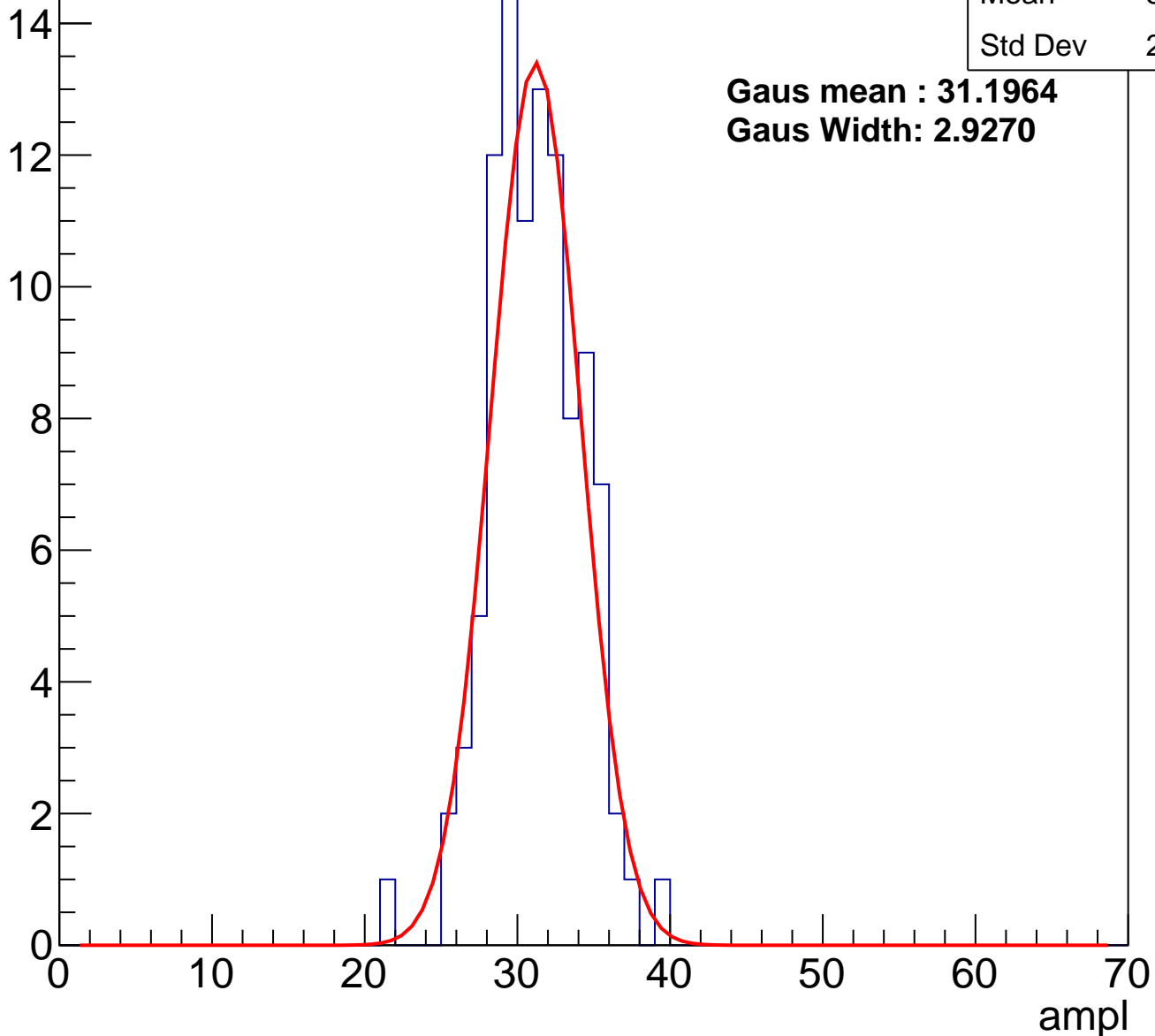
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	30.74
Std Dev	2.947

Gaus mean : 31.1664

Gaus Width: 2.9270

Entry



B1L001S, U21-ch8, adc1

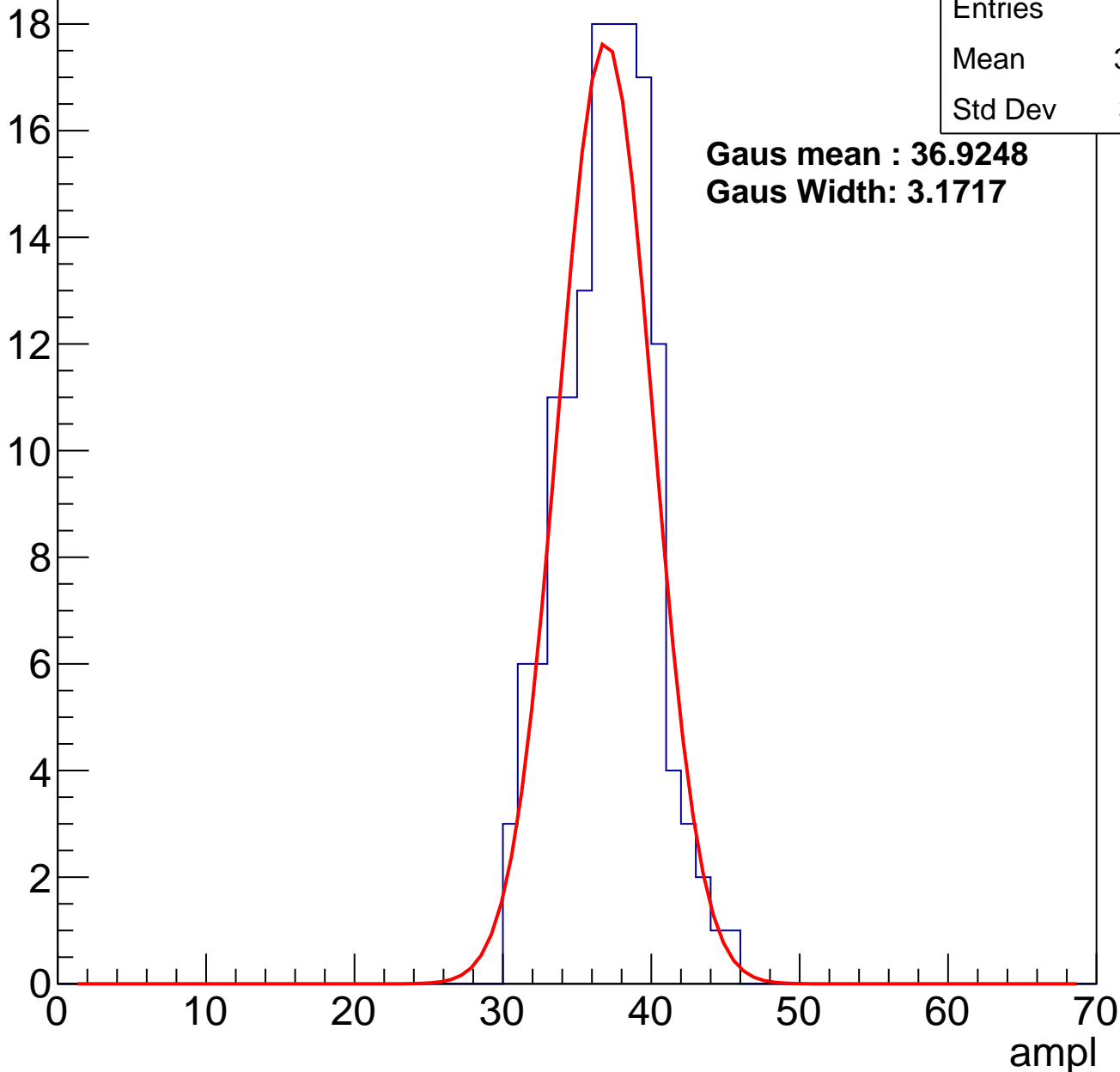
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	36.57
Std Dev	3.061

Gaus mean : 36.9248

Gaus Width: 3.1717

Entry



B1L001S, U21-ch8, adc2

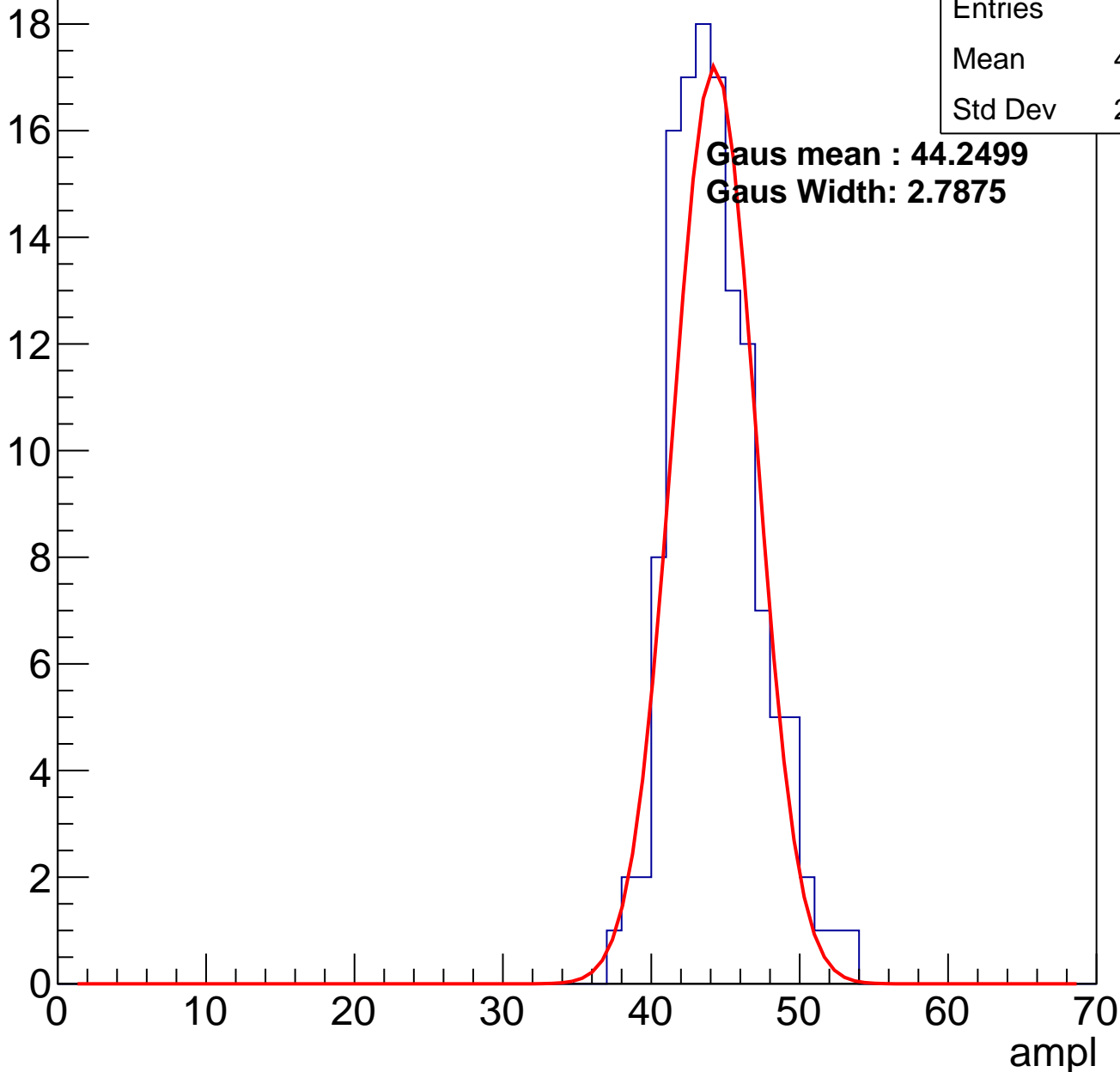
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	43.83
Std Dev	2.958

Gaus mean : 44.2499

Gaus Width: 2.7875

Entry



B1L001S, U21-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	49.95
Std Dev	2.912

Entry

16

14

12

10

8

6

4

2

0

0

10

20

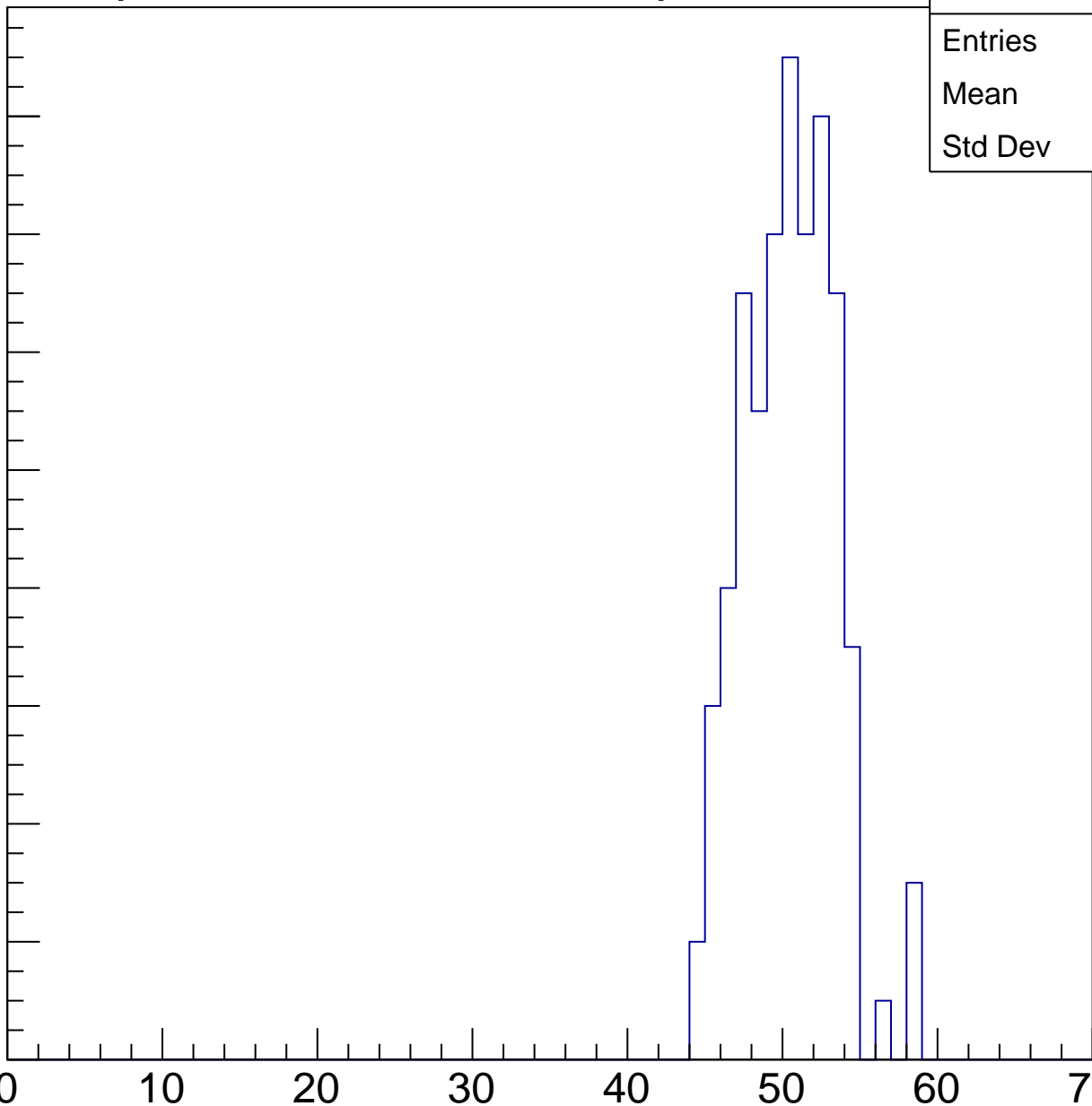
30

40

50

60

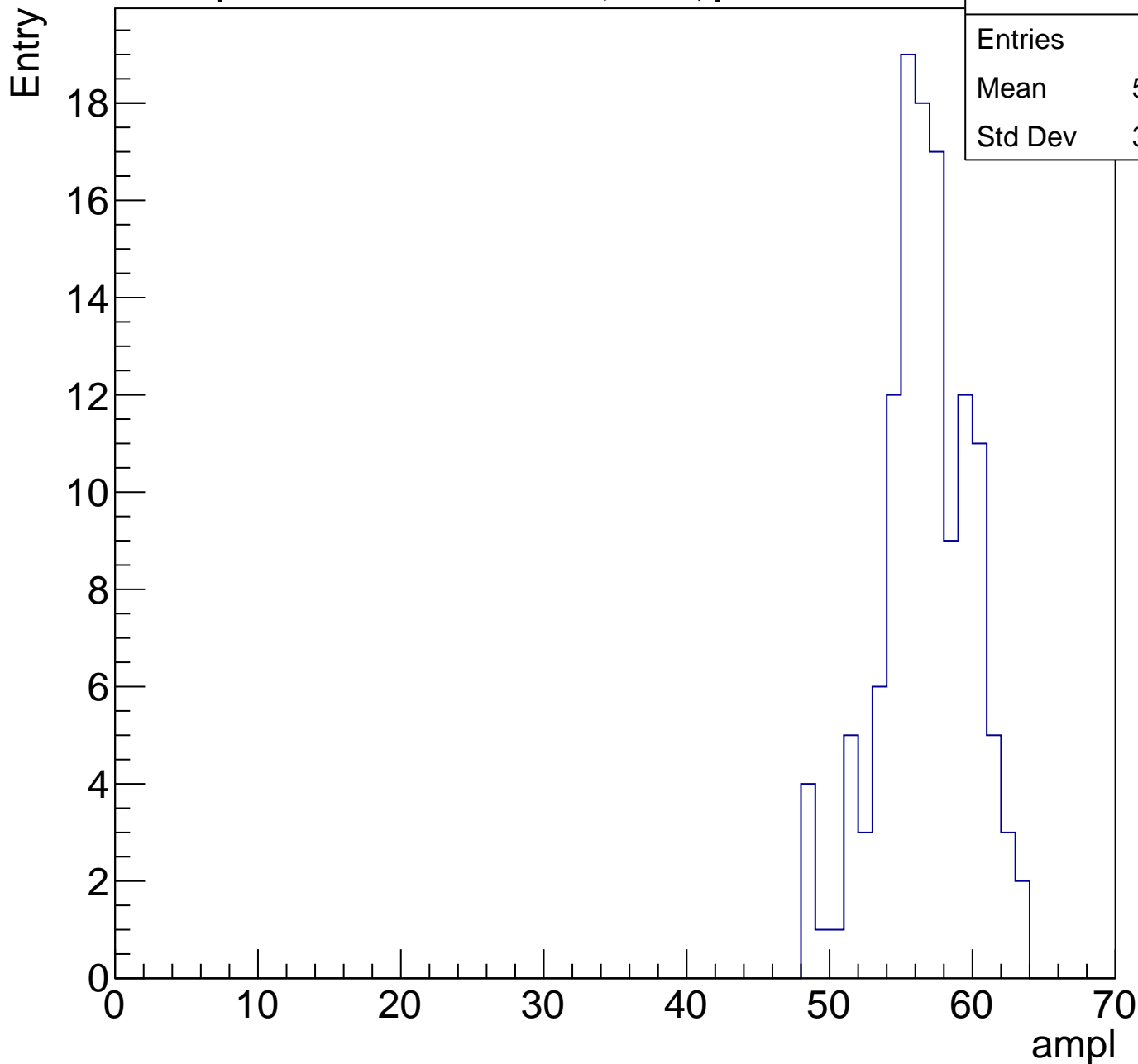
ampl



B1L001S, U21-ch8, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	56.23
Std Dev	3.183



B1L001S, U21-ch8, adc5

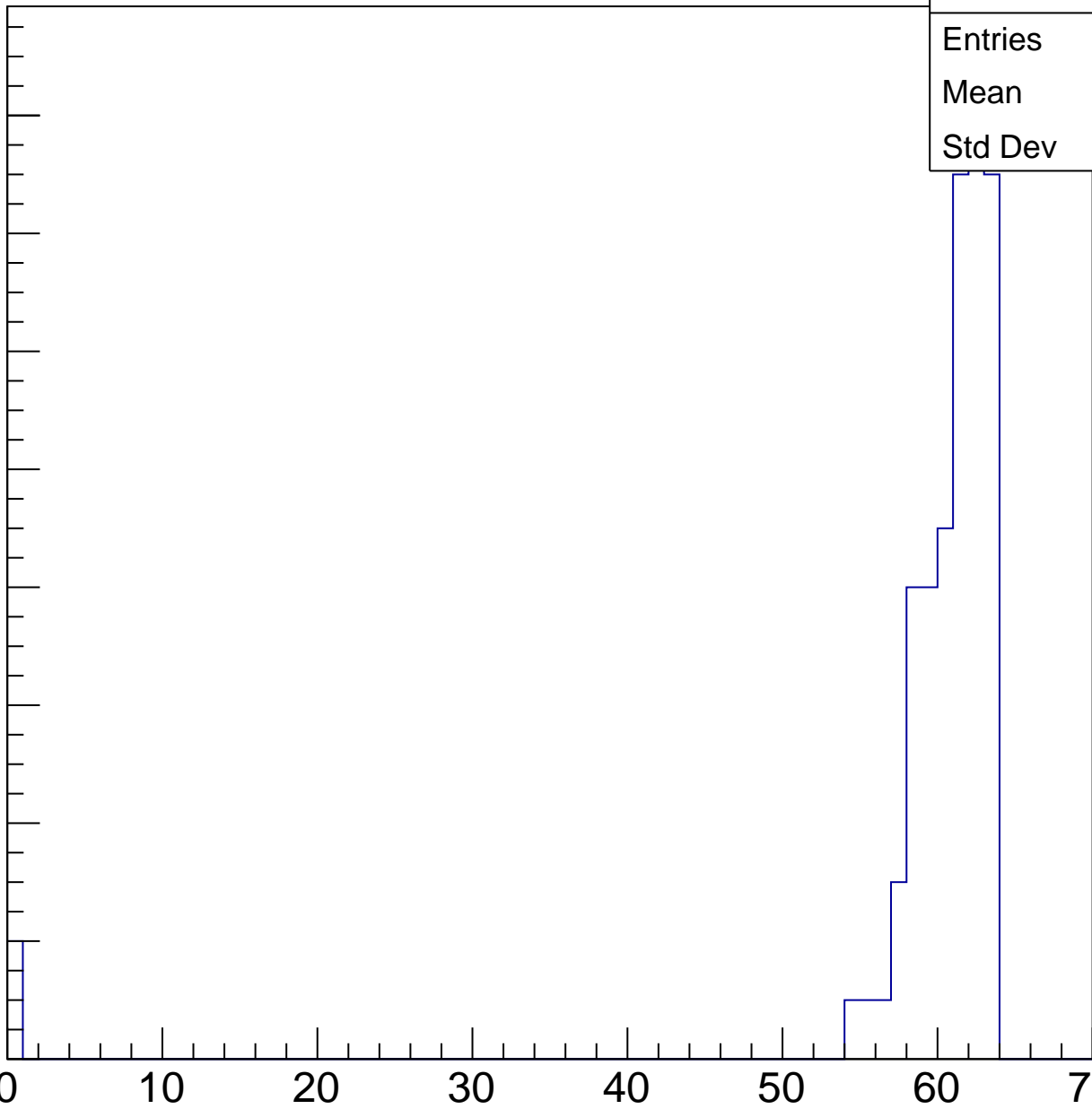
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.08
Std Dev	9.678

ampl



B1L001S, U21-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch8, adc7

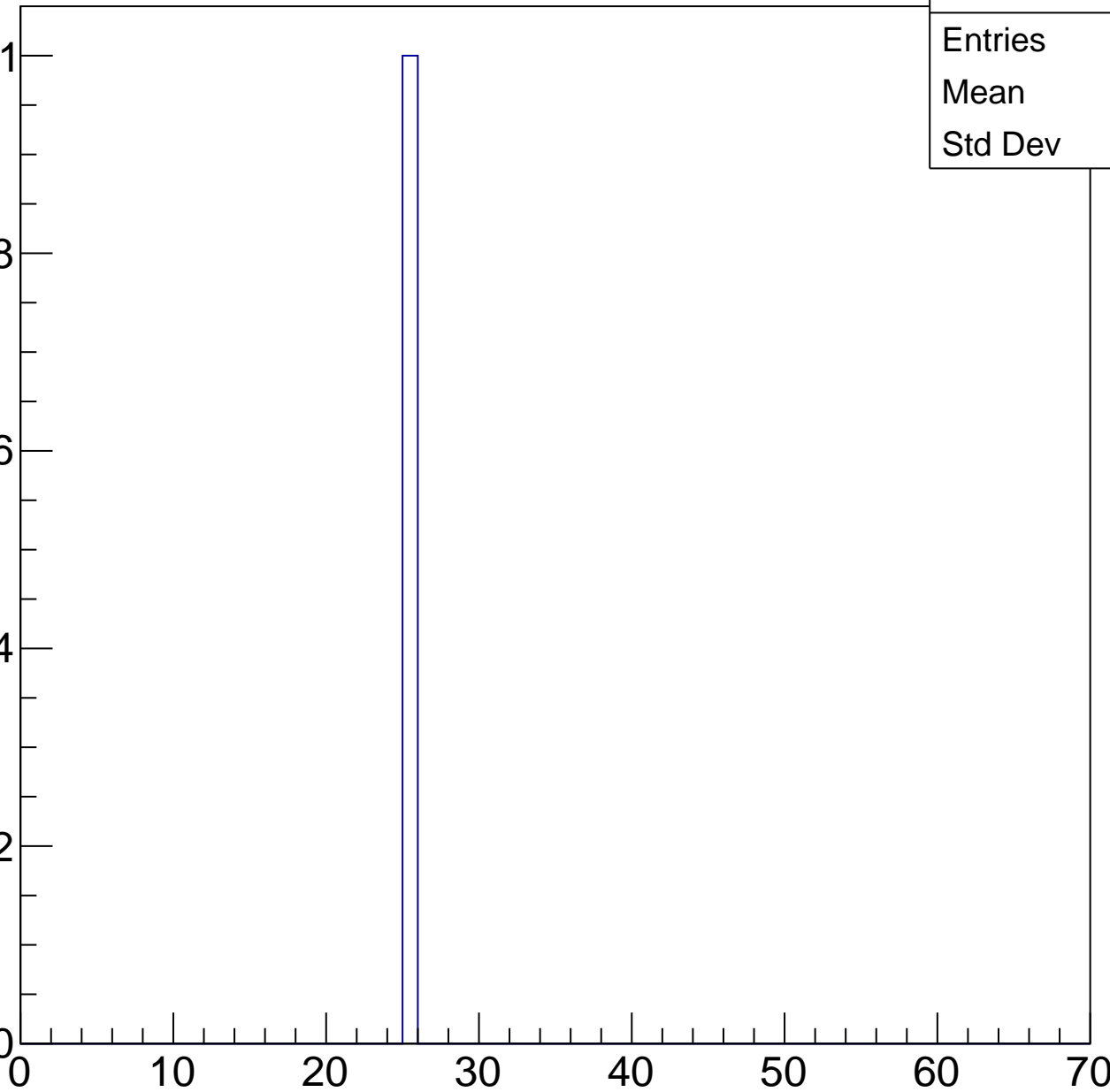
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L001S, U21-ch9, adc0

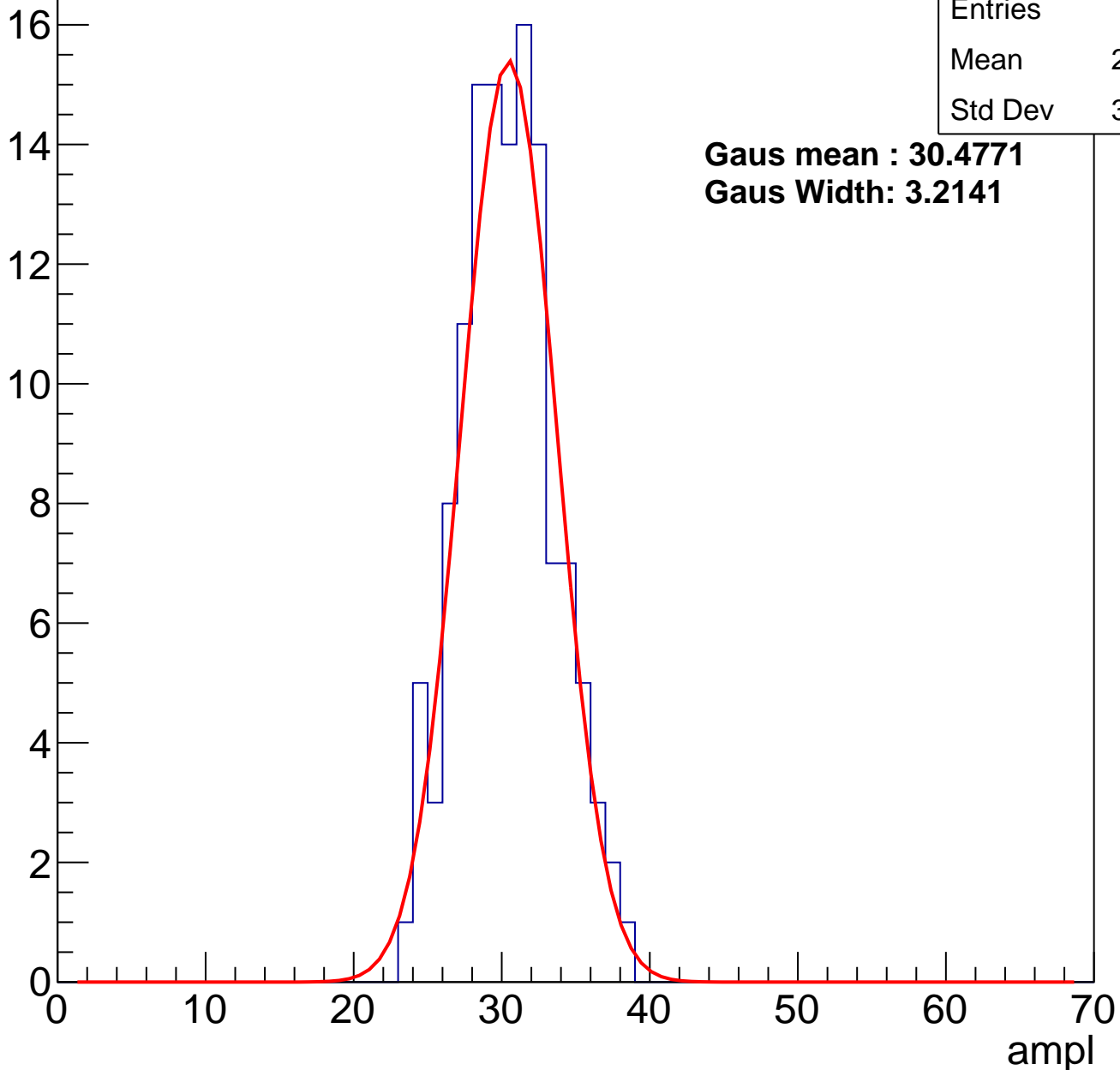
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	29.97
Std Dev	3.135

Gaus mean : 30.4771

Gaus Width: 3.2141

Entry



B1L001S, U21-ch9, adc1

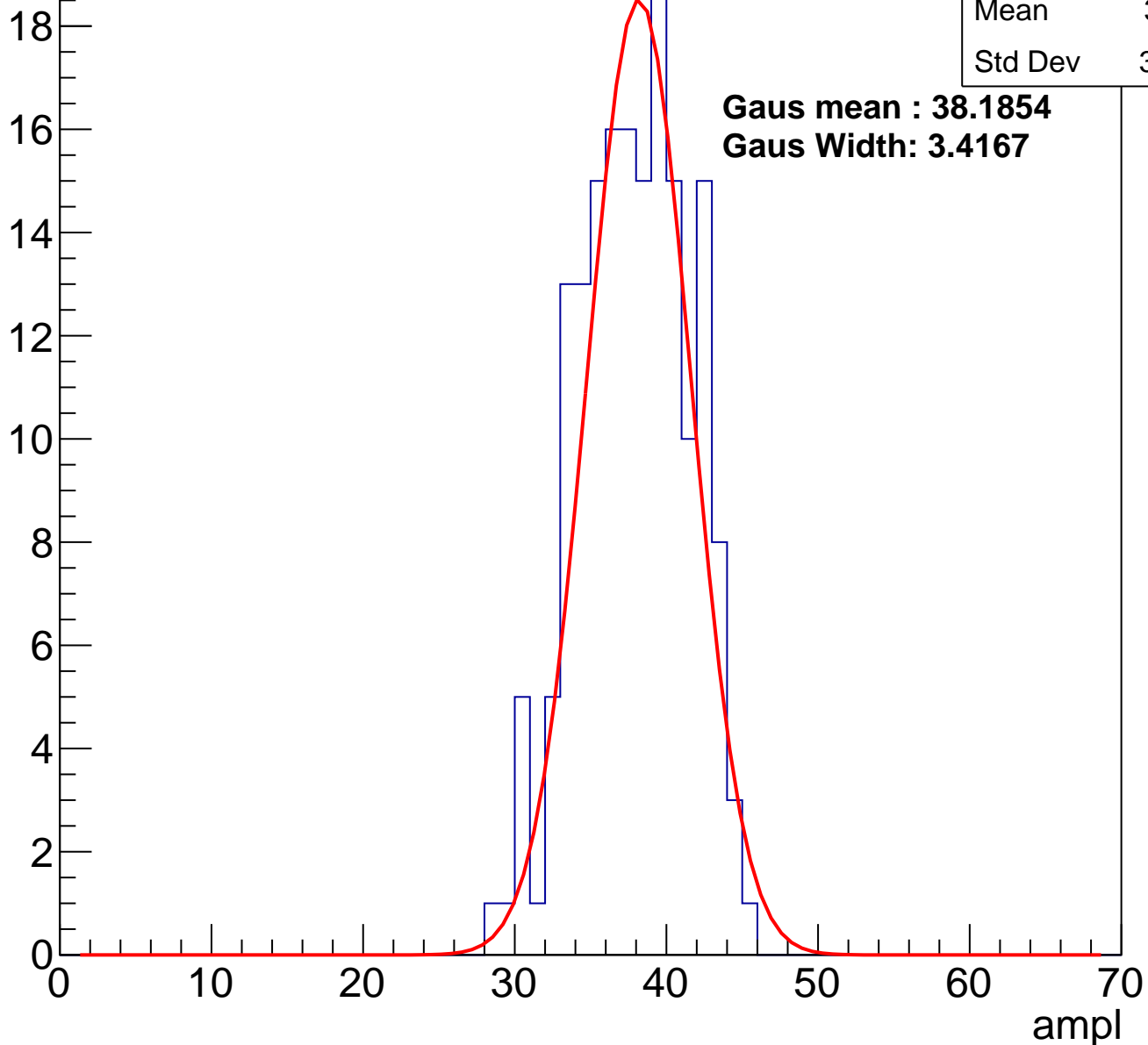
calib_packv5_042523_0143.root, FC#2, port C2

Entries	172
Mean	37.41
Std Dev	3.563

Gaus mean : 38.1854

Gaus Width: 3.4167

Entry



B1L001S, U21-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries 110

Mean 44.42

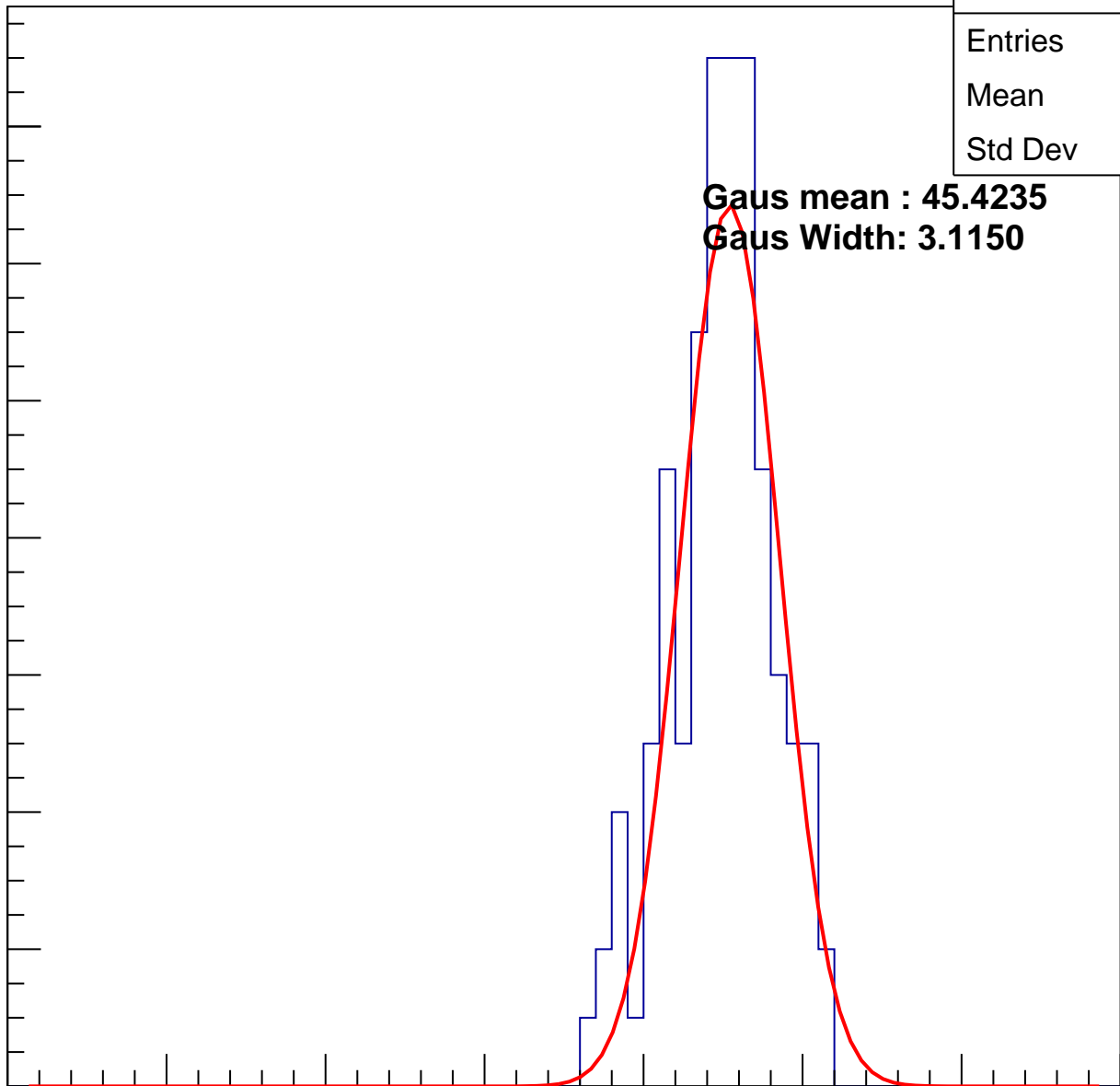
Std Dev 3.246

Gaus mean : 45.4235

Gaus Width: 3.1150

ampl

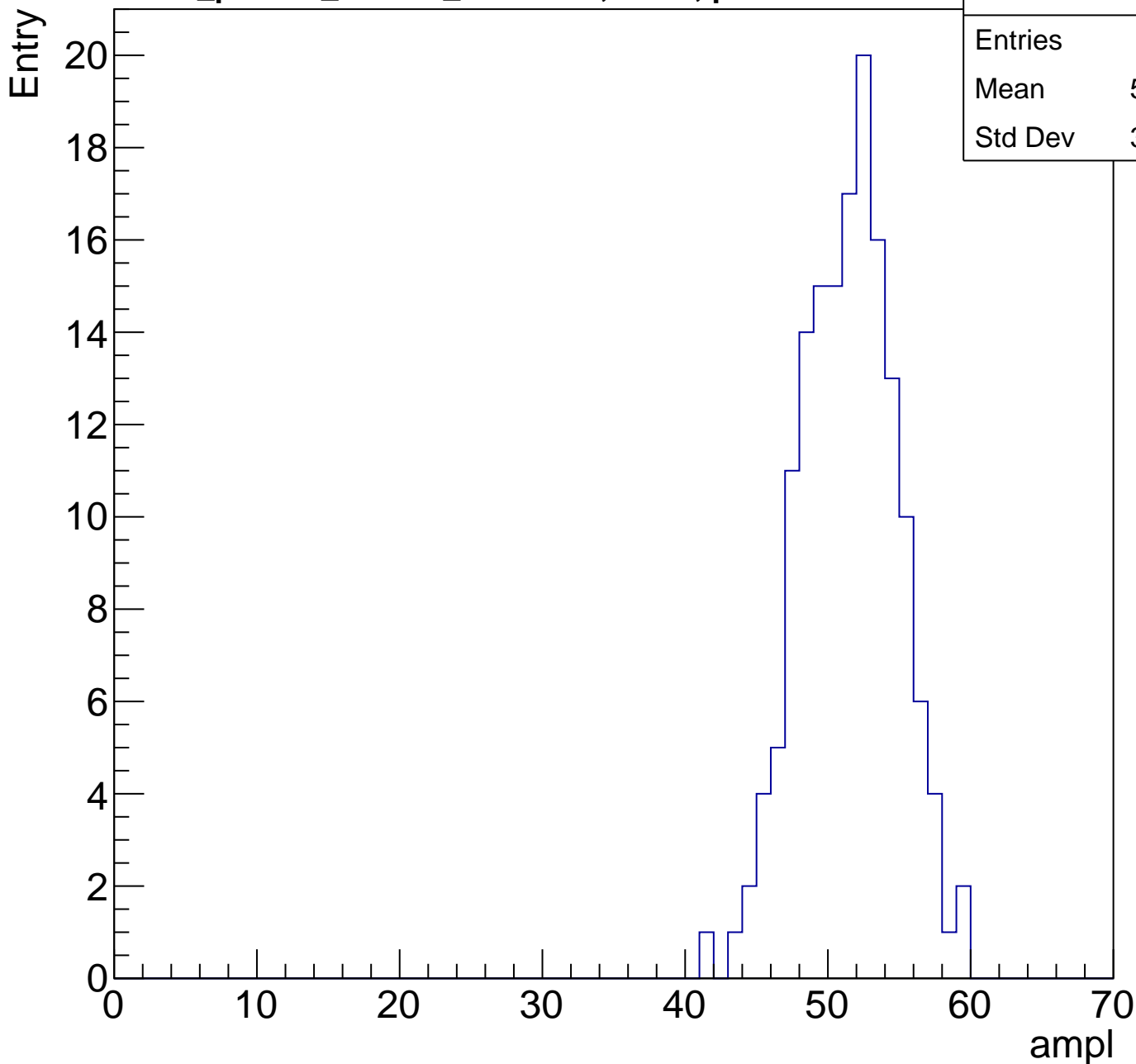
0 10 20 30 40 50 60 70



B1L001S, U21-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	50.97
Std Dev	3.329



B1L001S, U21-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	78
Mean	57.28
Std Dev	2.465

B1L001S, U21-ch9, adc5

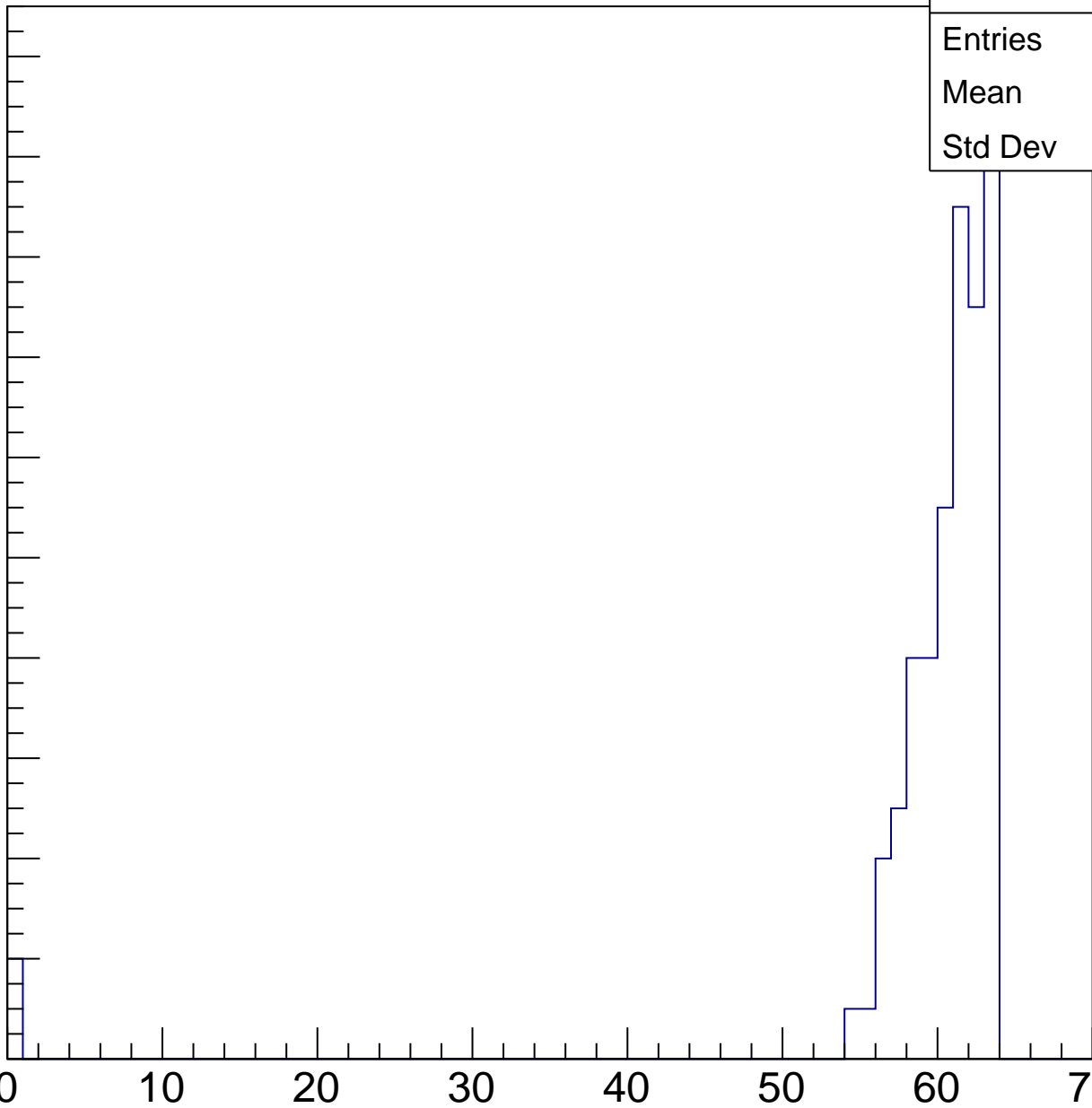
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.14
Std Dev	9.088

ampl



B1L001S, U21-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



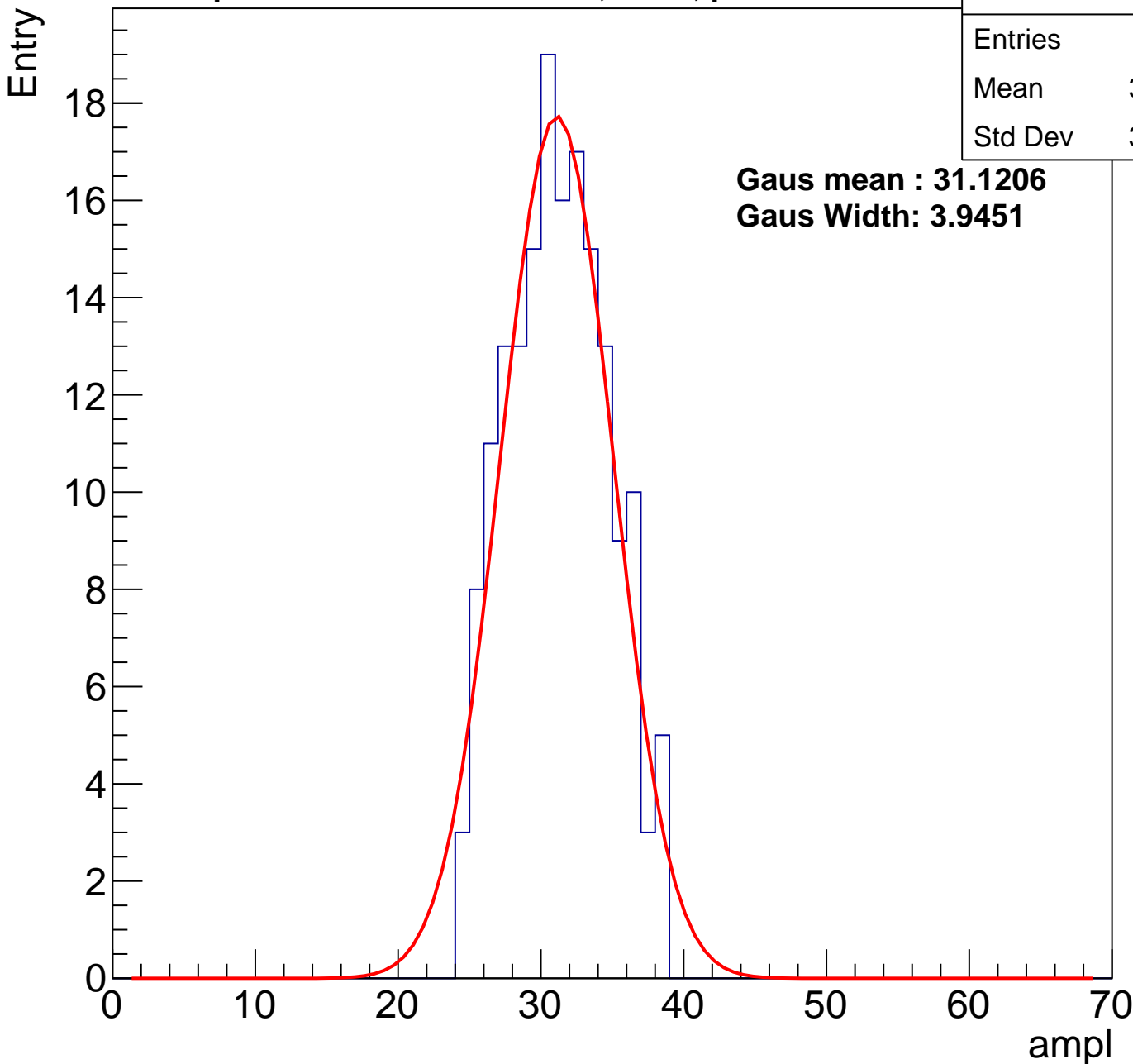
Entries	2
Mean	11
Std Dev	11

B1L001S, U21-ch10, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	30.77
Std Dev	3.469

Gaus mean : 31.1206
Gaus Width: 3.9451



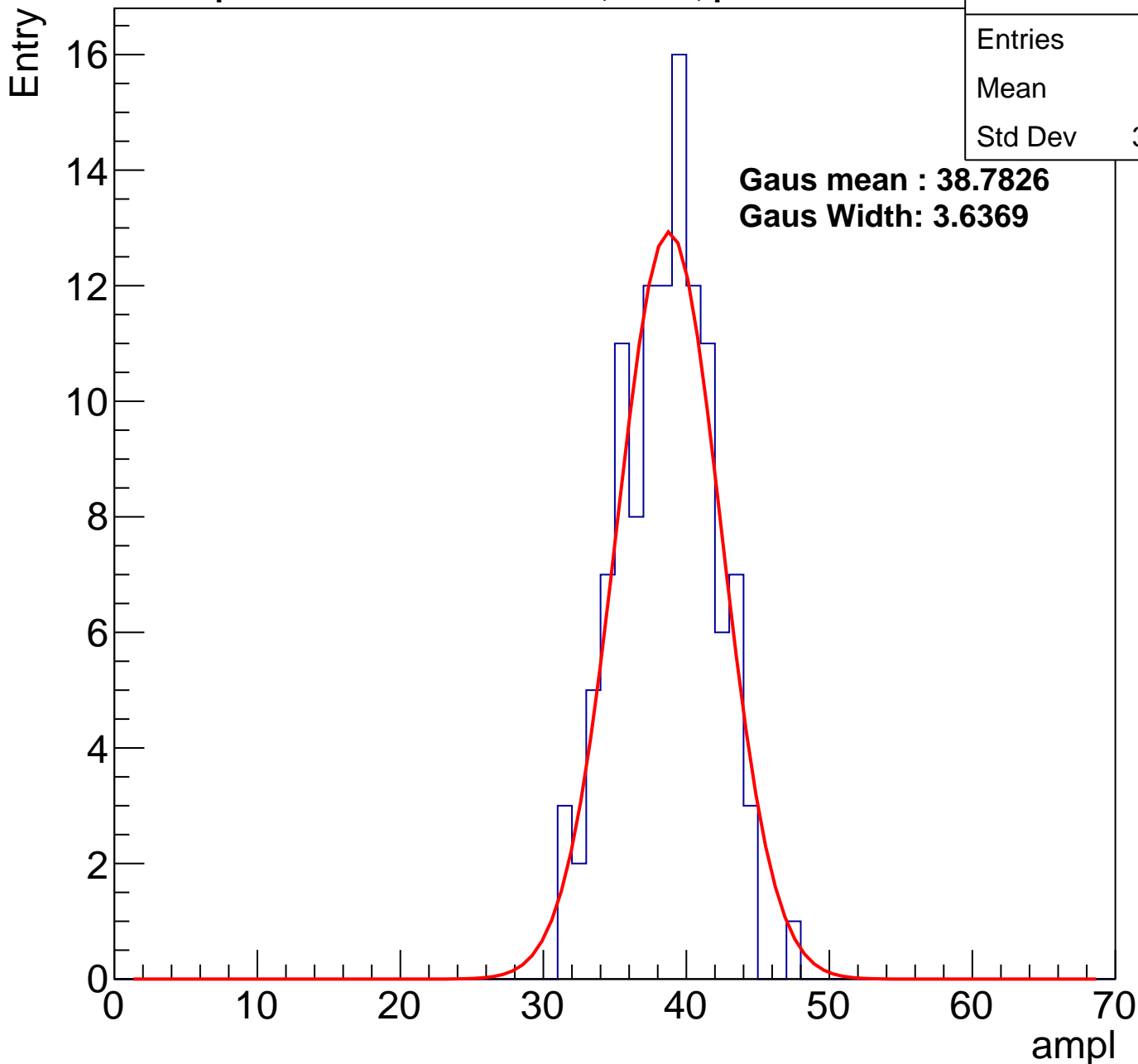
B1L001S, U21-ch10, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	38.1
Std Dev	3.244

Gaus mean : 38.7826

Gaus Width: 3.6369



B1L001S, U21-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

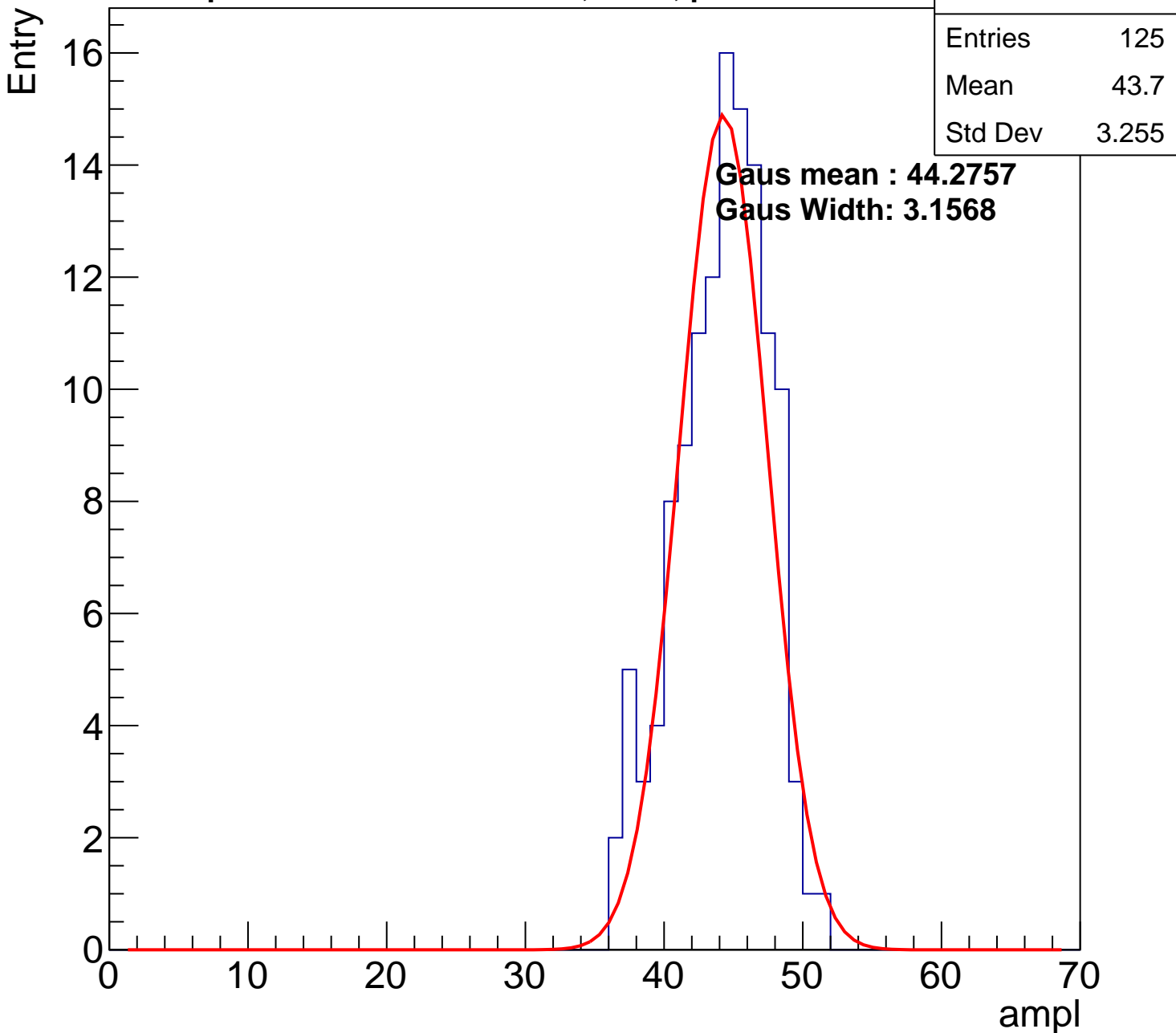
Entries	125
Mean	43.7
Std Dev	3.255

Gaus mean : 44.2757

Gaus Width: 3.1568

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

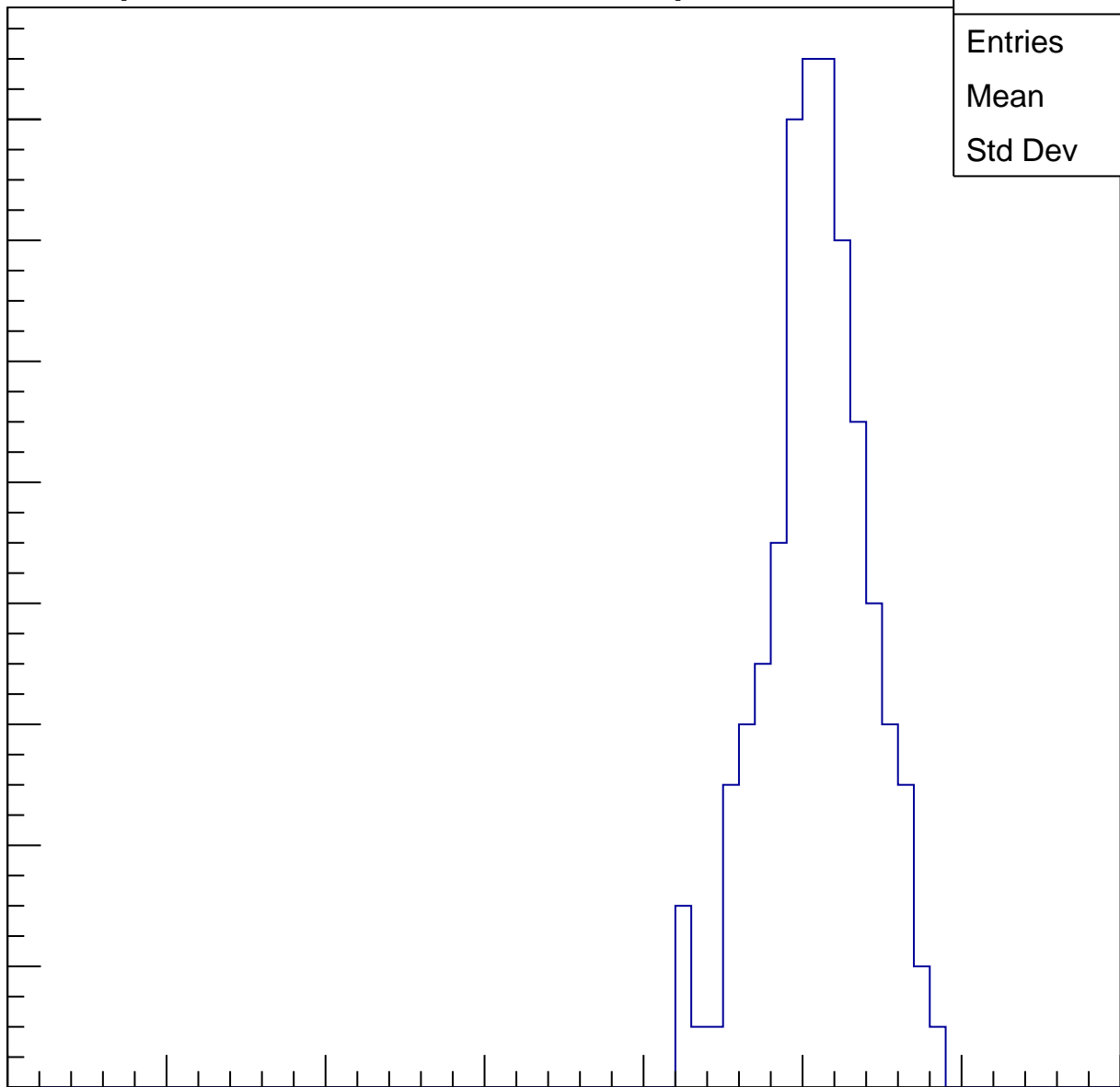
Entries	129
Mean	50.4
Std Dev	3.276

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

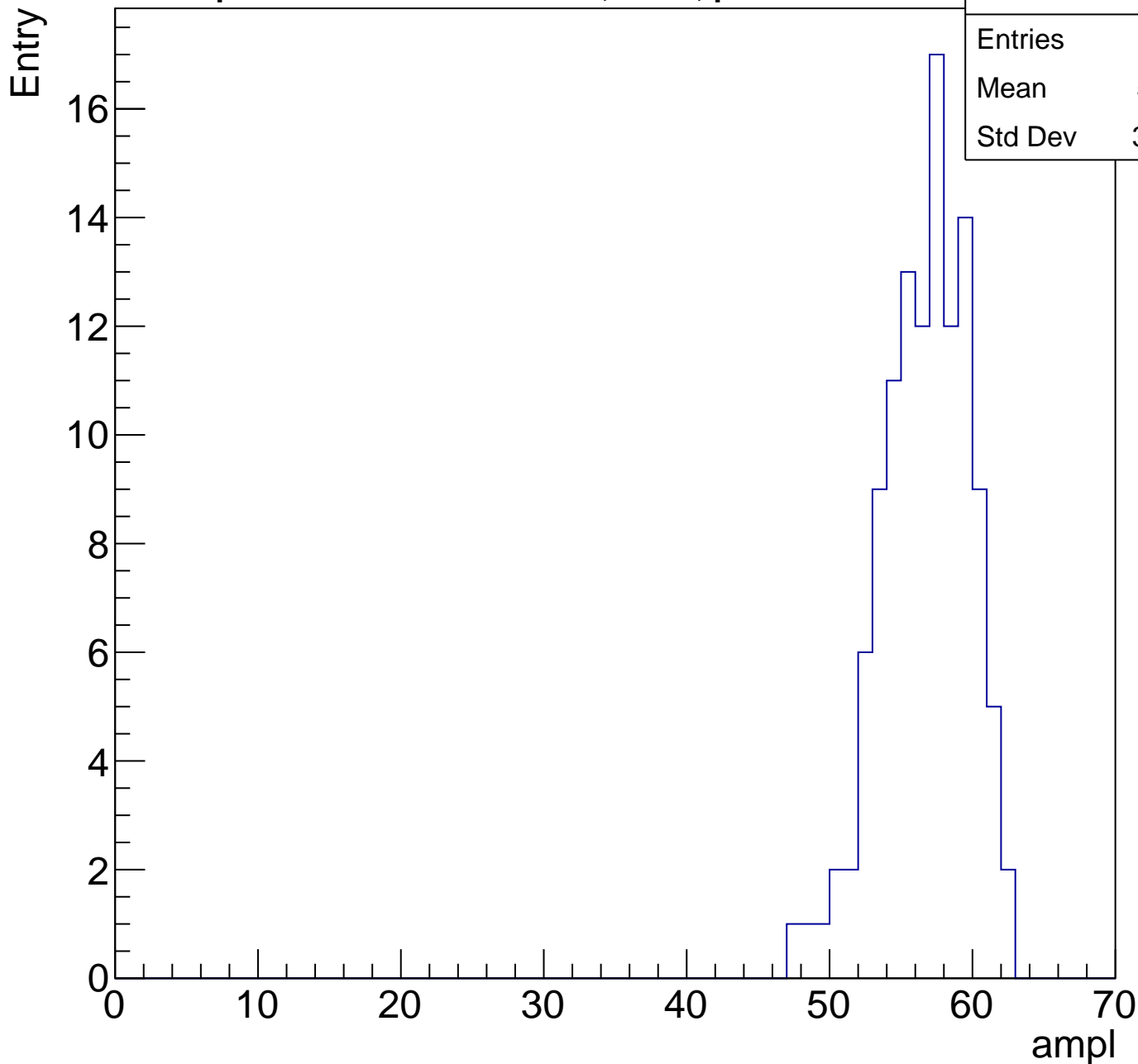
ampl



B1L001S, U21-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	56.21
Std Dev	3.034



B1L001S, U21-ch10, adc5

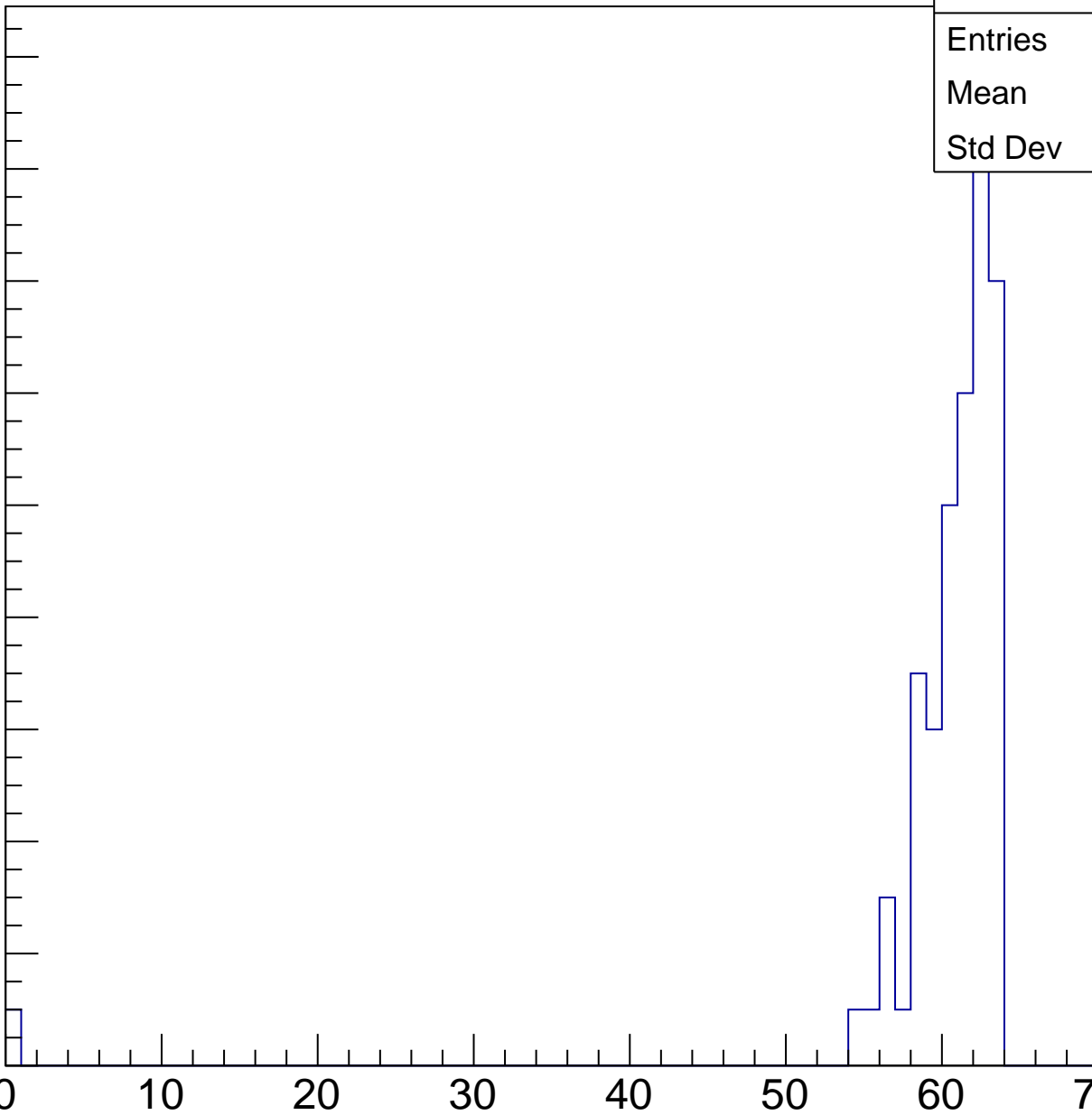
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.78
Std Dev	7.314

ampl

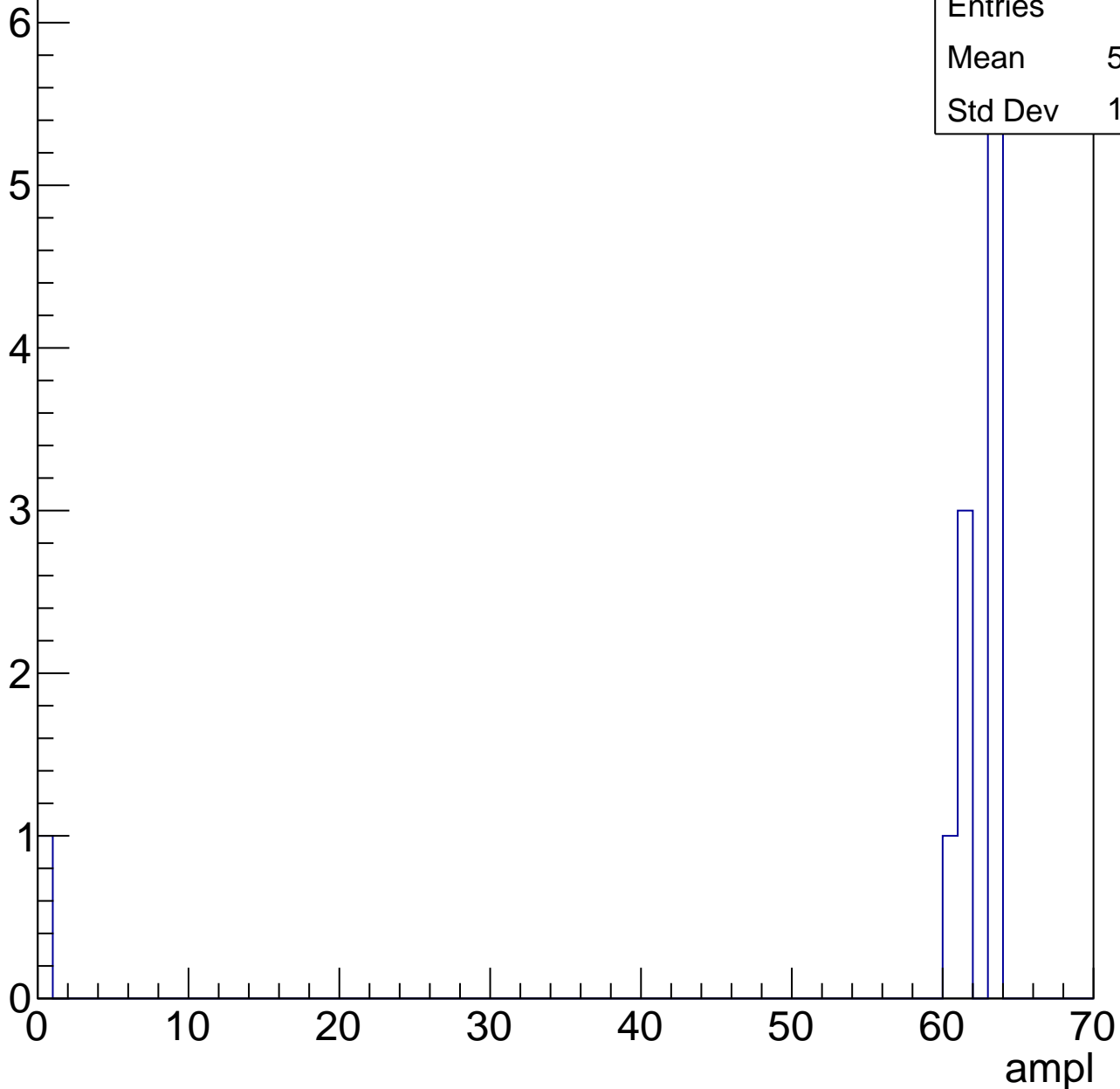


B1L001S, U21-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.45
Std Dev	17.89



B1L001S, U21-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch11, adc0

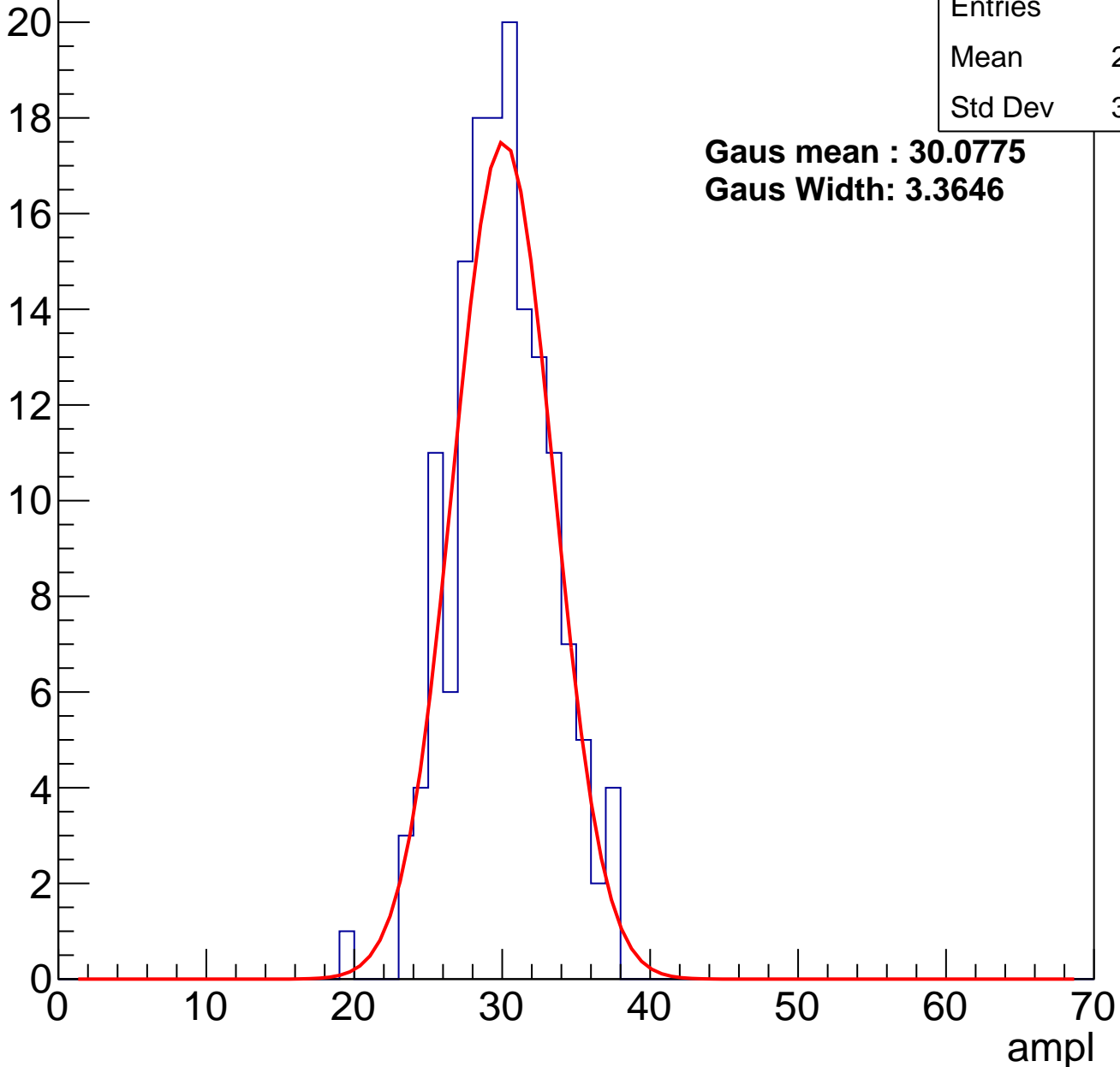
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.55
Std Dev	3.292

Gaus mean : 30.0775

Gaus Width: 3.3646

Entry



B1L001S, U21-ch11, adc1

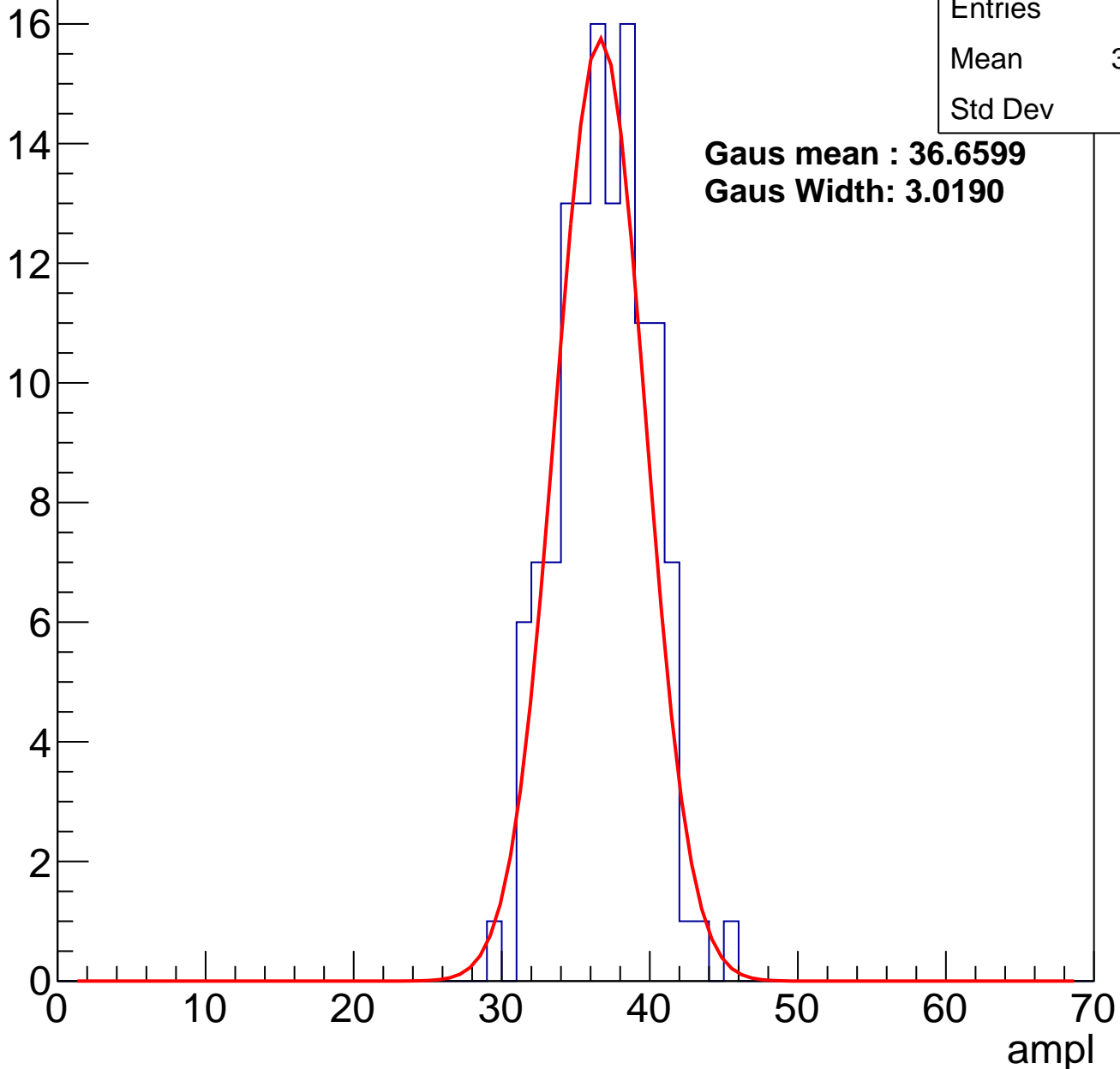
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.44
Std Dev	2.99

Gaus mean : 36.6599

Gaus Width: 3.0190

Entry



B1L001S, U21-ch11, adc2

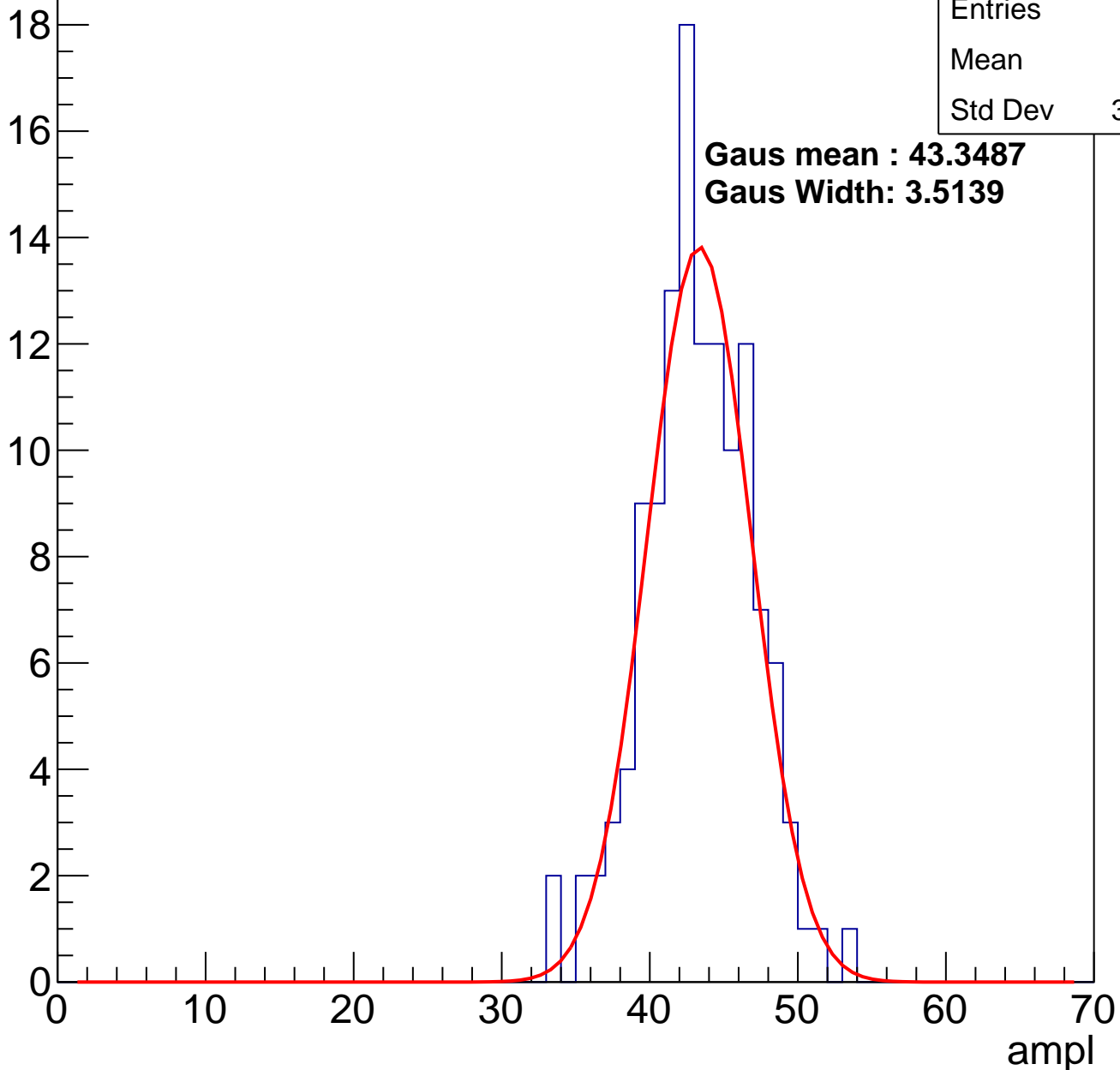
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	42.8
Std Dev	3.623

Gaus mean : 43.3487

Gaus Width: 3.5139

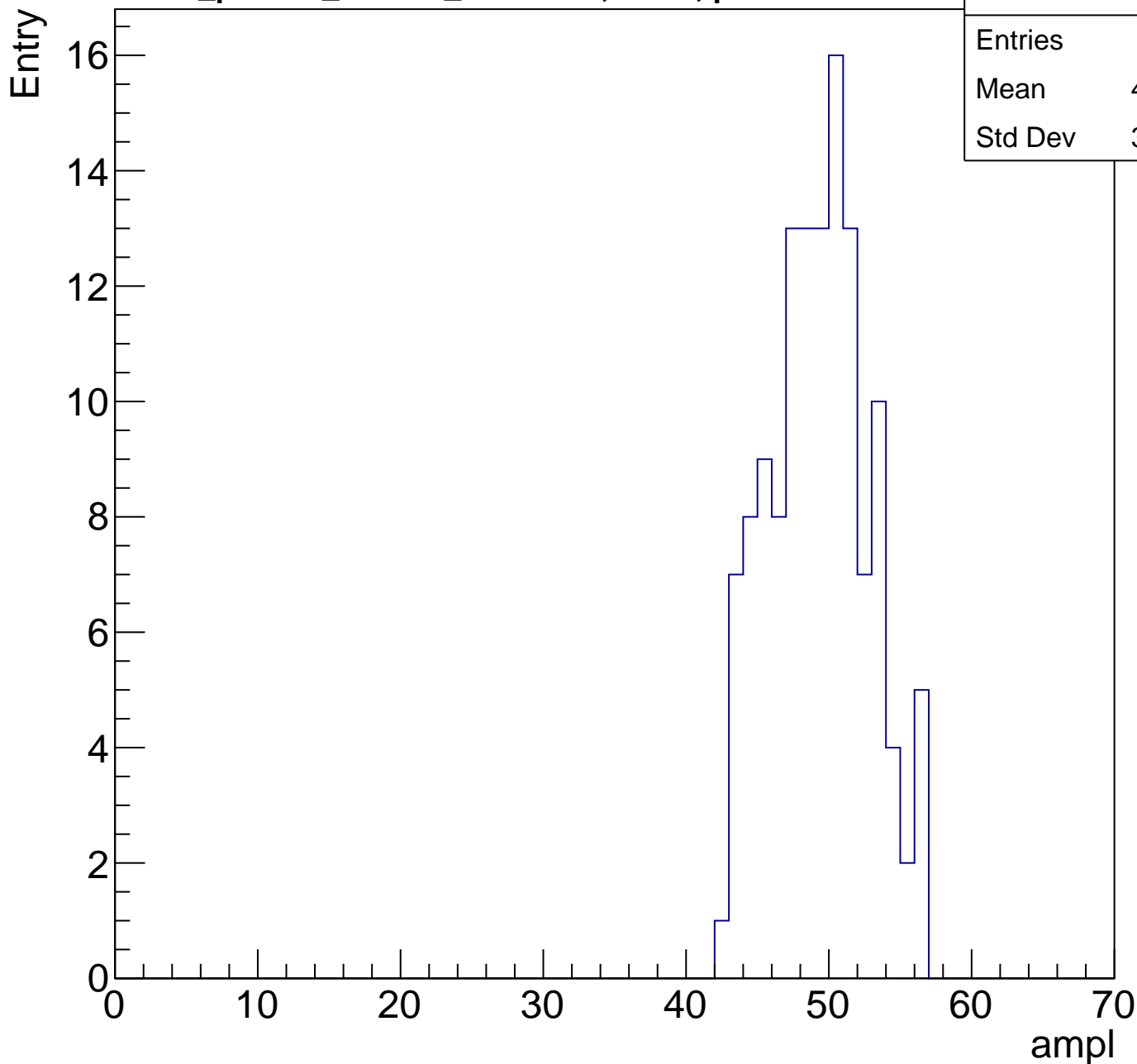
Entry



B1L001S, U21-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	48.86
Std Dev	3.396



B1L001S, U21-ch11, adc4

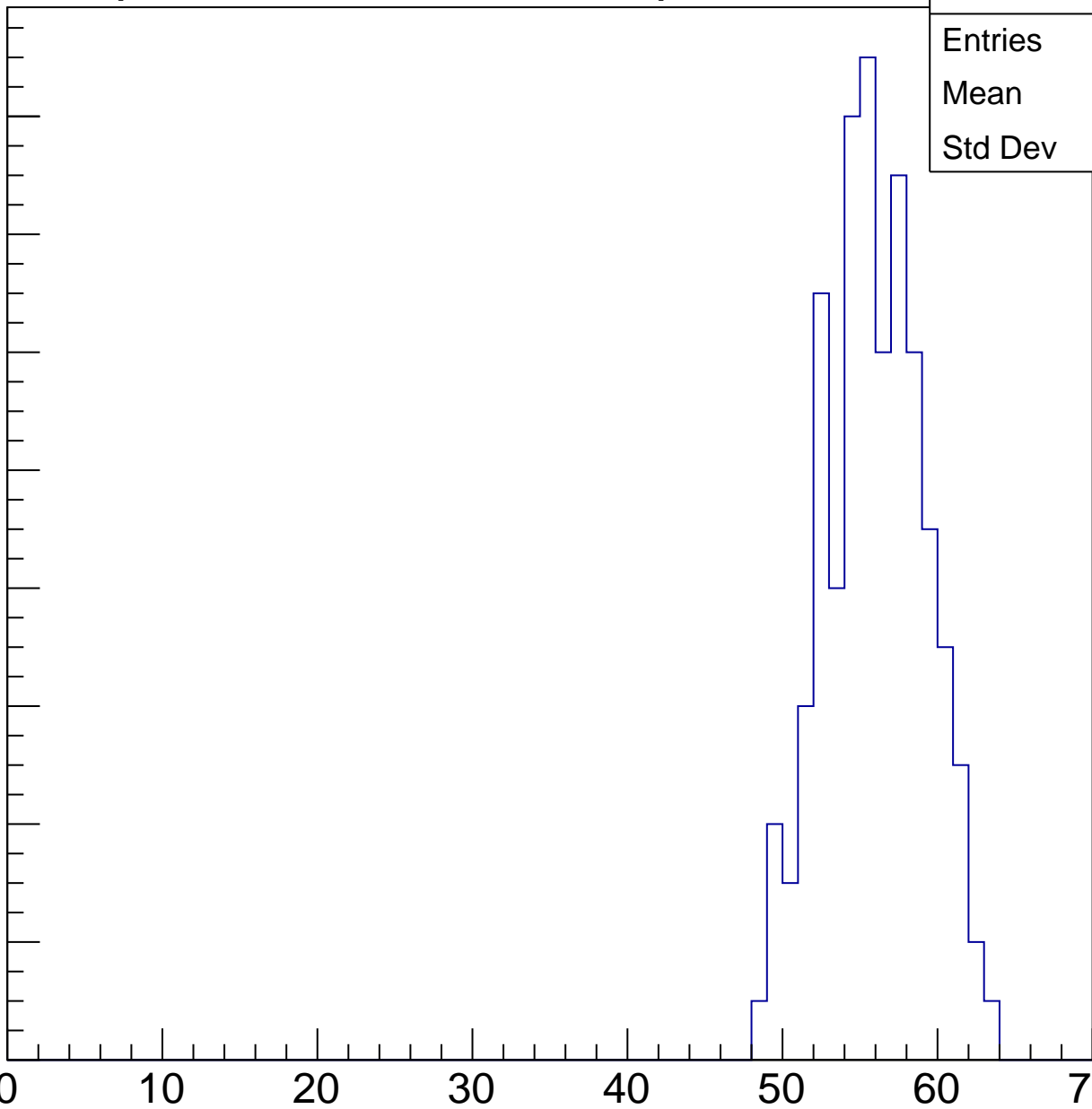
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	131
Mean	55.46
Std Dev	3.201

ampl



B1L001S, U21-ch11, adc5

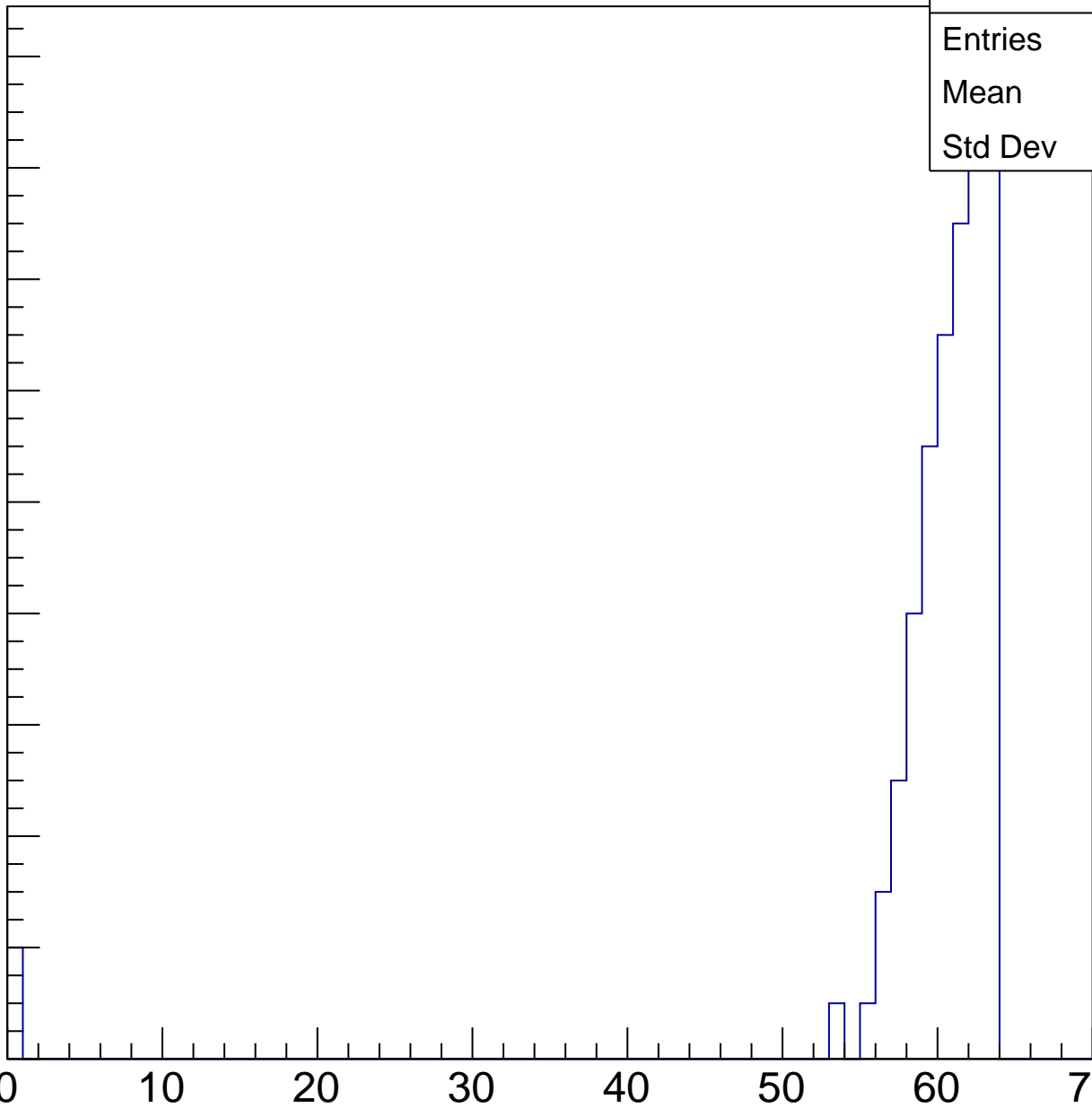
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.11
Std Dev	8.977

ampl



B1L001S, U21-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch12, adc0

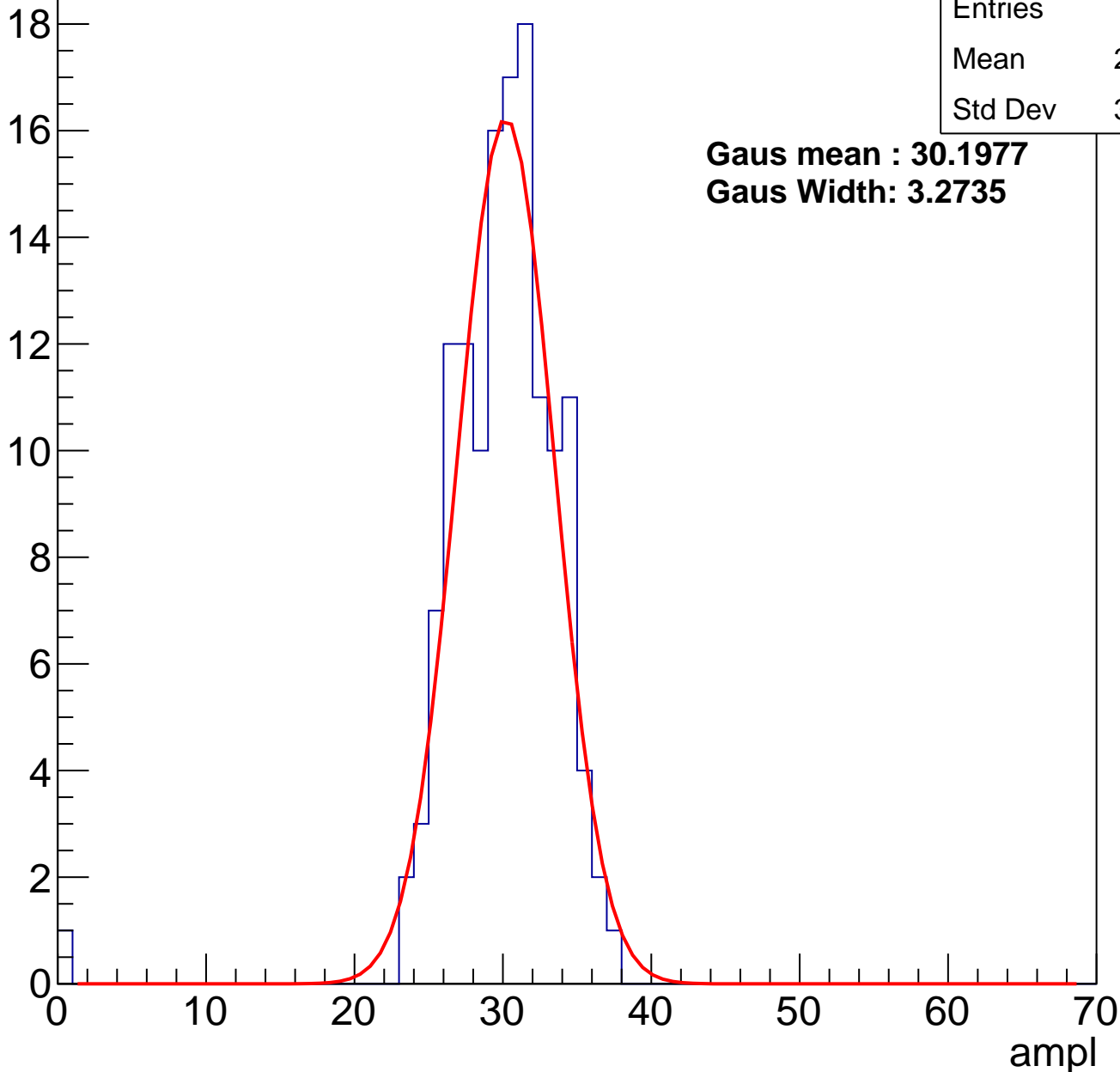
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.53
Std Dev	3.967

Gaus mean : 30.1977

Gaus Width: 3.2735

Entry



B1L001S, U21-ch12, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.35
Std Dev	3.186

Gaus mean : 36.9774

Gaus Width: 3.1819

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

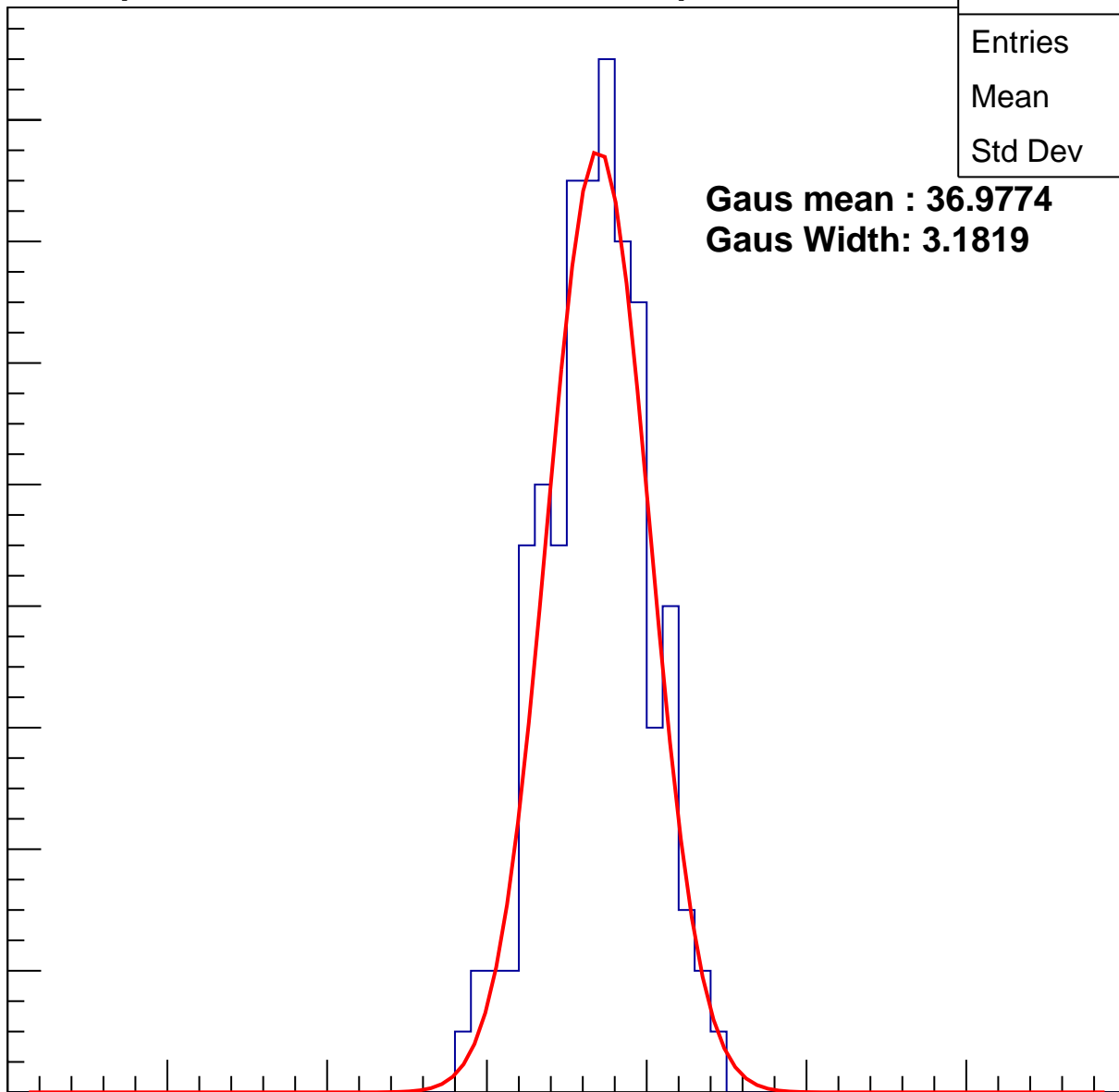
40

50

60

70

ampl



B1L001S, U21-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

117

Mean

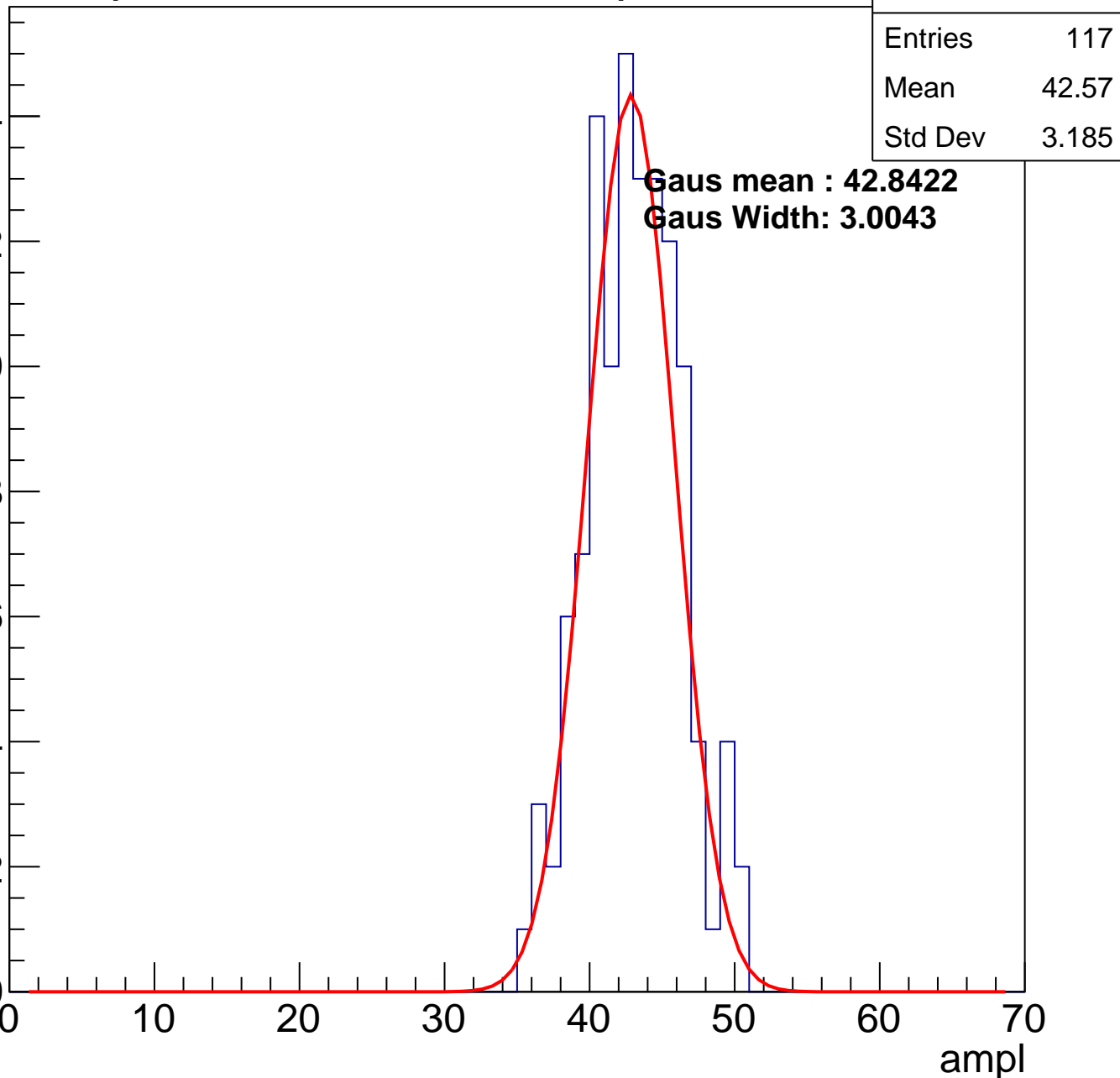
42.57

Std Dev

3.185

Gaus mean : 42.8422

Gaus Width: 3.0043

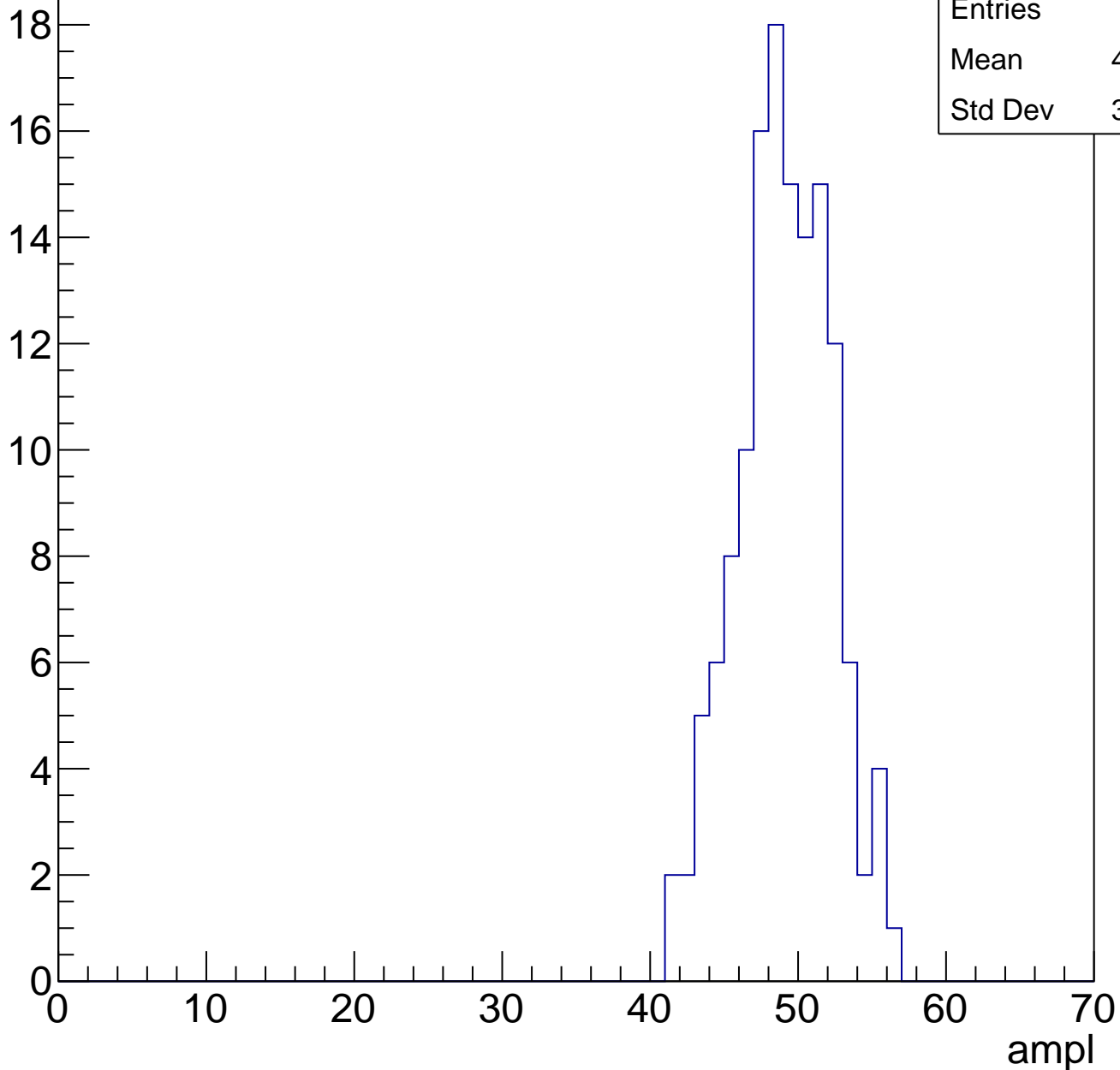


B1L001S, U21-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	48.58
Std Dev	3.154

Entry



B1L001S, U21-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	55.48
Std Dev	2.88

Entry

16

14

12

10

8

6

4

2

0

0

10

20

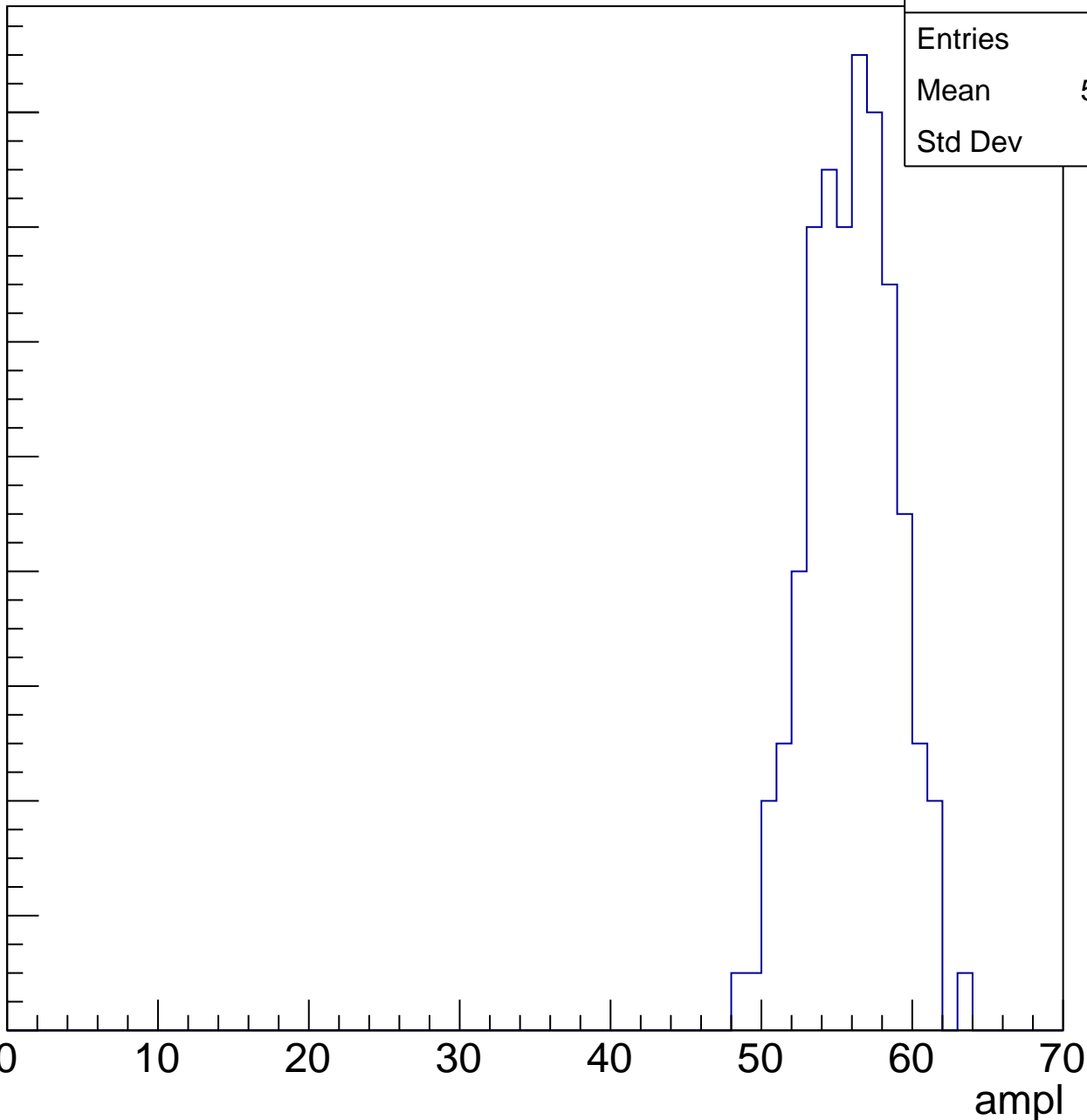
30

40

50

60

ampl



B1L001S, U21-ch12, adc5

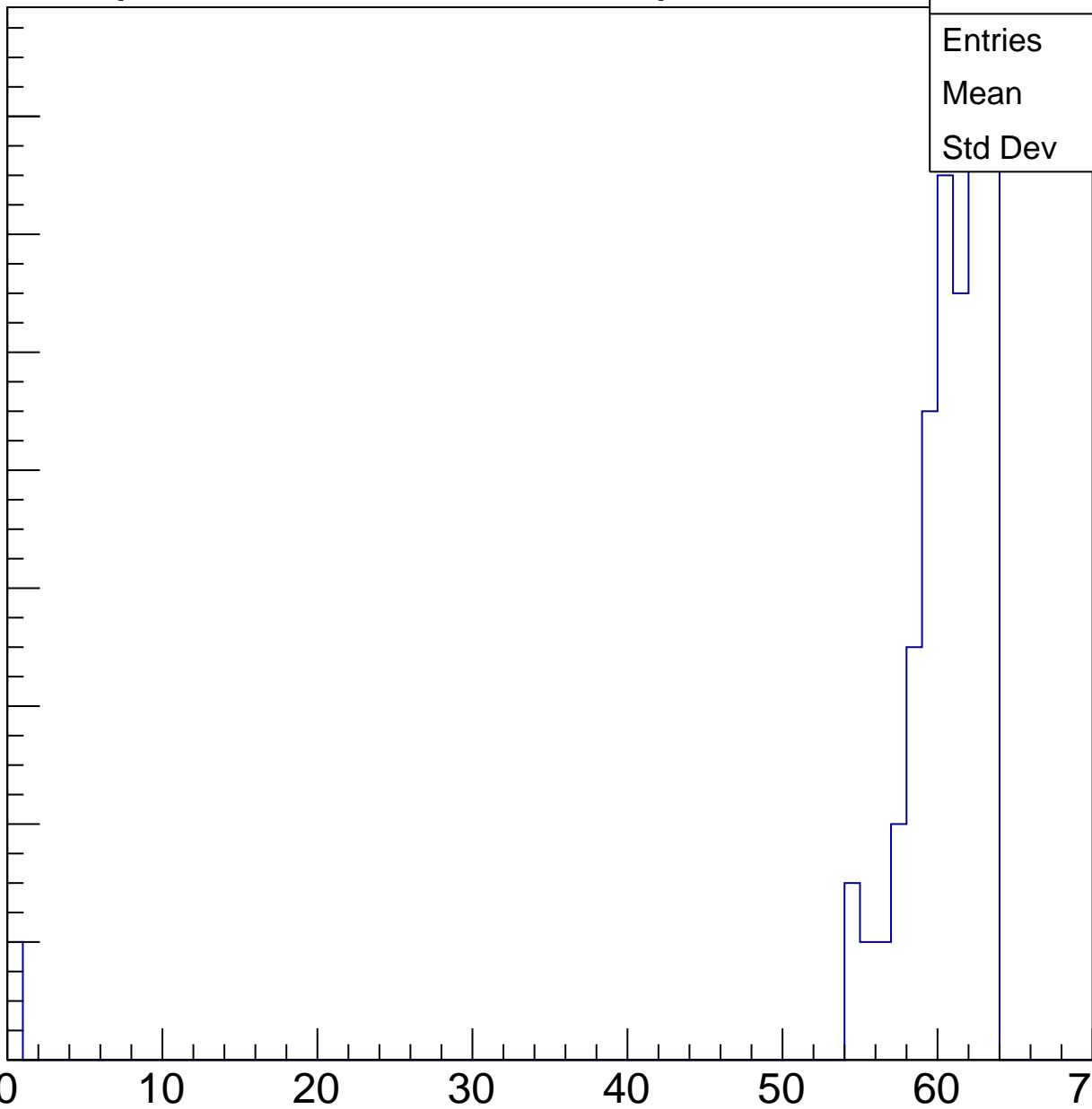
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.95
Std Dev	9.085

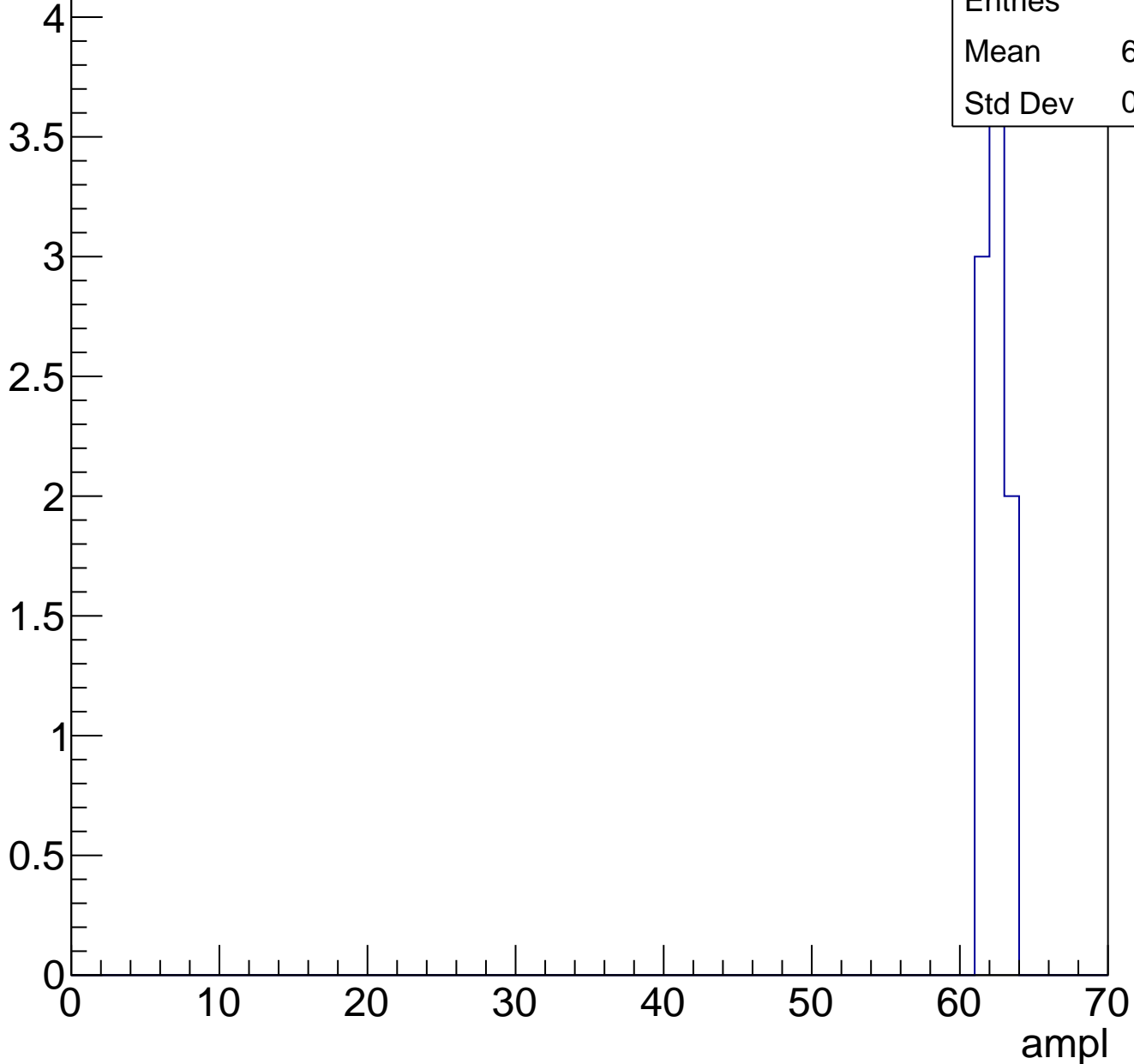
ampl



B1L001S, U21-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch12, adc7

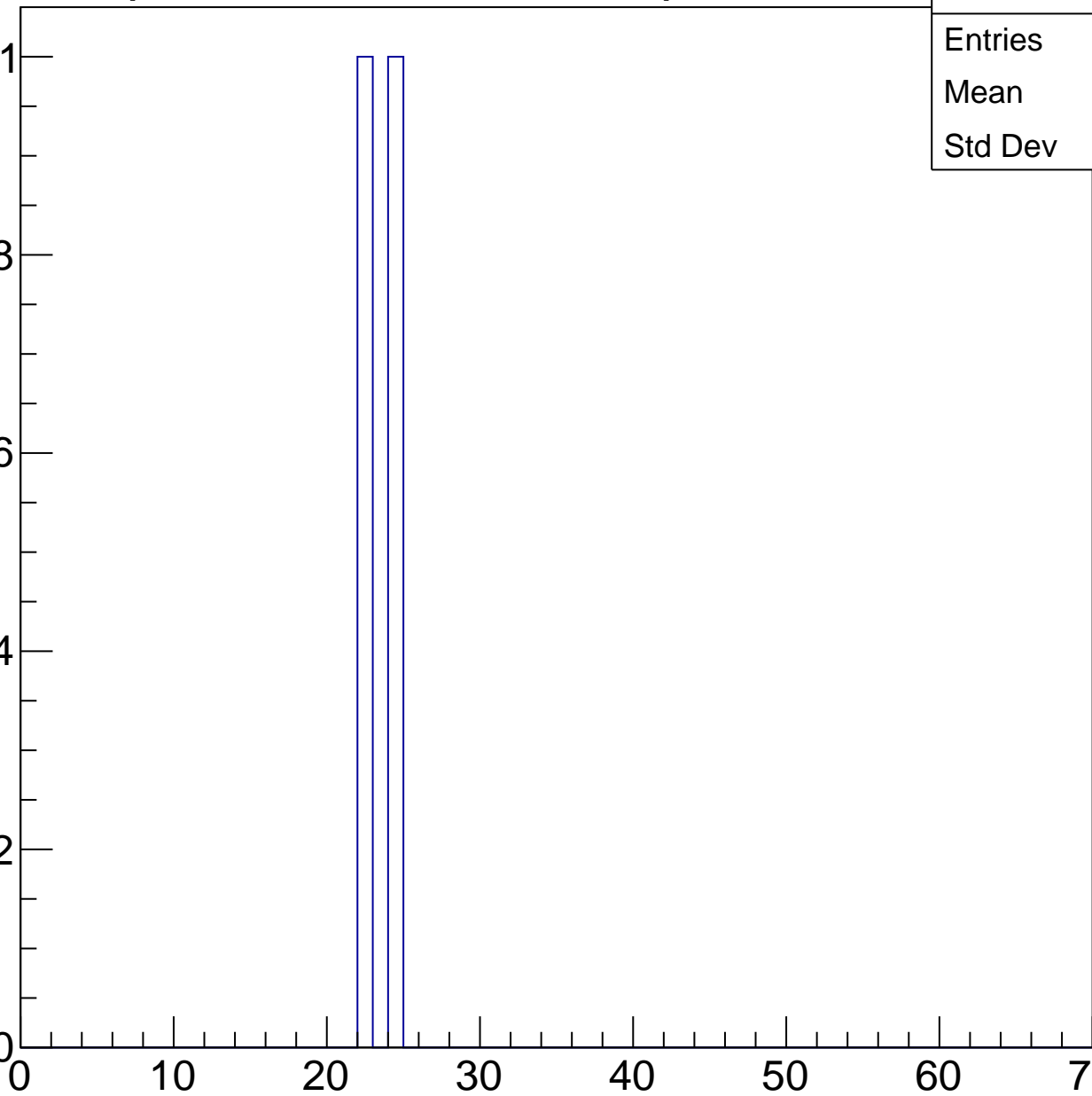
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23
Std Dev	1

ampl



B1L001S, U21-ch13, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	29.56
Std Dev	3.121

Gaus mean : 29.9787

Gaus Width: 3.3294

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

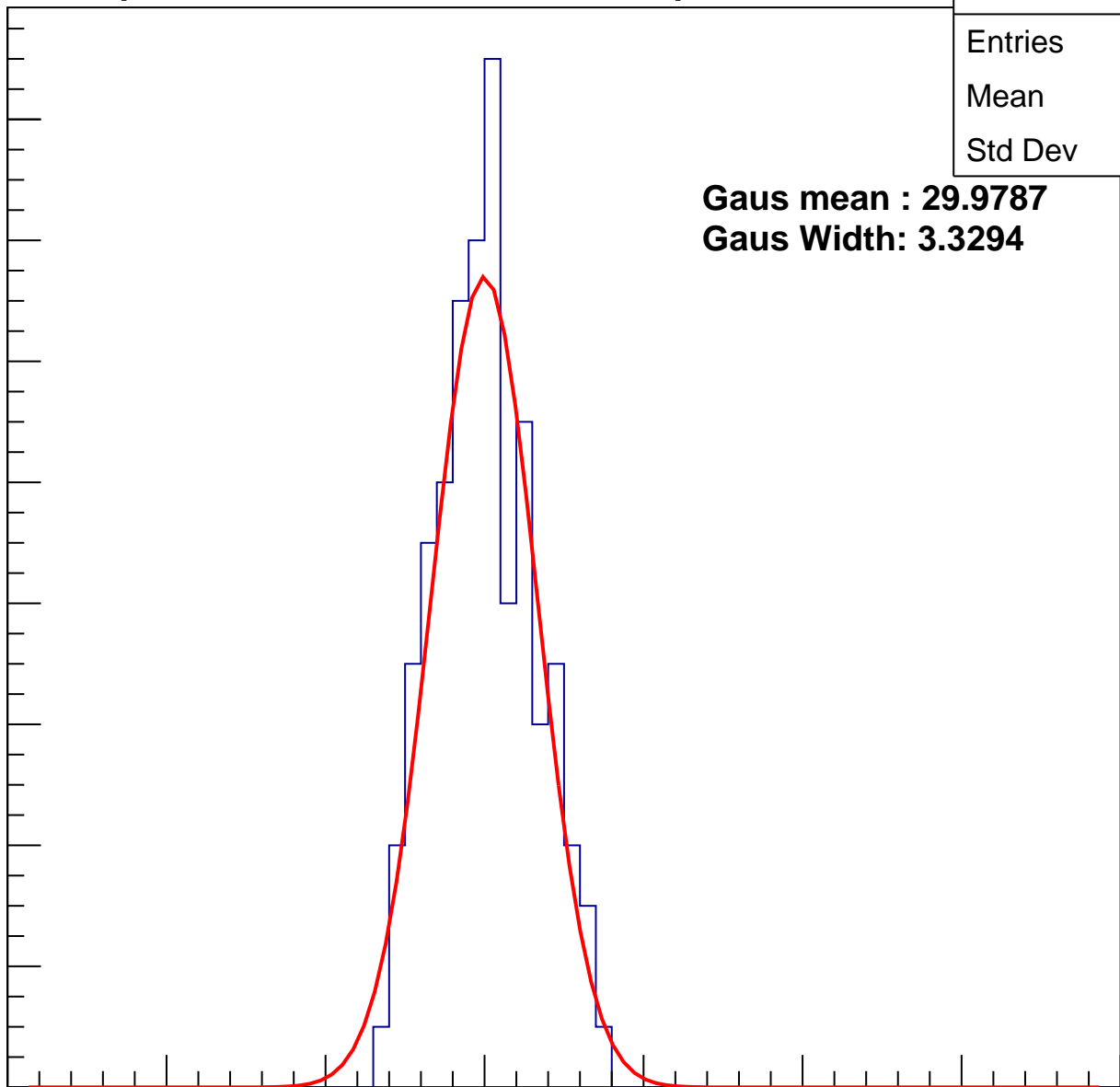
40

50

60

70

ampl



B1L001S, U21-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	36.21
Std Dev	3.532

Gaus mean : 36.6163

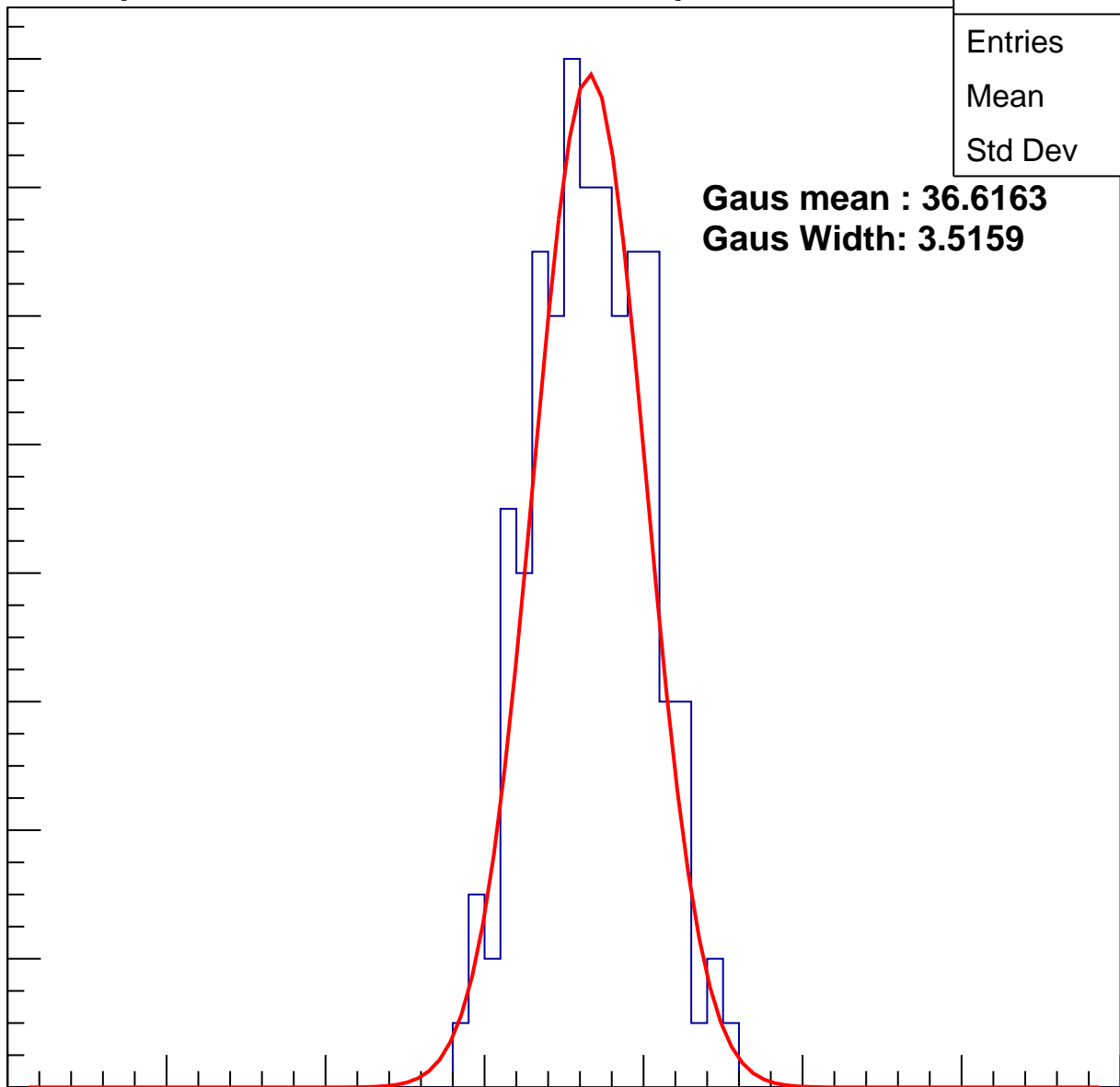
Gaus Width: 3.5159

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch13, adc2

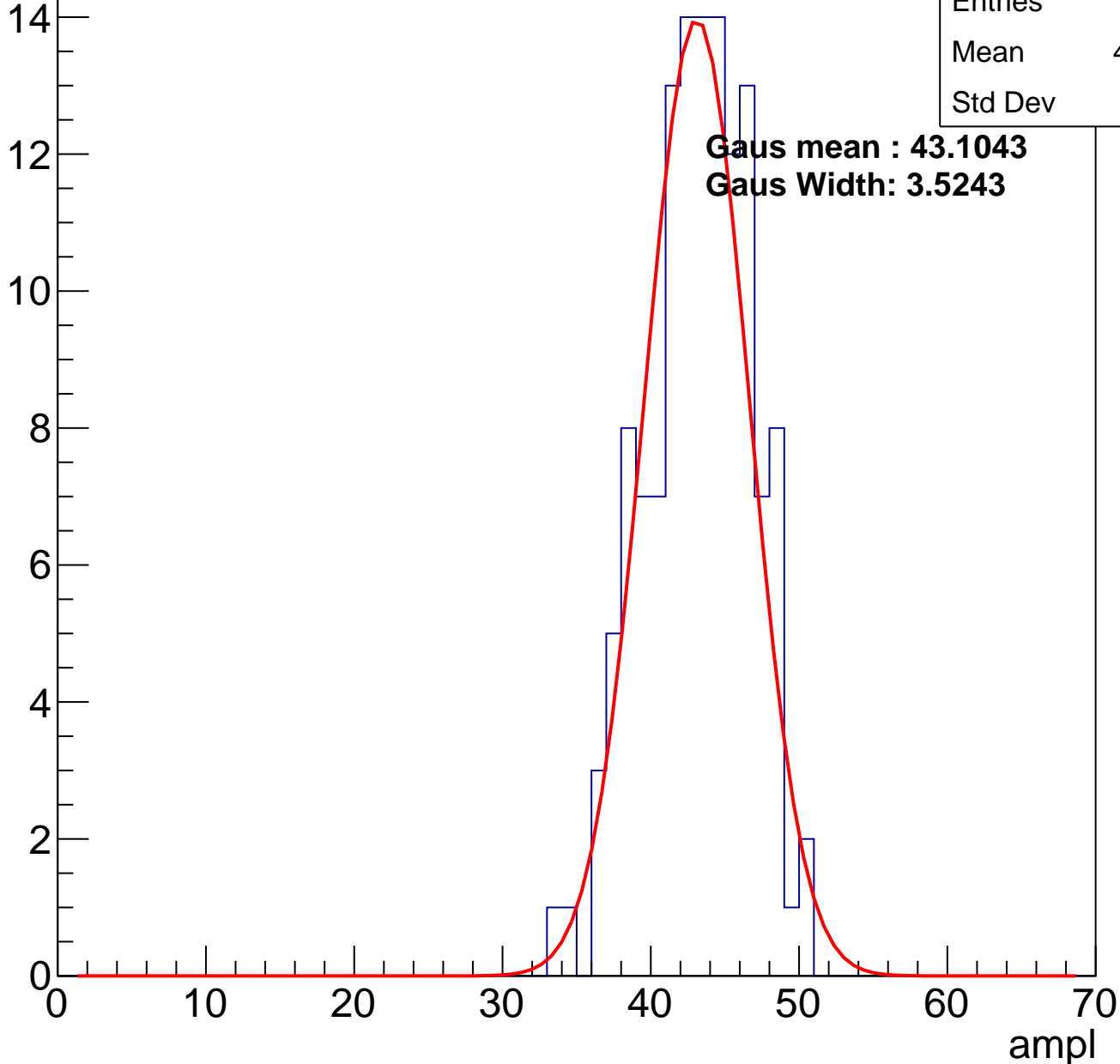
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	130
Mean	42.74
Std Dev	3.47

Gaus mean : 43.1043

Gaus Width: 3.5243



B1L001S, U21-ch13, adc3

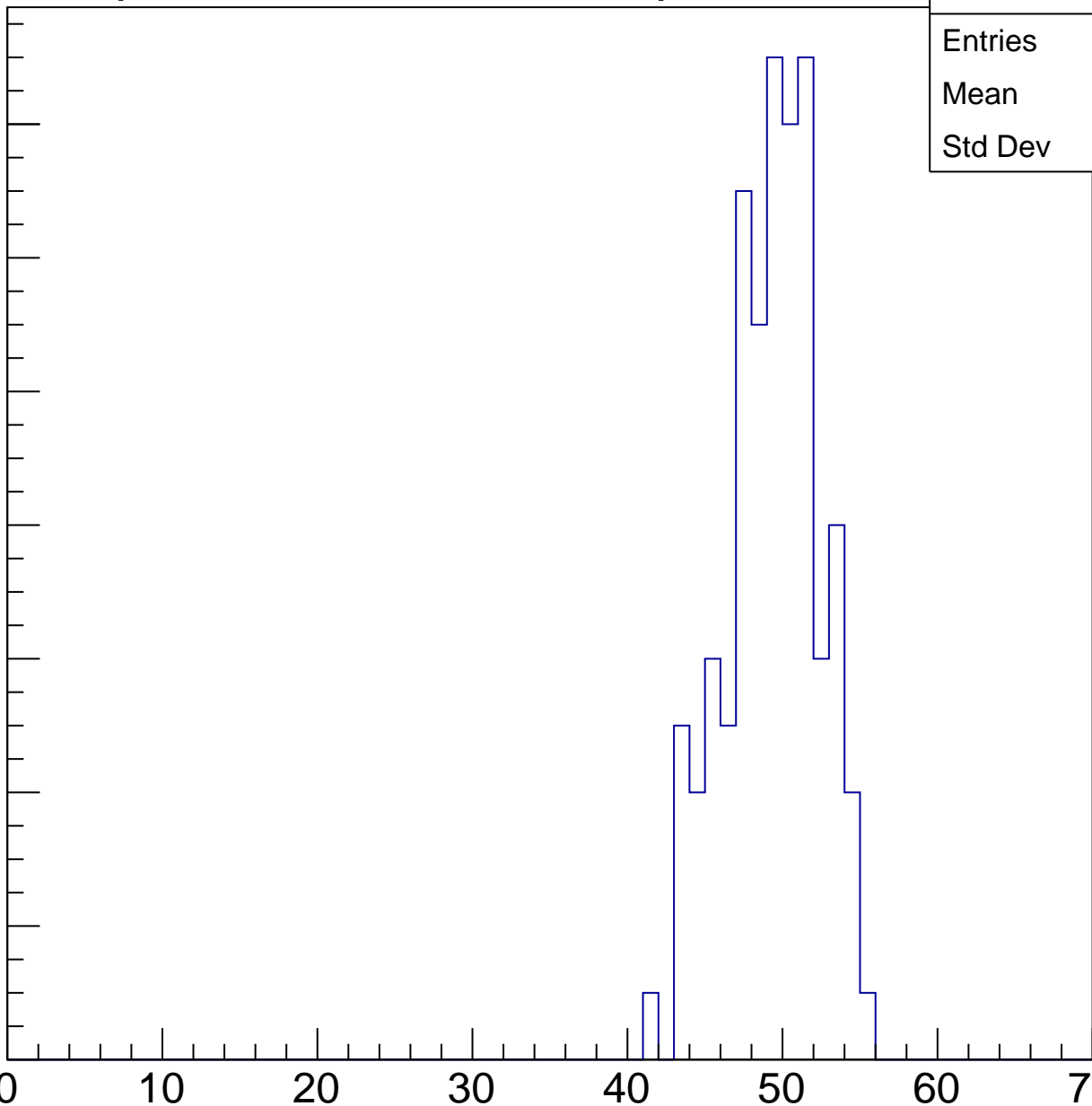
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	48.87
Std Dev	2.947

ampl

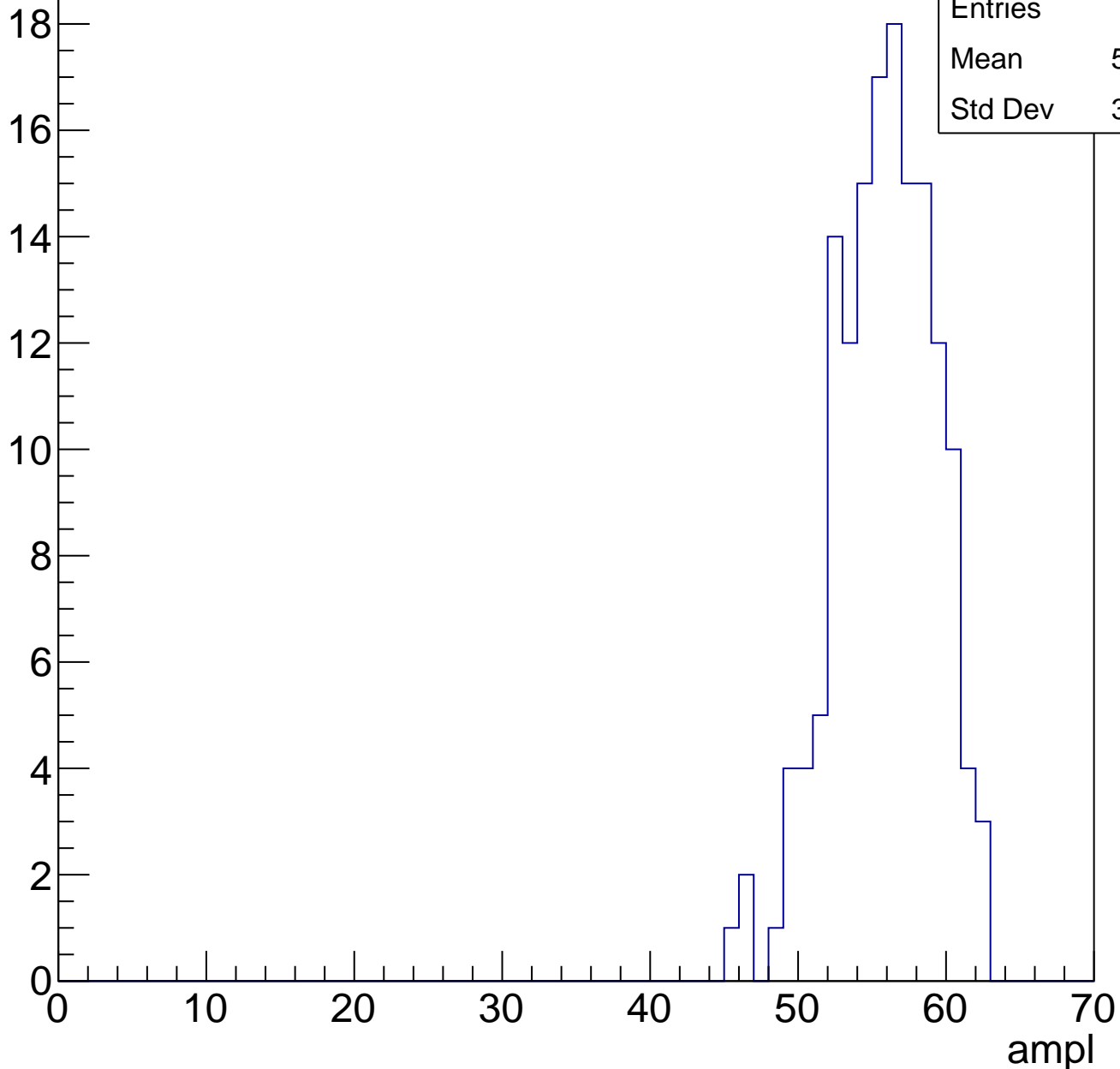


B1L001S, U21-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	55.37
Std Dev	3.392

Entry



B1L001S, U21-ch13, adc5

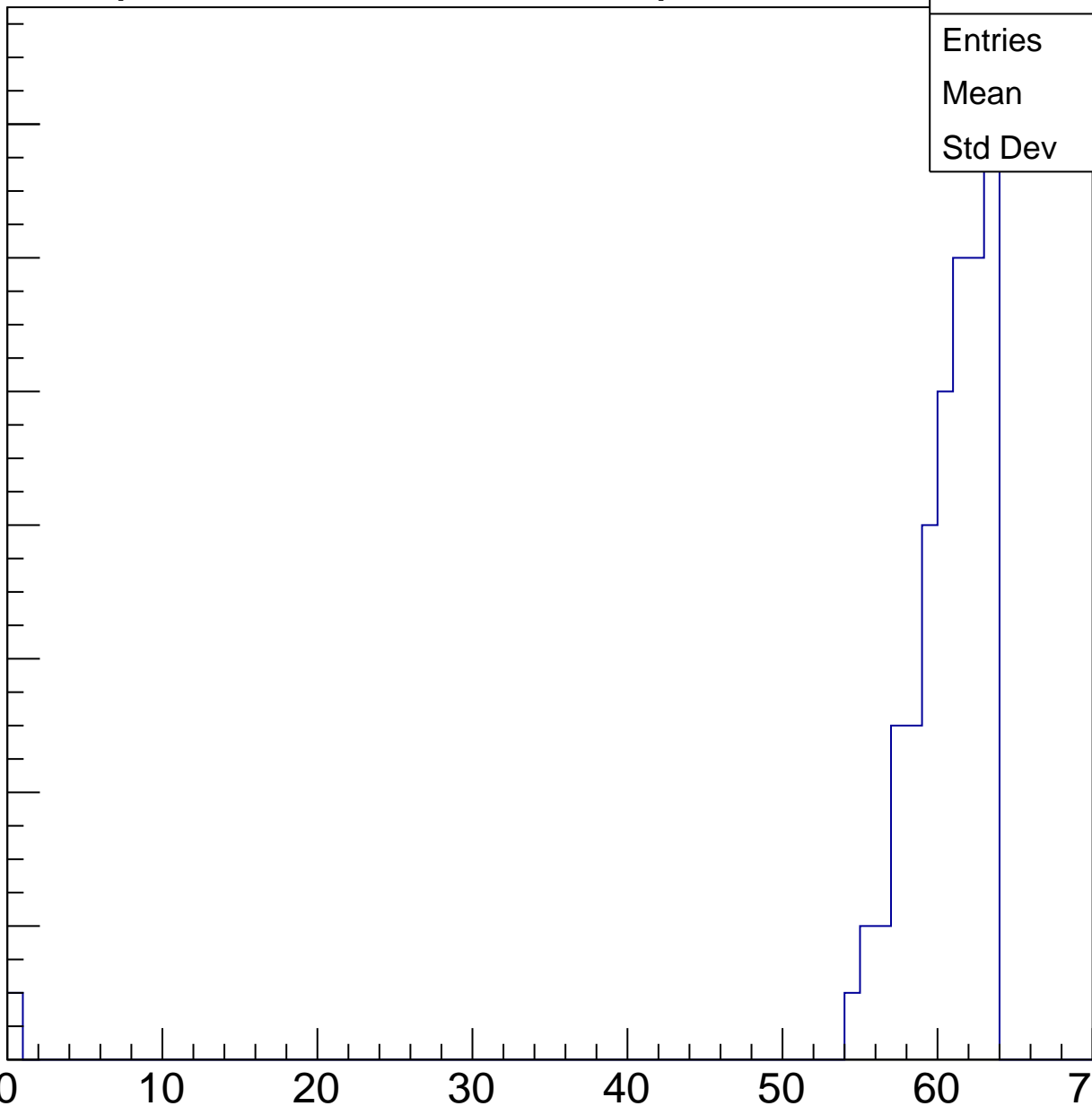
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	59.51
Std Dev	7.373

ampl

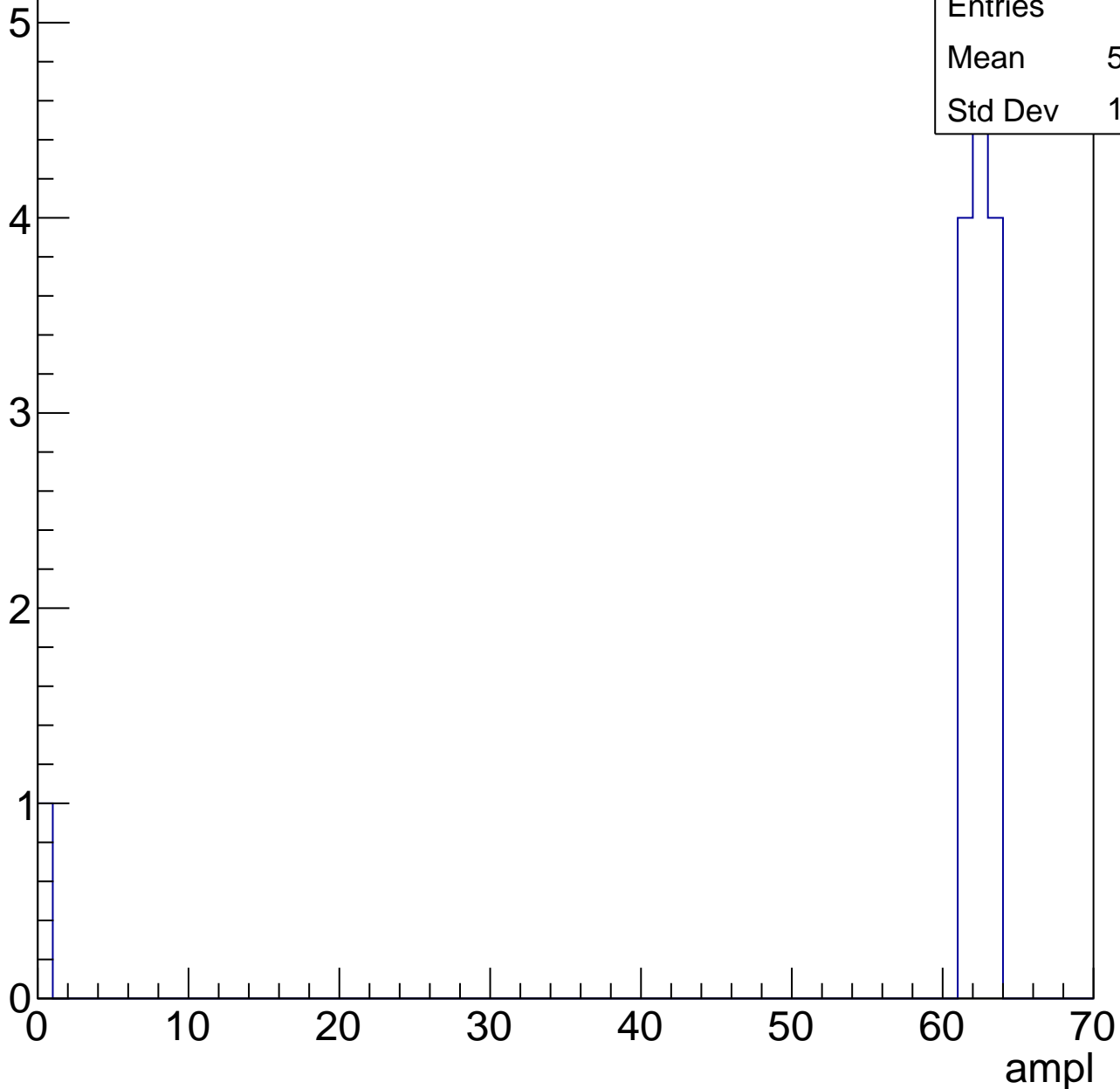


B1L001S, U21-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

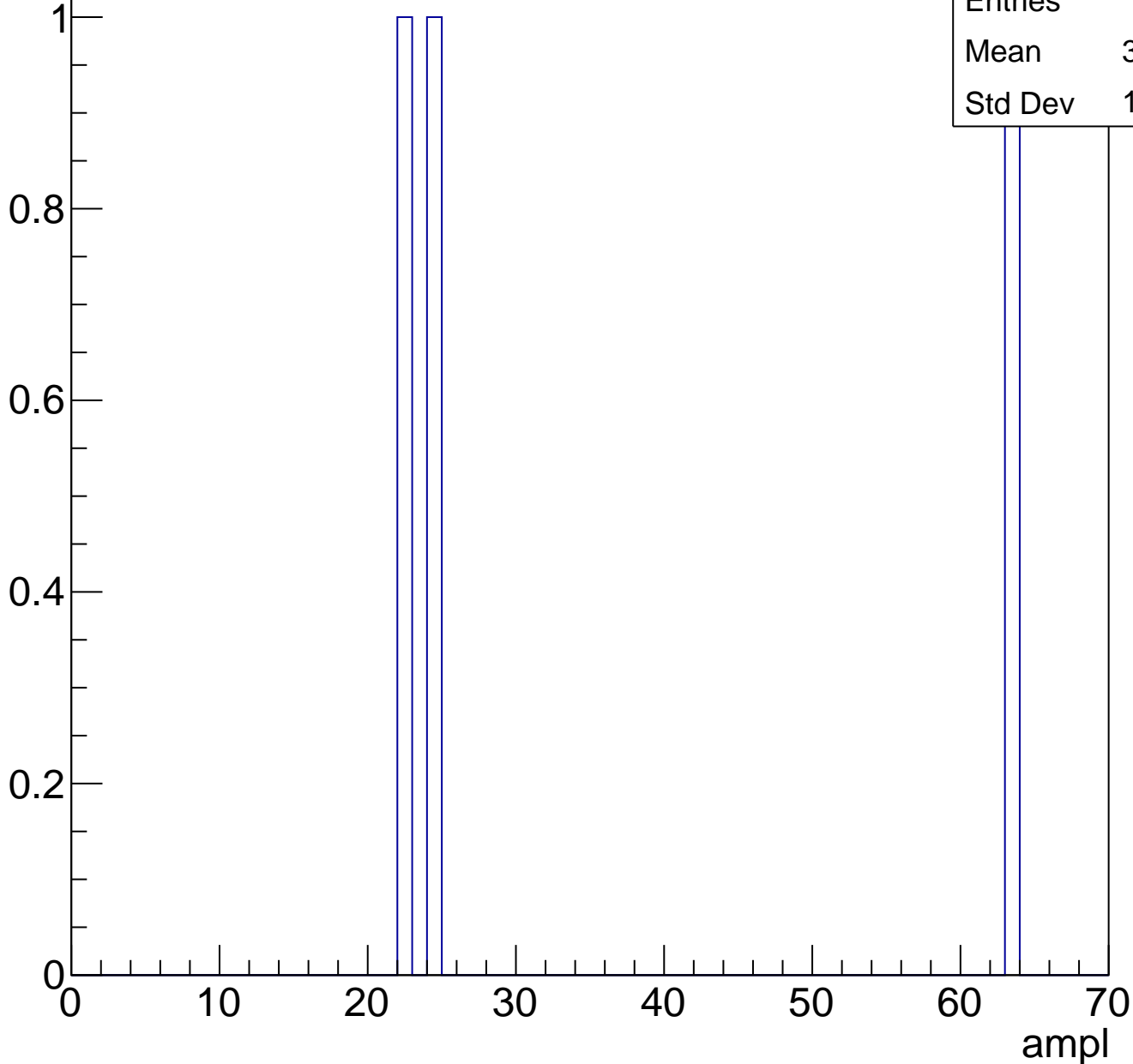
Entries	14
Mean	57.57
Std Dev	15.99



B1L001S, U21-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

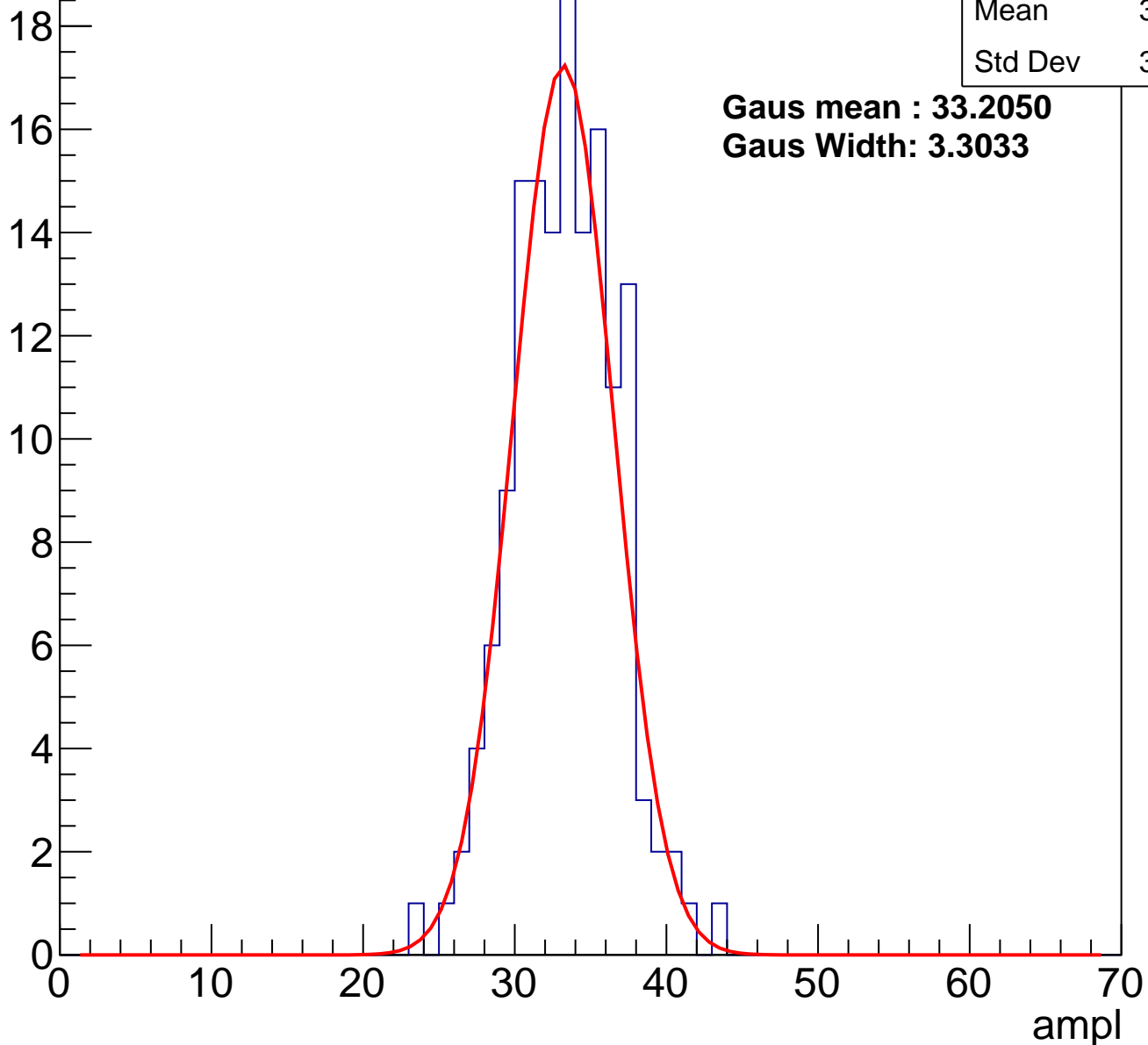
Entry



B1L001S, U21-ch14, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	39.21
Std Dev	3.104

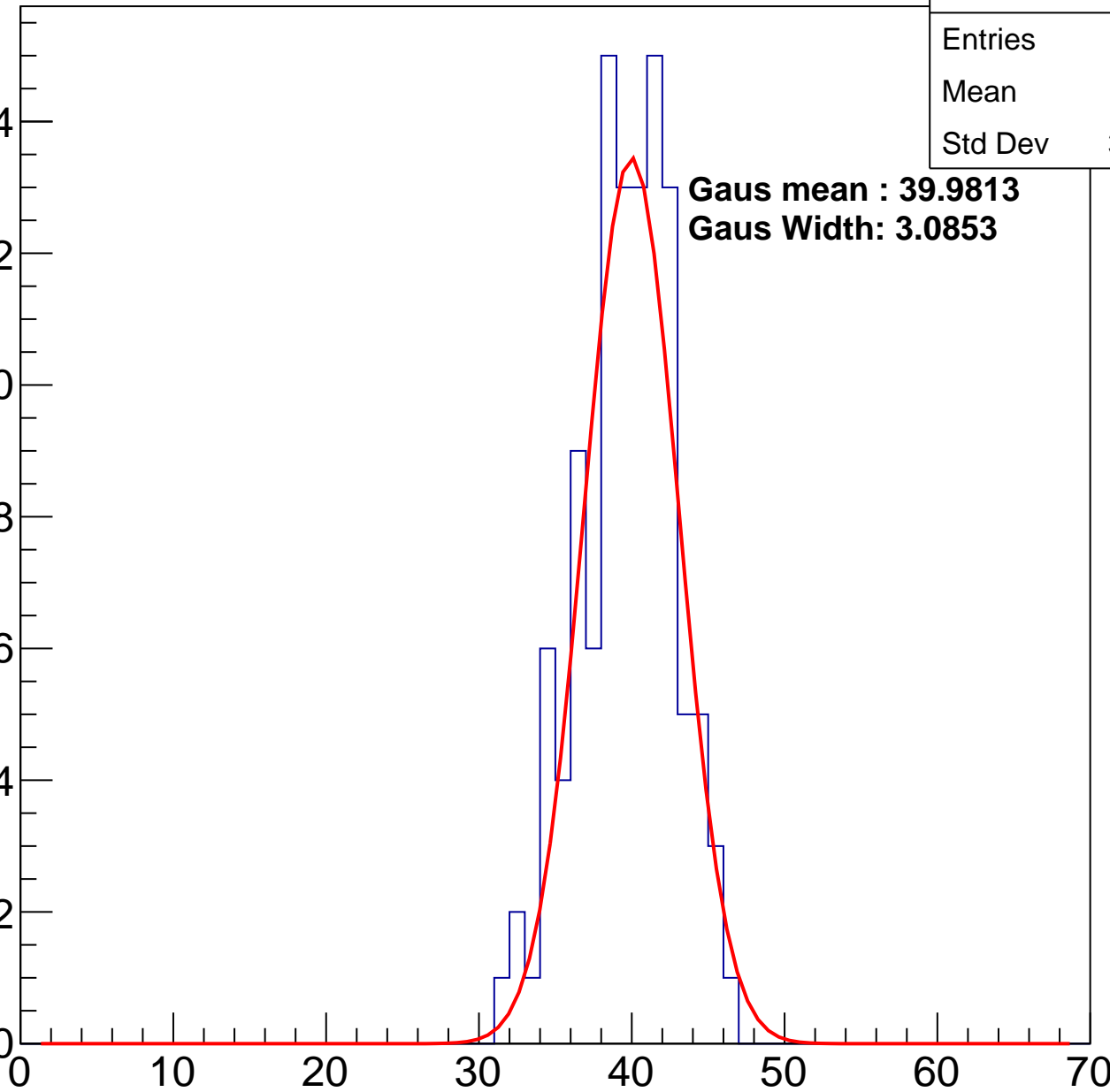
Gaus mean : 39.9813

Gaus Width: 3.0853

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch14, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

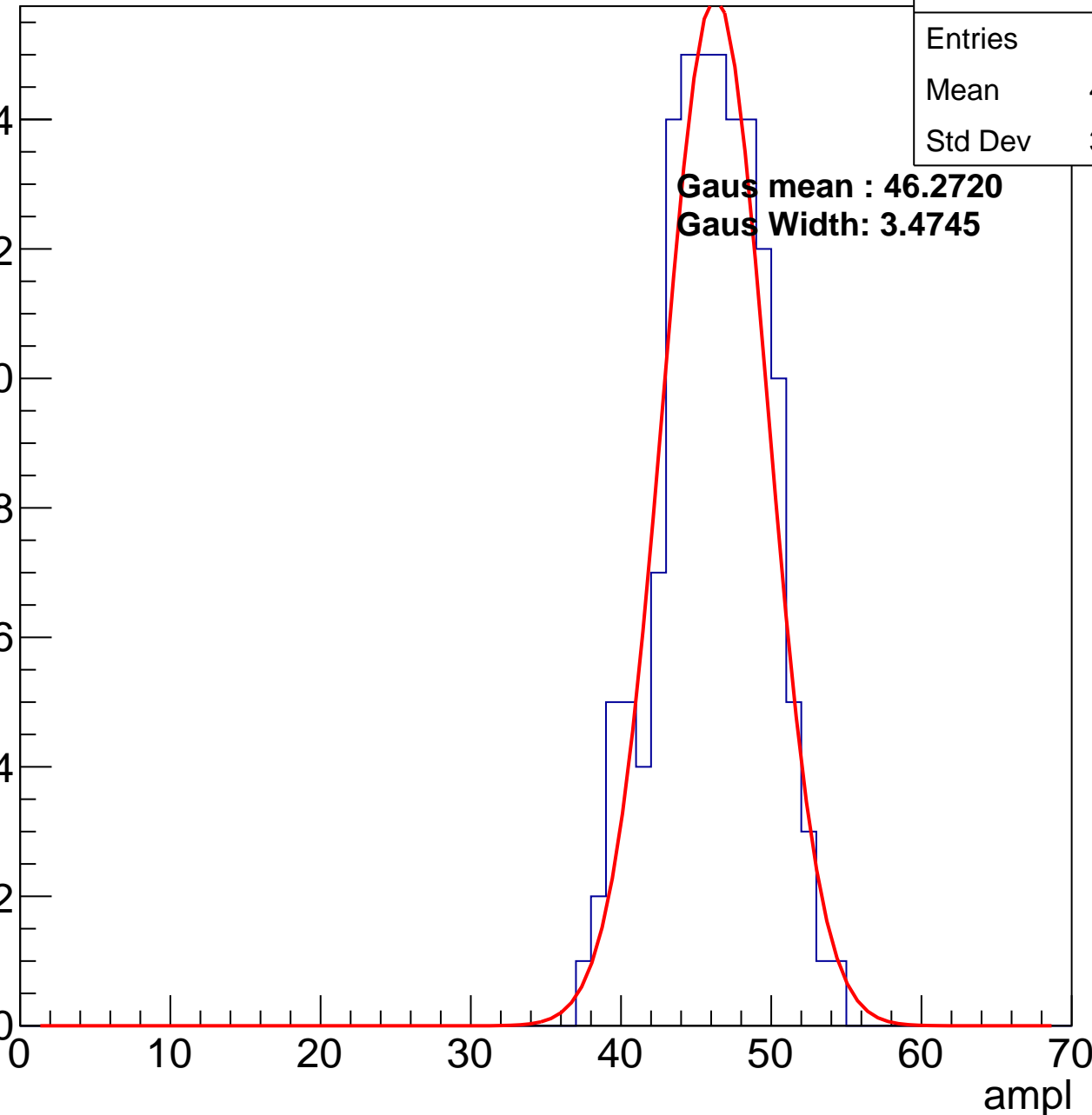
60

ampl

Entries	143
Mean	45.66
Std Dev	3.462

Gaus mean : 46.2720

Gaus Width: 3.4745



B1L001S, U21-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	51.81
Std Dev	3.219

Entry

12

10

8

6

4

2

0

0

10

20

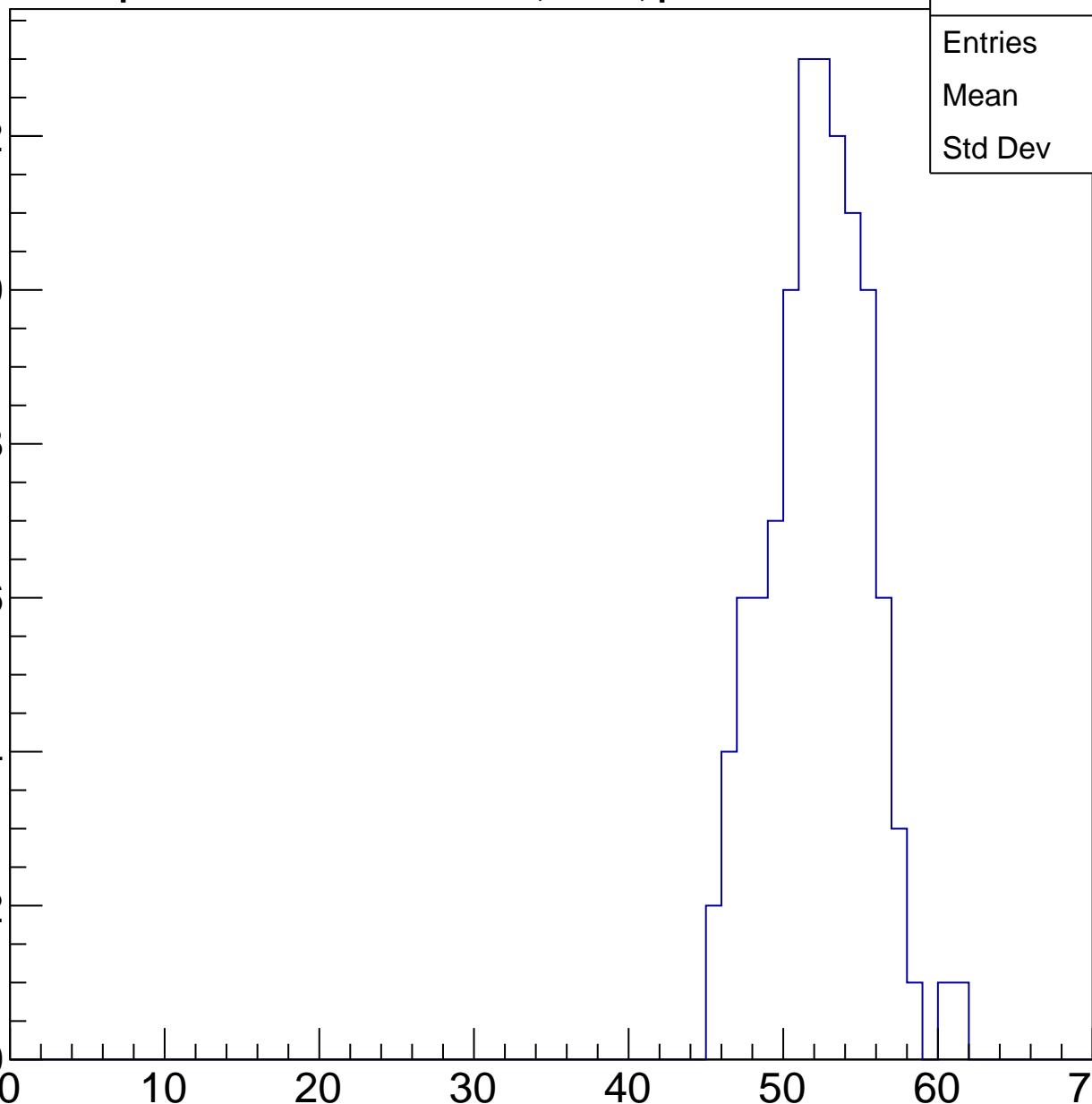
30

40

50

60

ampl



B1L001S, U21-ch14, adc4

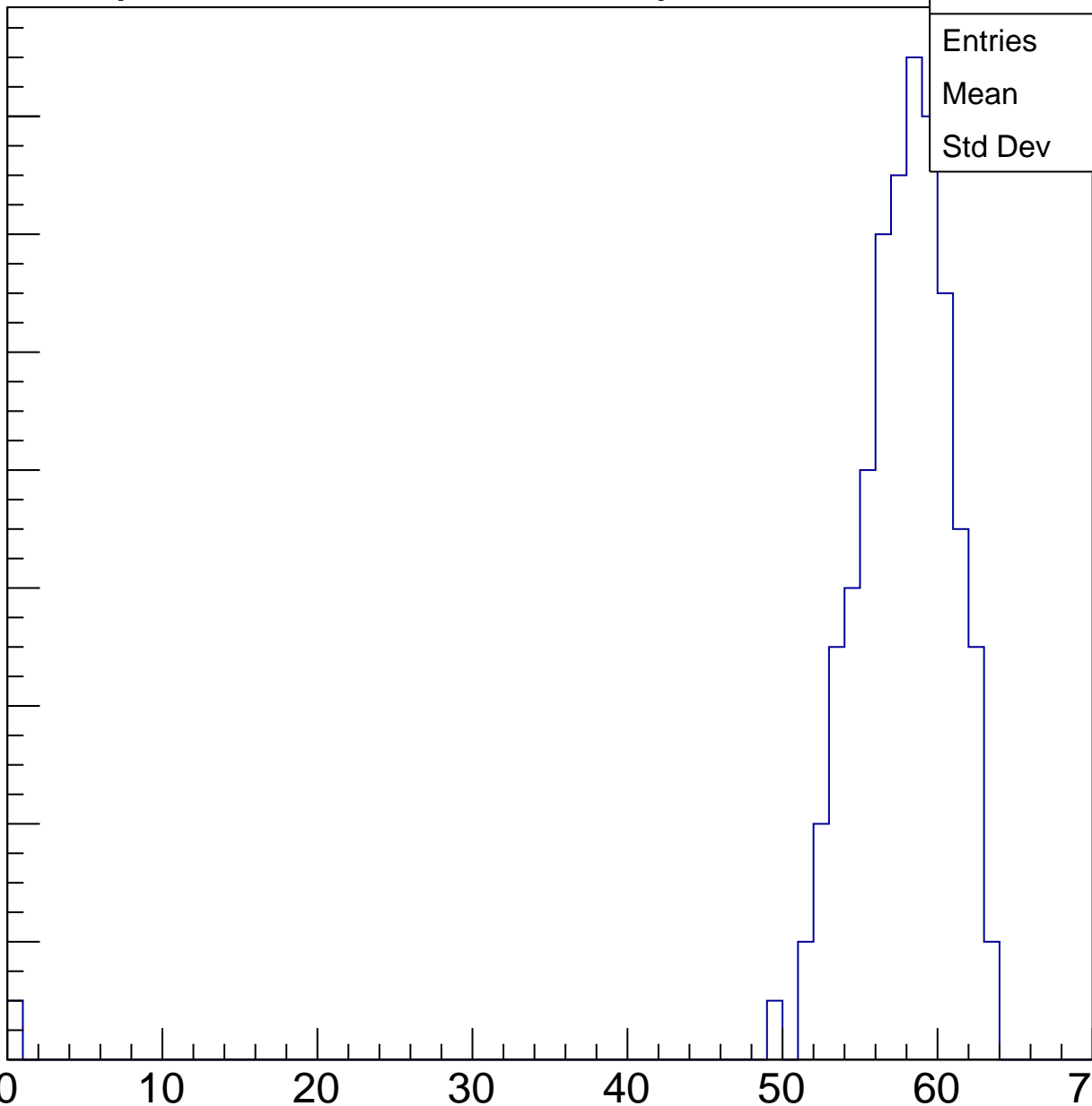
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	56.9
Std Dev	5.849

ampl



B1L001S, U21-ch14, adc5

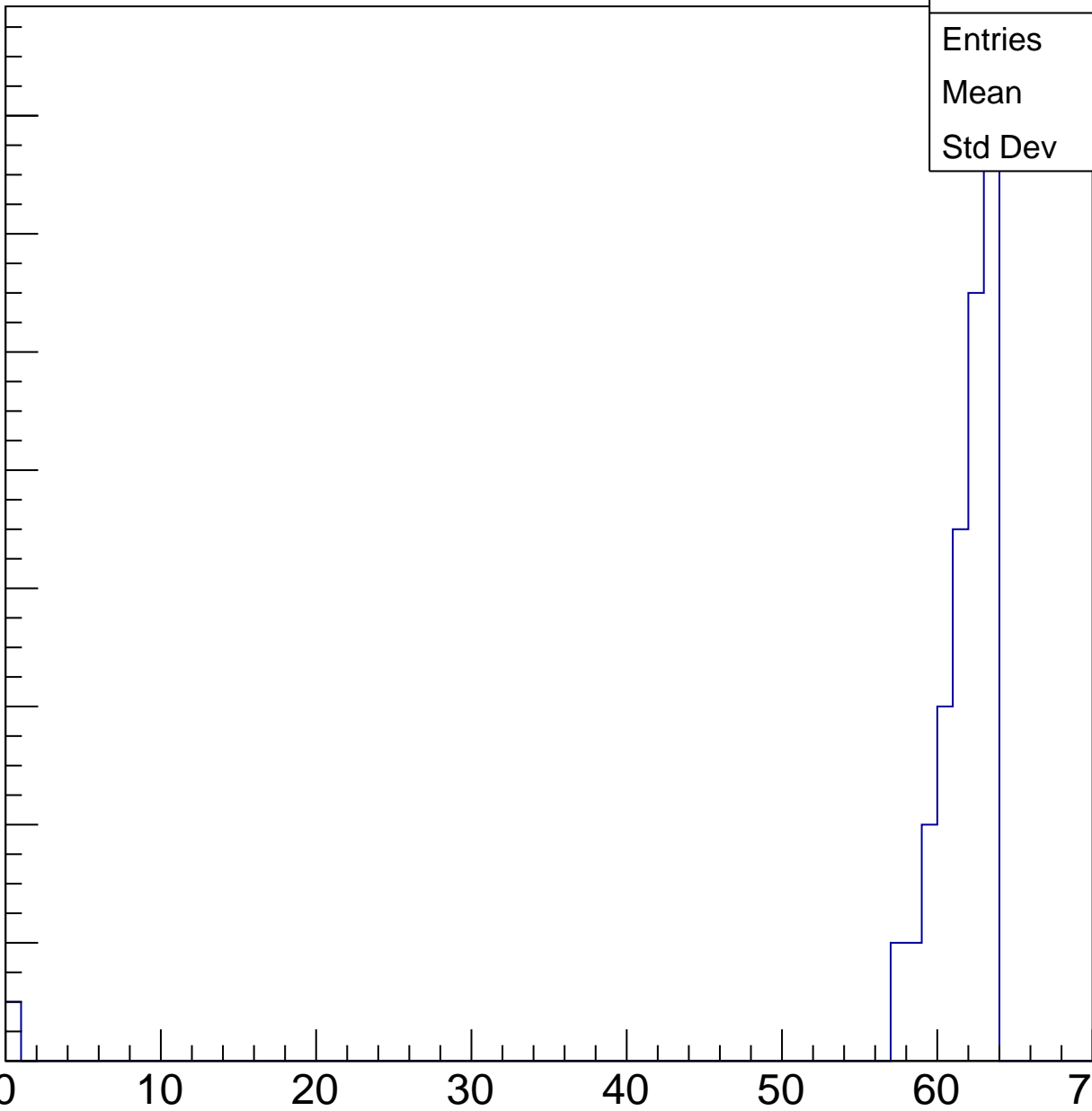
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	54
Mean	60.22
Std Dev	8.434

ampl



B1L001S, U21-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

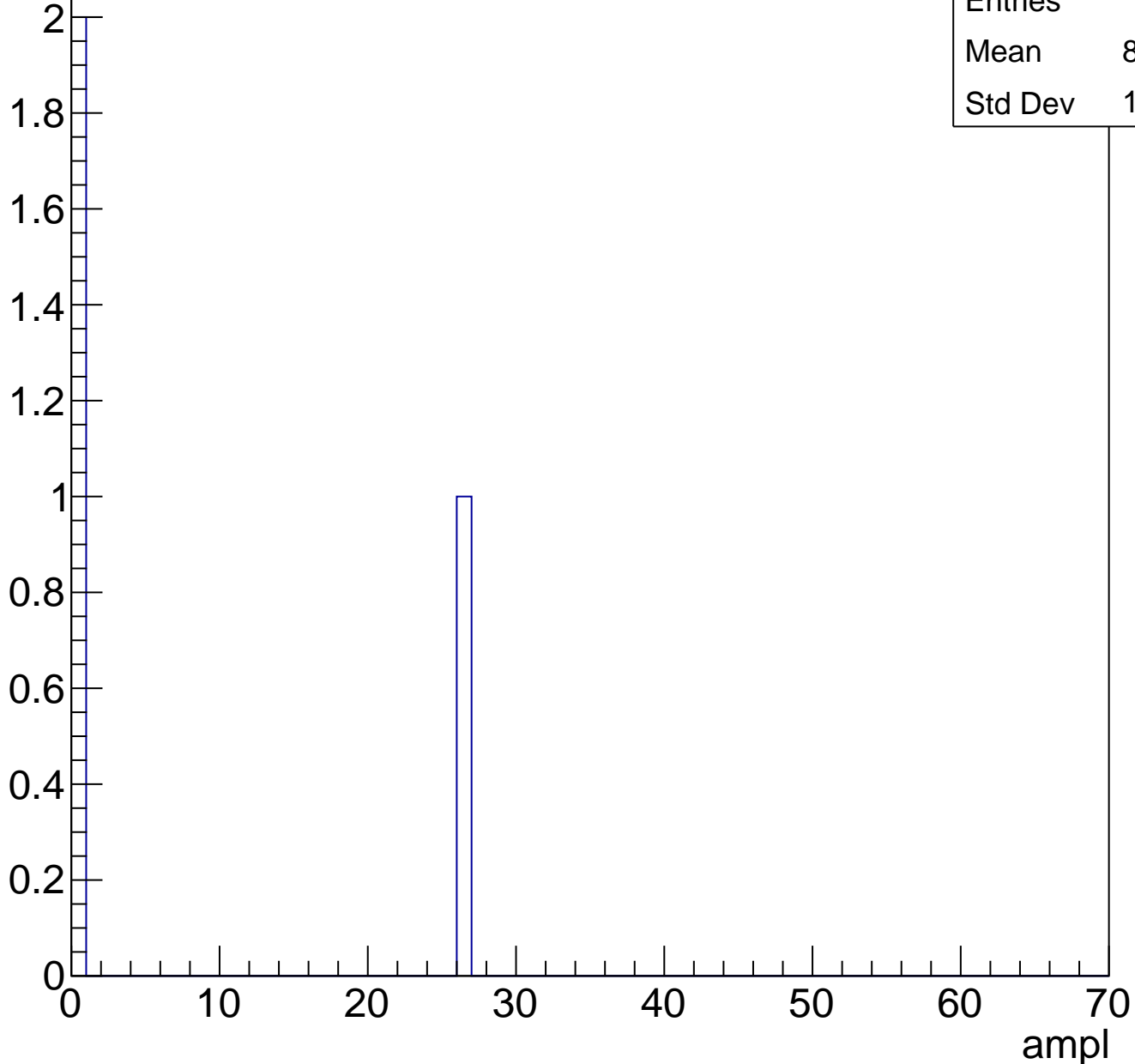
Entry



B1L001S, U21-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8.667
Std Dev	12.26

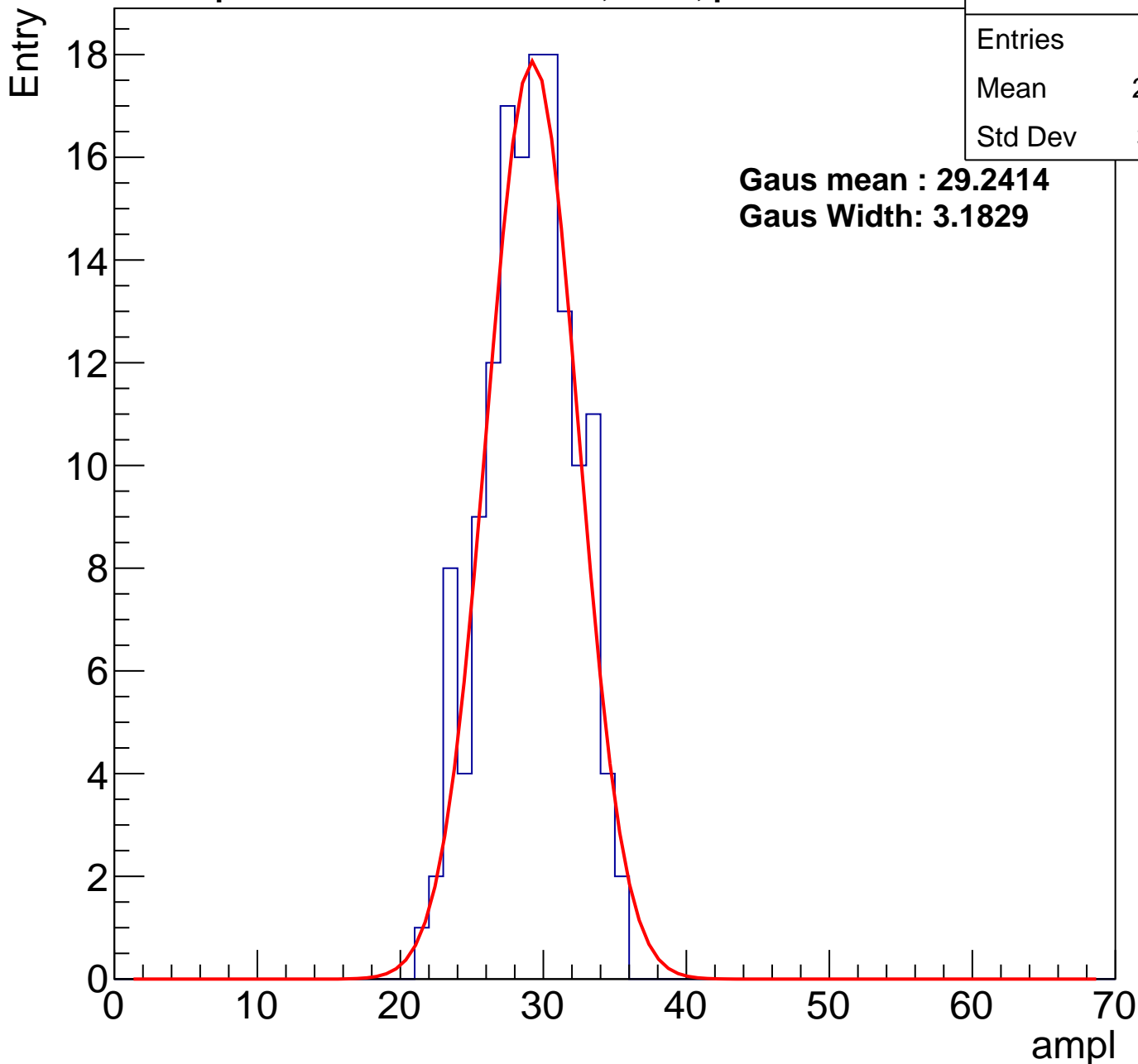
B1L001S, U21-ch15, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	28.57
Std Dev	3.071

Gaus mean : 29.2414

Gaus Width: 3.1829

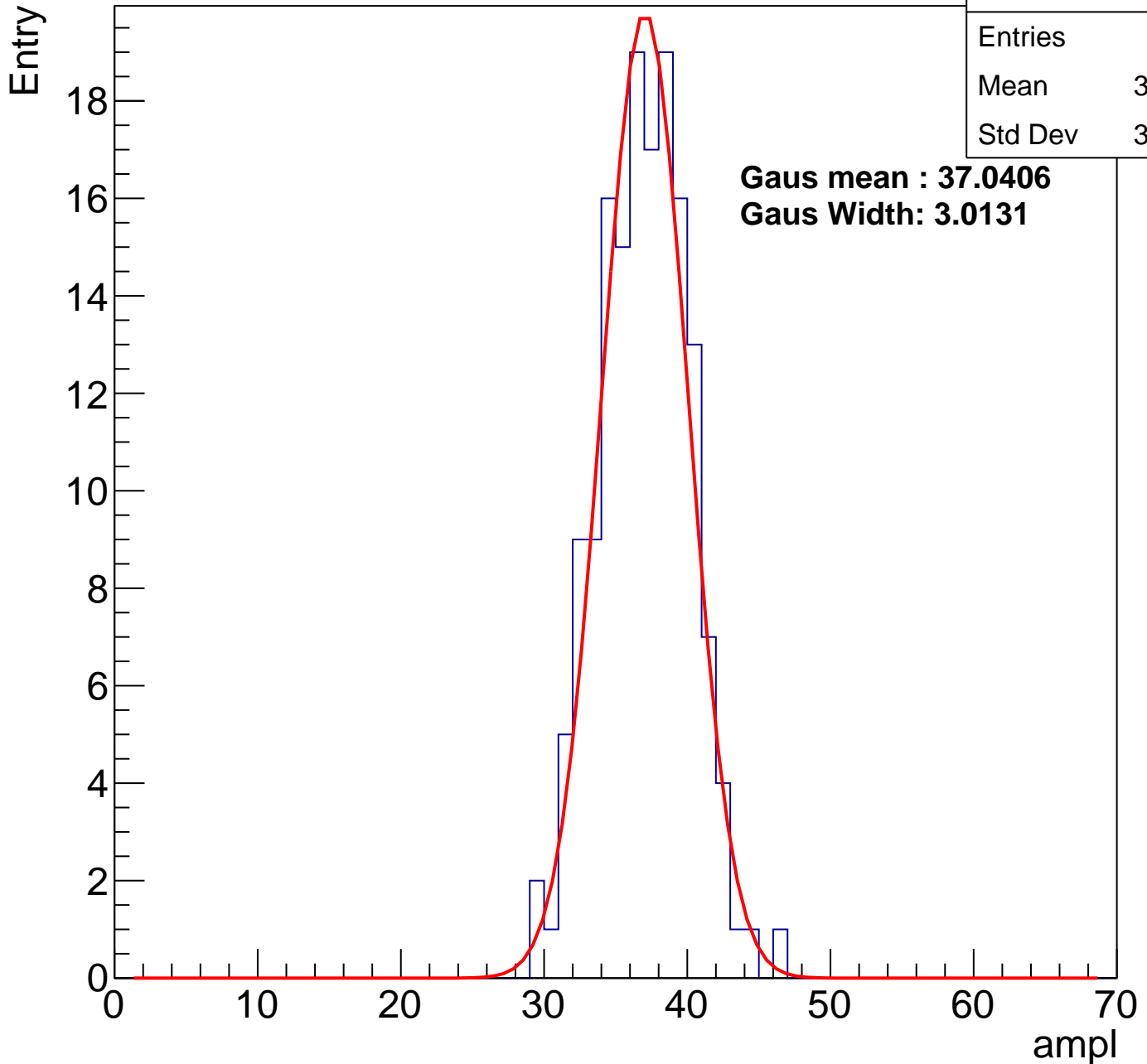


B1L001S, U21-ch15, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.54
Std Dev	3.105

Gaus mean : 37.0406
Gaus Width: 3.0131



B1L001S, U21-ch15, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

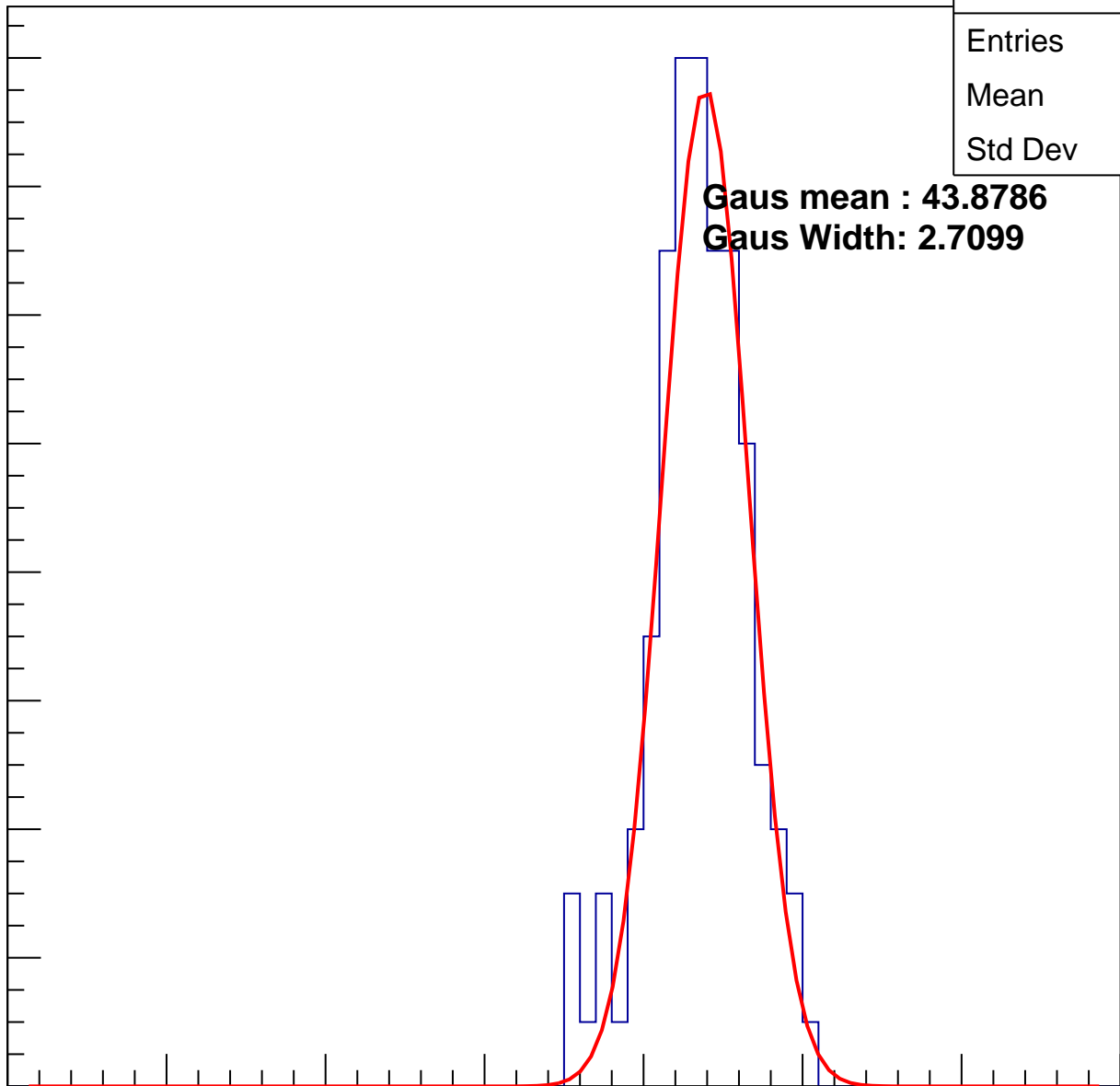
Entries	113
Mean	43.01
Std Dev	3.064

Gaus mean : 43.8786

Gaus Width: 2.7099

ampl

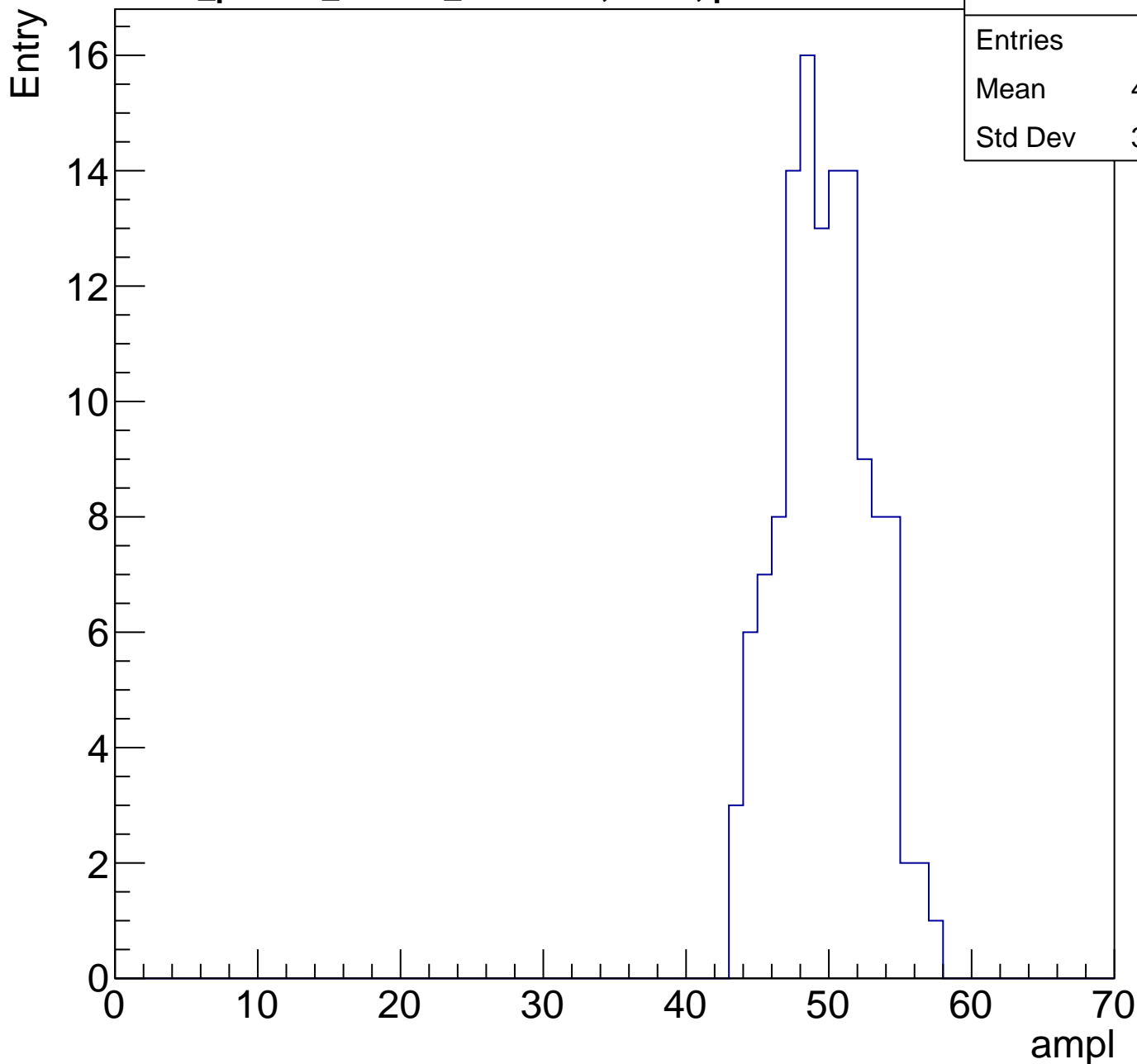
0 10 20 30 40 50 60 70



B1L001S, U21-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	49.25
Std Dev	3.123



B1L001S, U21-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

130

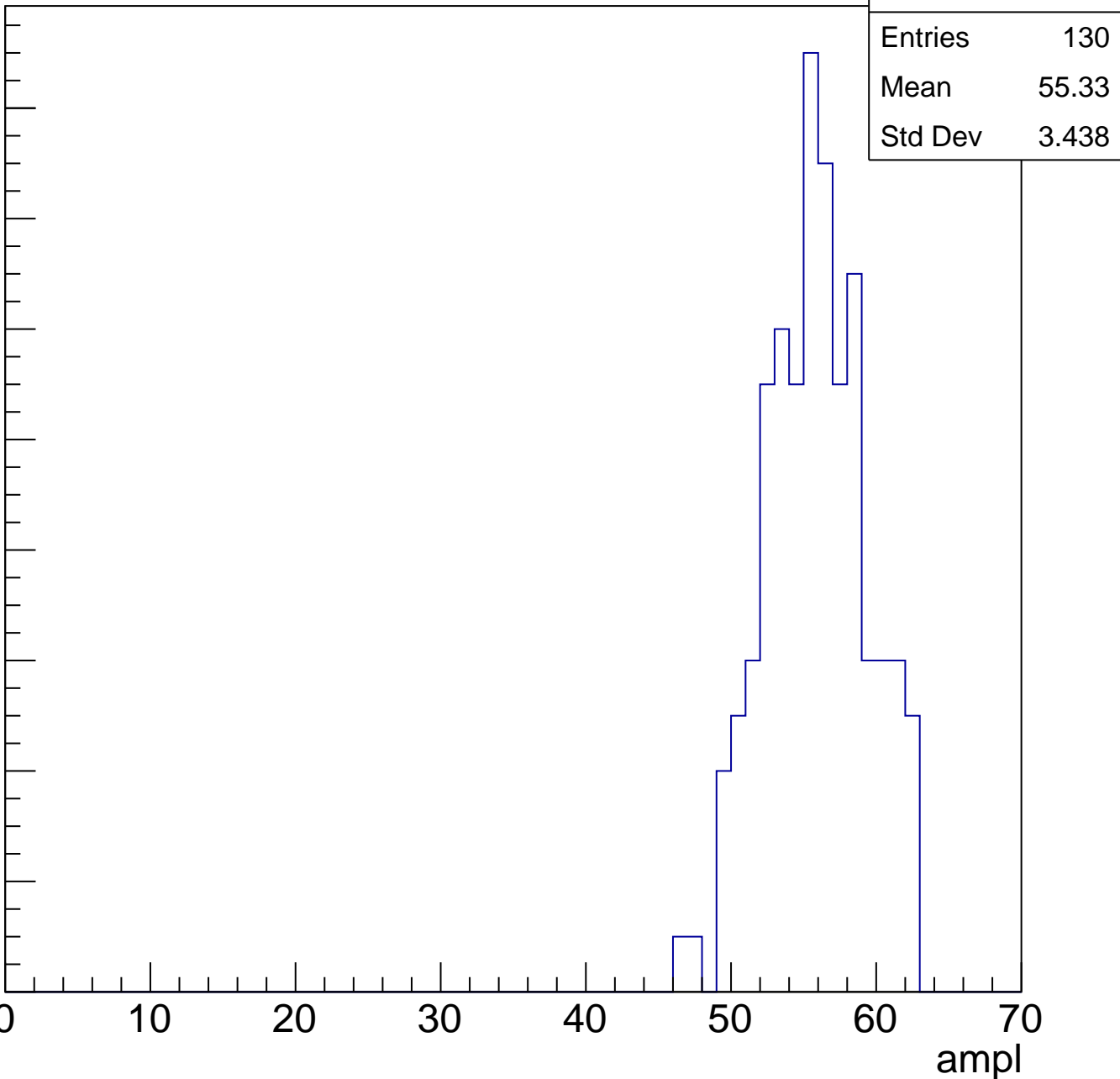
Mean

55.33

Std Dev

3.438

ampl



B1L001S, U21-ch15, adc5

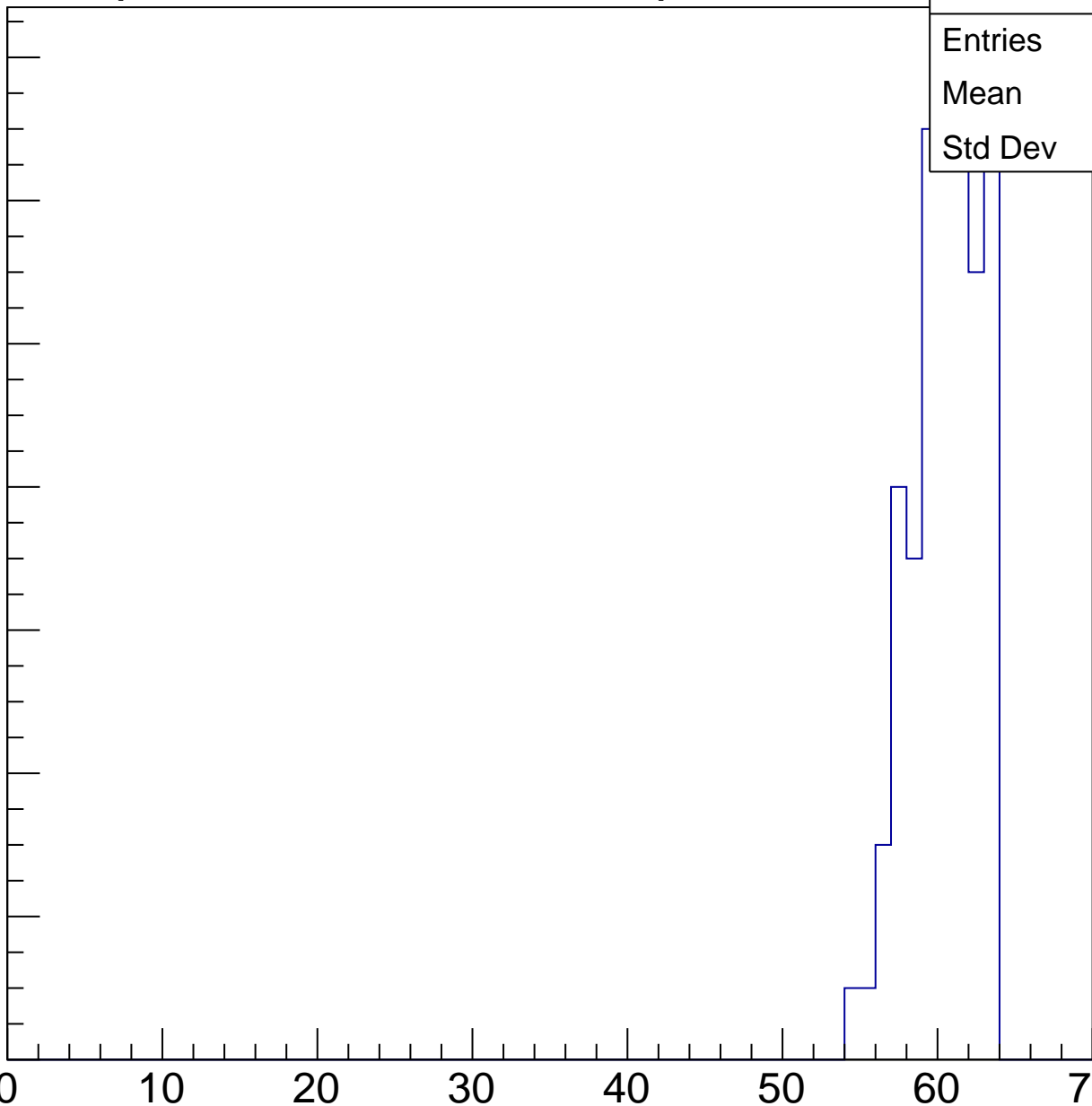
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	85
Mean	60.04
Std Dev	2.183

ampl

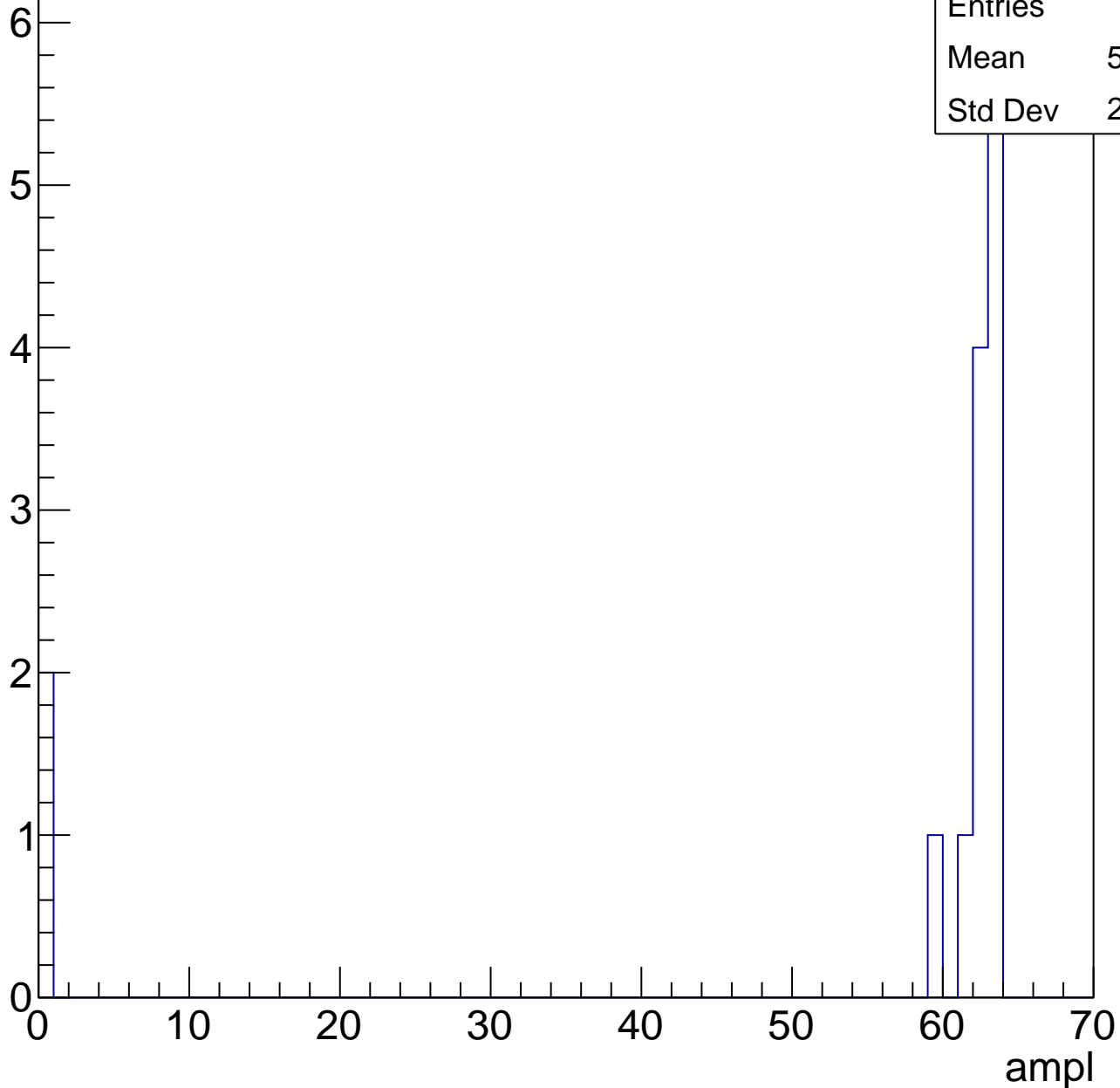


B1L001S, U21-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	53.29
Std Dev	21.78



B1L001S, U21-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch16, adc0

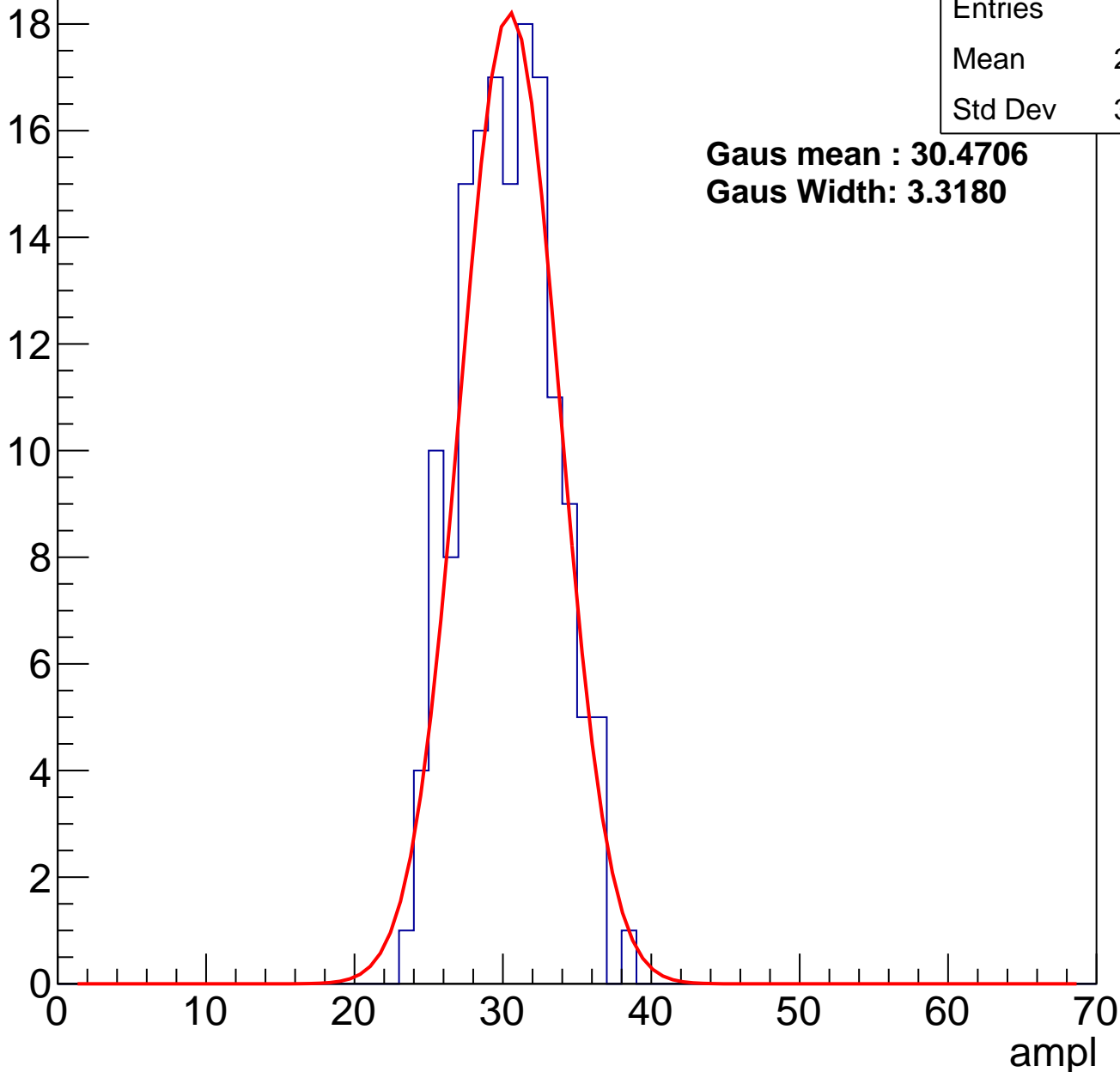
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.85
Std Dev	3.122

Gaus mean : 30.4706

Gaus Width: 3.3180

Entry



B1L001S, U21-ch16, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	36.26
Std Dev	2.949

Gaus mean : 36.9368

Gaus Width: 2.8107

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

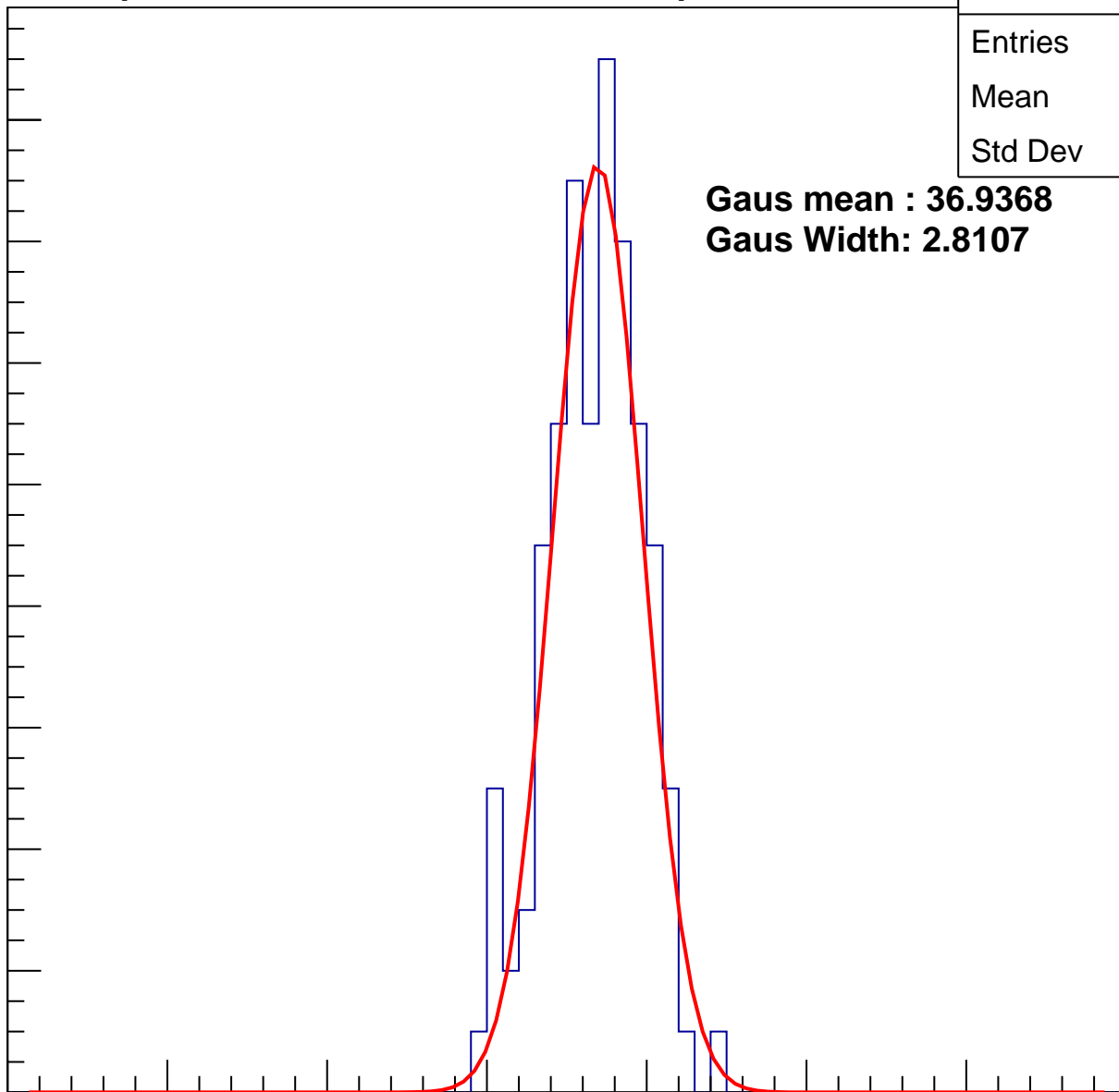
40

50

60

70

ampl



B1L001S, U21-ch16, adc2

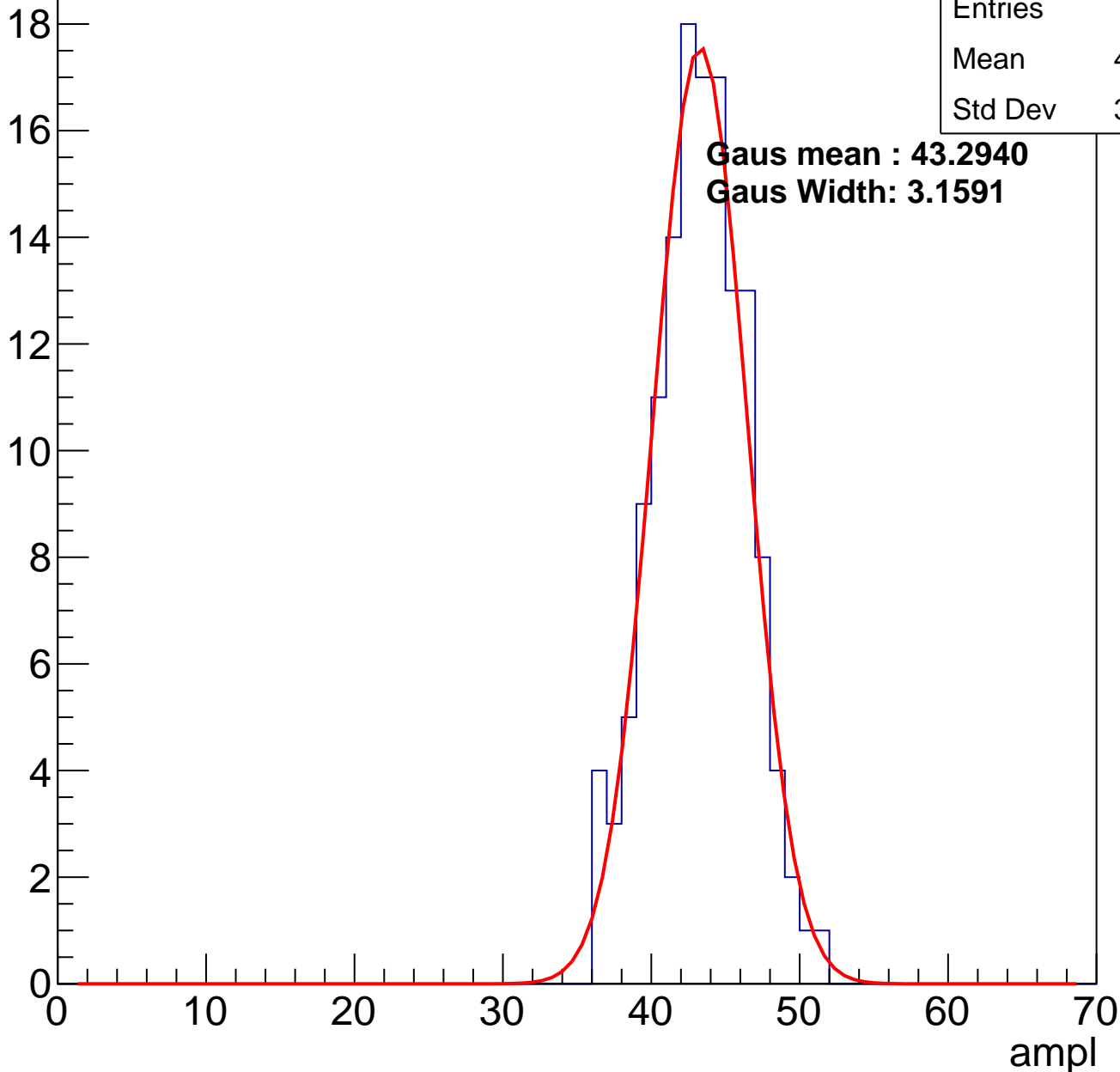
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	42.82
Std Dev	3.094

Gaus mean : 43.2940

Gaus Width: 3.1591

Entry



B1L001S, U21-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	49.69
Std Dev	3.214

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

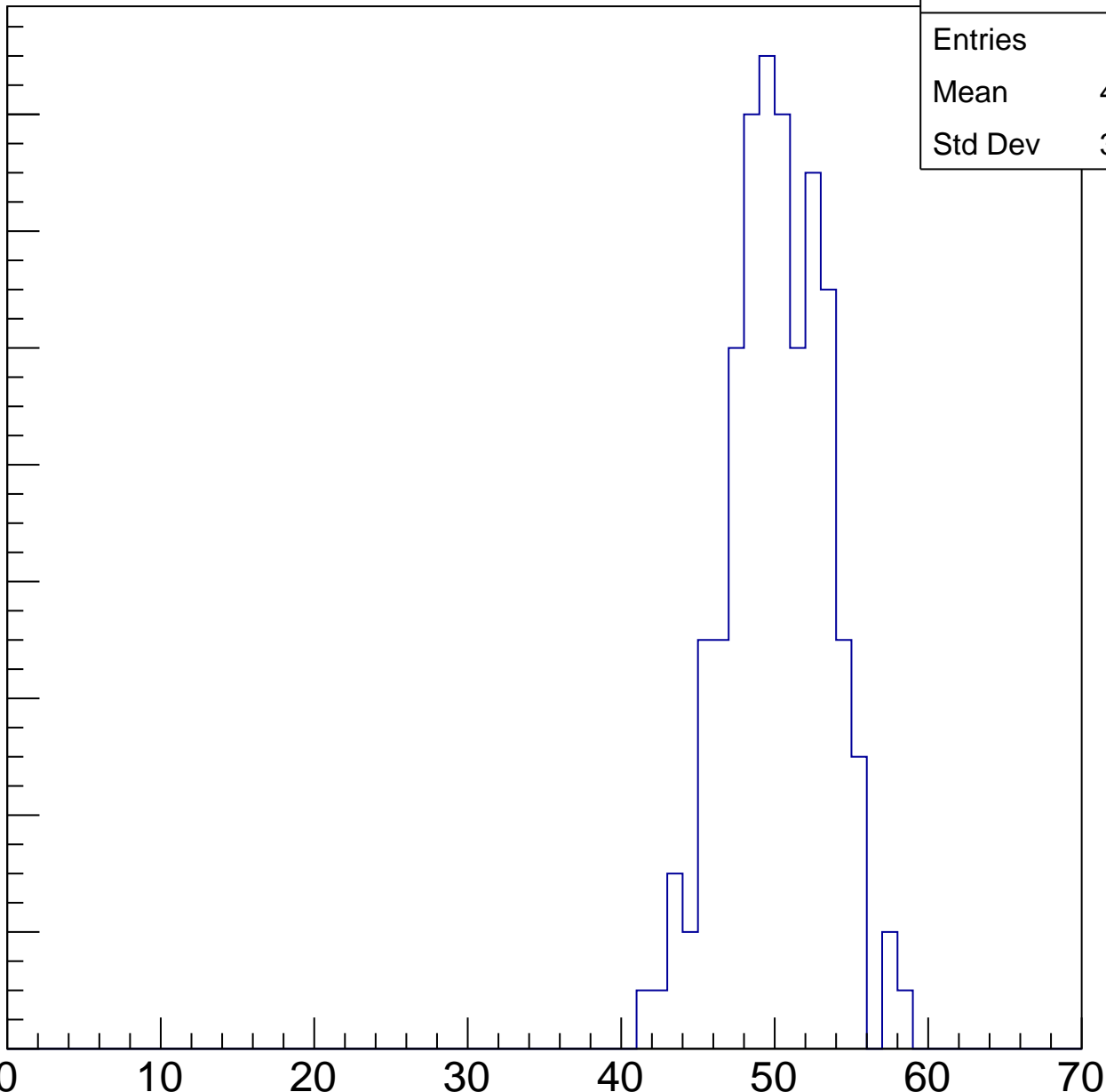
40

50

60

70

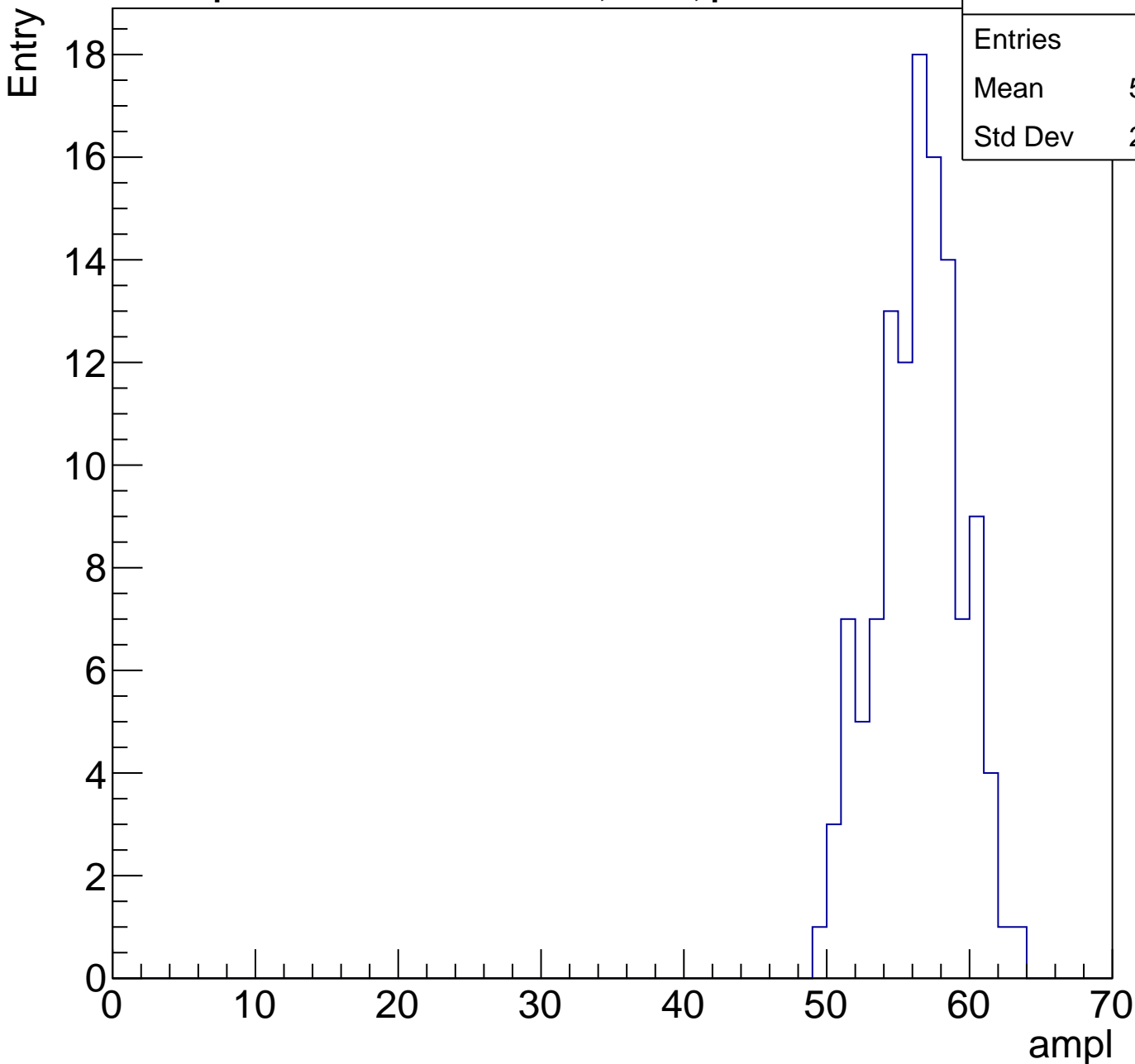
ampl



B1L001S, U21-ch16, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	55.96
Std Dev	2.915



B1L001S, U21-ch16, adc5

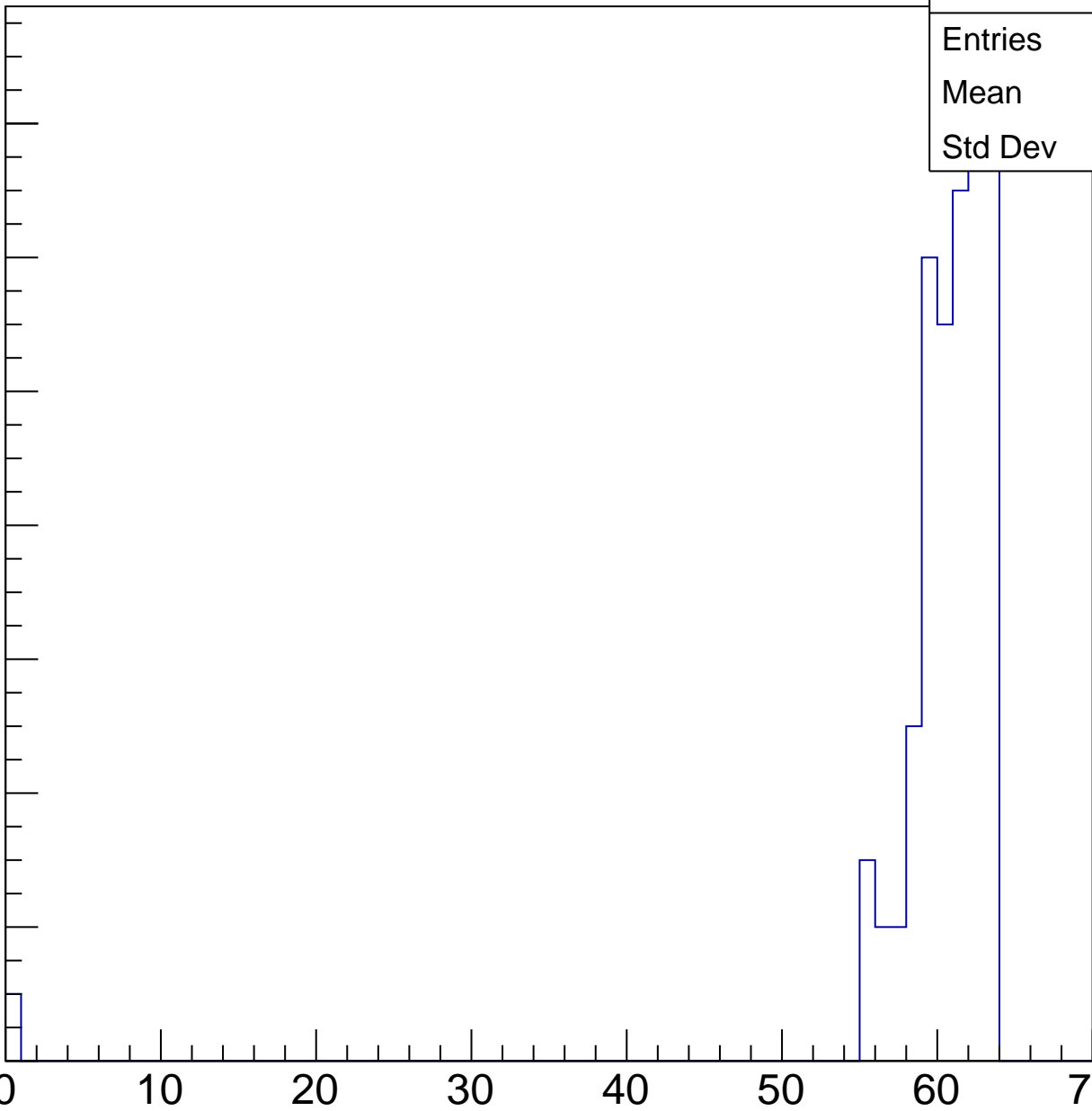
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.67
Std Dev	7.114

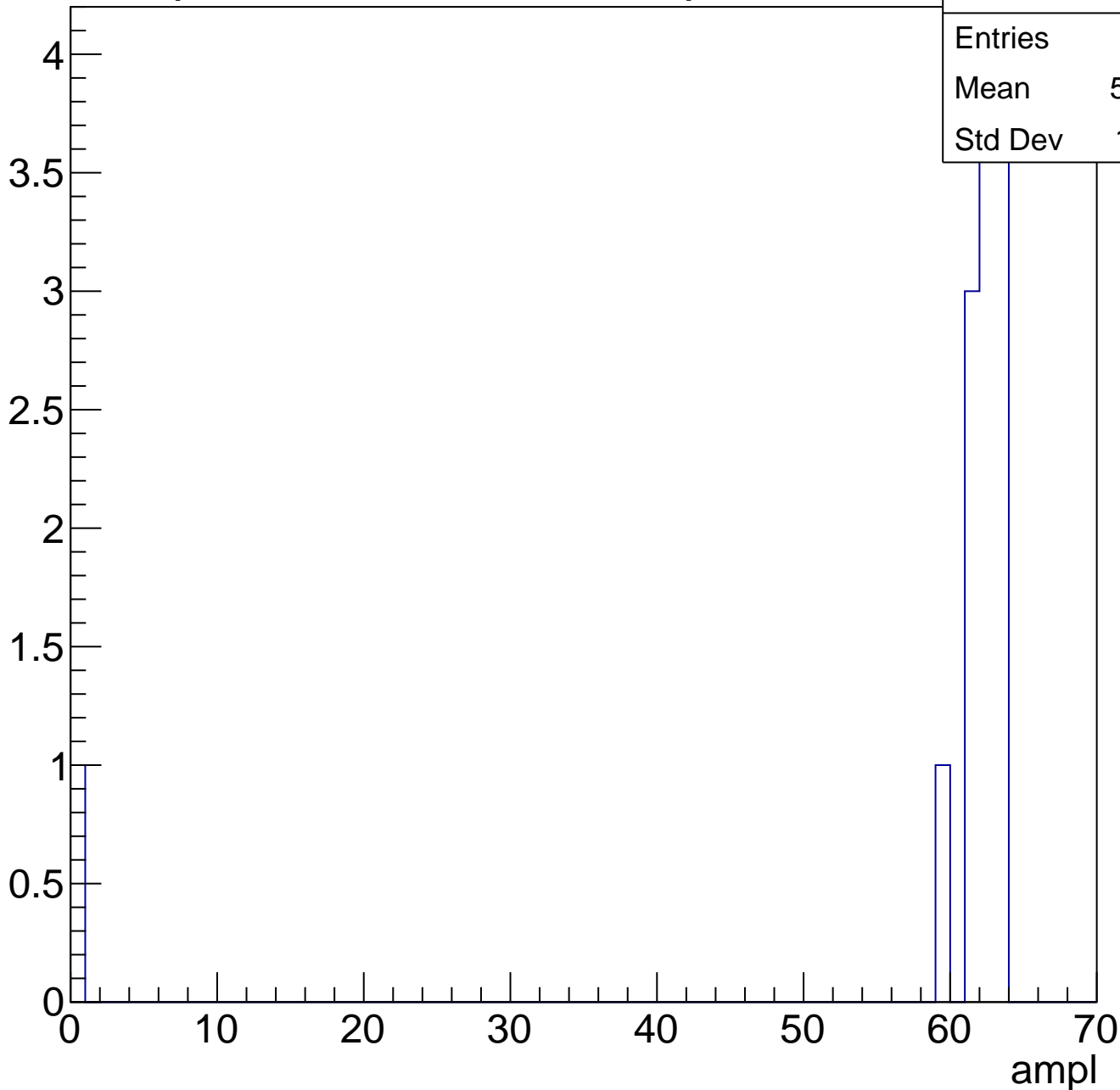
ampl



B1L001S, U21-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

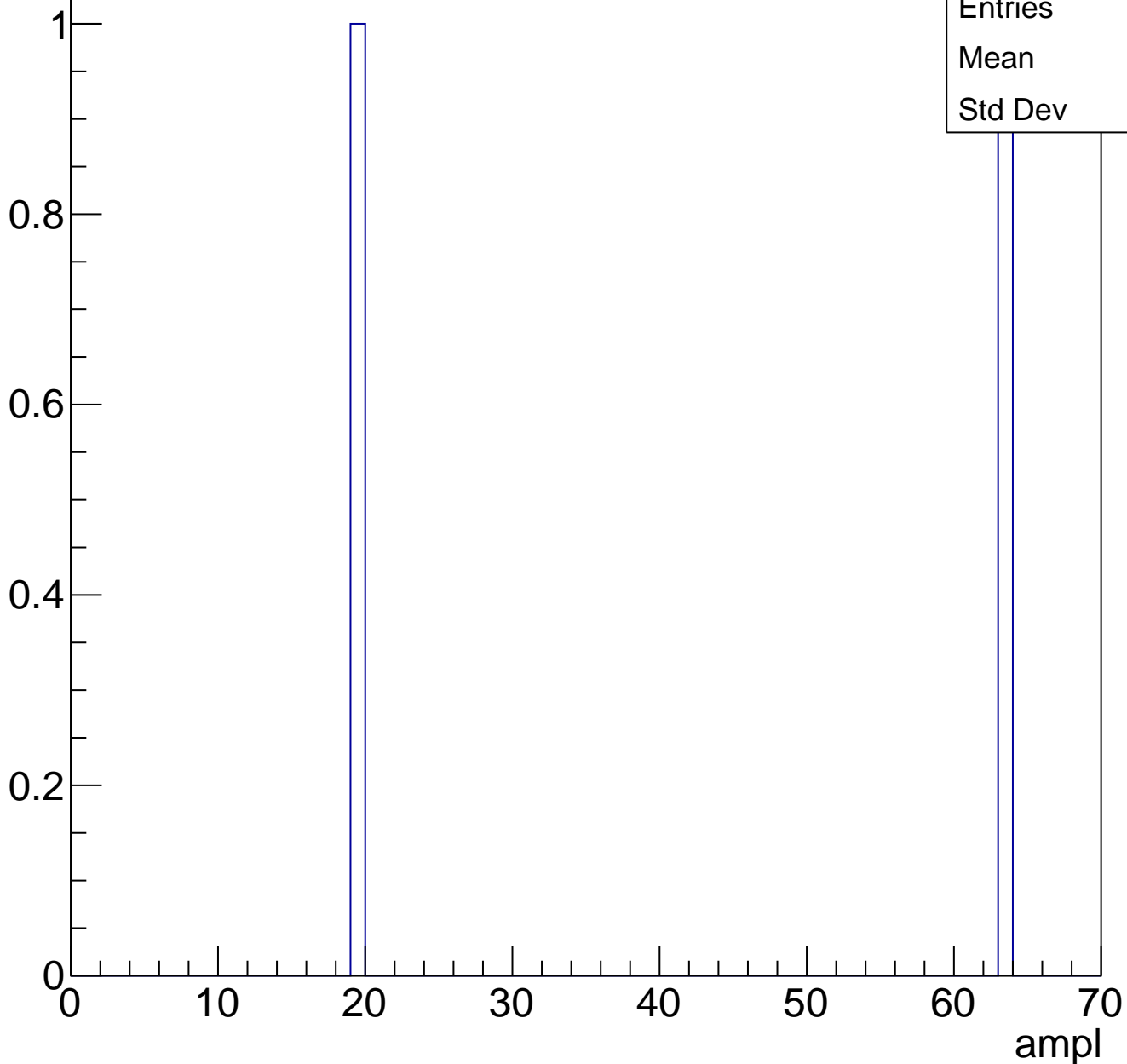
Entry



B1L001S, U21-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	41
Std Dev	22

B1L001S, U21-ch17, adc0

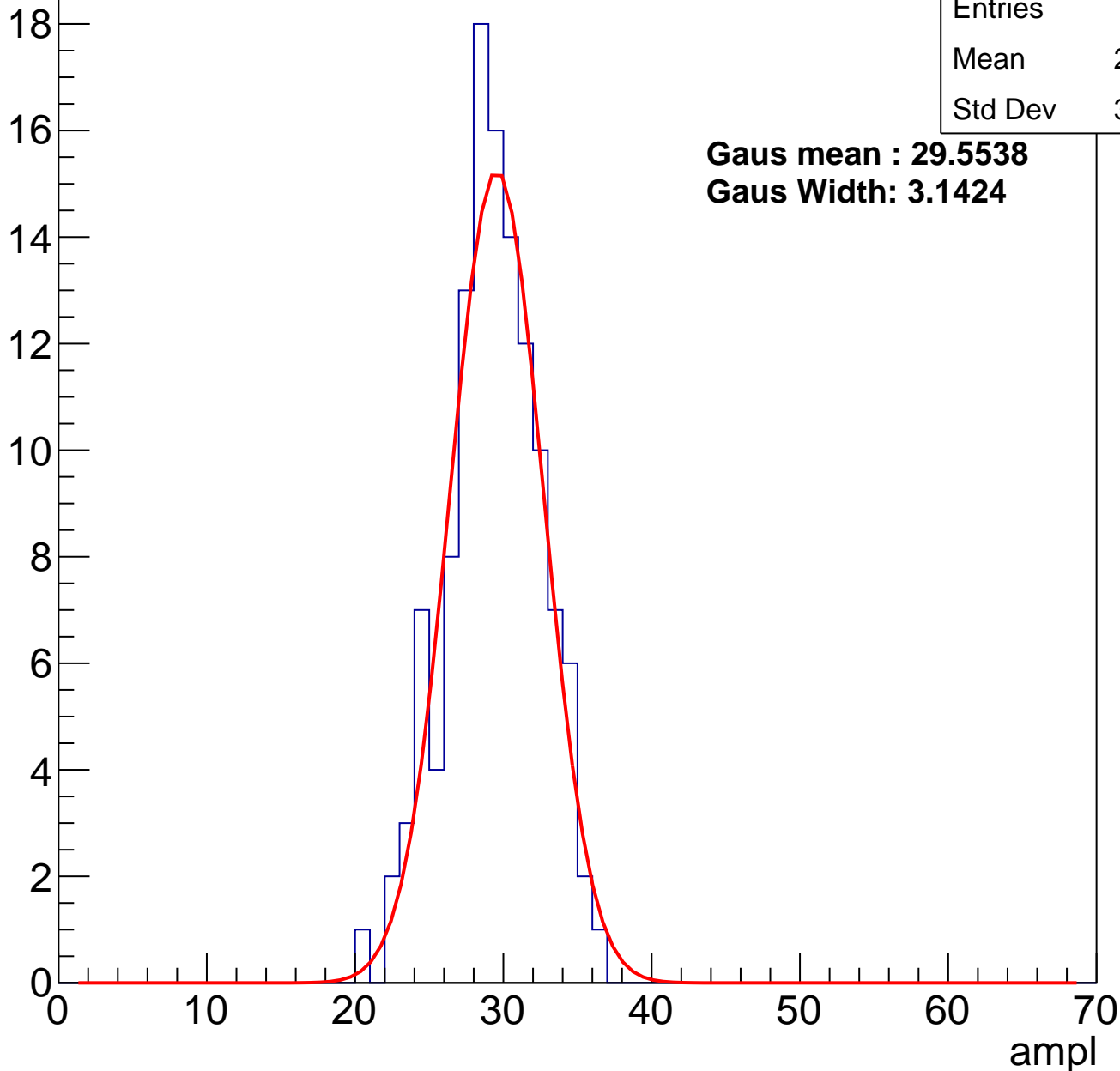
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	28.88
Std Dev	3.112

Gaus mean : 29.5538

Gaus Width: 3.1424

Entry



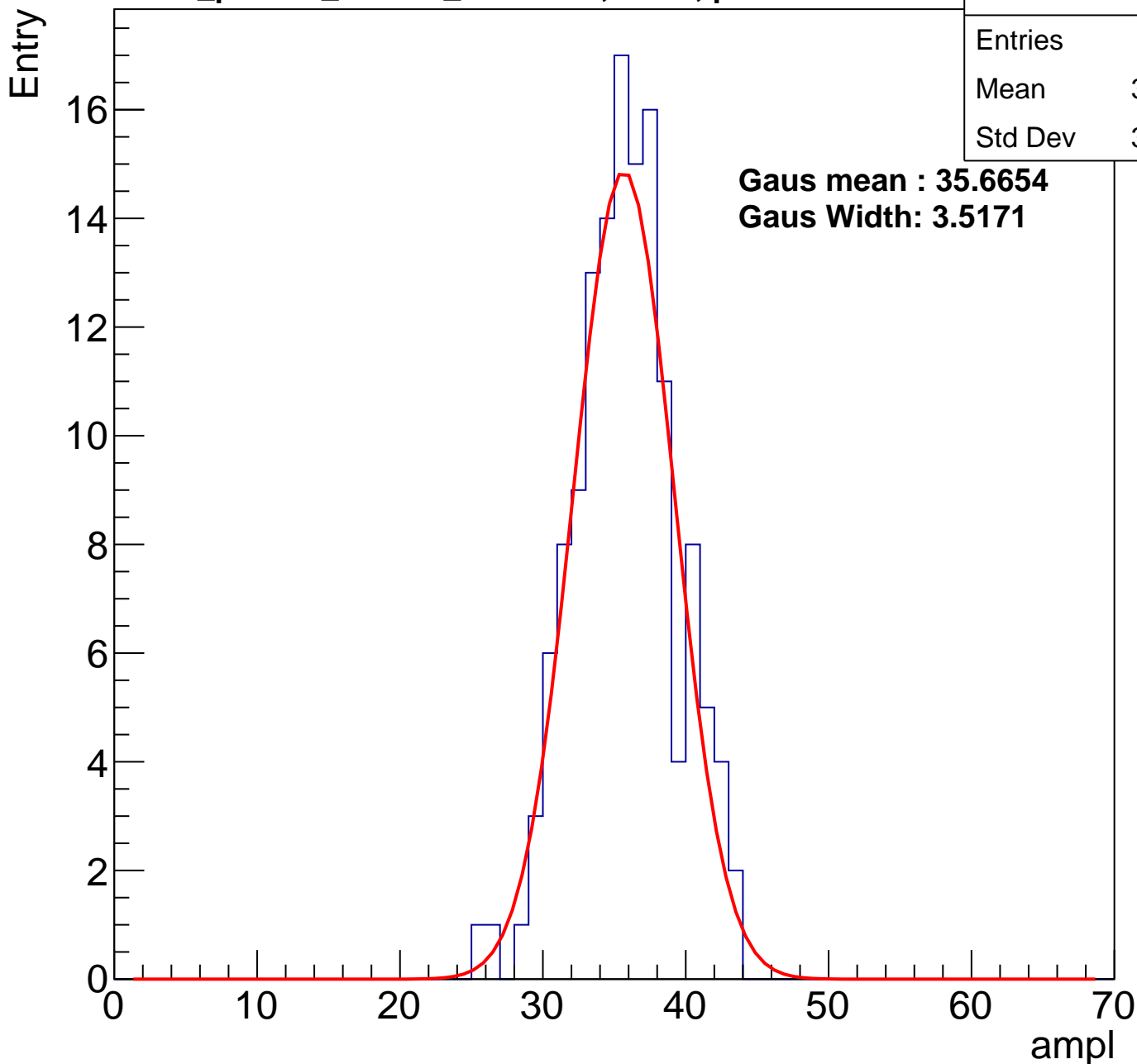
B1L001S, U21-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.27
Std Dev	3.509

Gaus mean : 35.6654

Gaus Width: 3.5171



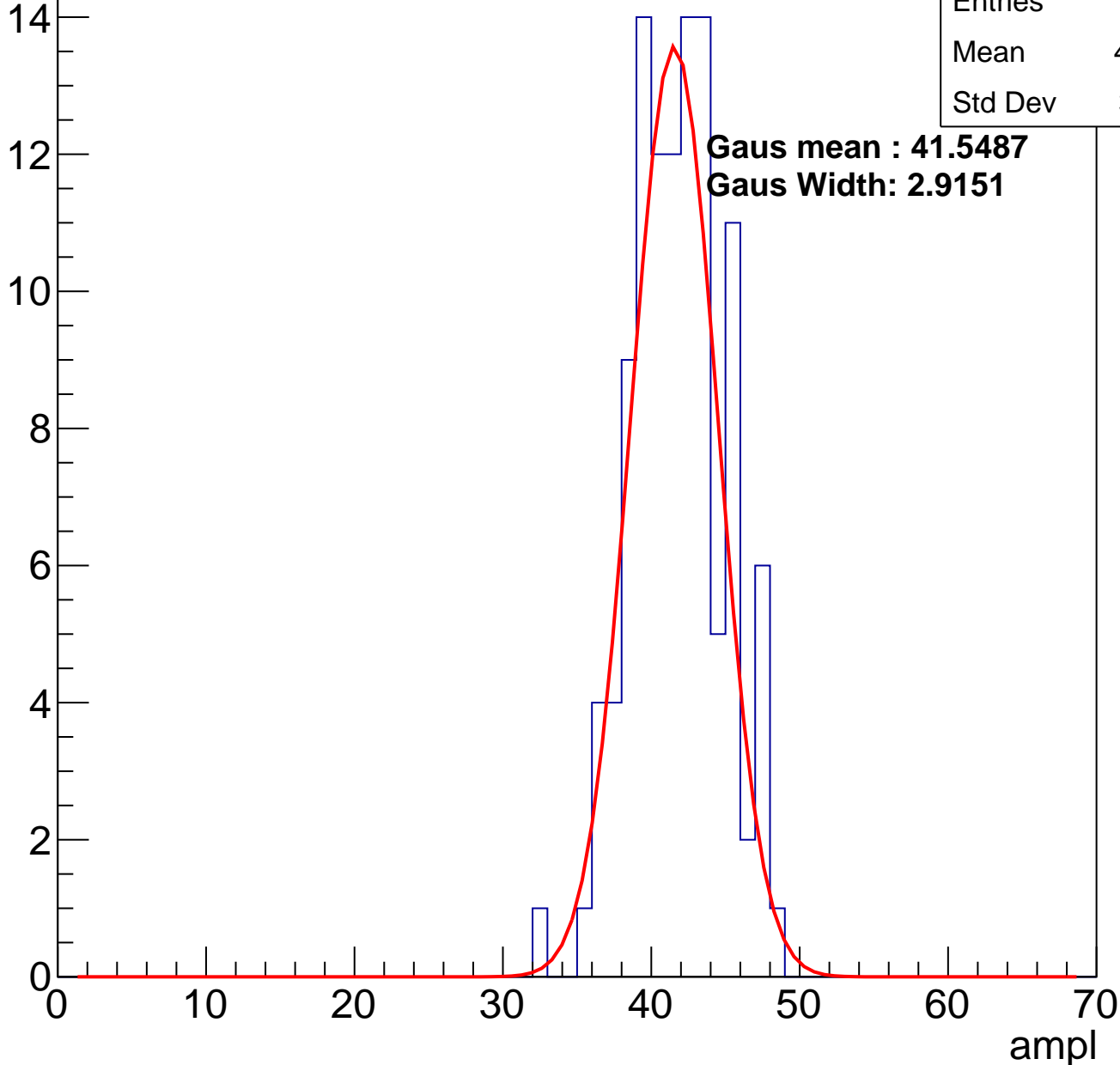
B1L001S, U21-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	41.33
Std Dev	3.051

Gaus mean : 41.5487
Gaus Width: 2.9151

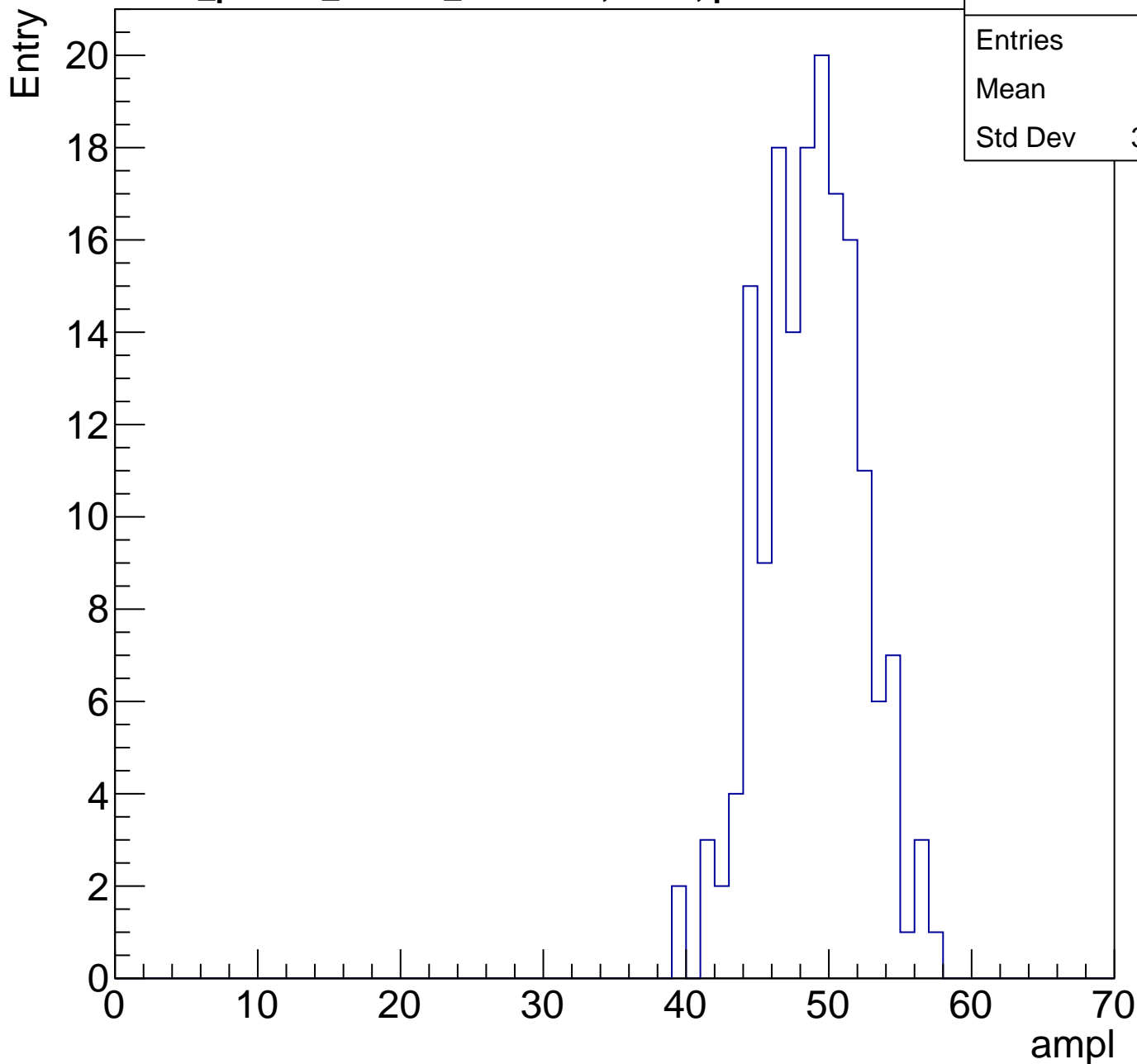
Entry



B1L001S, U21-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	48.3
Std Dev	3.465



B1L001S, U21-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

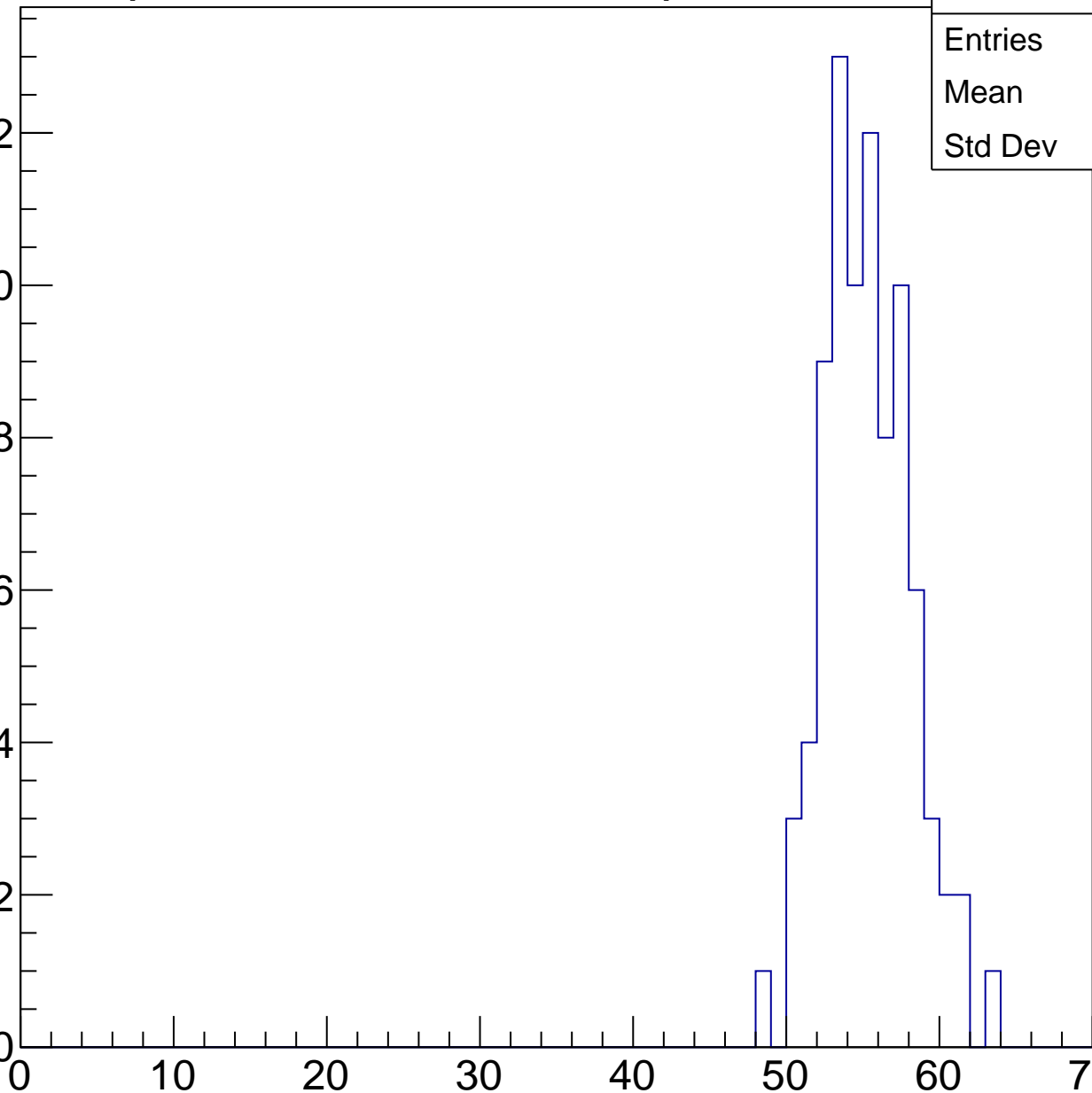
4

2

0

Entries	84
Mean	54.85
Std Dev	2.814

ampl



B1L001S, U21-ch17, adc5

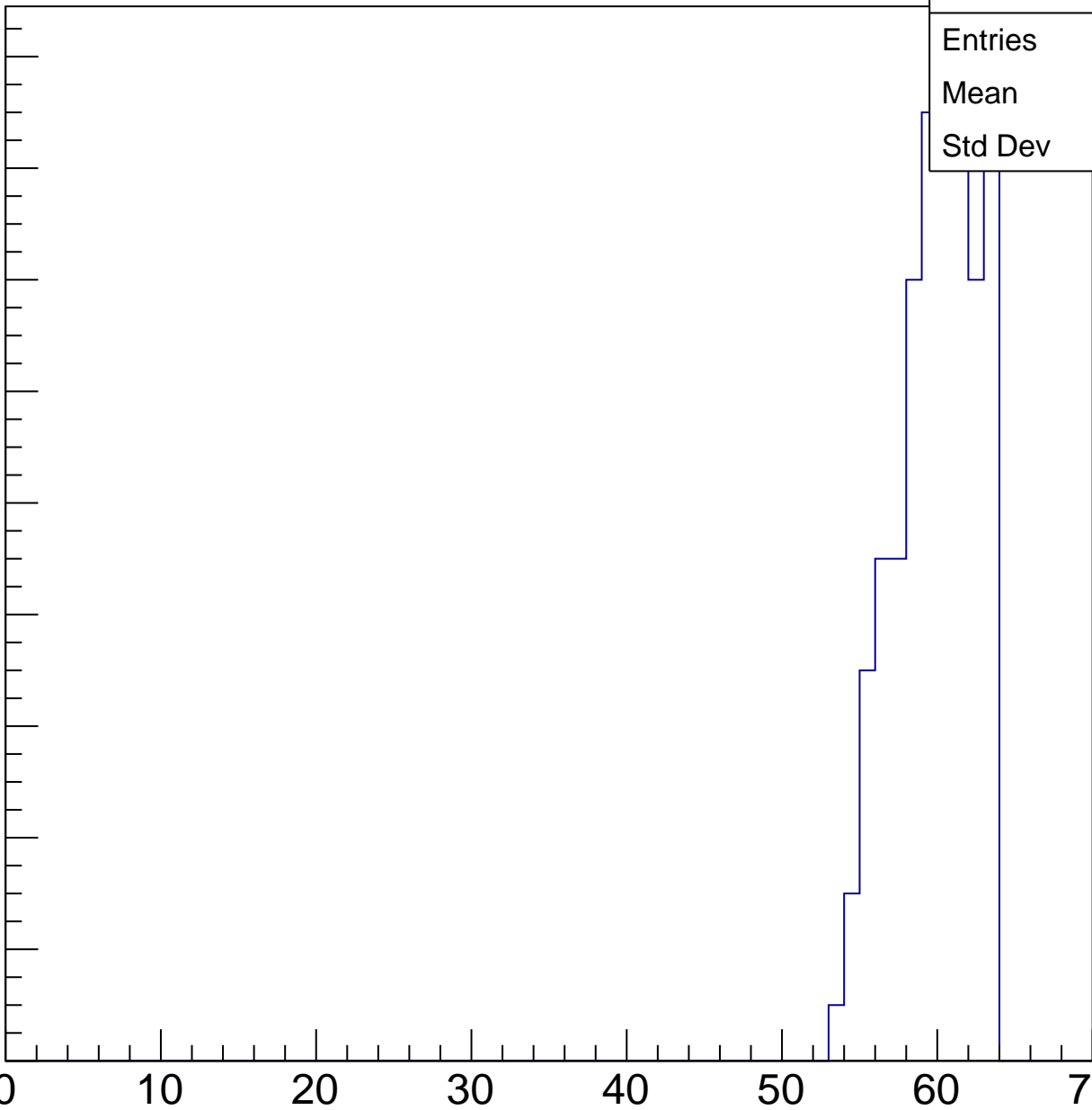
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	125
Mean	59.44
Std Dev	2.569

ampl

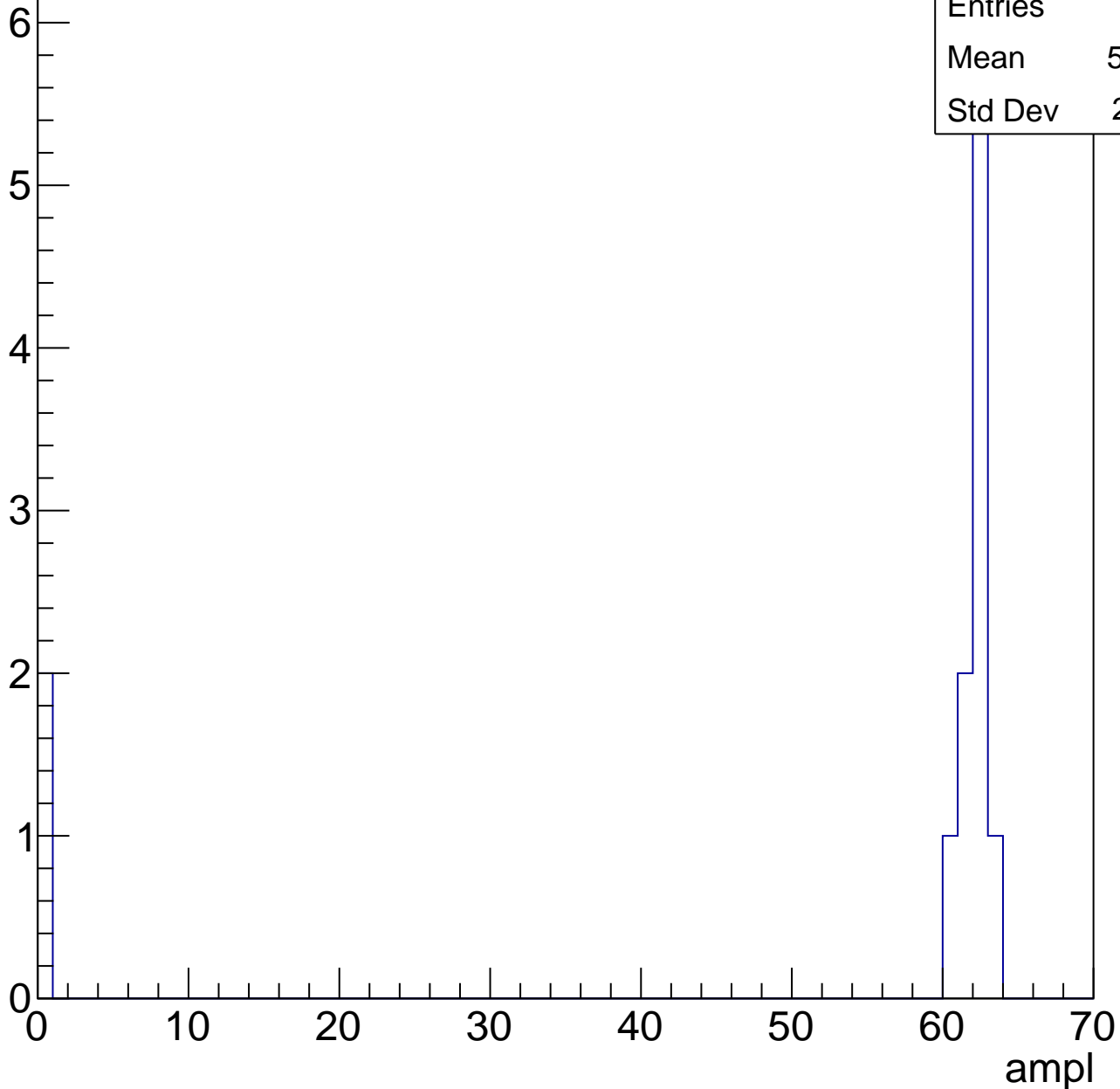


B1L001S, U21-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	51.42
Std Dev	23.01



B1L001S, U21-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch18, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	30.92
Std Dev	2.918

Gaus mean : 31.3323

Gaus Width: 3.1493

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

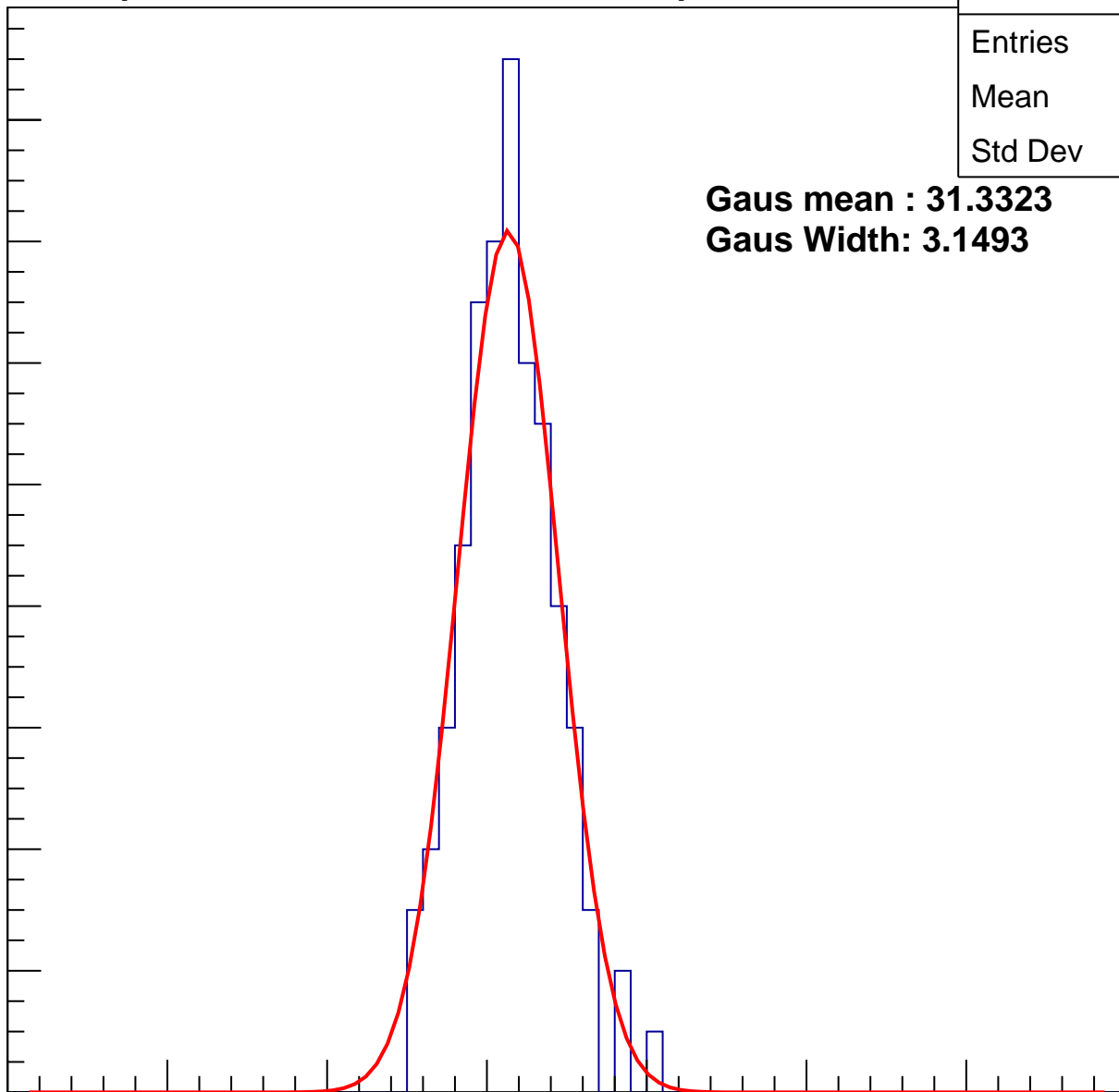
40

50

60

70

ampl



B1L001S, U21-ch18, adc1

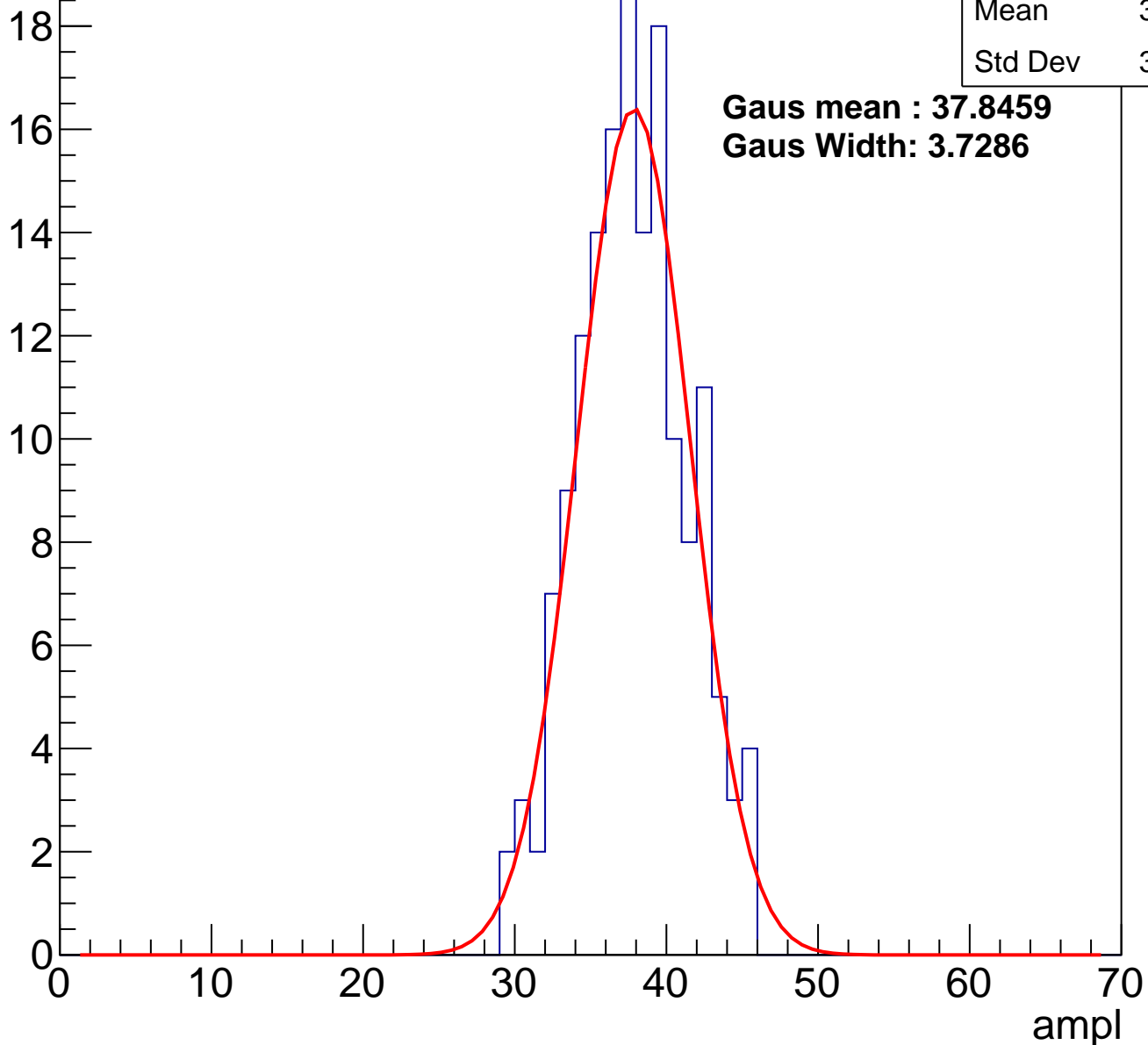
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	37.32
Std Dev	3.562

Gaus mean : 37.8459

Gaus Width: 3.7286

Entry



B1L001S, U21-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

133

Mean

44.3

Std Dev

3.212

Gaus mean : 45.1571

Gaus Width: 3.3700

0

10

20

30

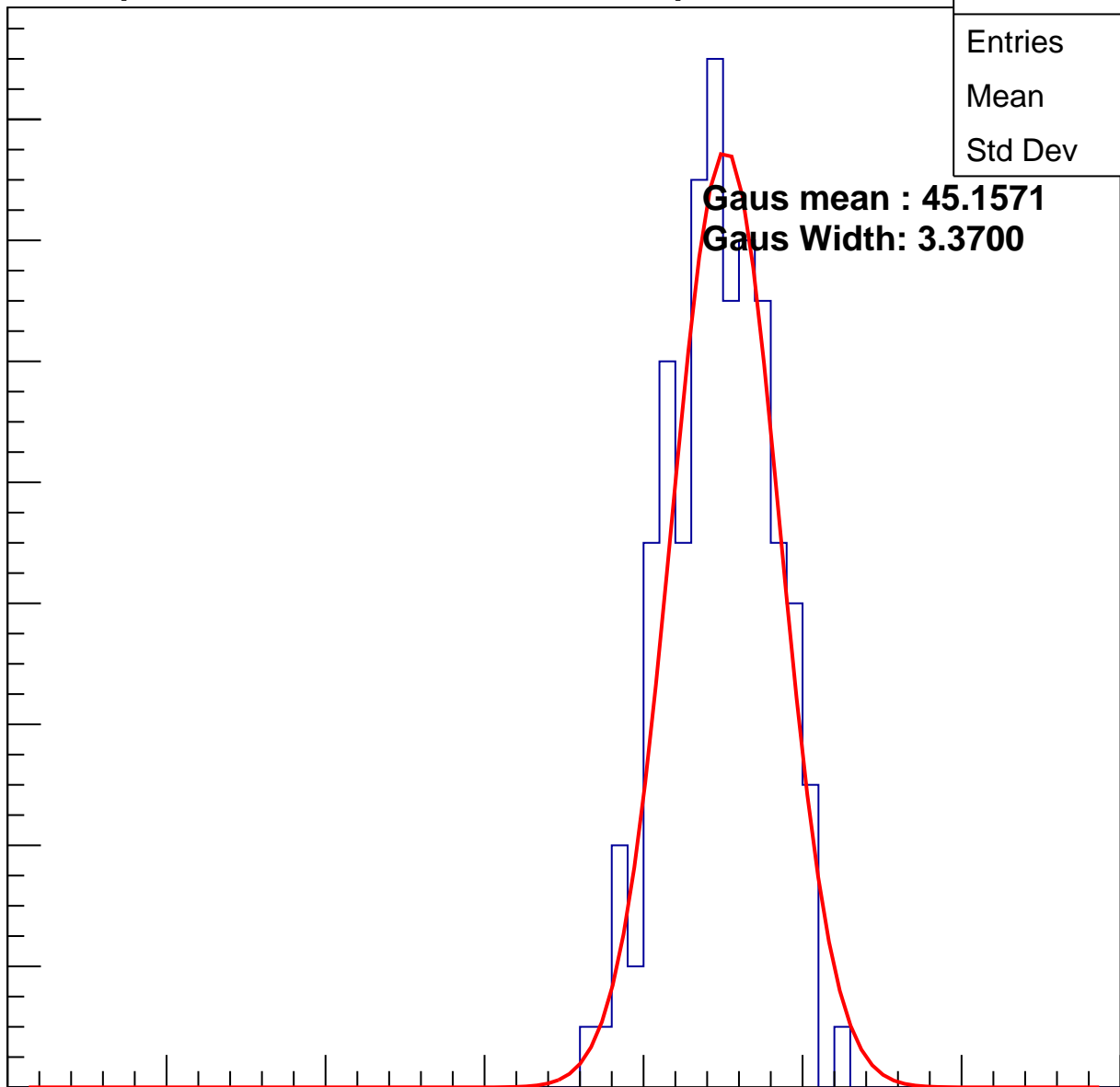
40

50

60

70

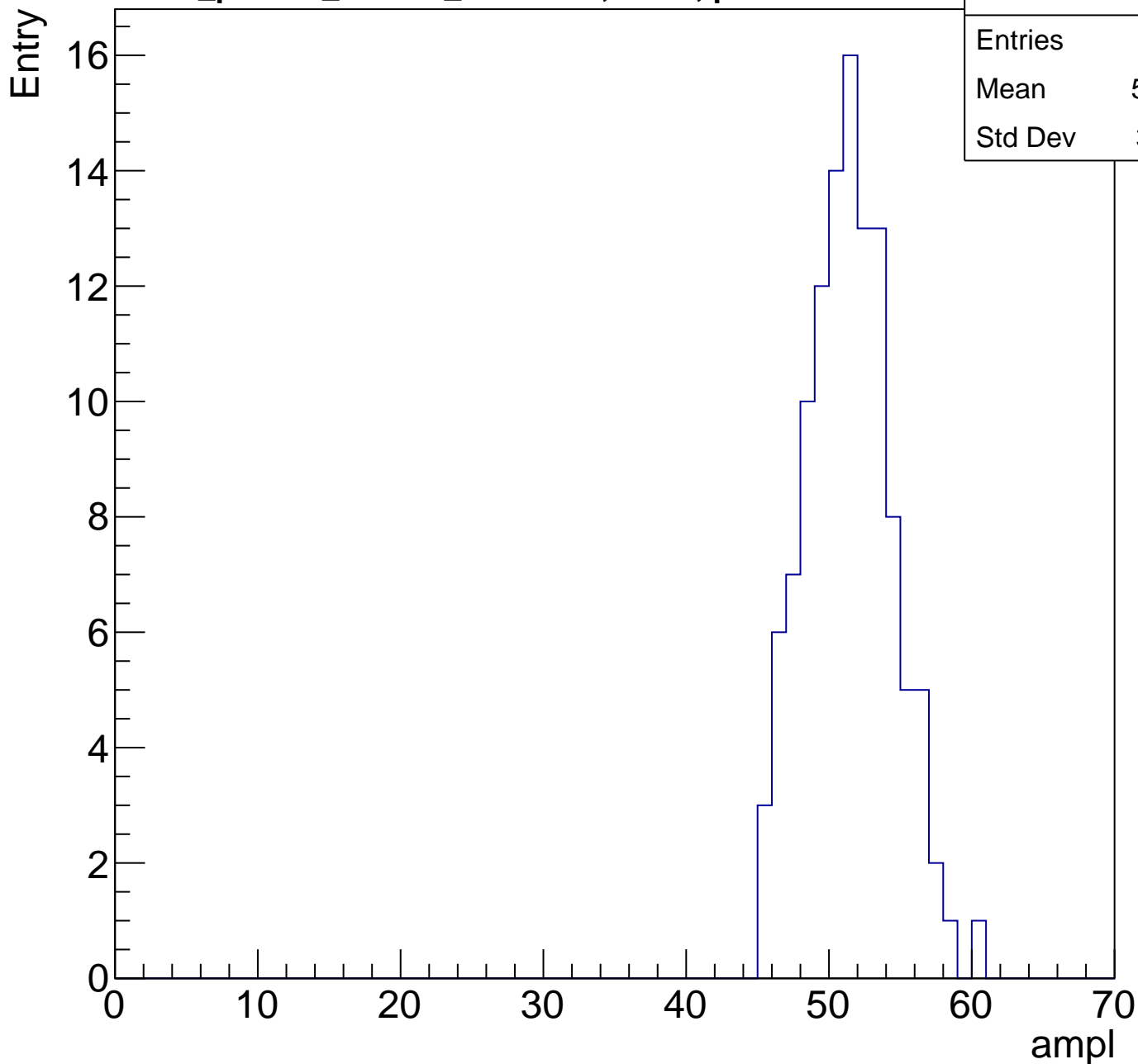
ampl



B1L001S, U21-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

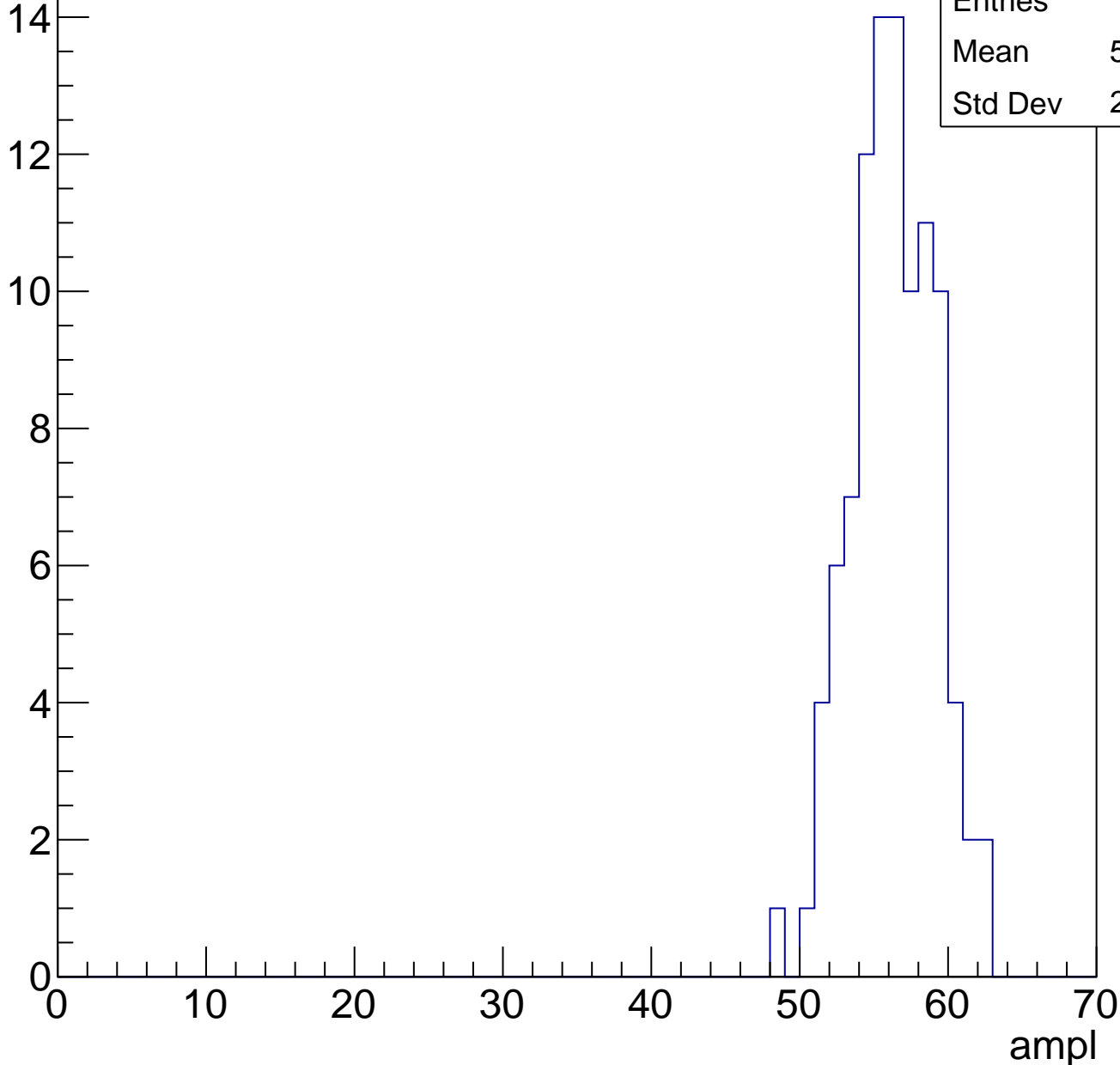
Entries	116
Mean	50.93
Std Dev	3.031



B1L001S, U21-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

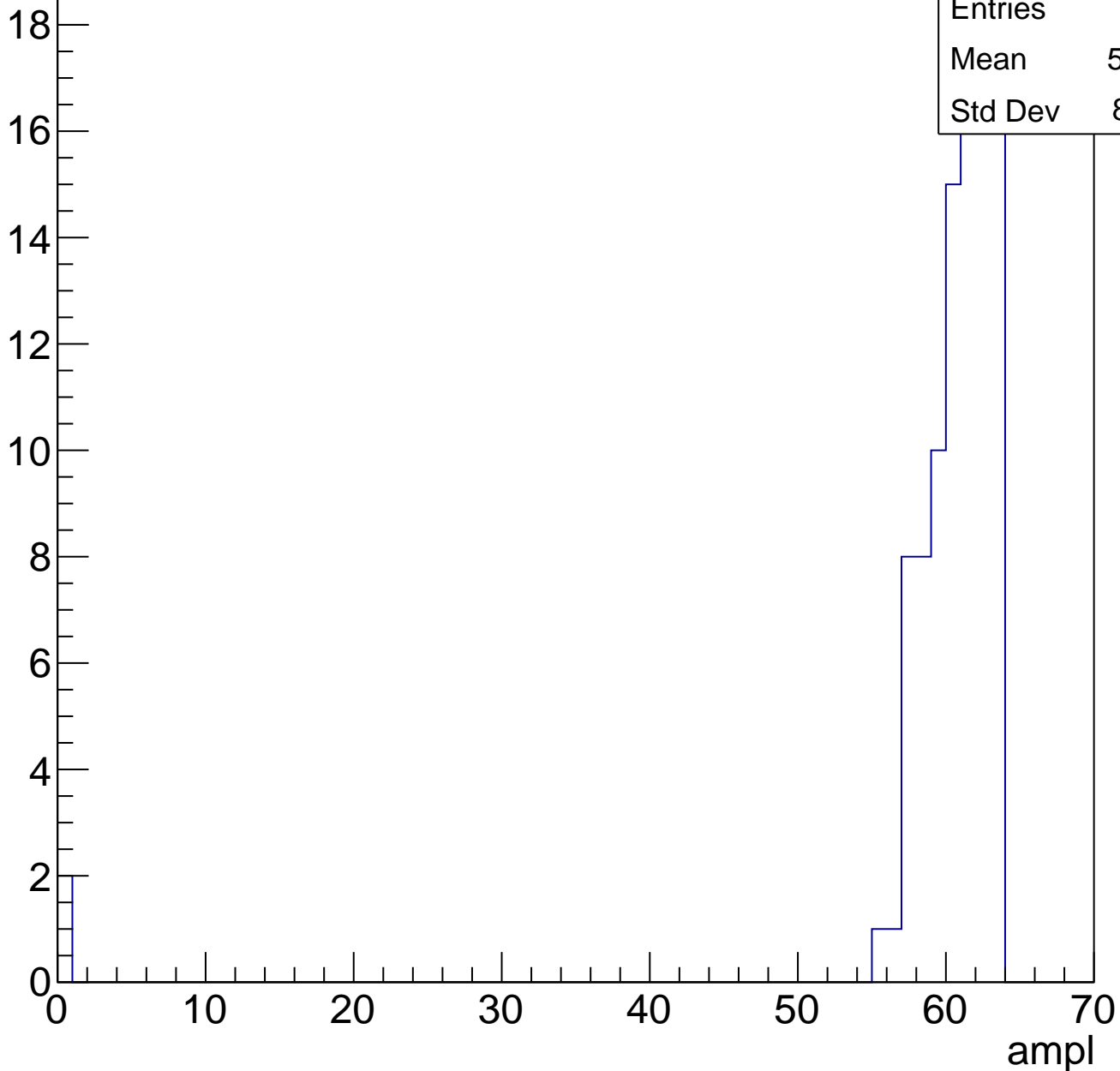


B1L001S, U21-ch18, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	59.18
Std Dev	8.851

Entry



B1L001S, U21-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch18, adc7

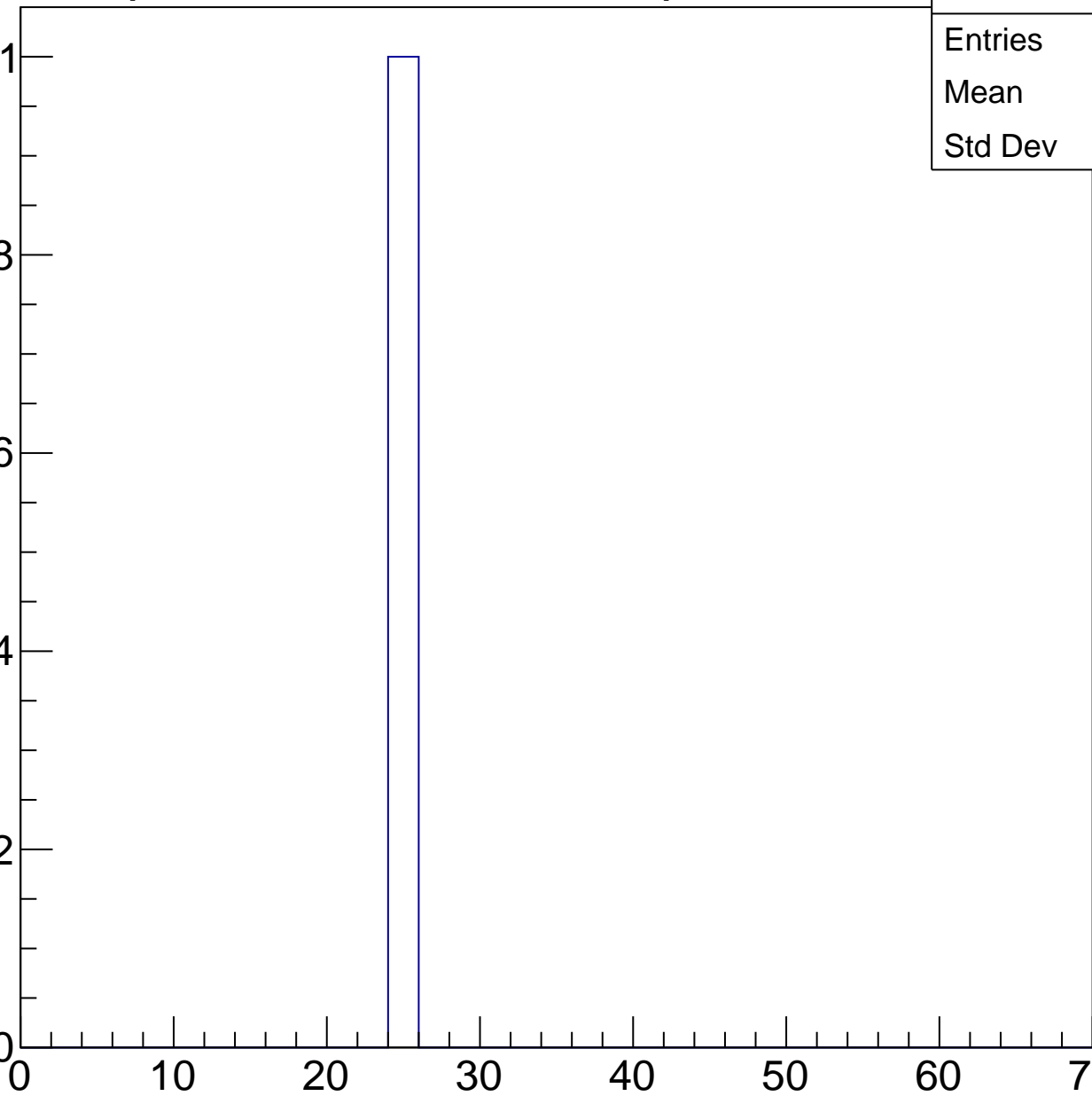
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	24.5
Std Dev	0.5

ampl



B1L001S, U21-ch19, adc0

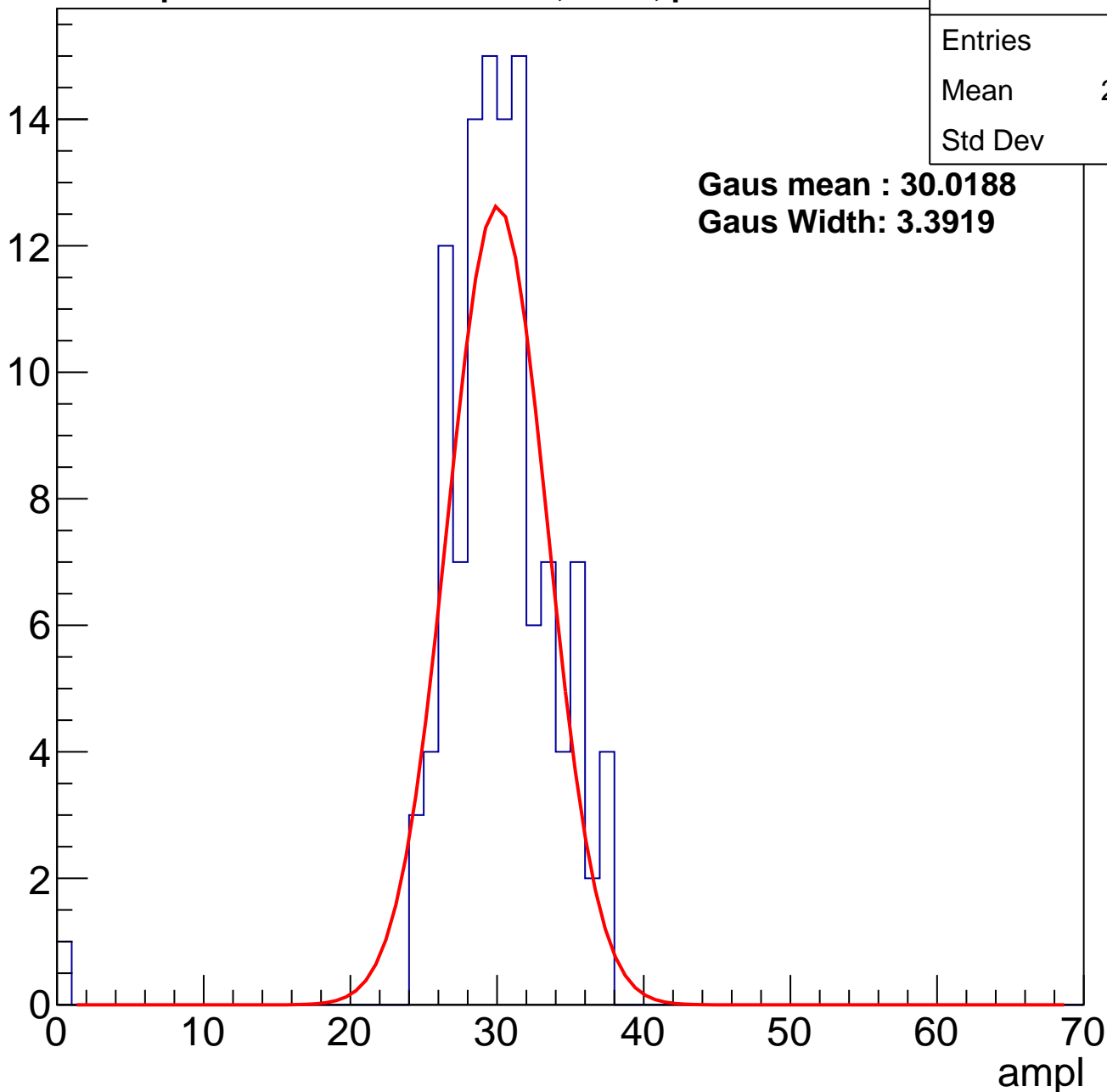
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	29.64
Std Dev	4.2

Gaus mean : 30.0188

Gaus Width: 3.3919

Entry



B1L001S, U21-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.68
Std Dev	3.385

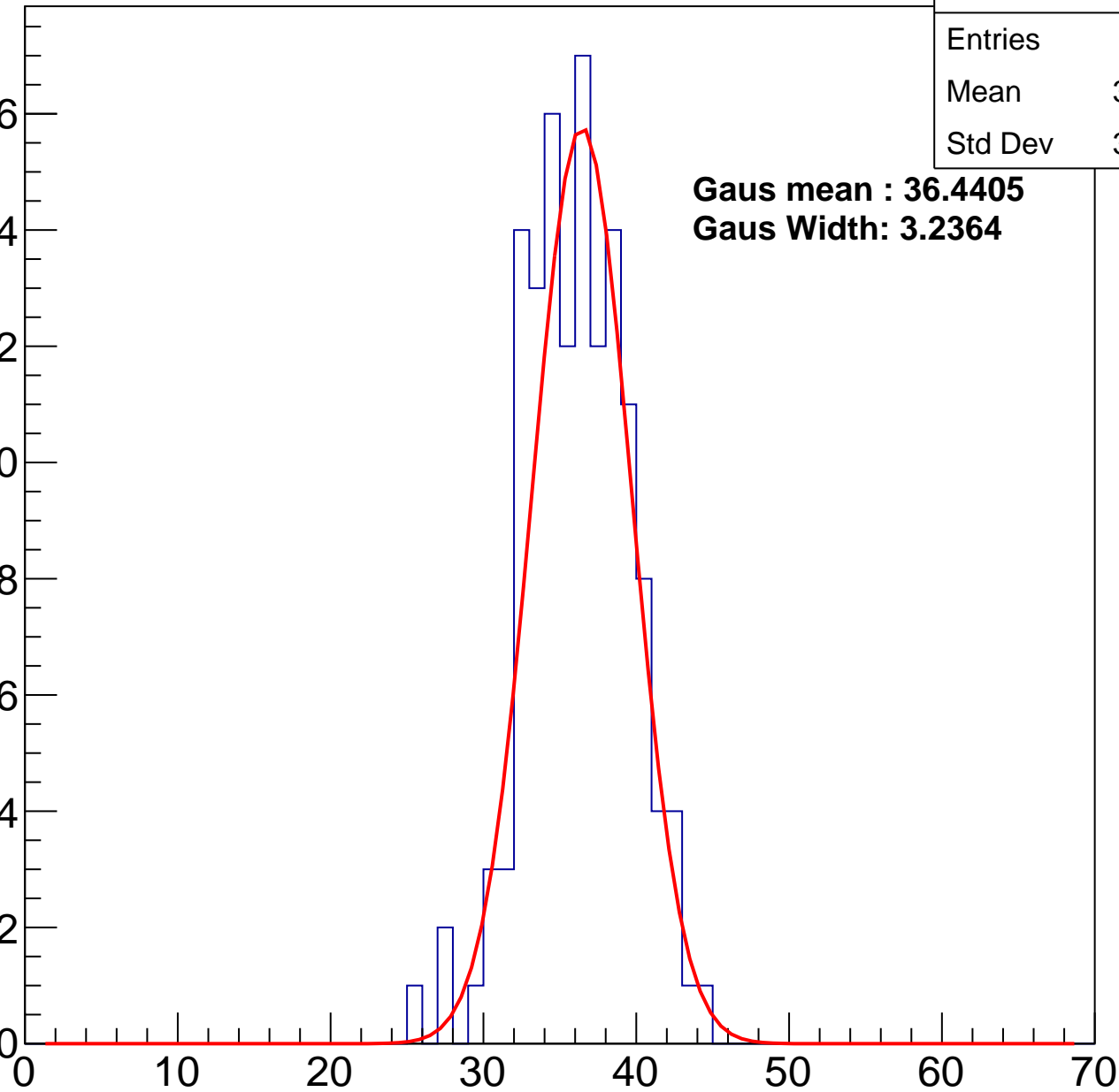
Gaus mean : 36.4405

Gaus Width: 3.2364

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch19, adc2

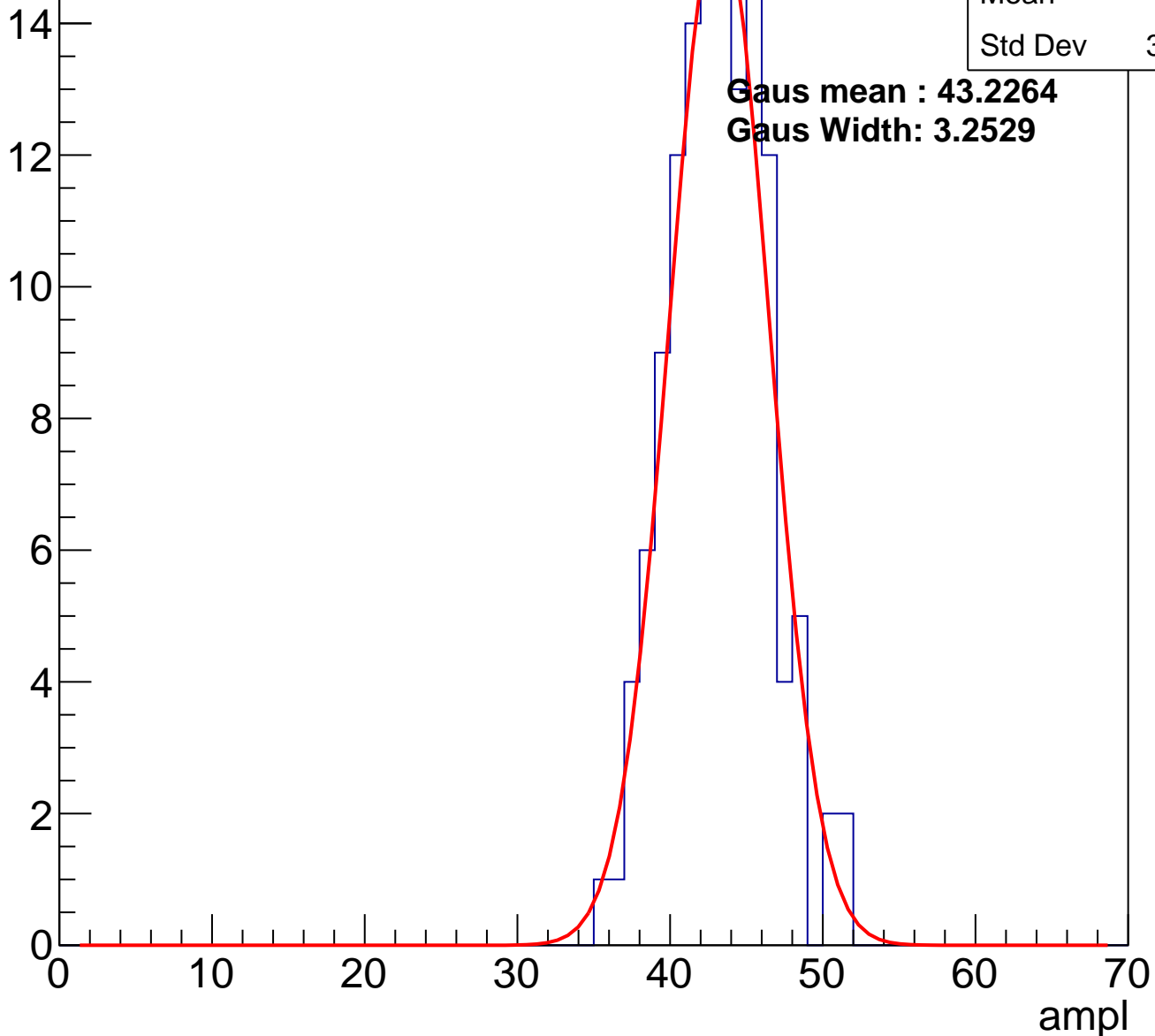
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	42.74
Std Dev	3.183

Gaus mean : 43.2264

Gaus Width: 3.2529

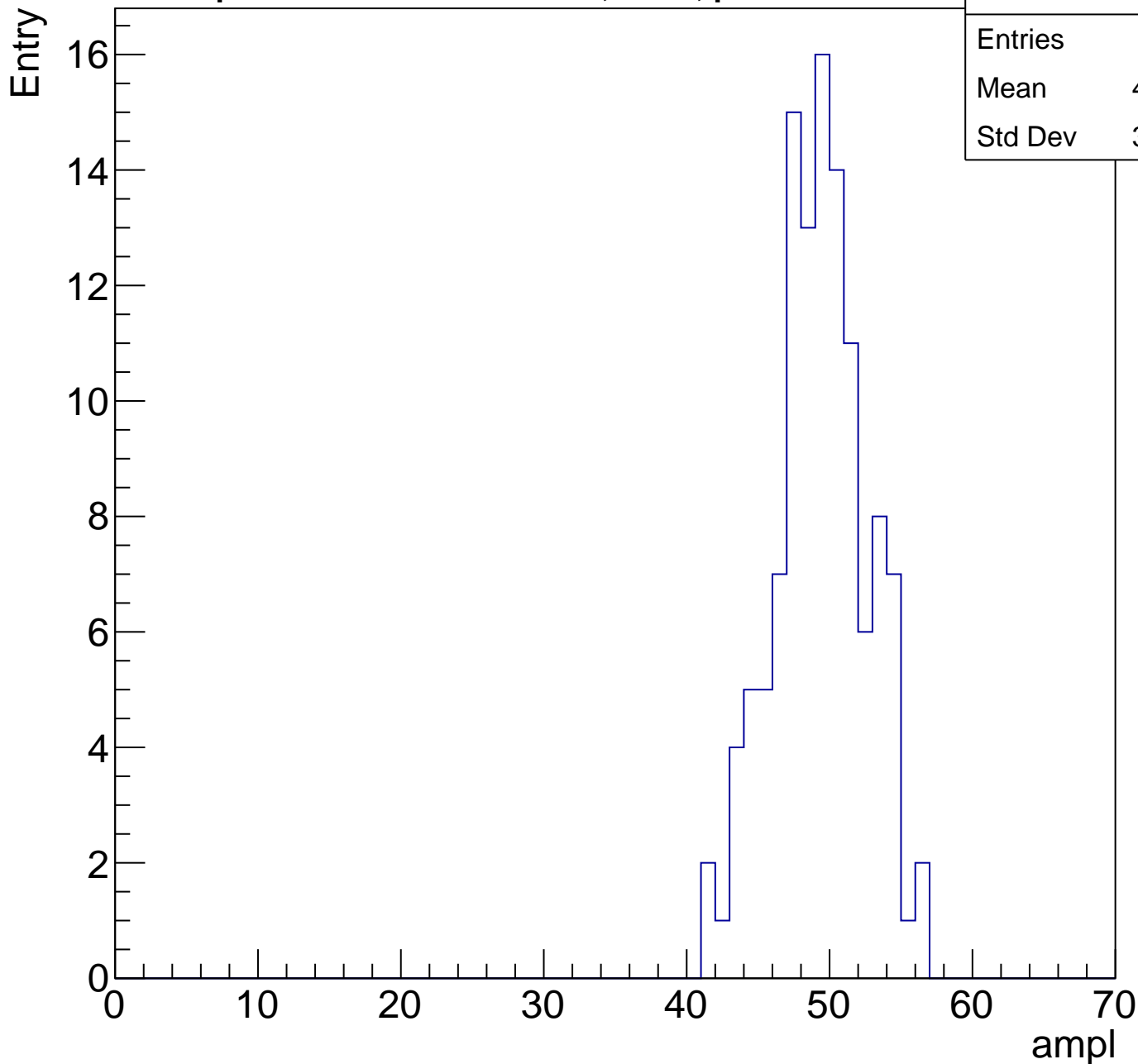
Entry



B1L001S, U21-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	48.87
Std Dev	3.215



B1L001S, U21-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

152

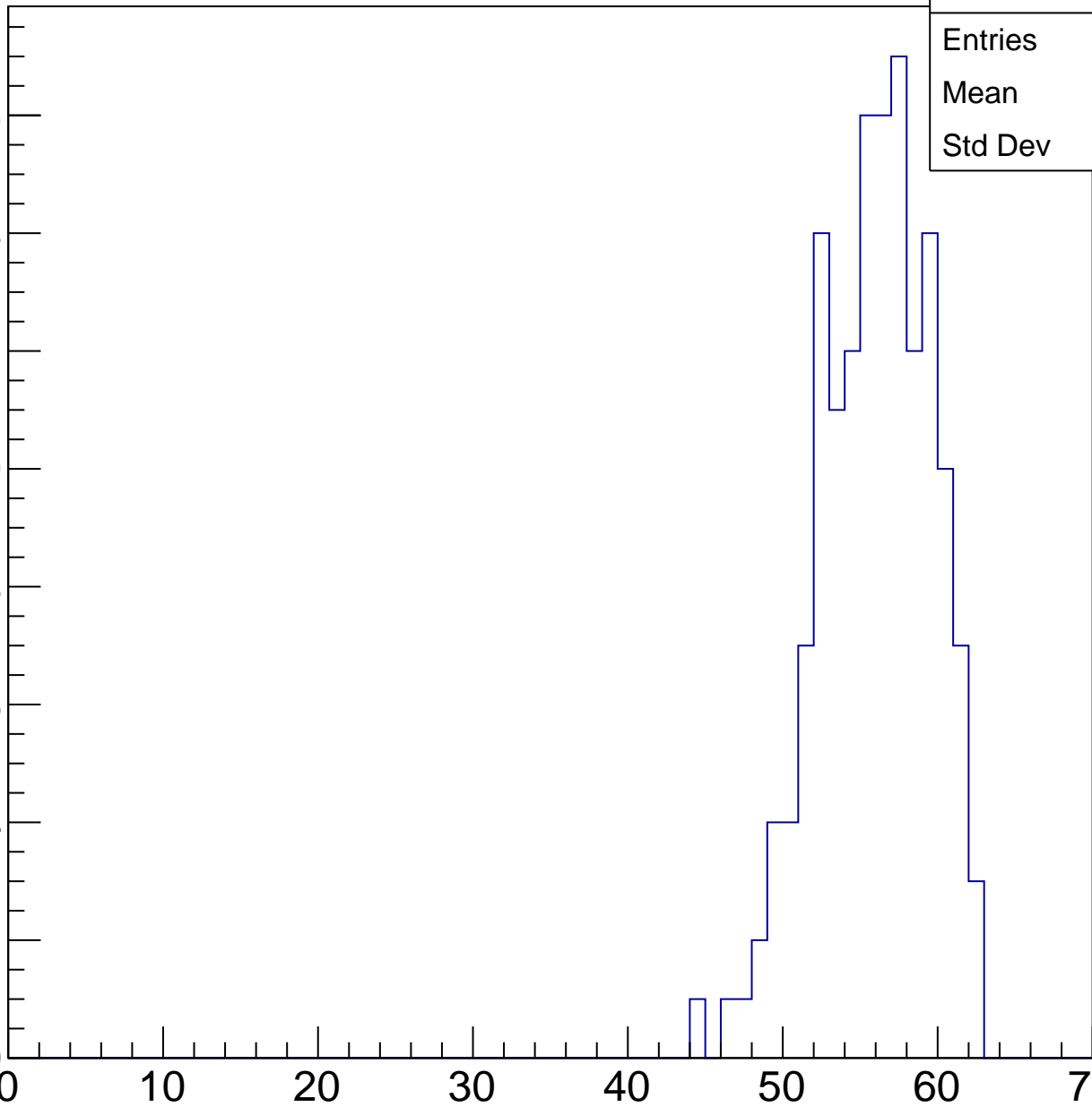
Mean

55.43

Std Dev

3.572

ampl



B1L001S, U21-ch19, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

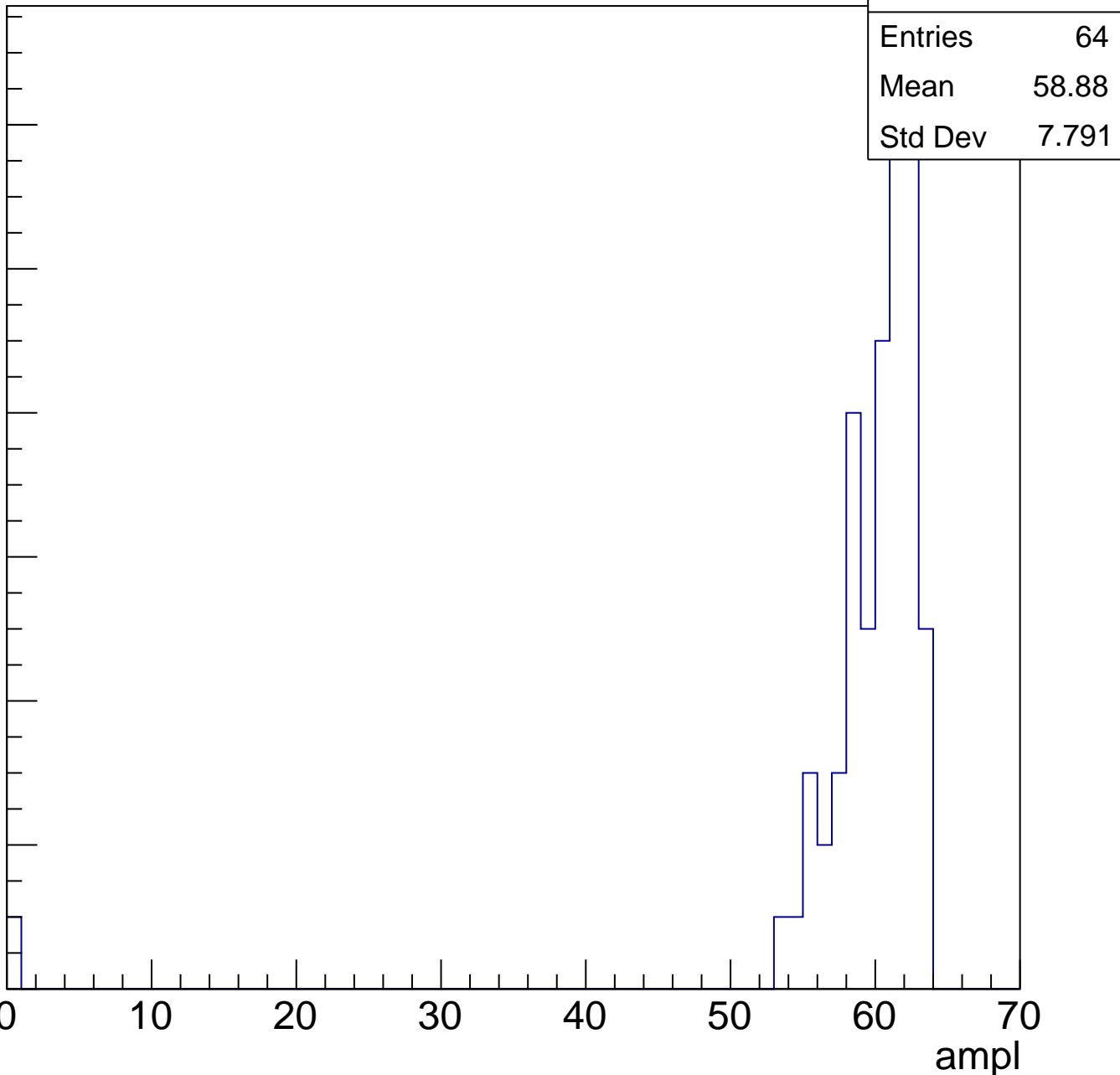
40

50

60

ampl

Entries	64
Mean	58.88
Std Dev	7.791



B1L001S, U21-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

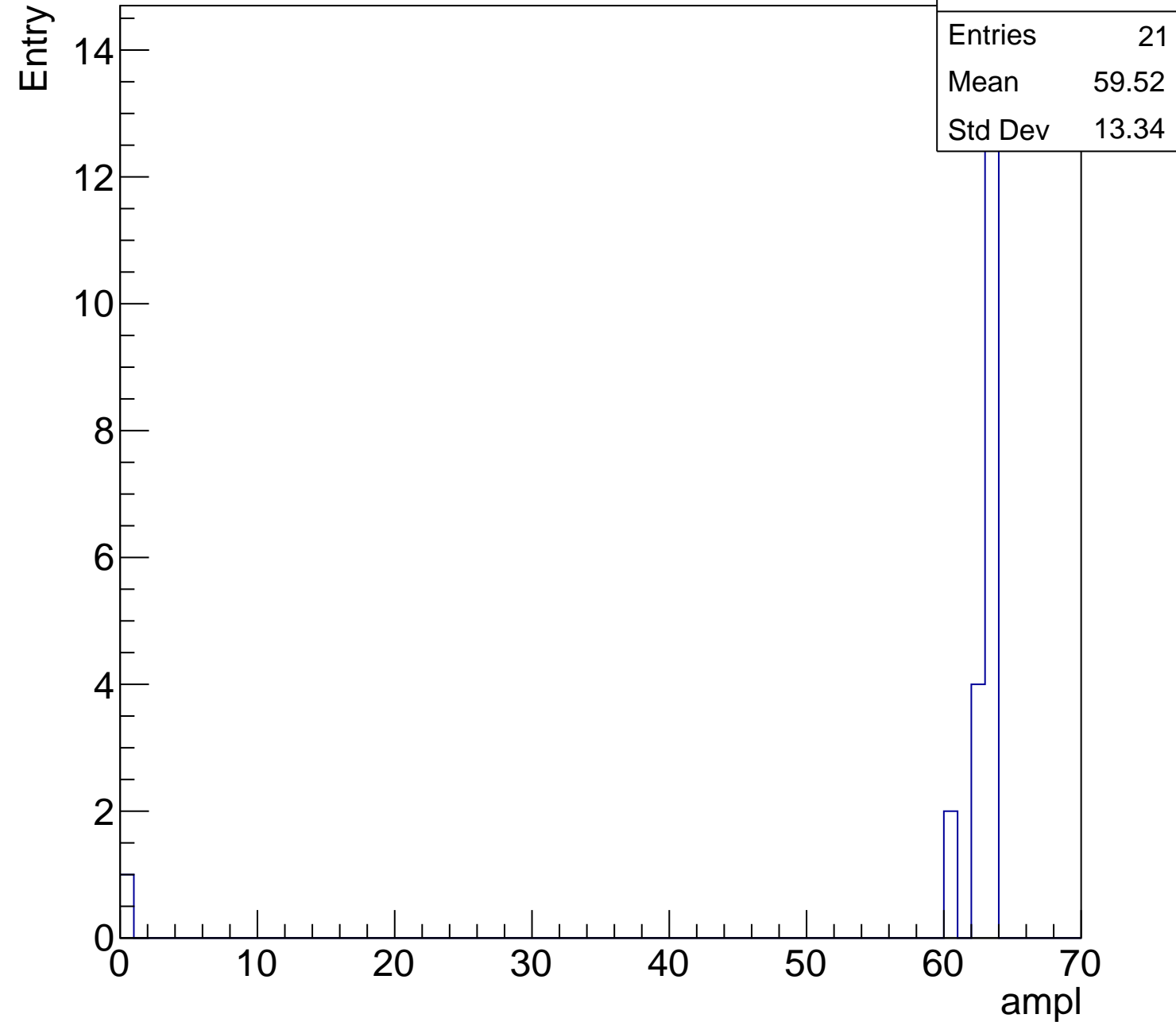
Entry

14
12
10
8
6
4
2
0

Entries	21
Mean	59.52
Std Dev	13.34

ampl

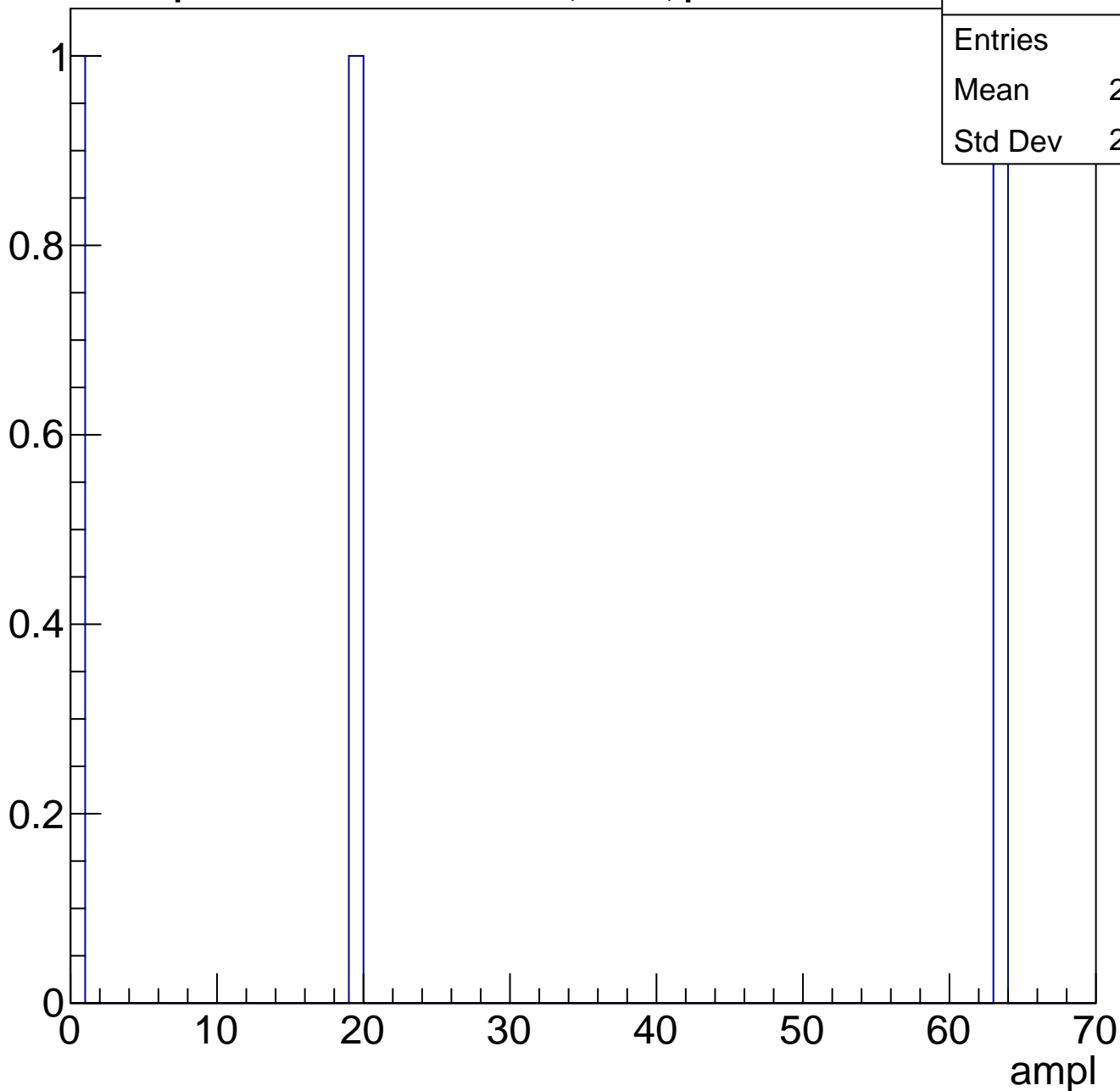
0 10 20 30 40 50 60 70



B1L001S, U21-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.33
Std Dev	26.39

B1L001S, U21-ch20, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.41
Std Dev	4.71

Gaus mean : 29.3113

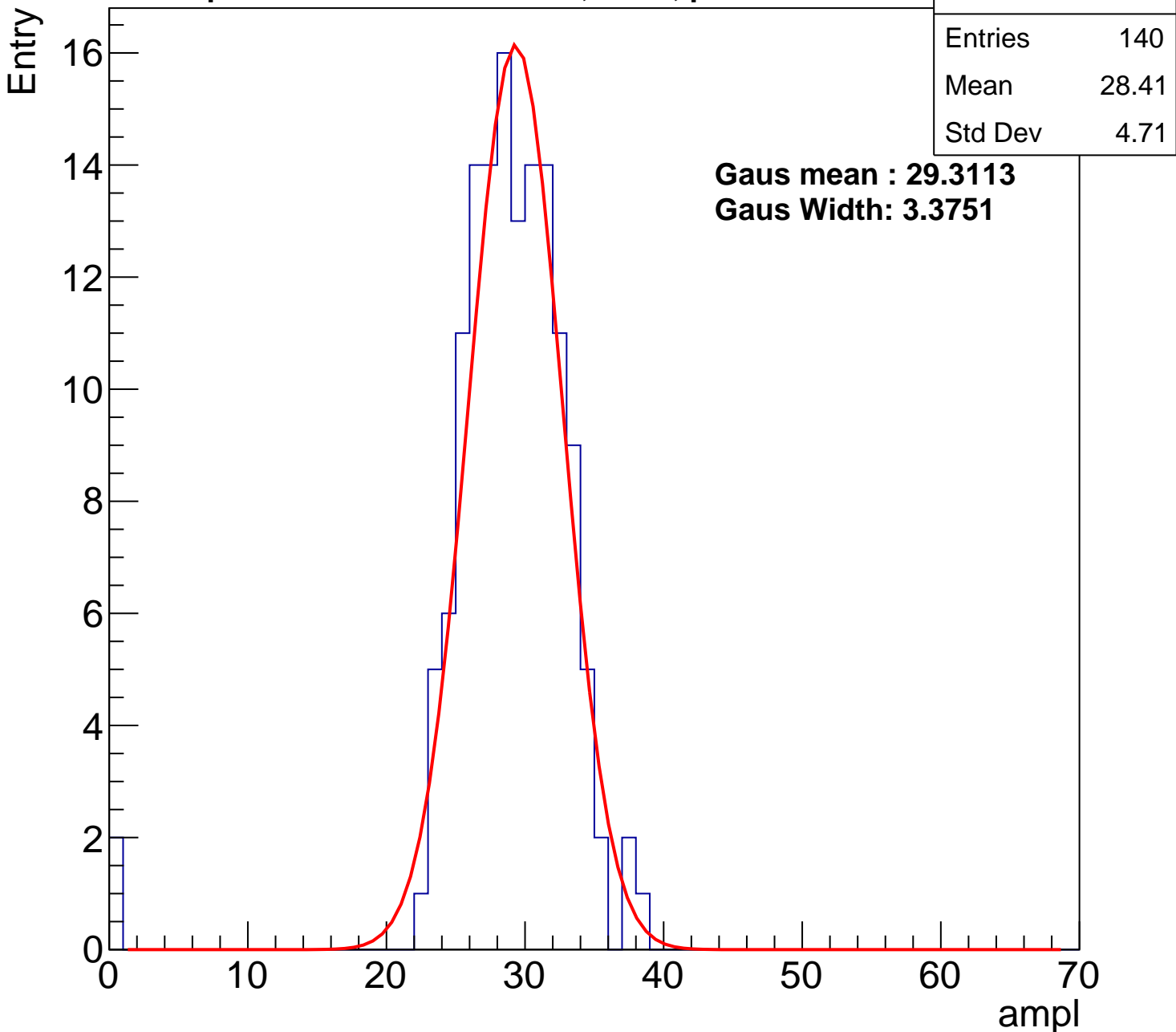
Gaus Width: 3.3751

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.76
Std Dev	3.17

Gaus mean : 36.0522

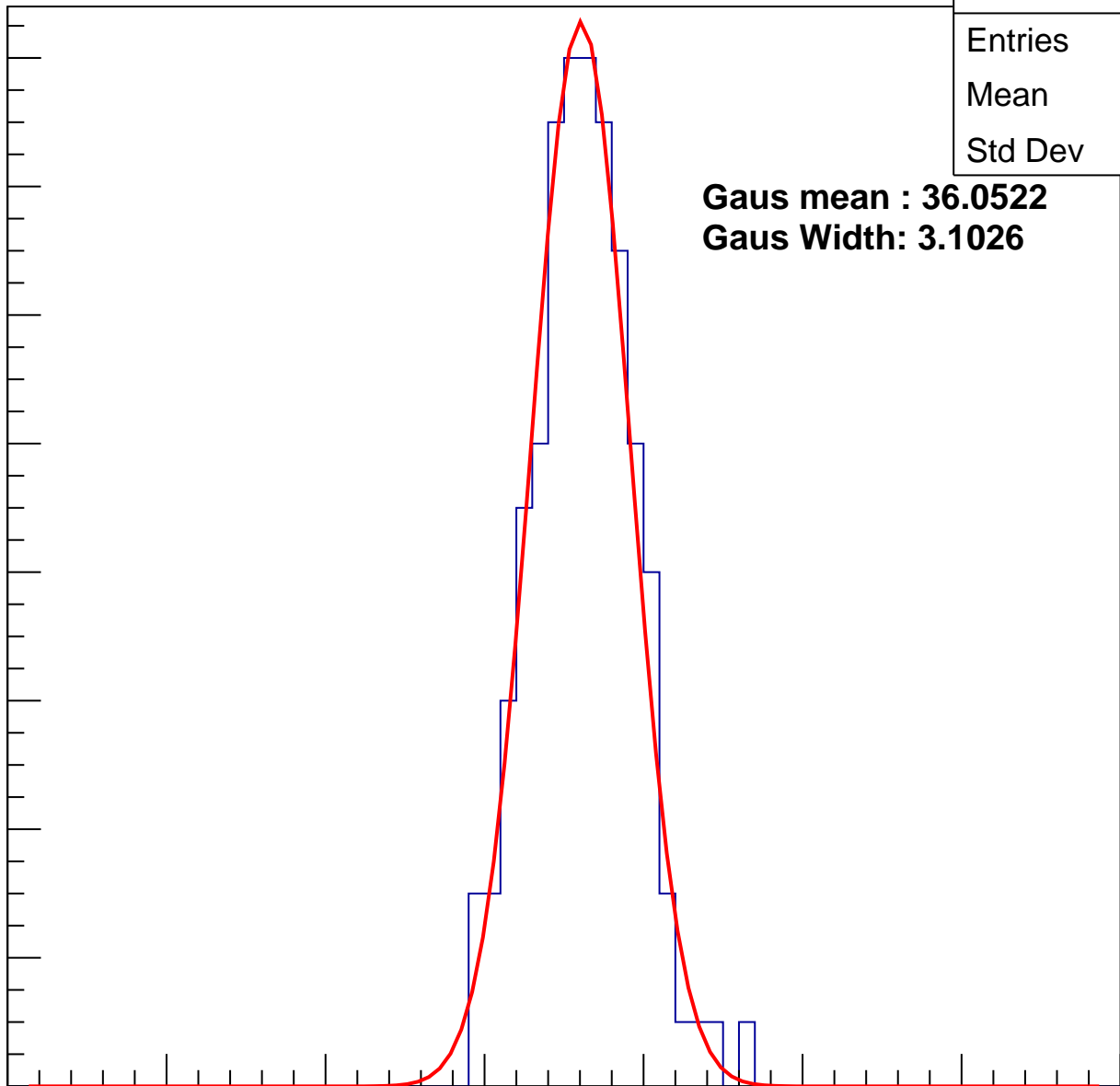
Gaus Width: 3.1026

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch20, adc2

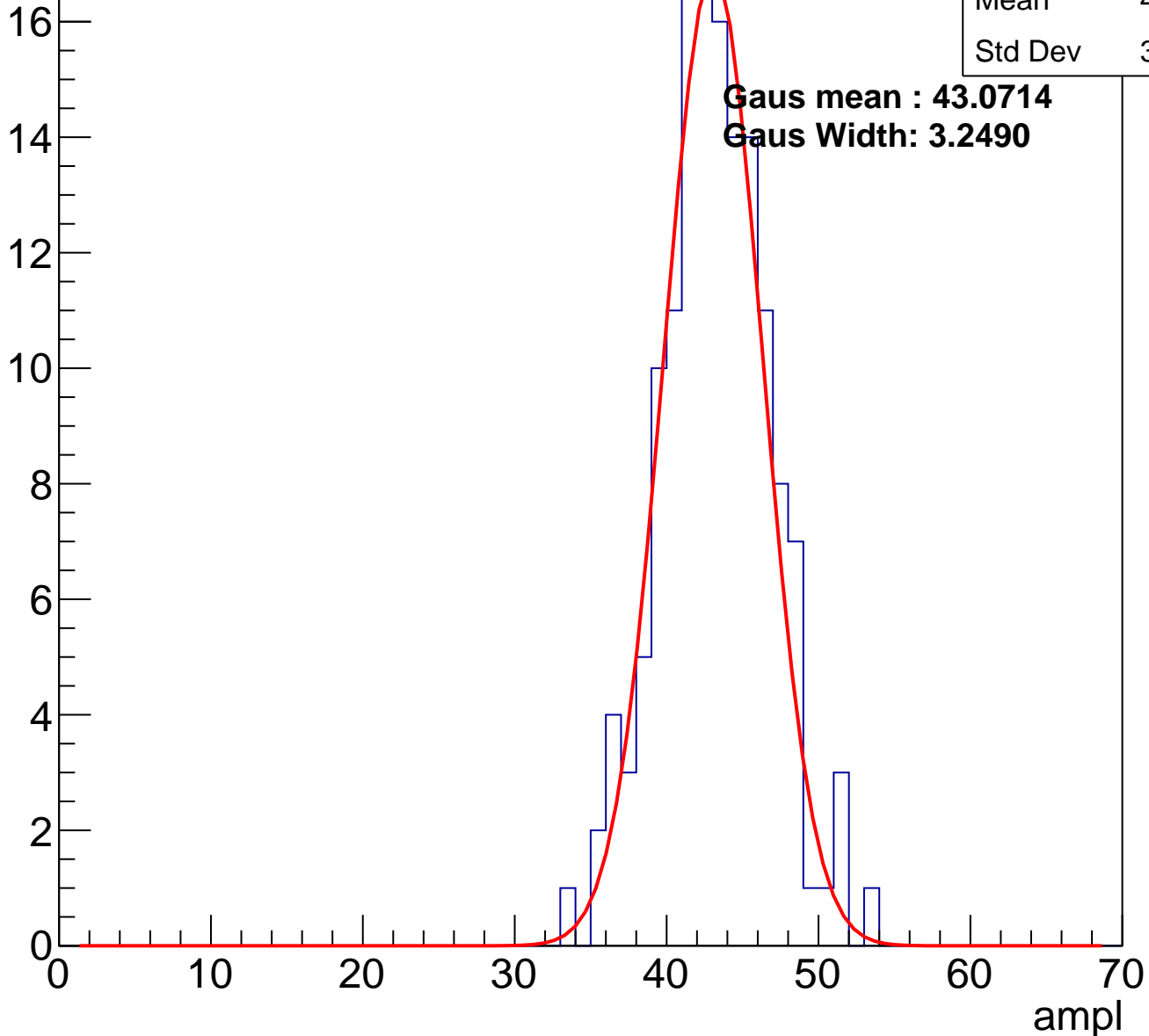
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	146
Mean	42.78
Std Dev	3.553

Gaus mean : 43.0714

Gaus Width: 3.2490

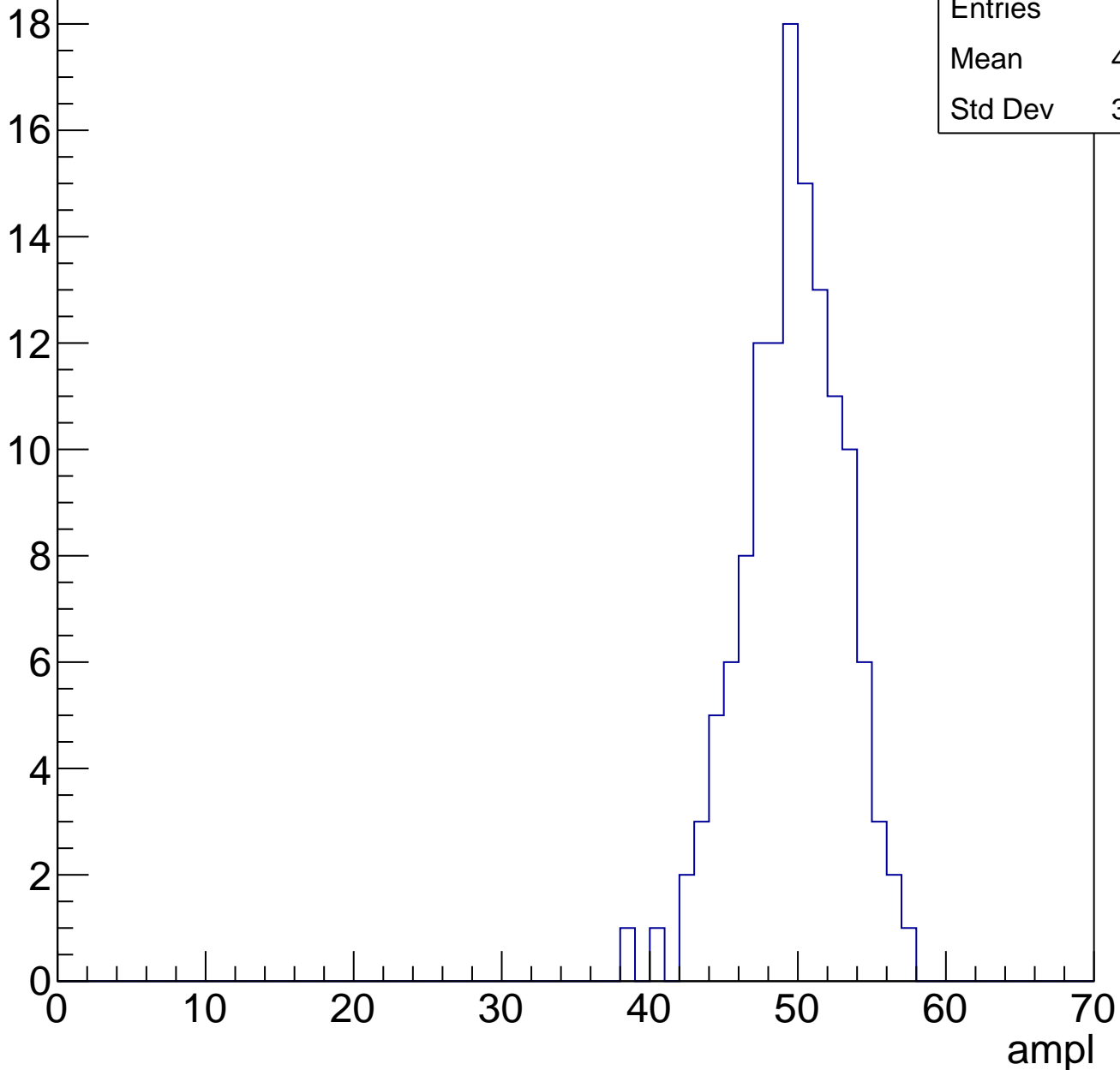


B1L001S, U21-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	49.18
Std Dev	3.413

Entry



B1L001S, U21-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

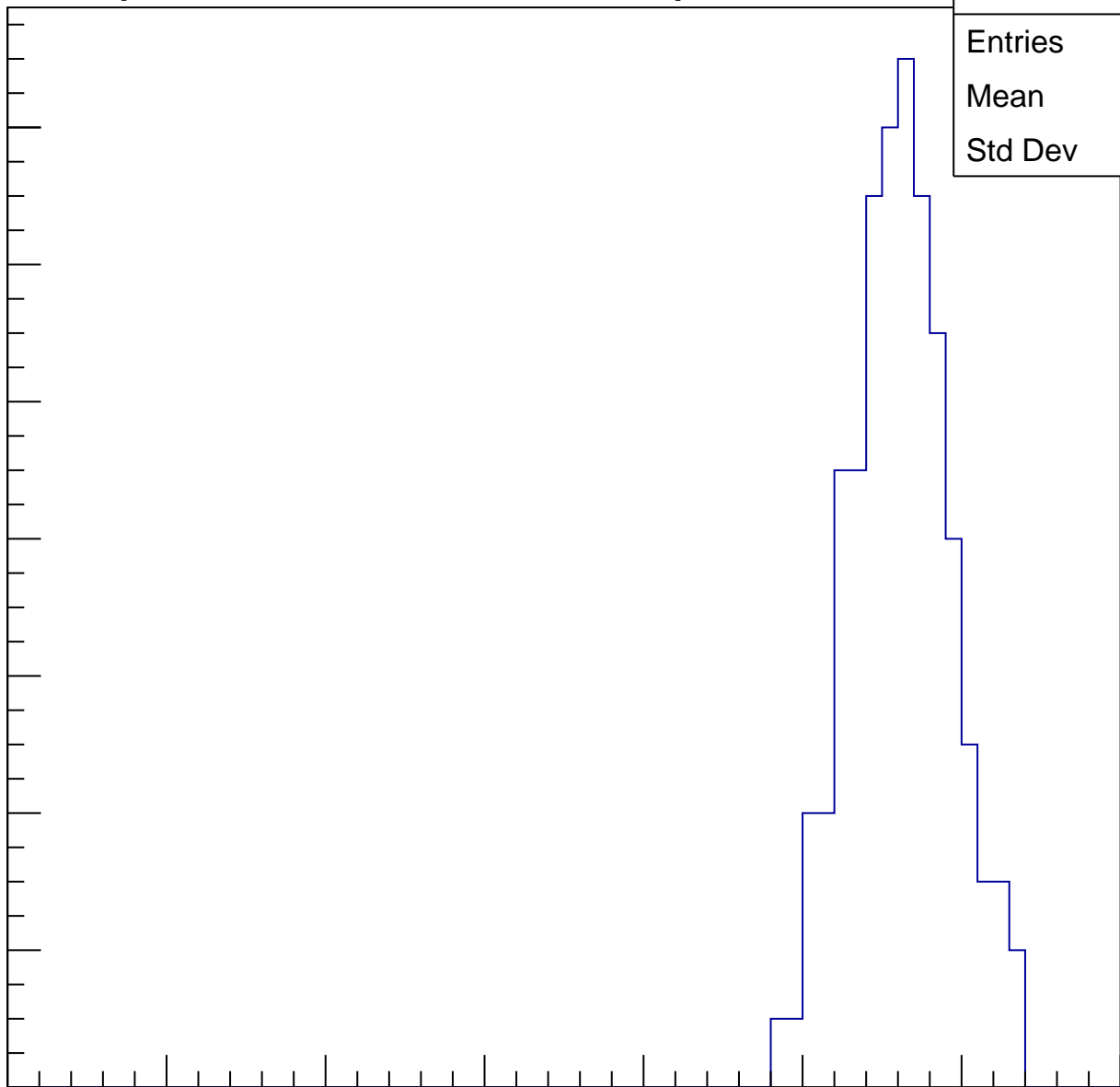
Entries	115
Mean	55.69
Std Dev	3.13

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch20, adc5

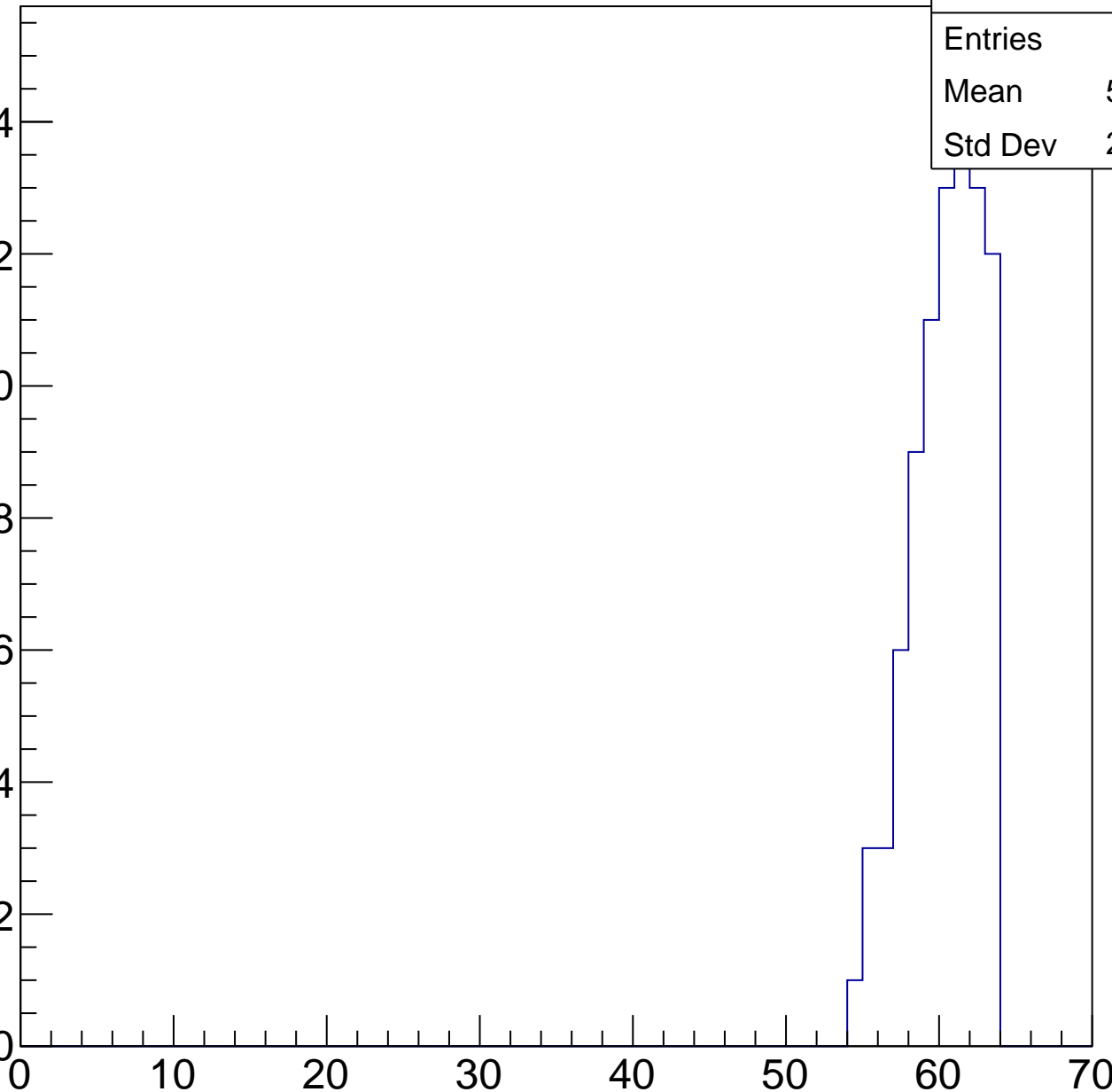
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	59.97
Std Dev	2.249

ampl

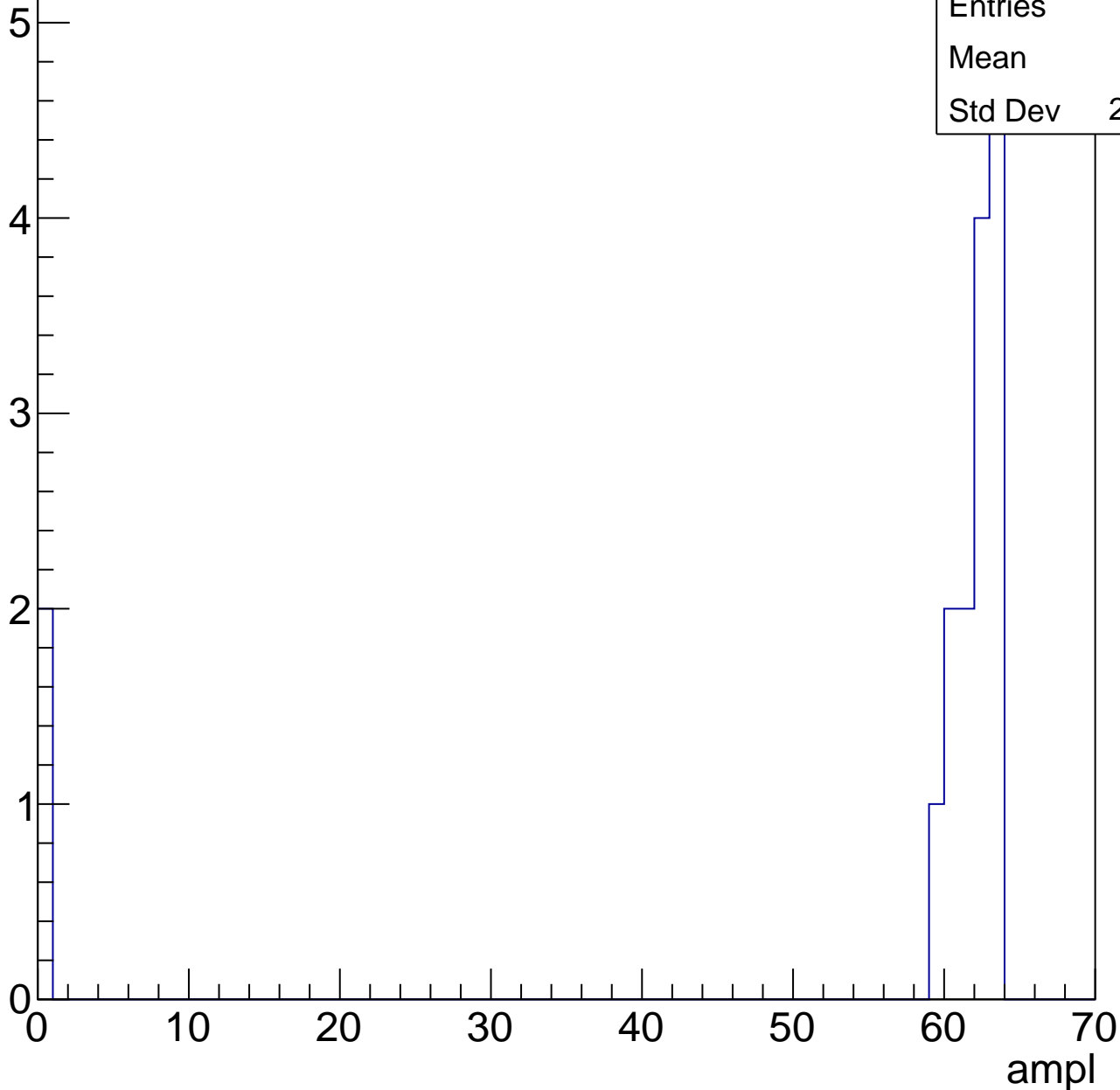


B1L001S, U21-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	54
Std Dev	20.45



B1L001S, U21-ch20, adc7

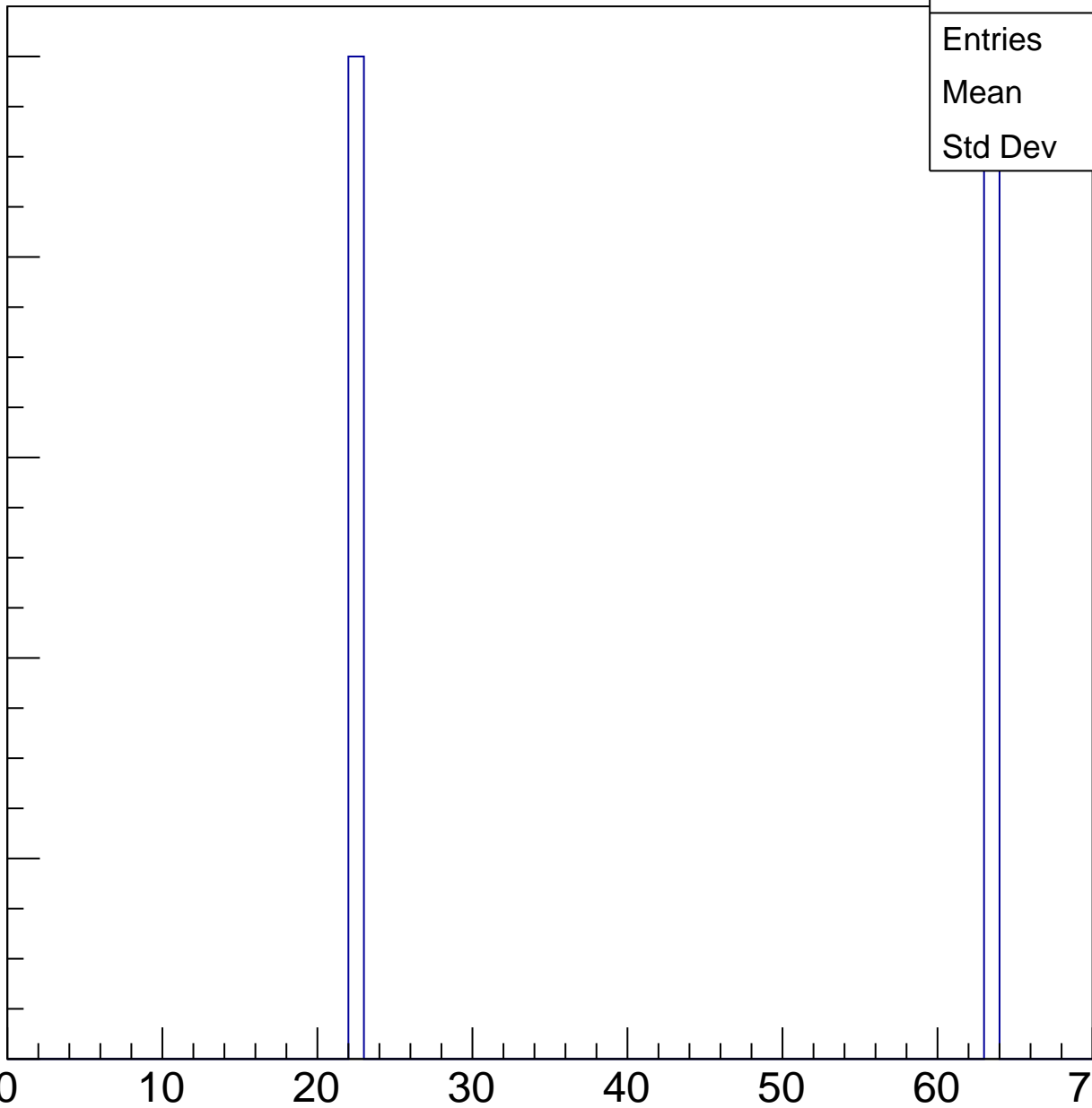
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

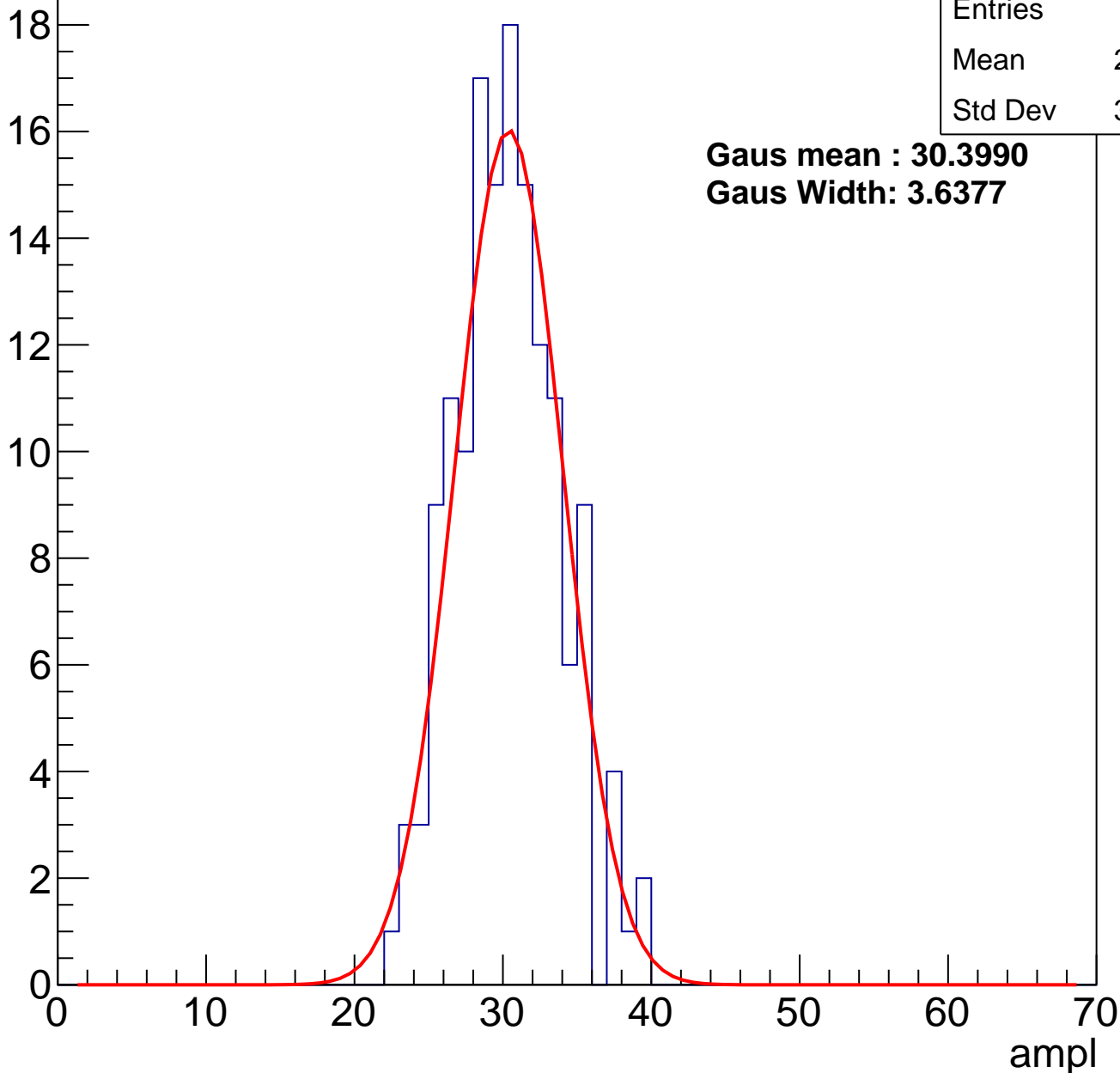
ampl



B1L001S, U21-ch21, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.97
Std Dev	3.407

Gaus mean : 37.9563

Gaus Width: 3.8394

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

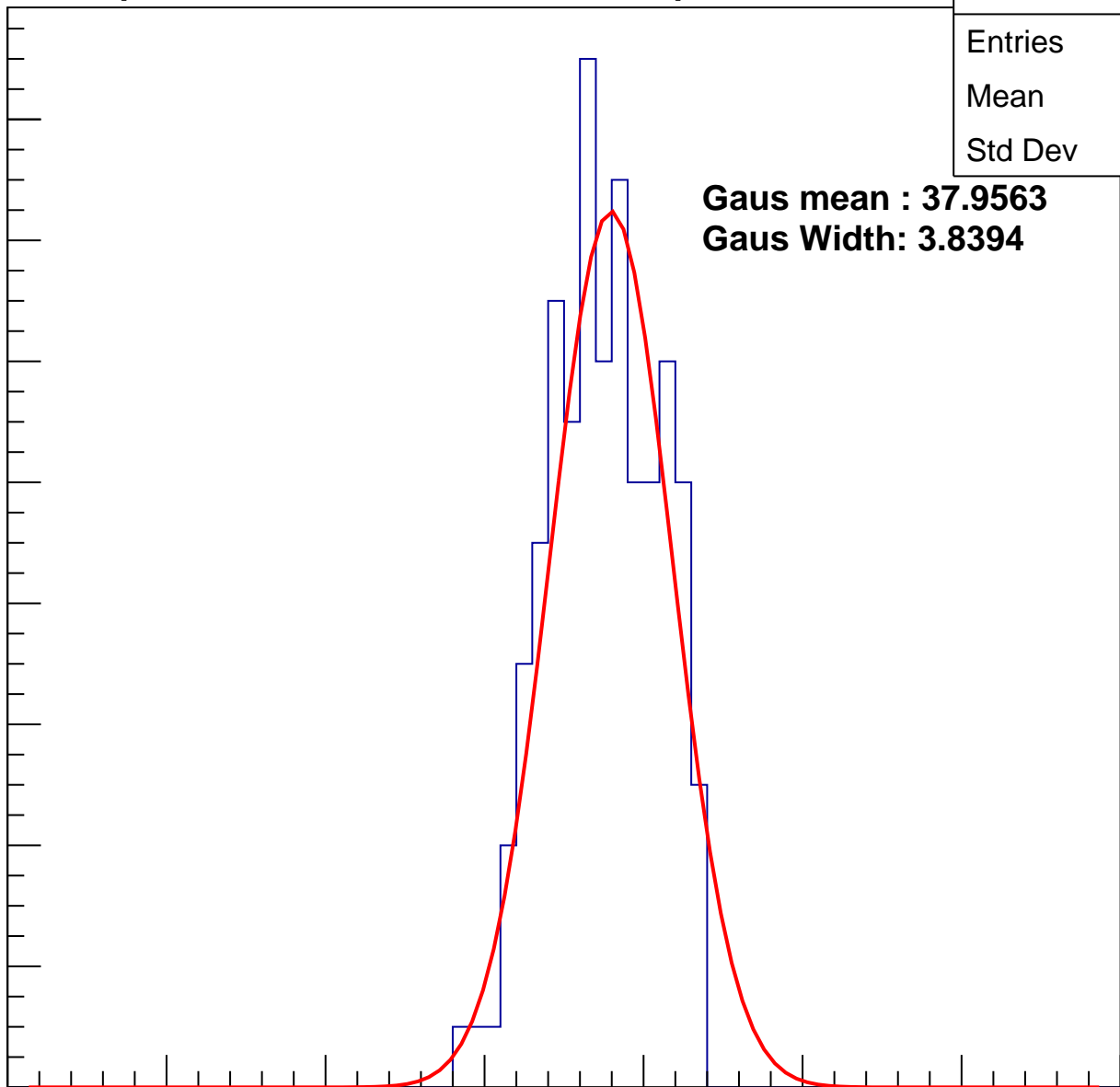
40

50

60

70

ampl



B1L001S, U21-ch21, adc2

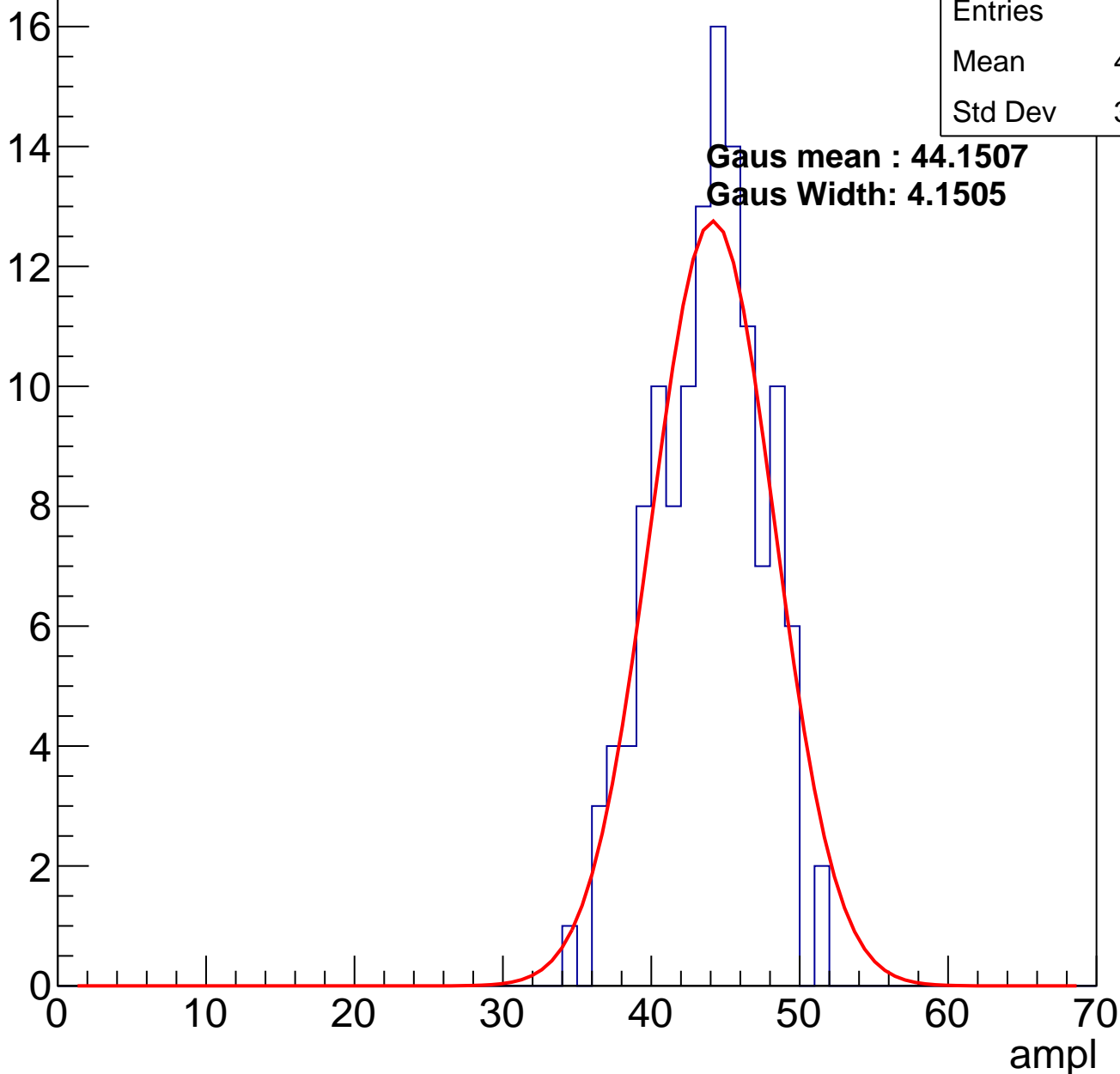
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	43.35
Std Dev	3.542

Gaus mean : 44.1507

Gaus Width: 4.1505

Entry



B1L001S, U21-ch21, adc3

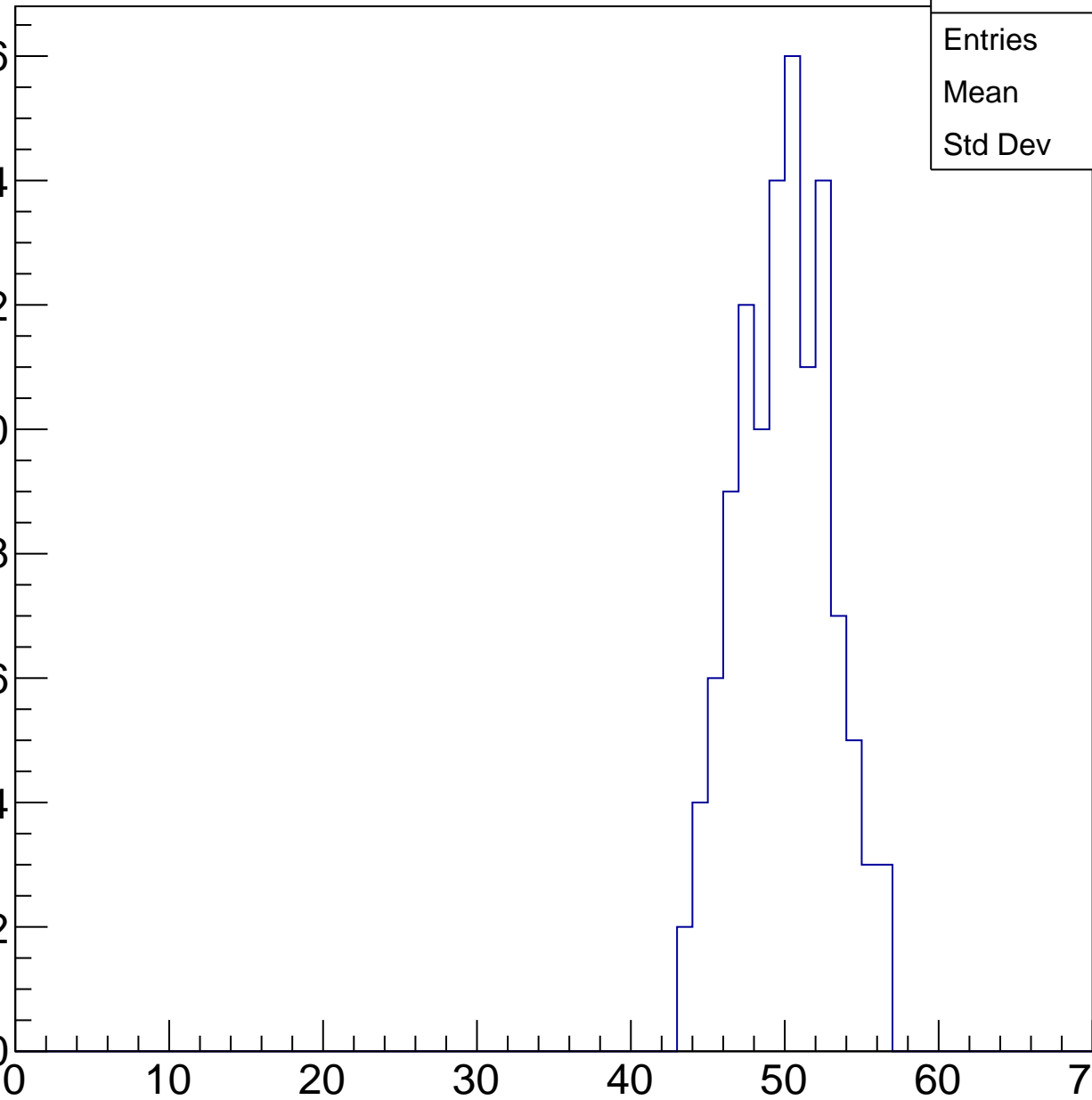
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	49.47
Std Dev	3.021

Entry

16
14
12
10
8
6
4
2
0

ampl

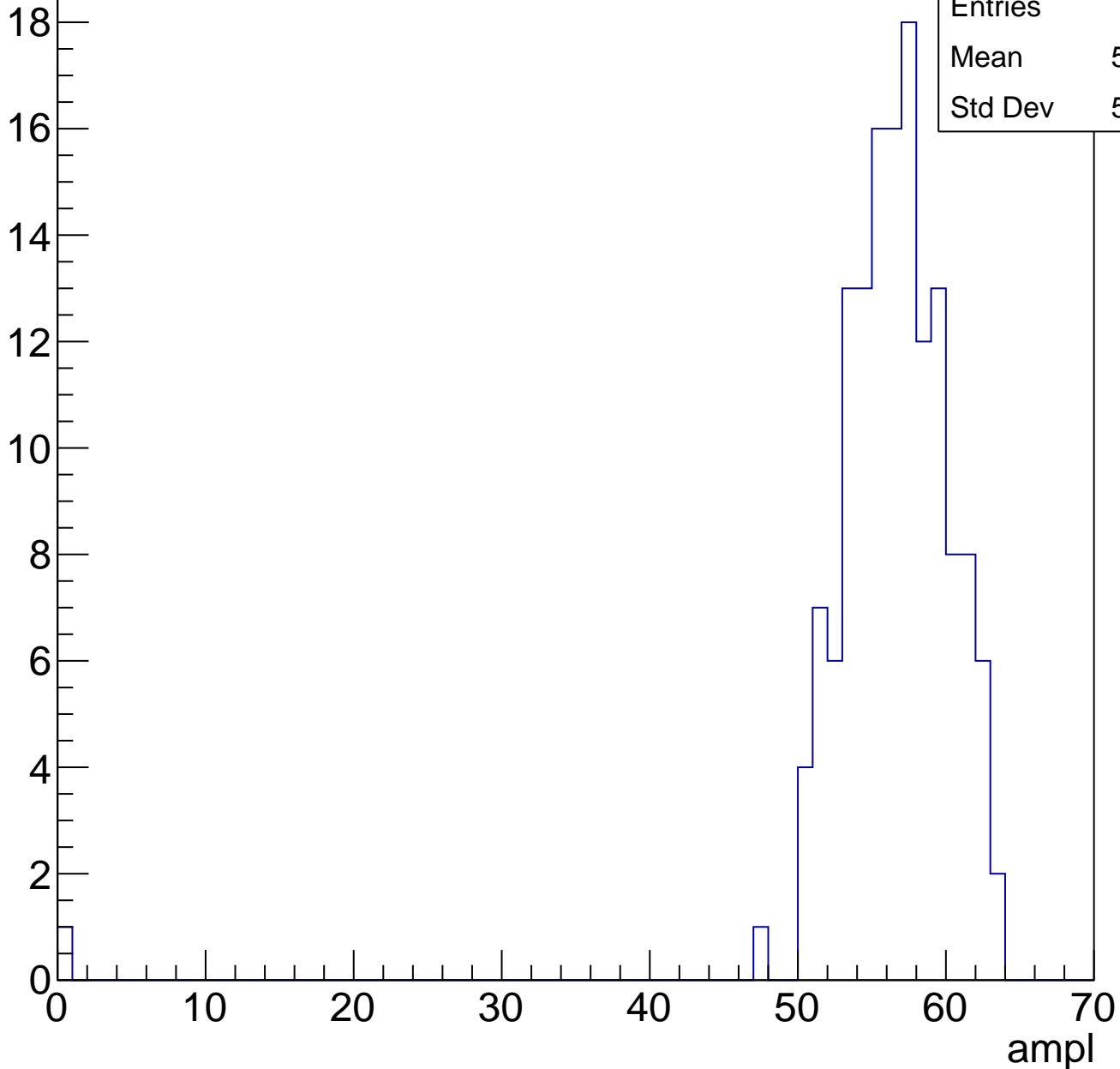


B1L001S, U21-ch21, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	55.82
Std Dev	5.675

Entry



B1L001S, U21-ch21, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	60
Mean	59.18
Std Dev	8.022

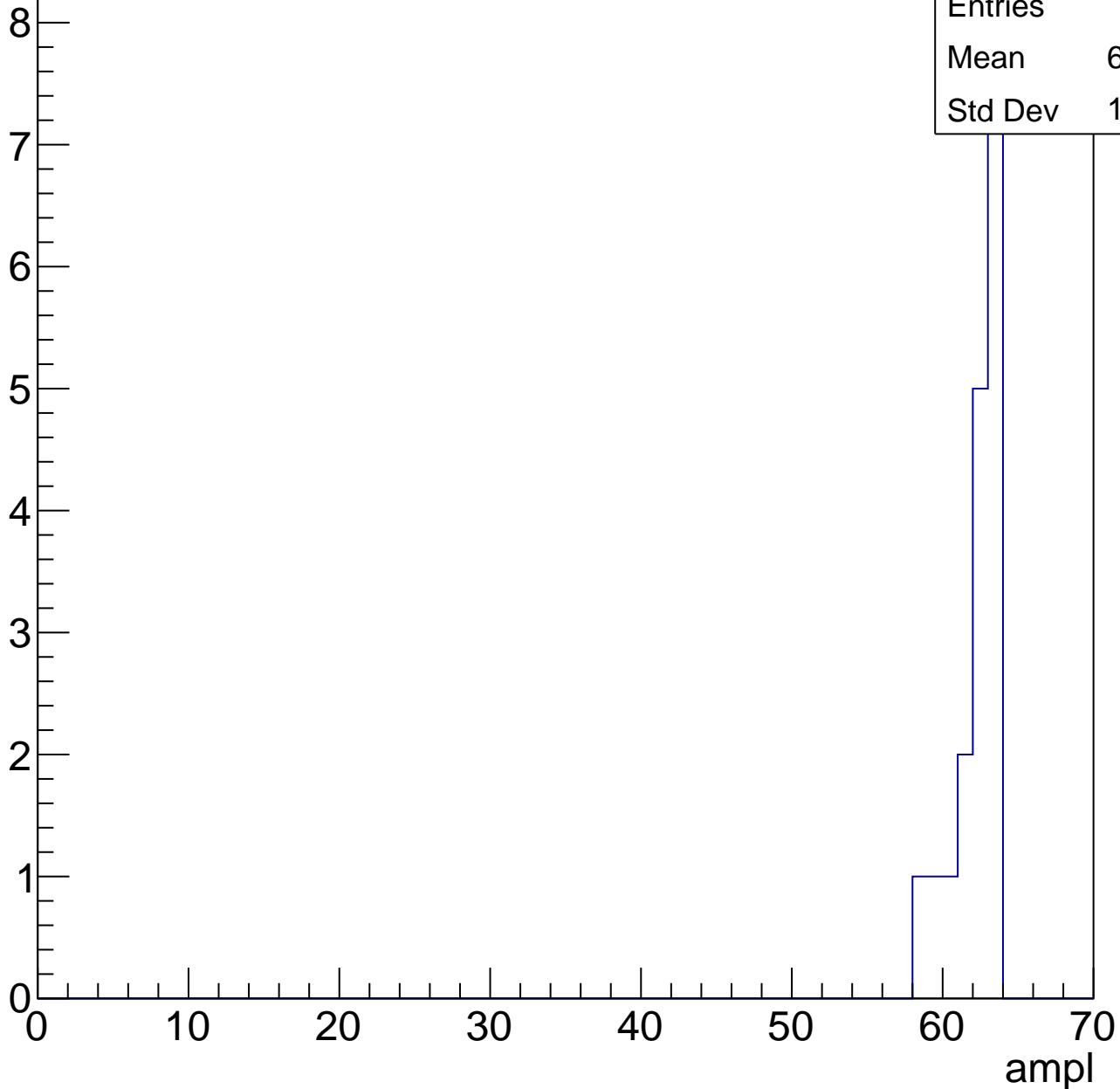
0

B1L001S, U21-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	61.83
Std Dev	1.462



B1L001S, U21-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

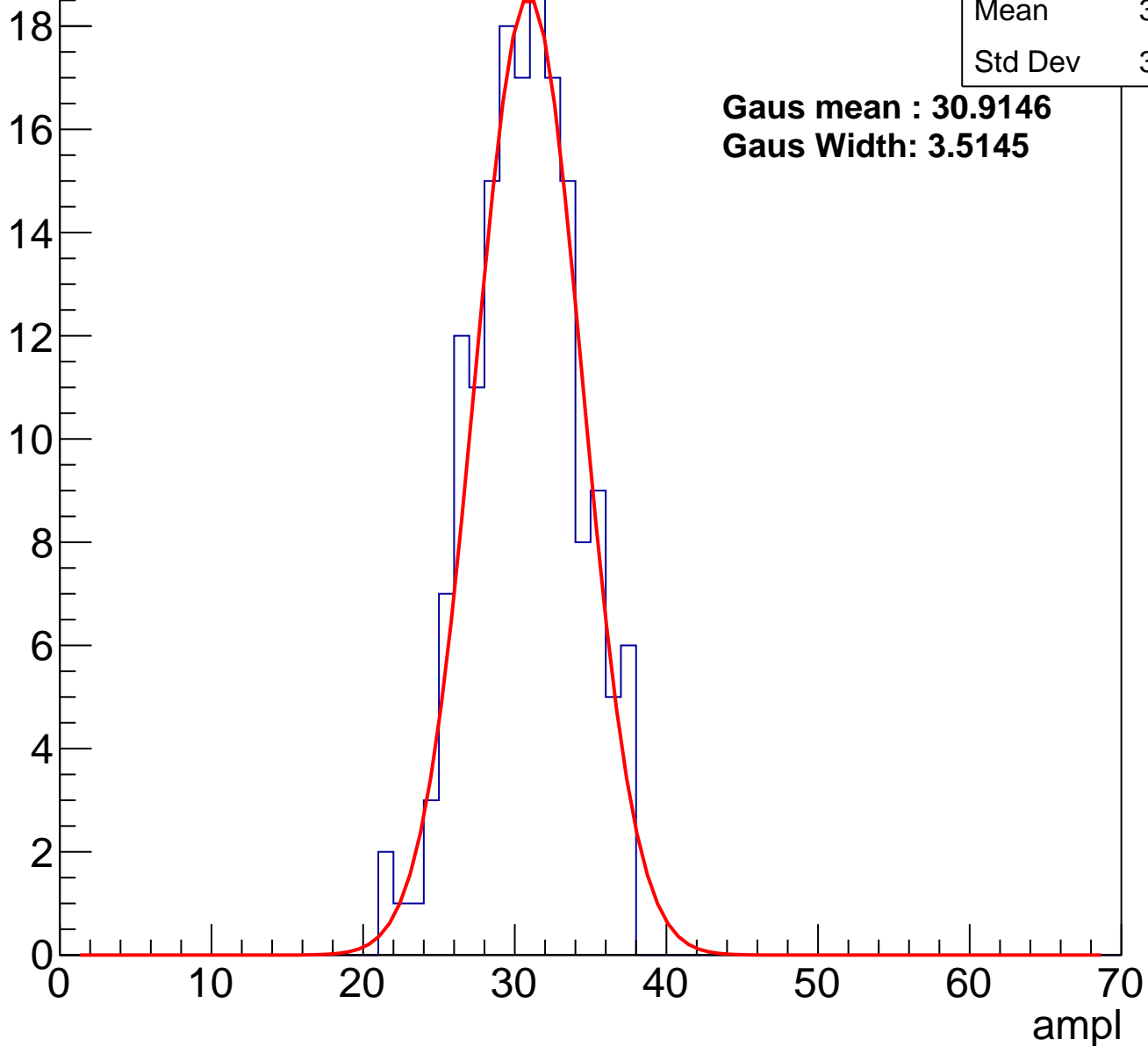


Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch22, adc1

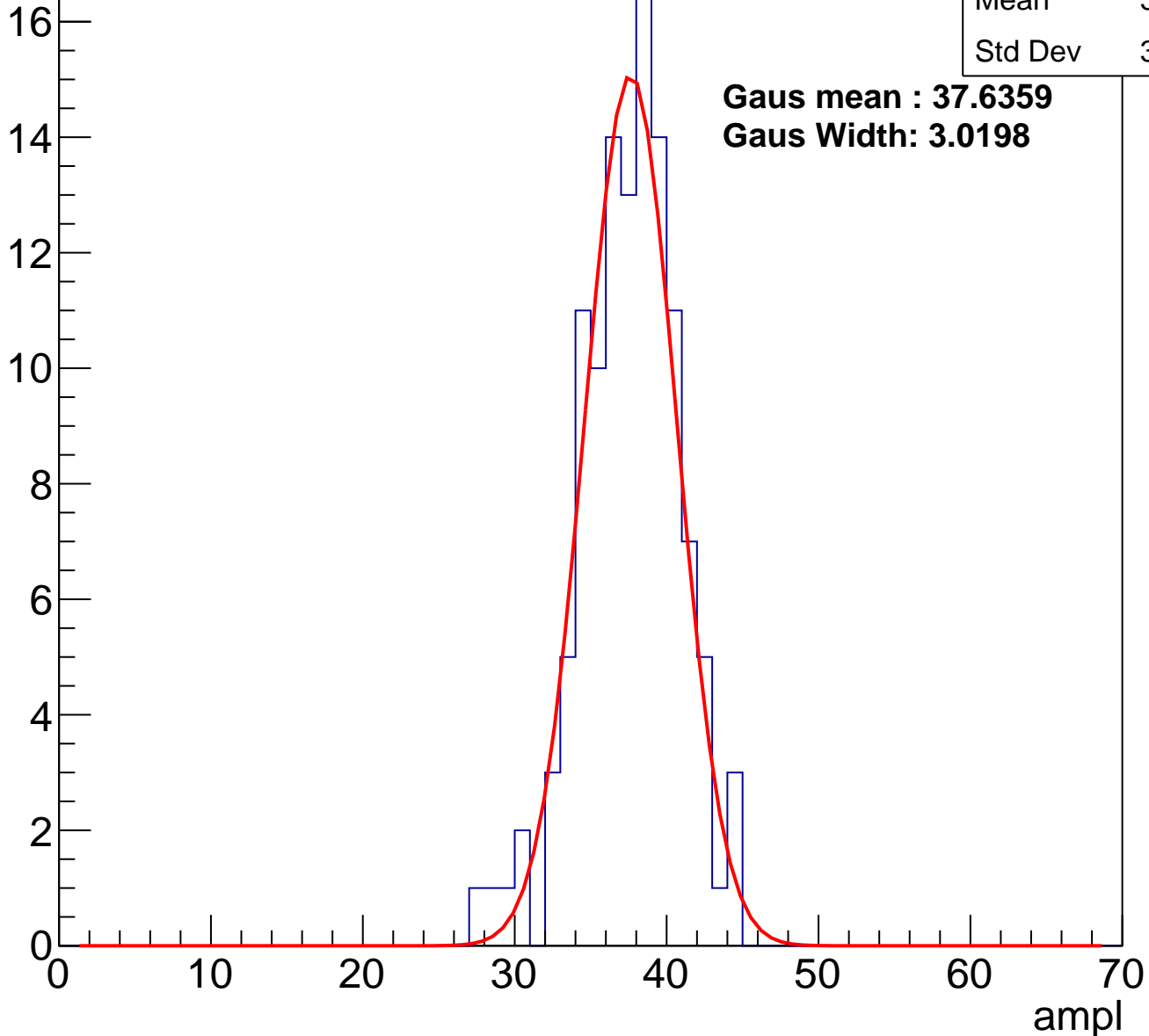
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.13
Std Dev	3.229

Gaus mean : 37.6359

Gaus Width: 3.0198

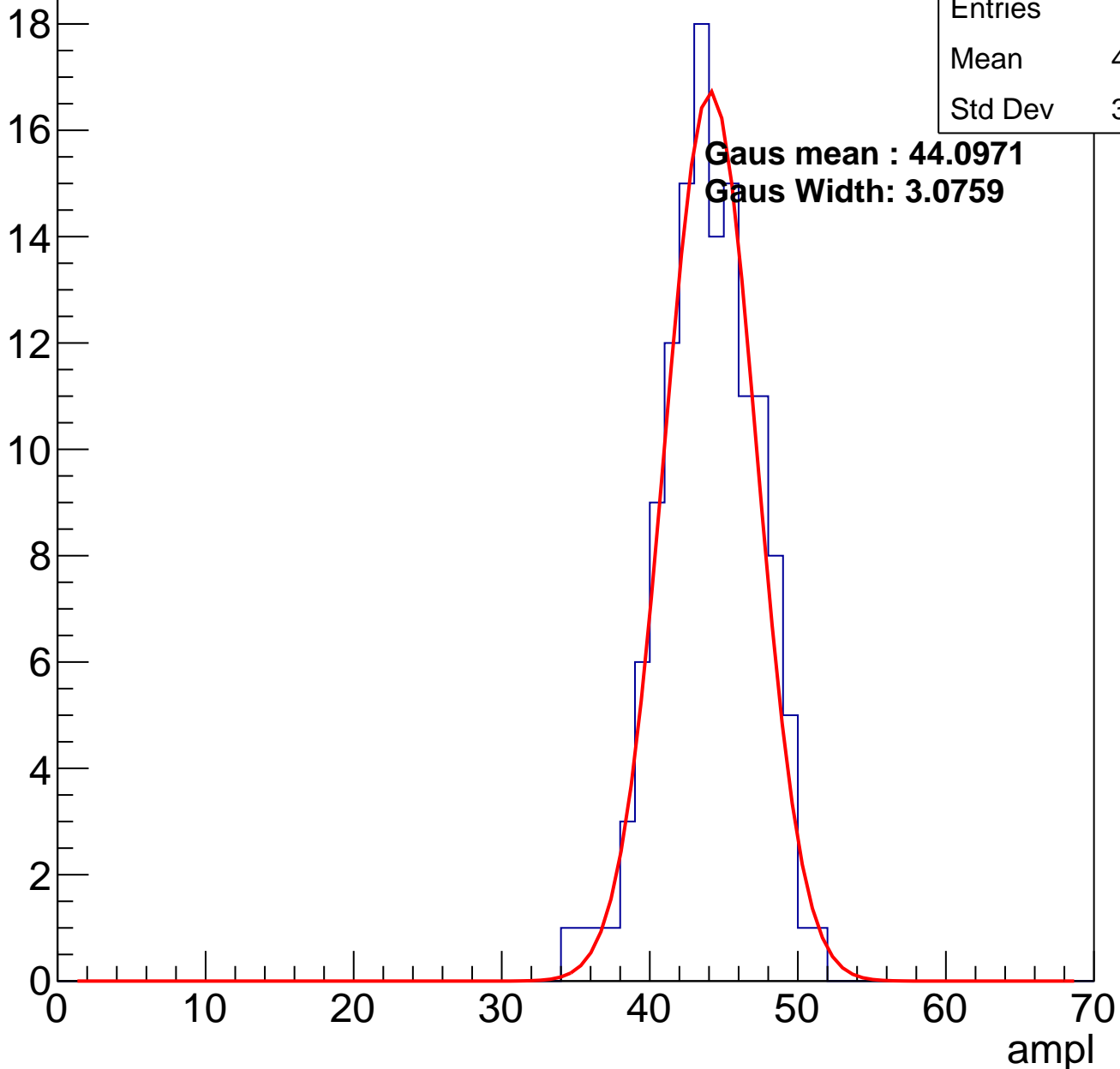
Entry



B1L001S, U21-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



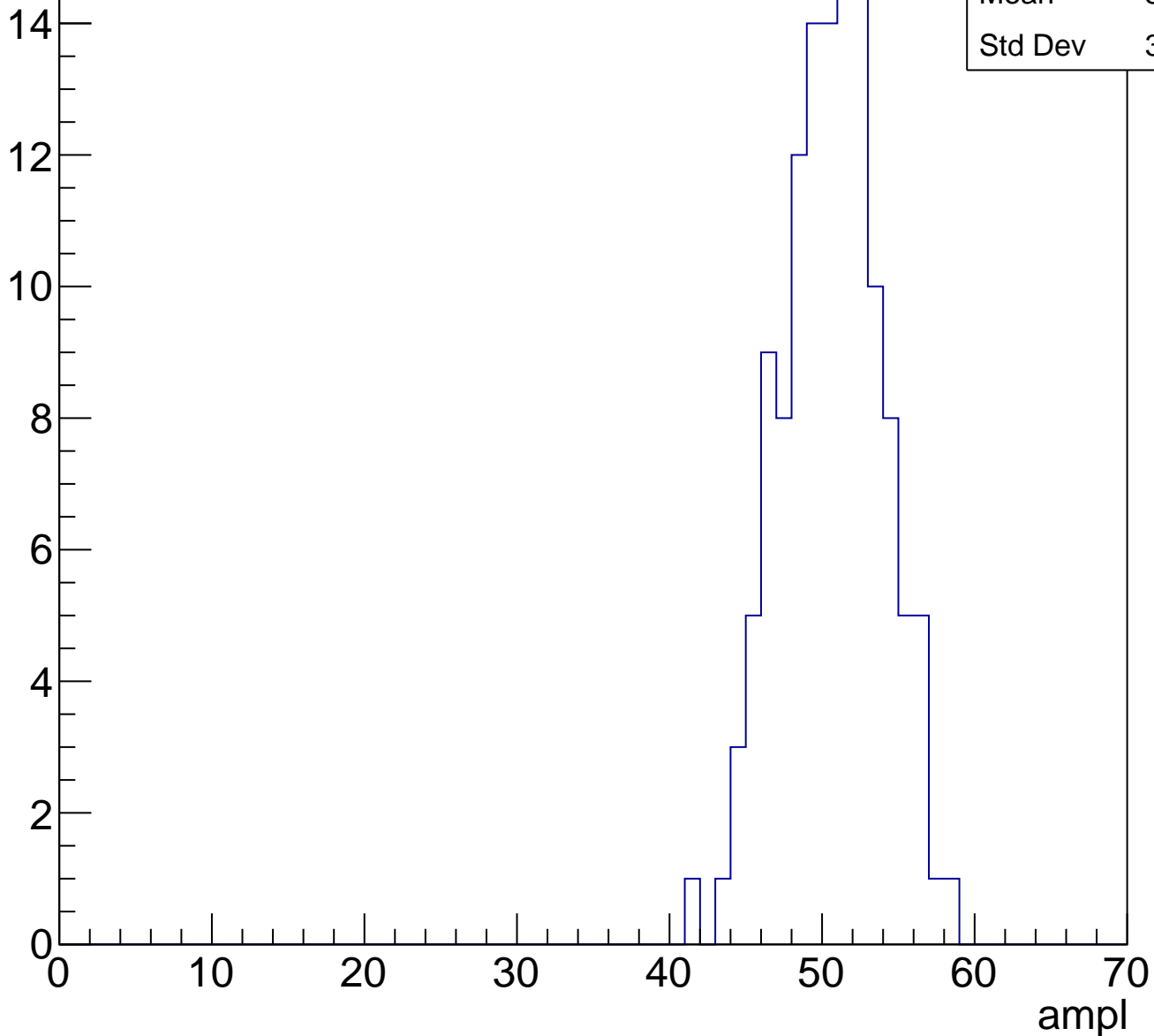
Entries	133
Mean	43.53
Std Dev	3.185

B1L001S, U21-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	50.16
Std Dev	3.259

Entry



B1L001S, U21-ch22, adc4

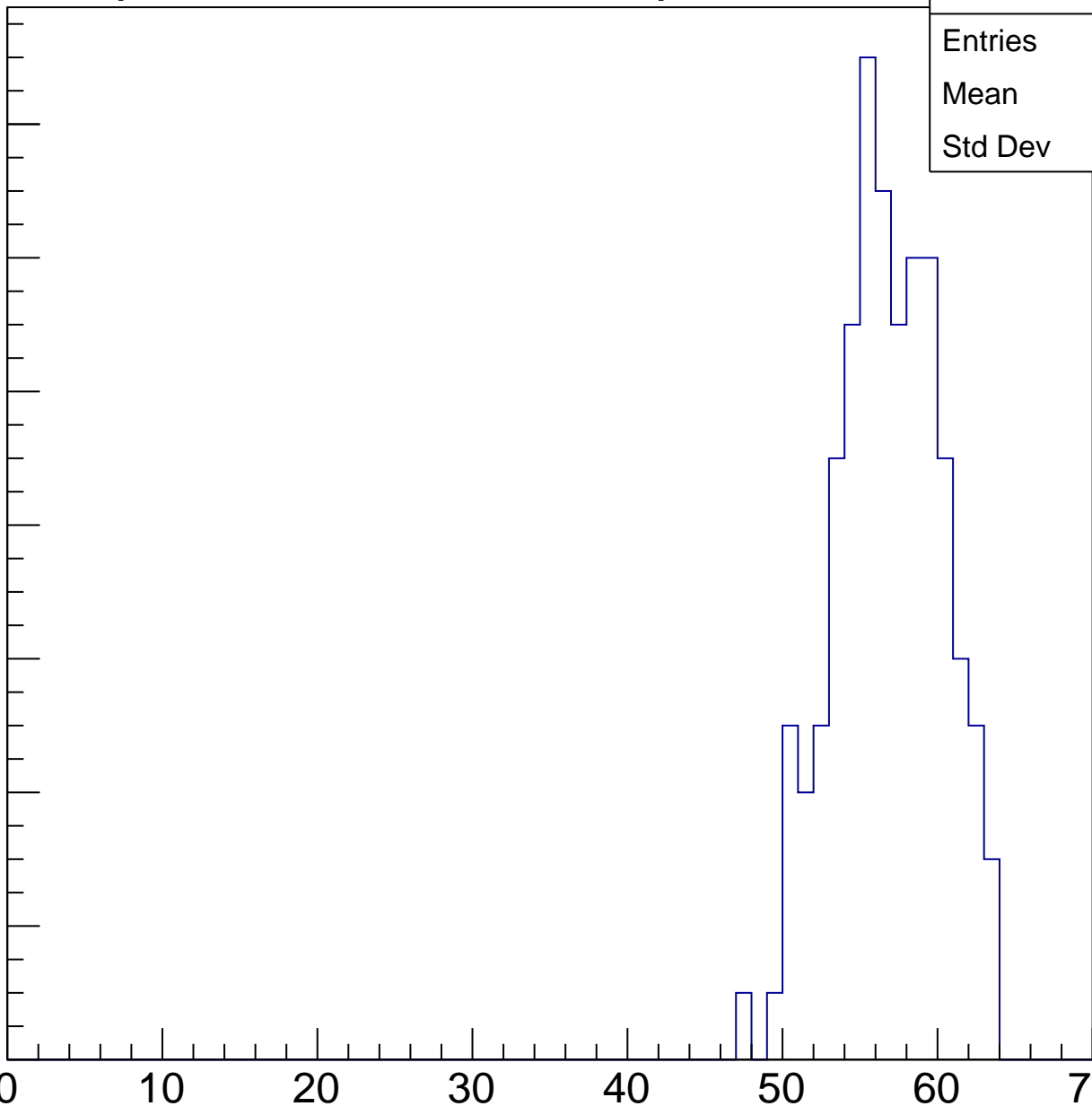
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	56.31
Std Dev	3.395

ampl



B1L001S, U21-ch22, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	75
Mean	59.28
Std Dev	7.262

ampl

0

10

20

30

40

50

60

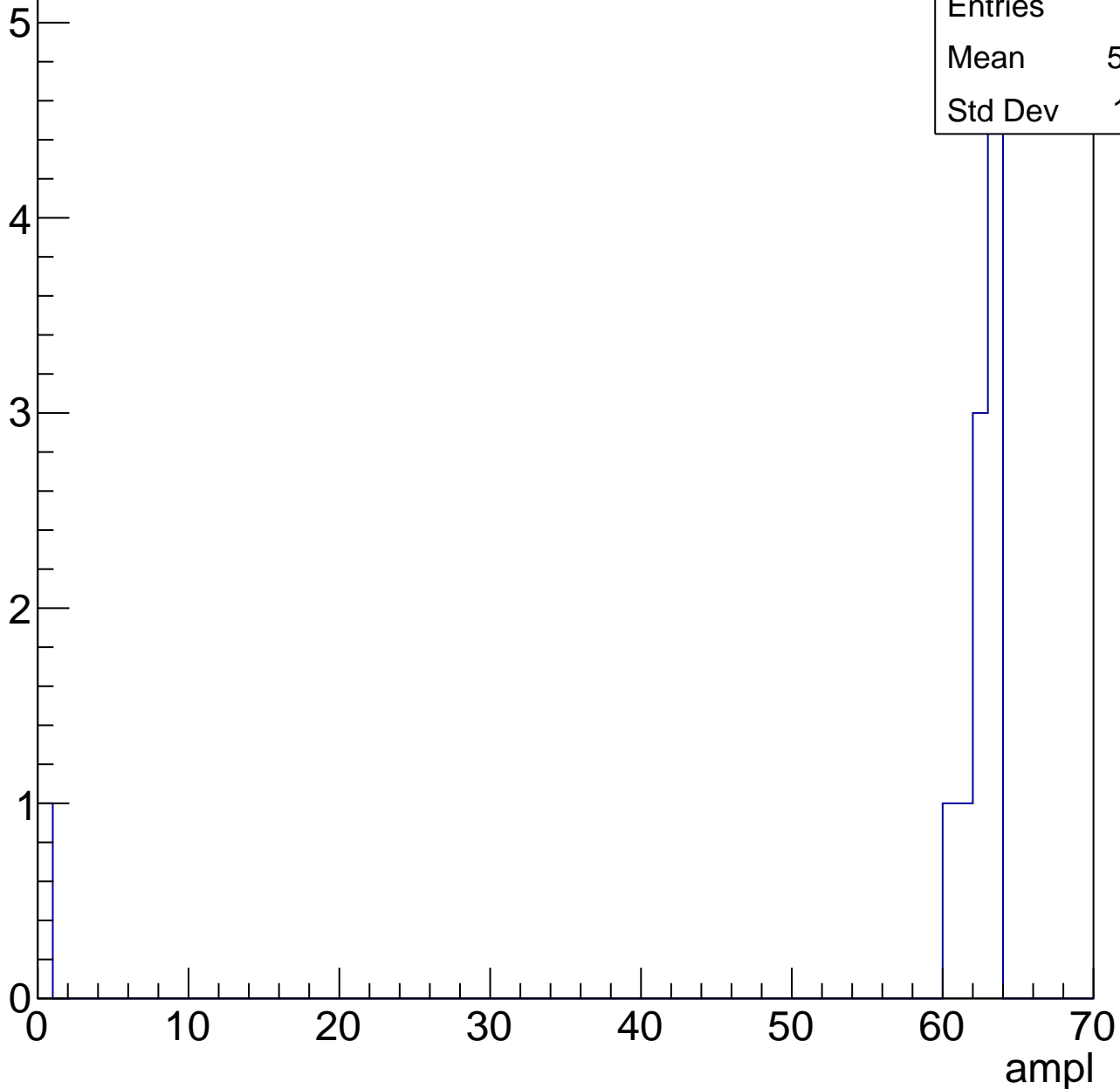
70

B1L001S, U21-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.55
Std Dev	17.91



B1L001S, U21-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

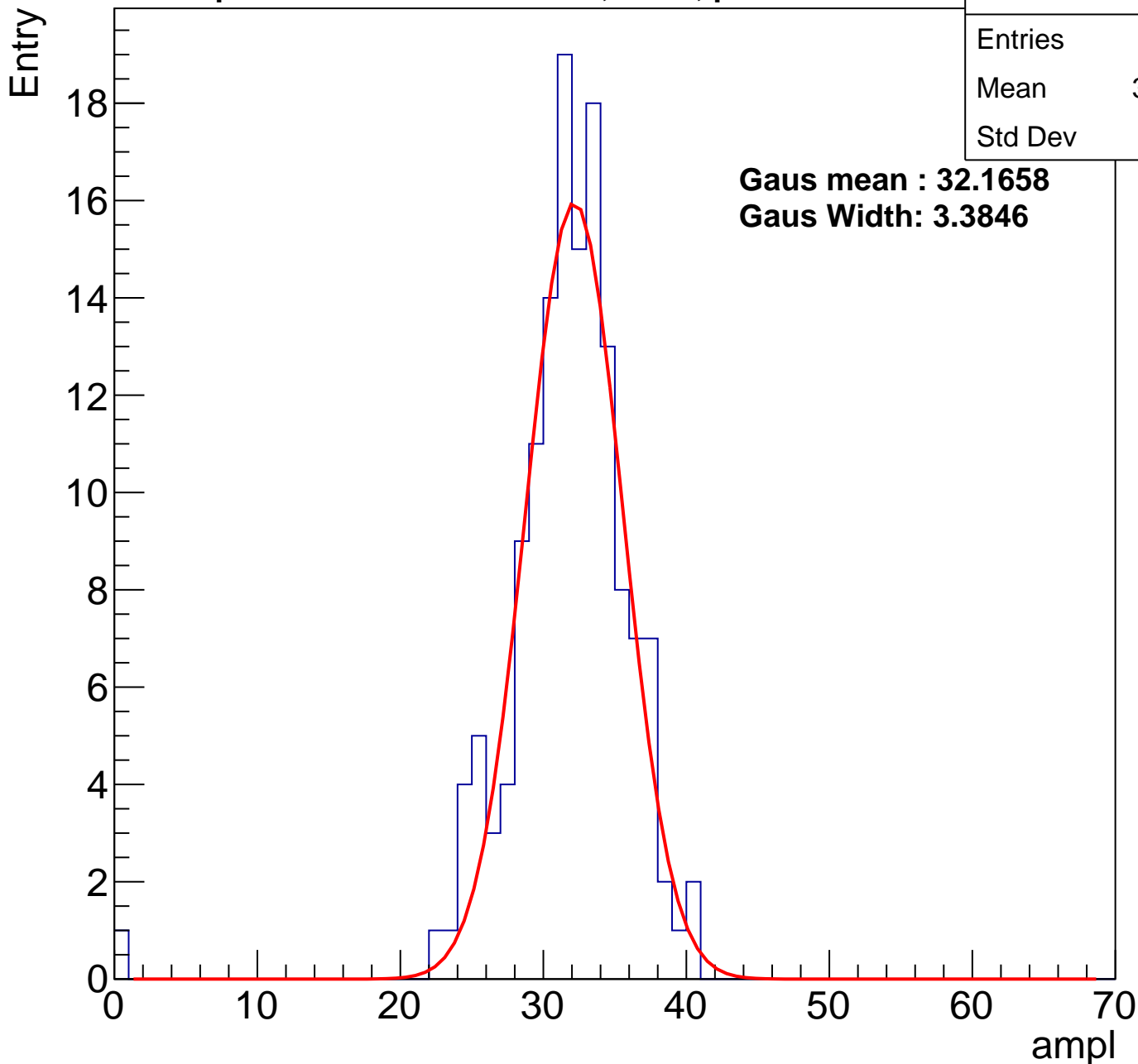
B1L001S, U21-ch23, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	31.27
Std Dev	4.42

Gaus mean : 32.1658

Gaus Width: 3.3846



B1L001S, U21-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	38.85
Std Dev	3.218

Gaus mean : 39.2252

Gaus Width: 3.3338

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

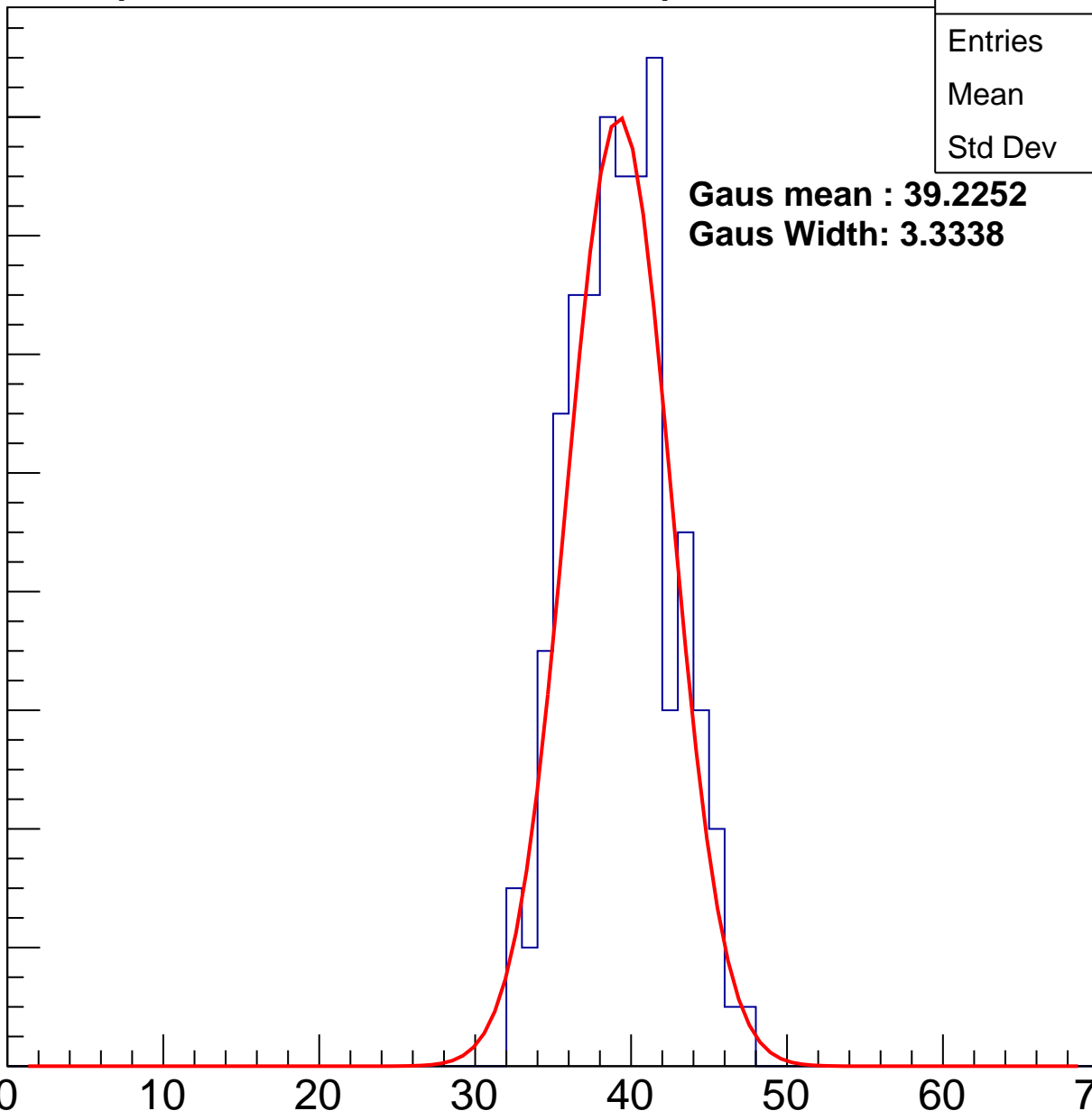
40

50

60

70

ampl



B1L001S, U21-ch23, adc2

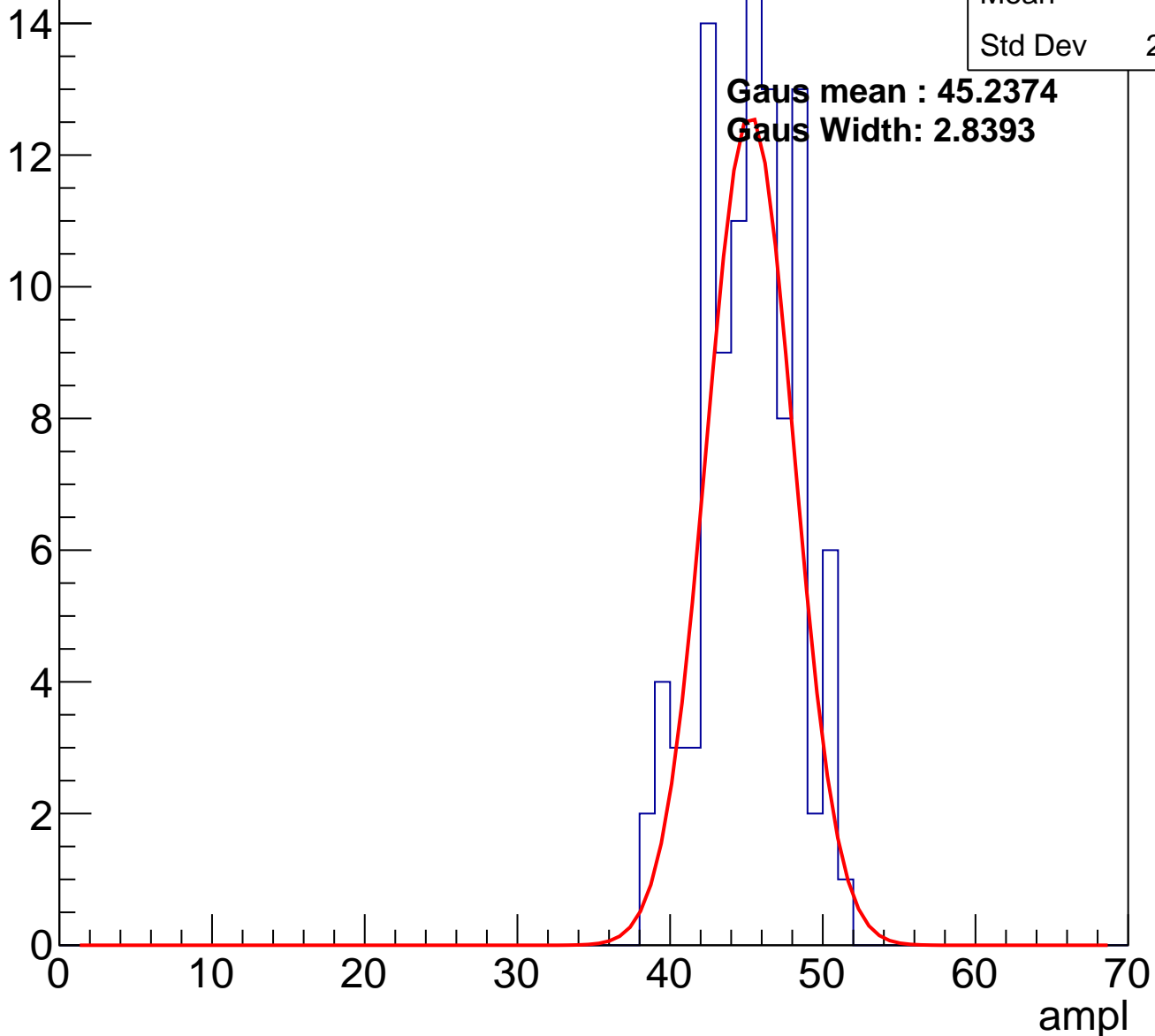
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	44.77
Std Dev	2.962

Gaus mean : 45.2374

Gaus Width: 2.8393

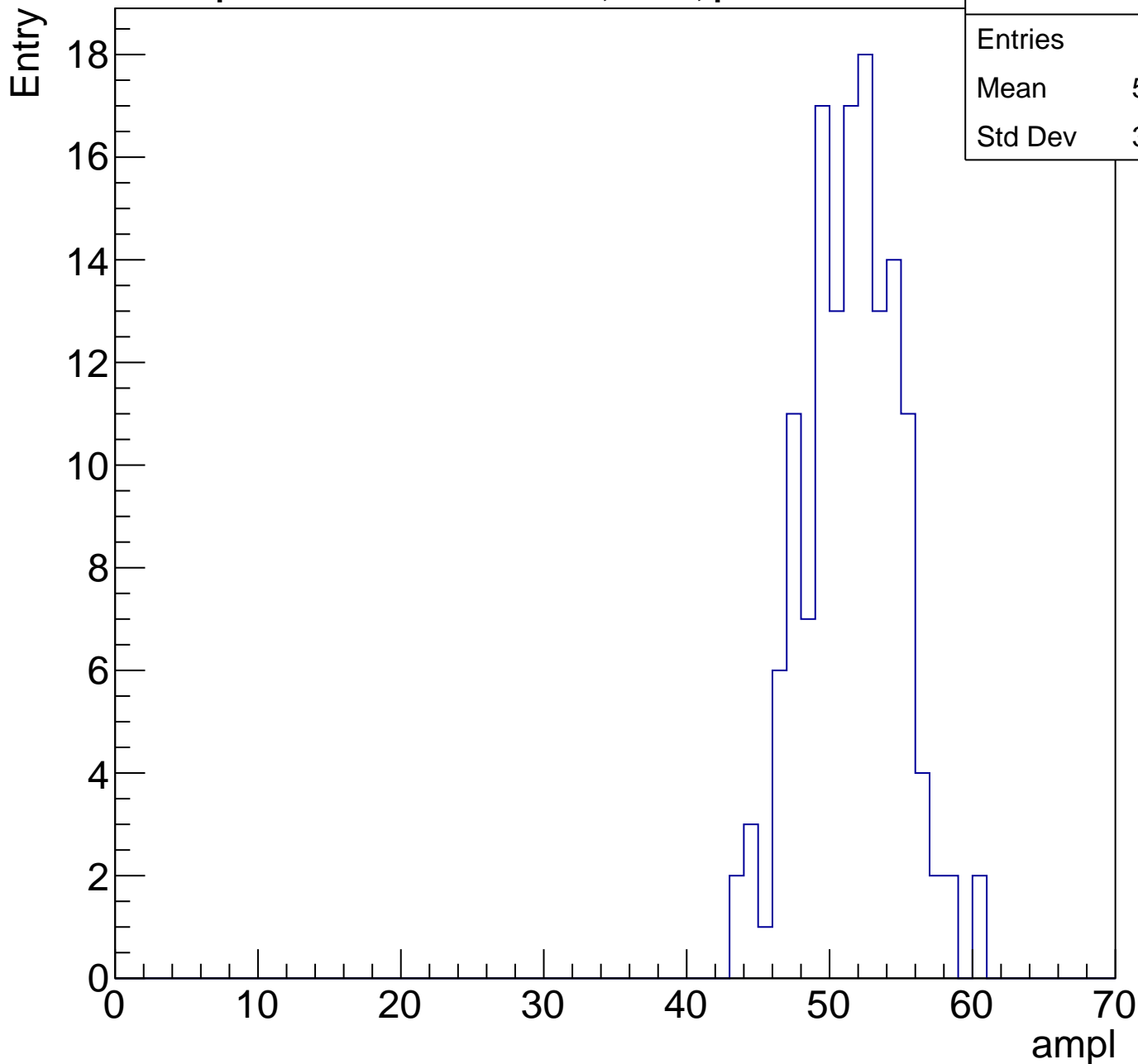
Entry



B1L001S, U21-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	51.06
Std Dev	3.329

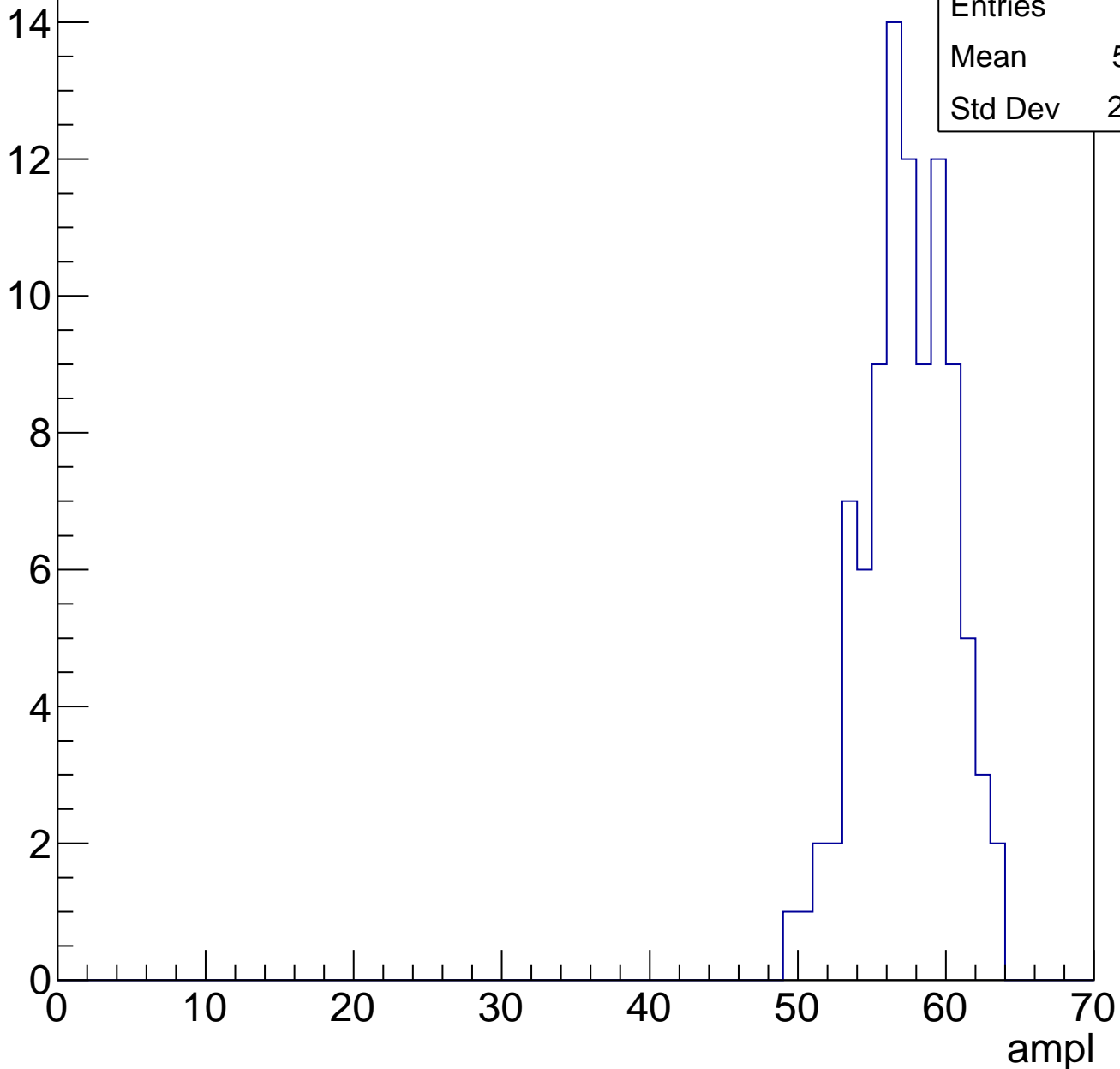


B1L001S, U21-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	56.91
Std Dev	2.945

Entry



B1L001S, U21-ch23, adc5

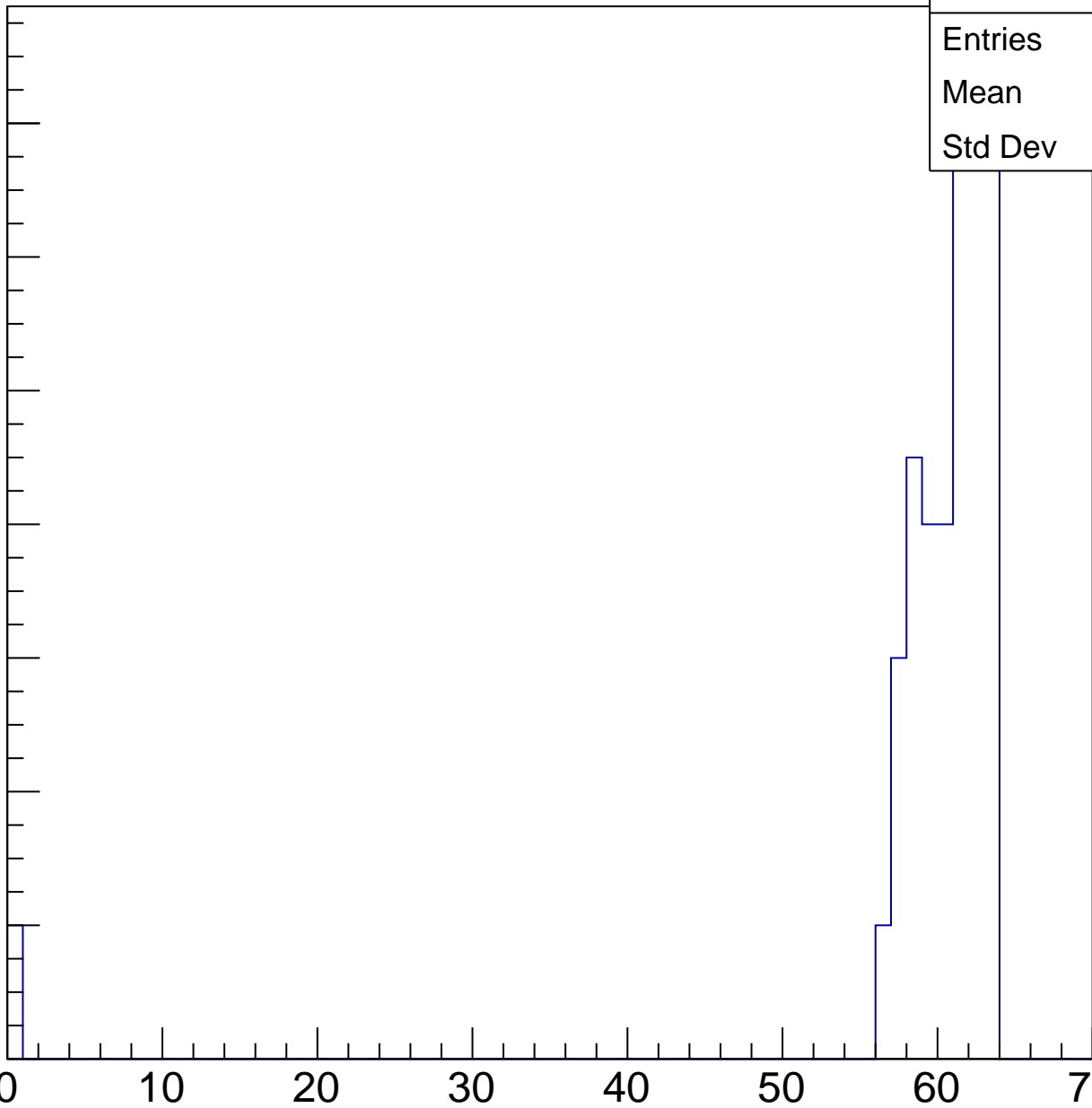
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.98
Std Dev	9.654

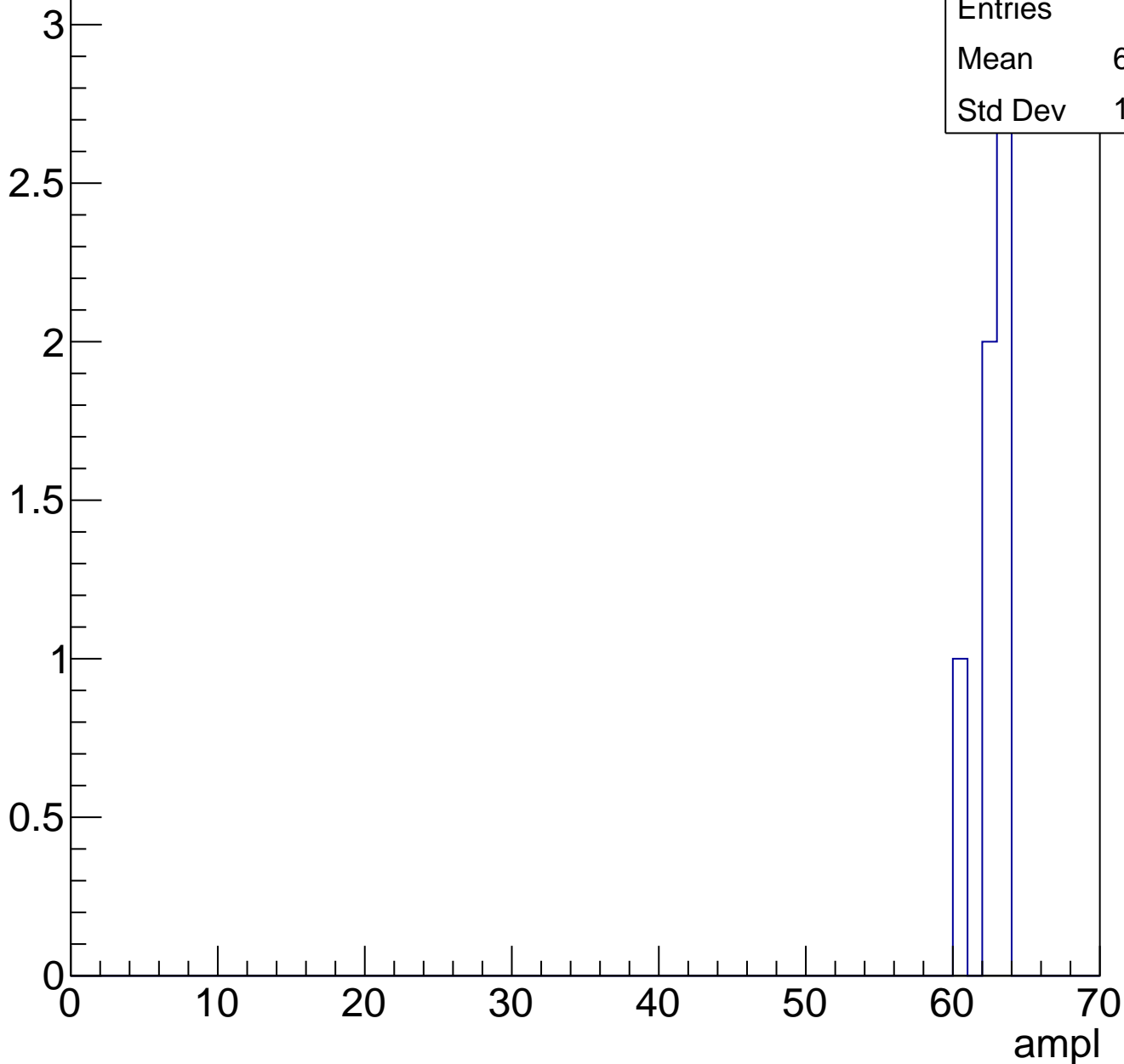
ampl



B1L001S, U21-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	28.4
Std Dev	2.803

Gaus mean : 28.5457

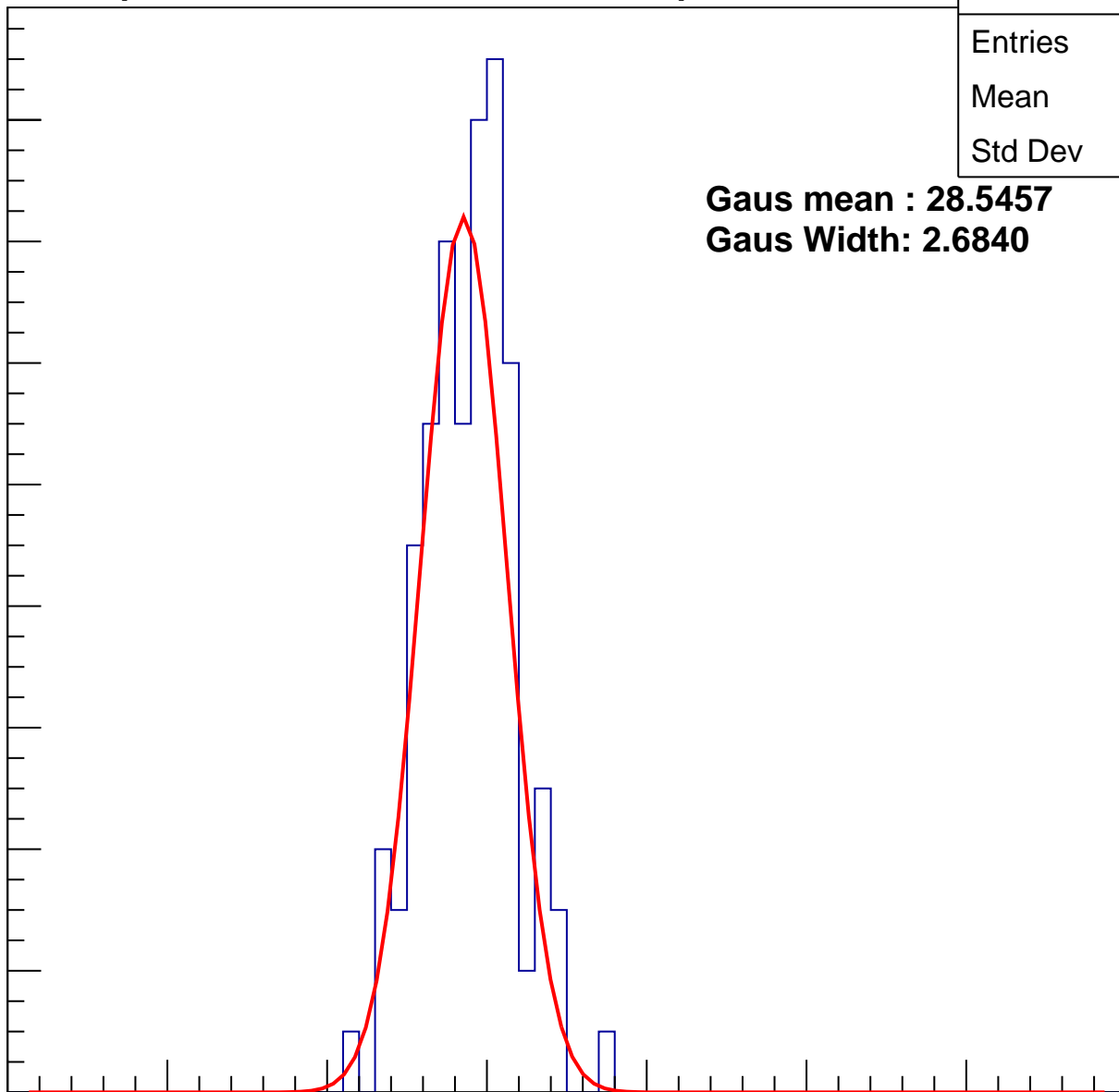
Gaus Width: 2.6840

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



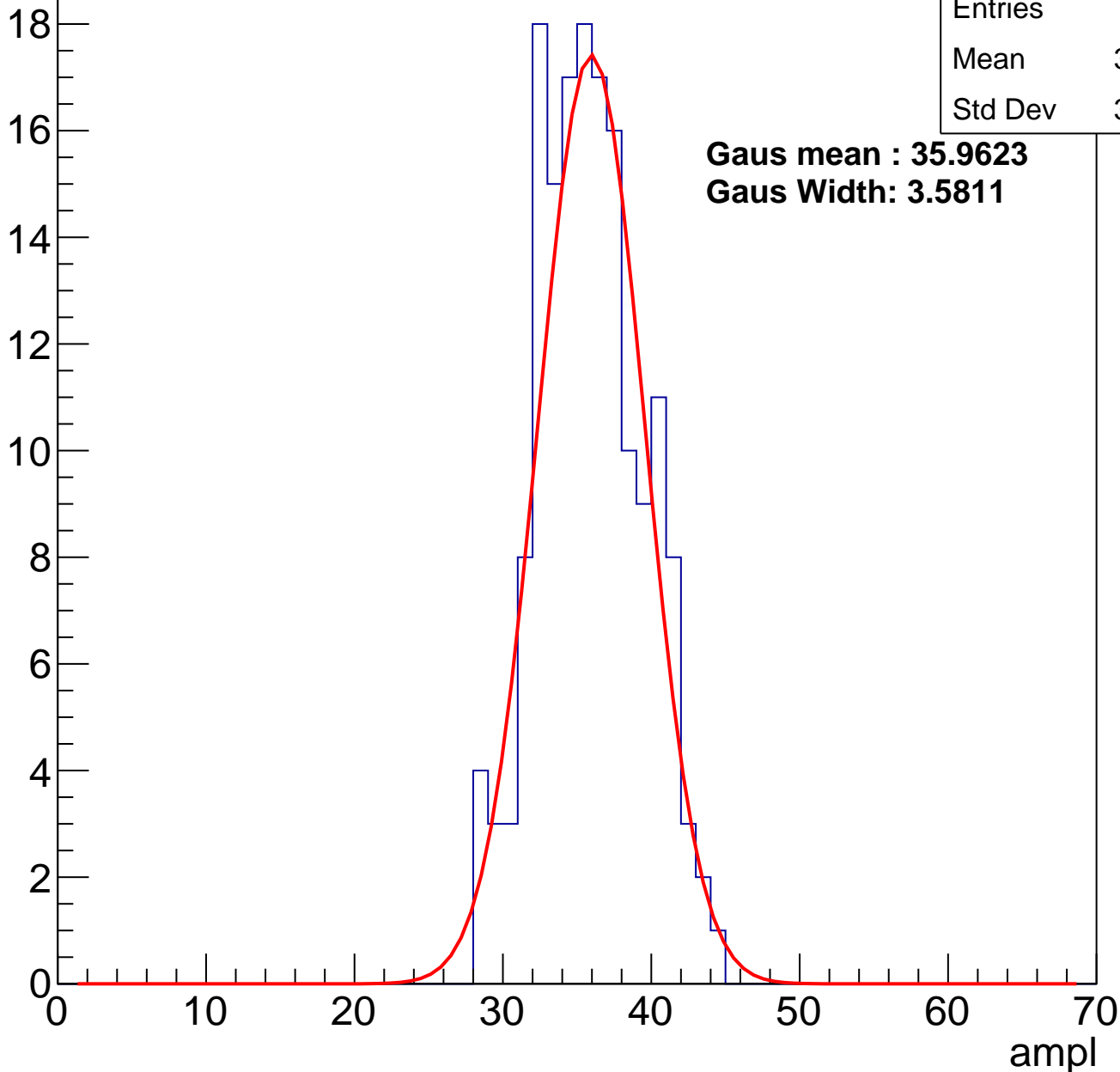
B1L001S, U21-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	35.43
Std Dev	3.459

Gaus mean : 35.9623
Gaus Width: 3.5811

Entry



B1L001S, U21-ch24, adc2

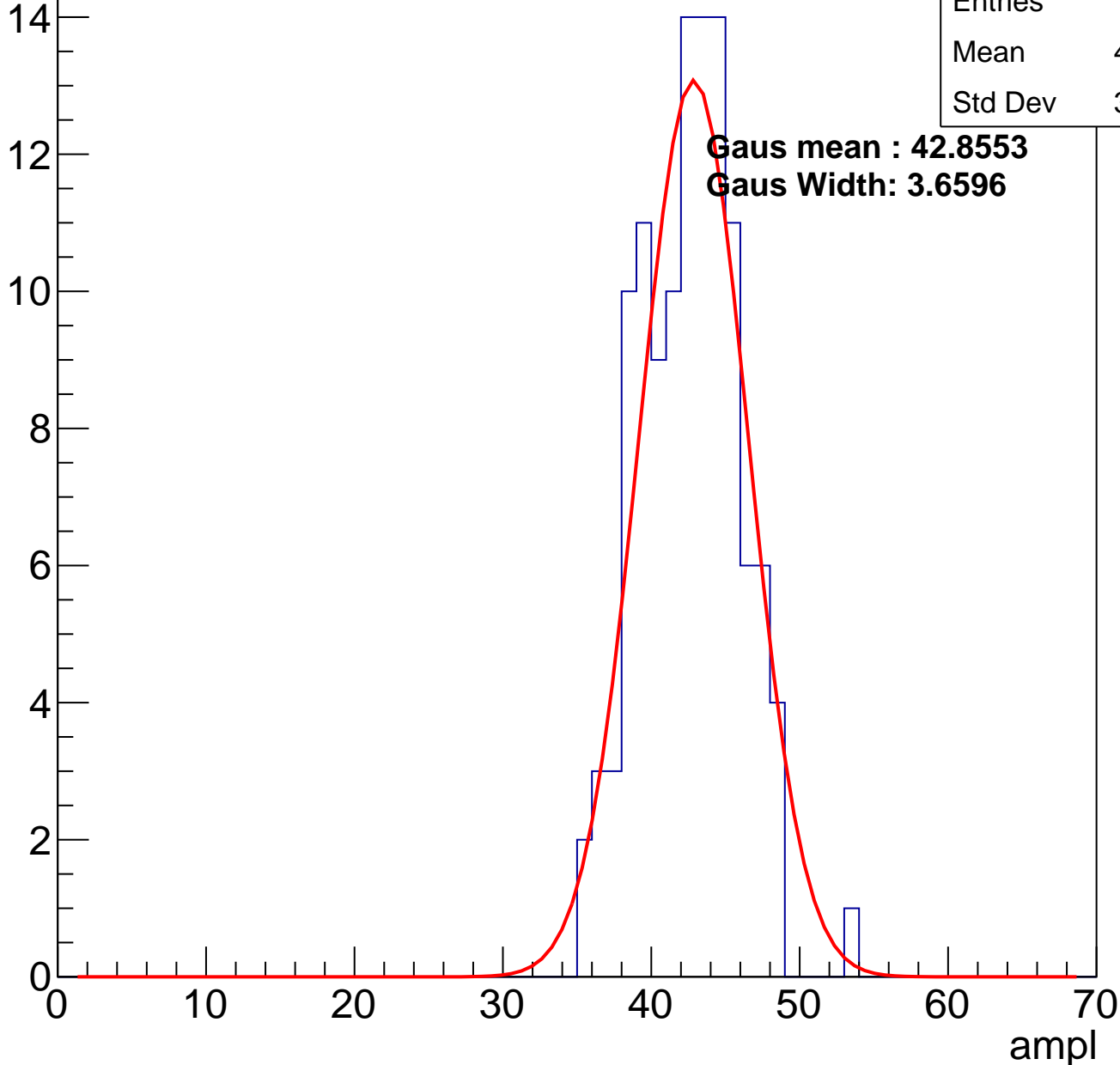
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.14
Std Dev	3.273

Gaus mean : 42.8553

Gaus Width: 3.6596

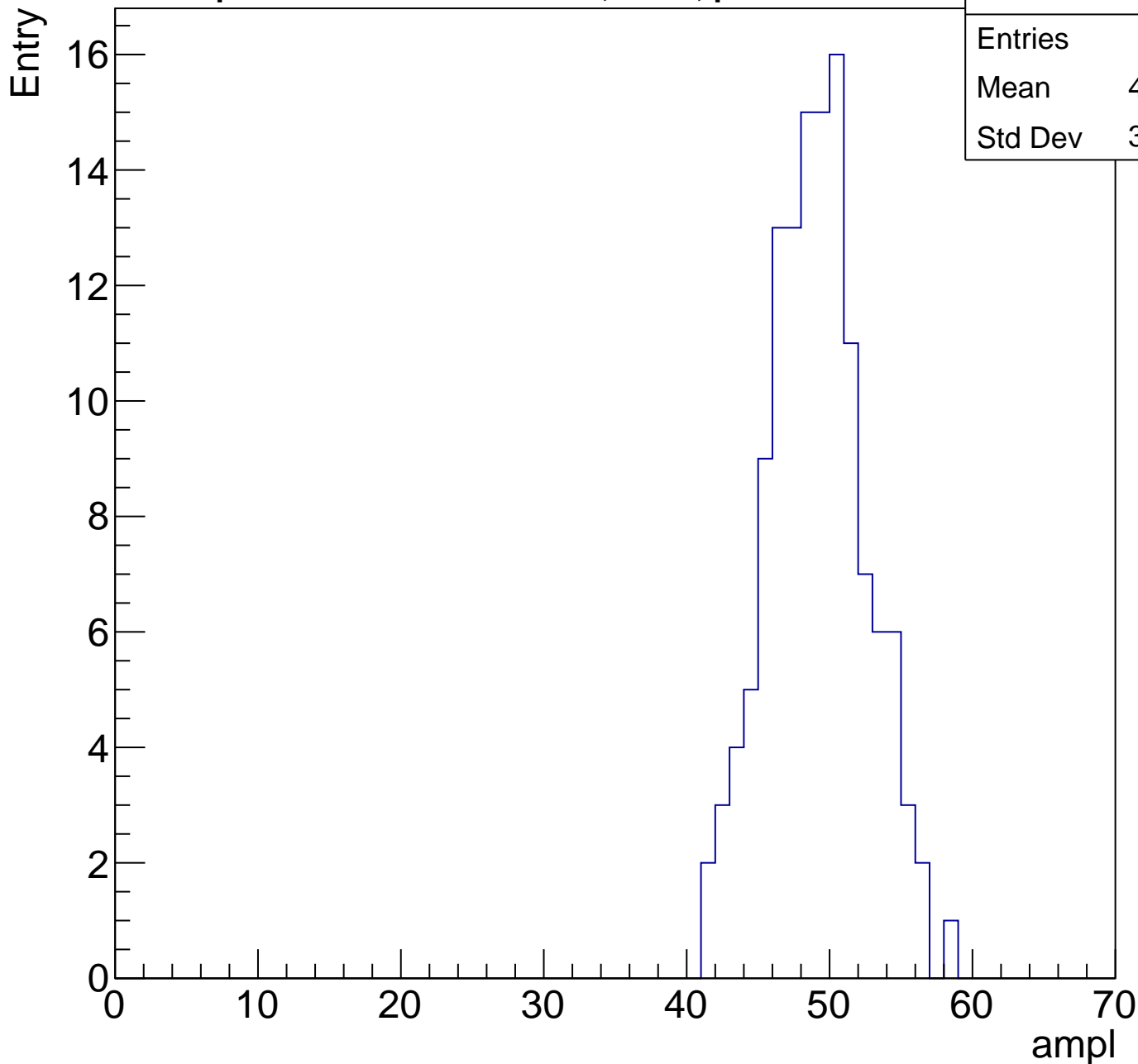
Entry



B1L001S, U21-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

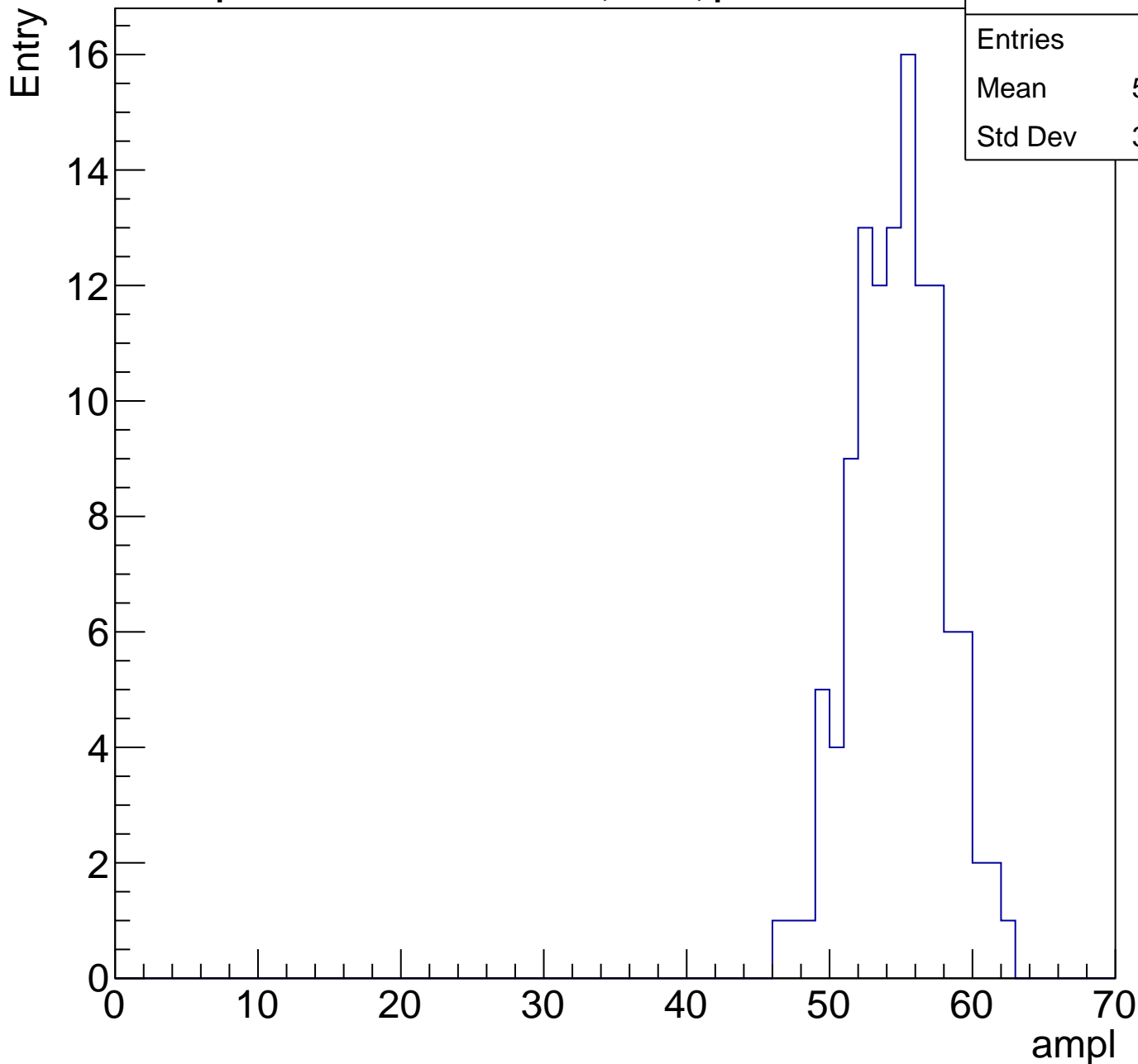
Entries	131
Mean	48.63
Std Dev	3.396



B1L001S, U21-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	54.32
Std Dev	3.112



B1L001S, U21-ch24, adc5

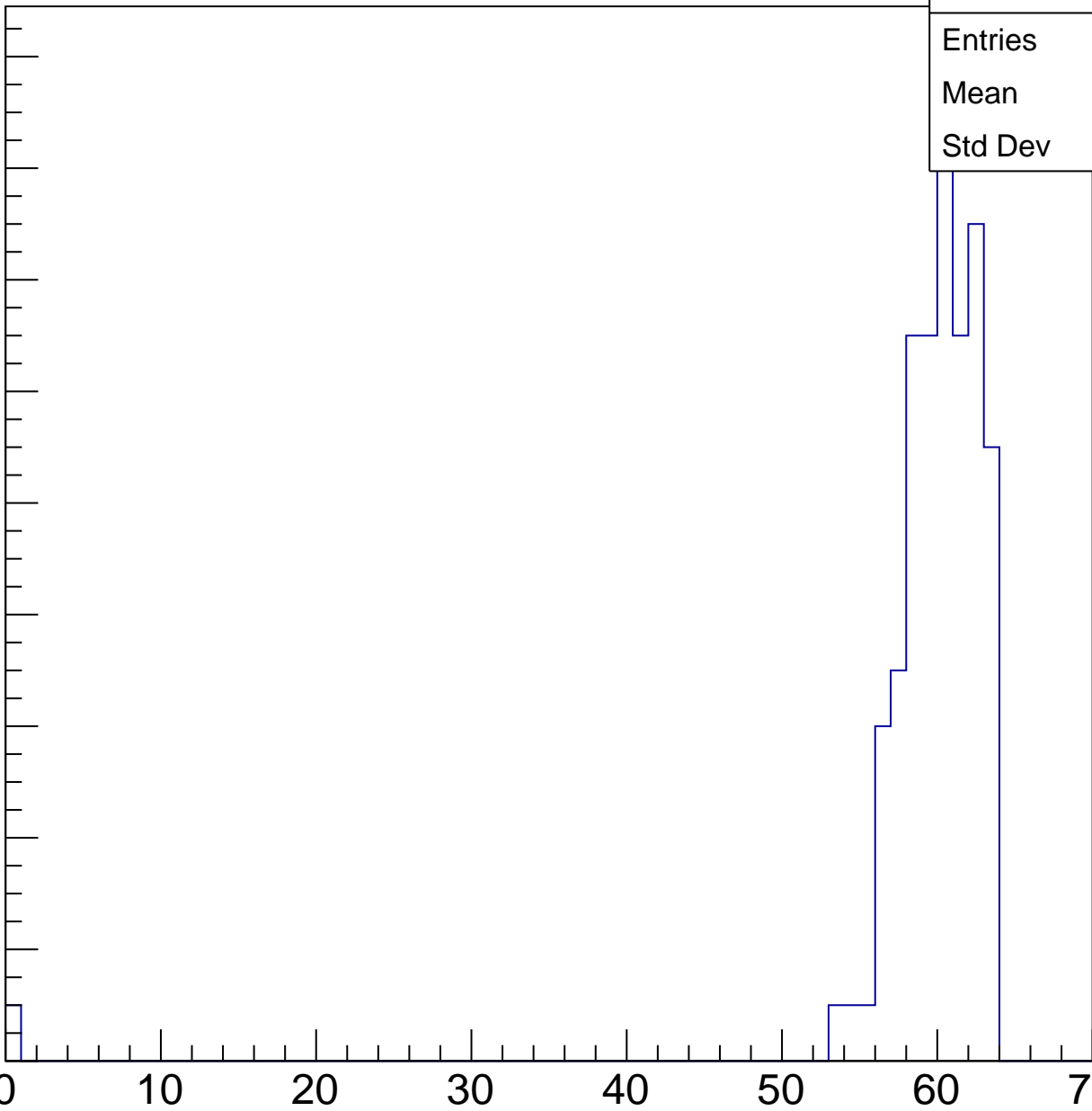
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.14
Std Dev	6.35

ampl

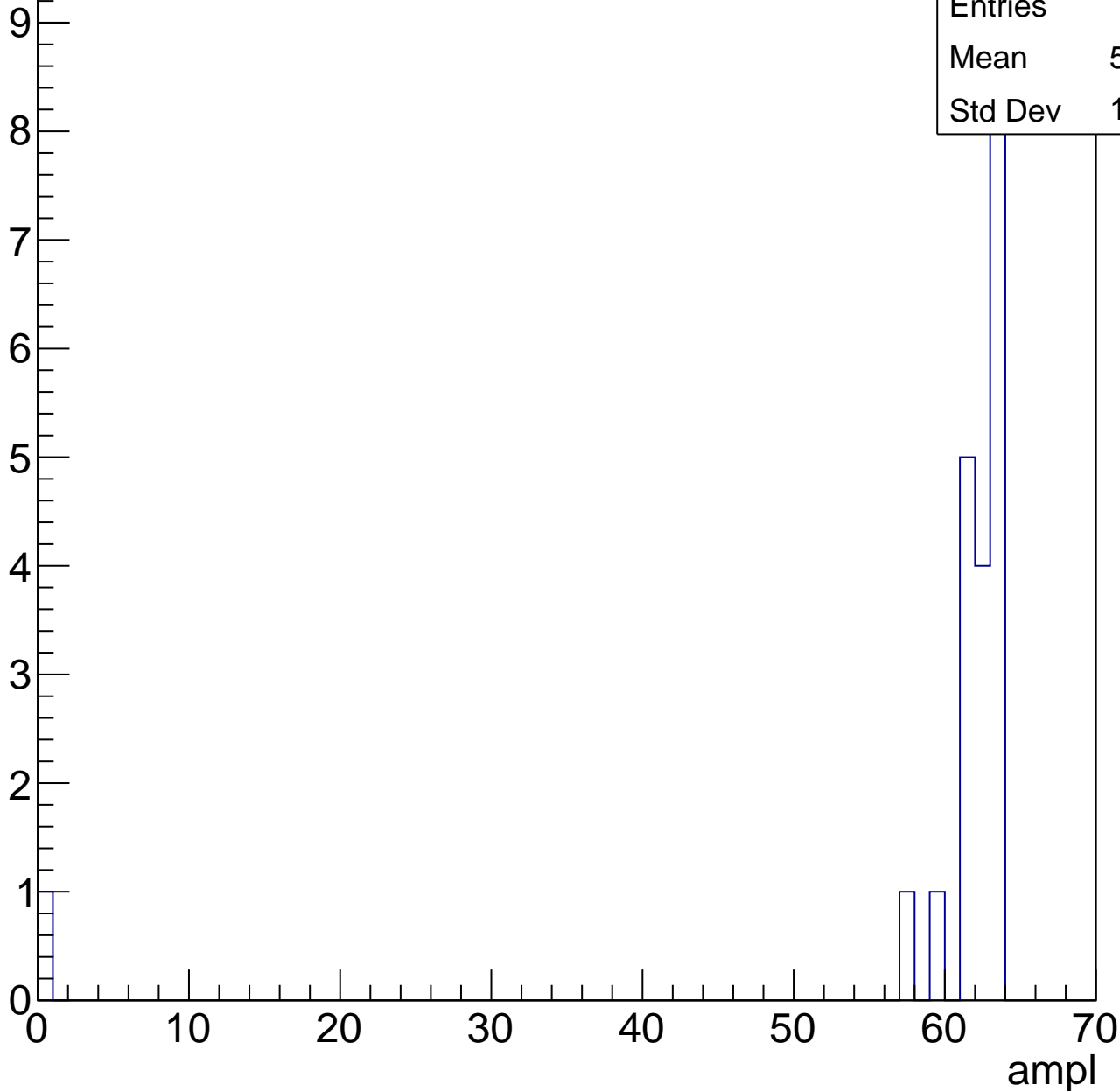


B1L001S, U21-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	58.86
Std Dev	13.25



B1L001S, U21-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch25, adc0

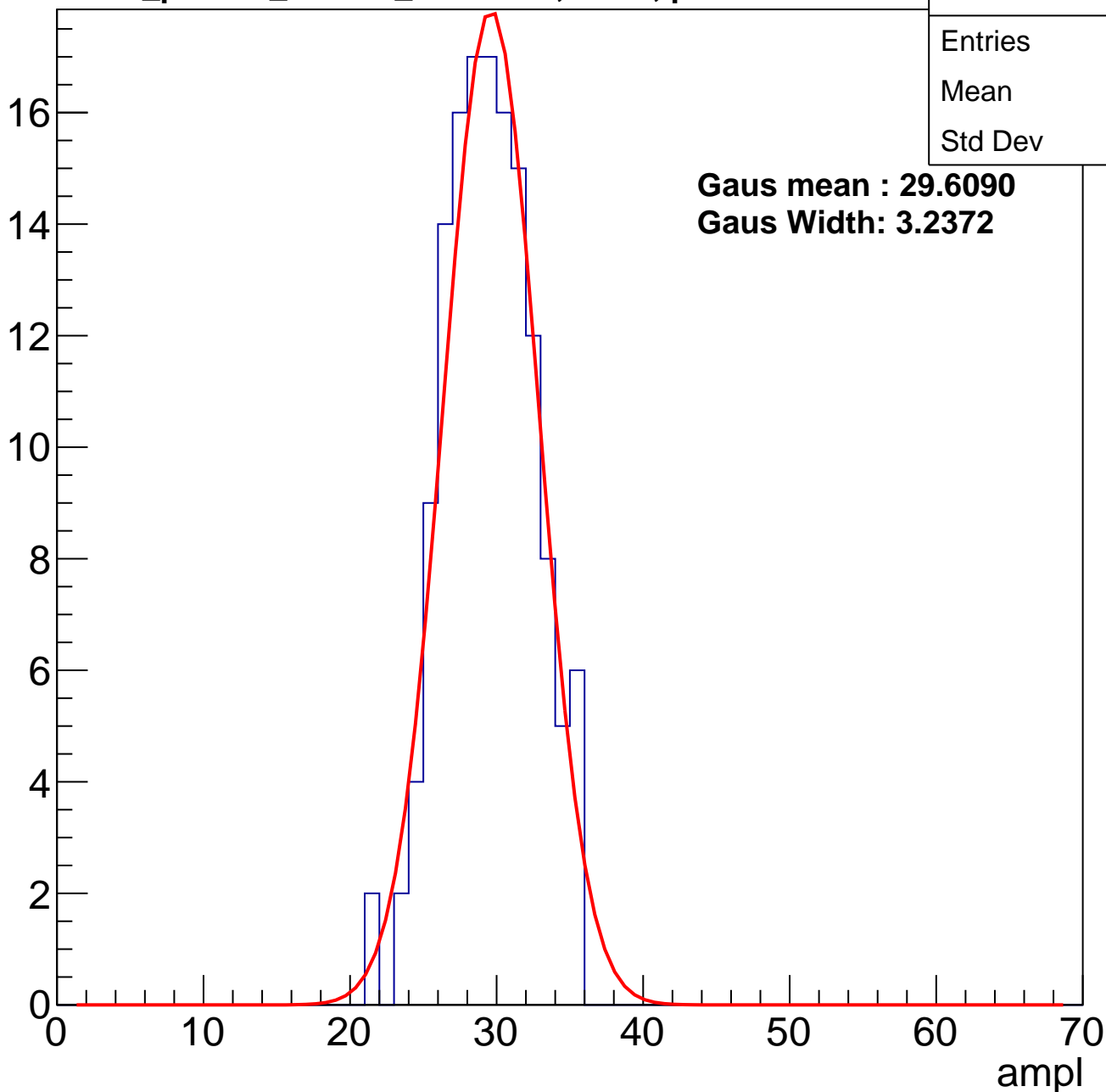
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29
Std Dev	3.02

Gaus mean : 29.6090

Gaus Width: 3.2372

Entry



B1L001S, U21-ch25, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	35.93
Std Dev	3.145

Gaus mean : 36.4375

Gaus Width: 3.0724

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

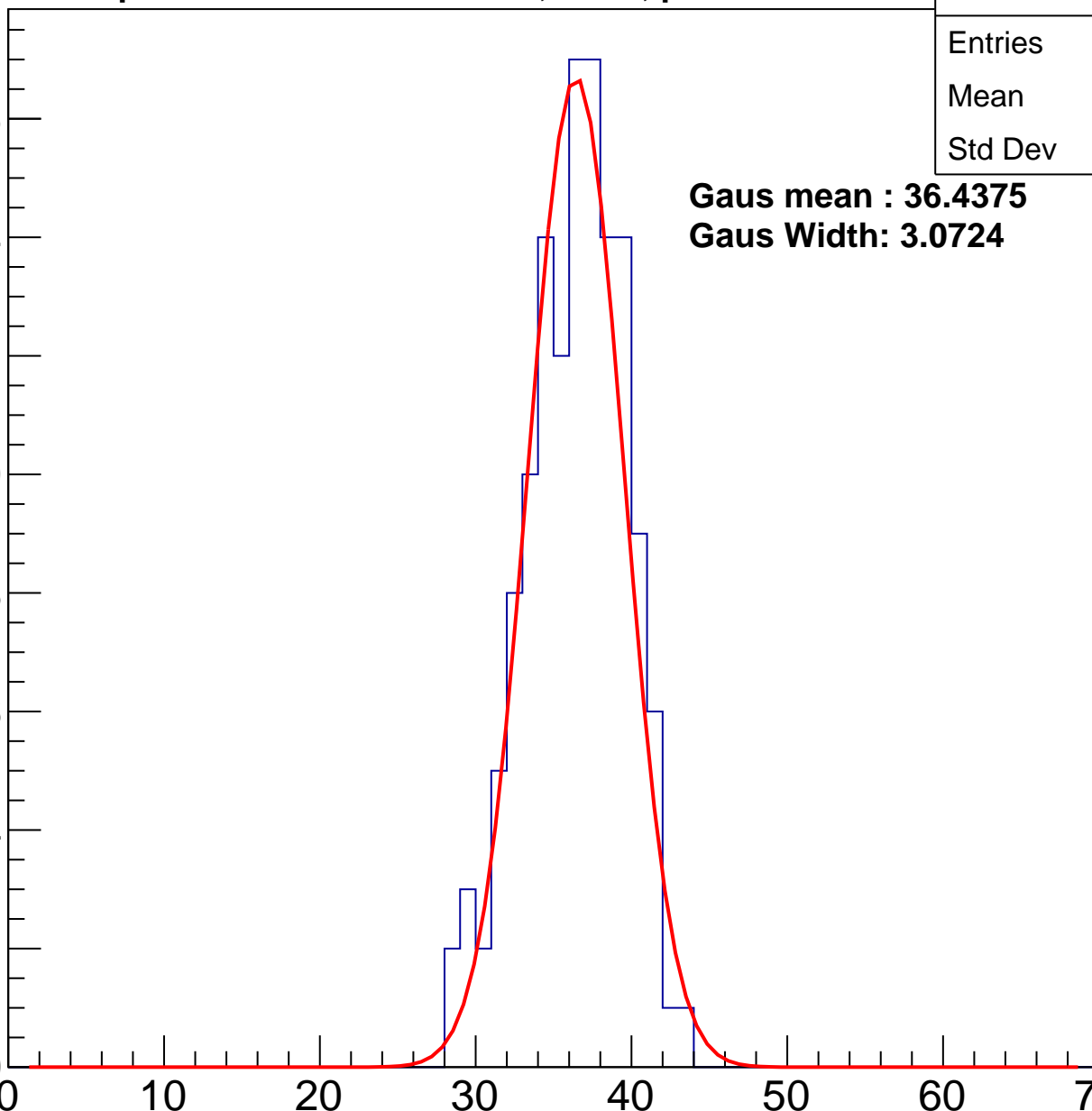
40

50

60

70

ampl



B1L001S, U21-ch25, adc2

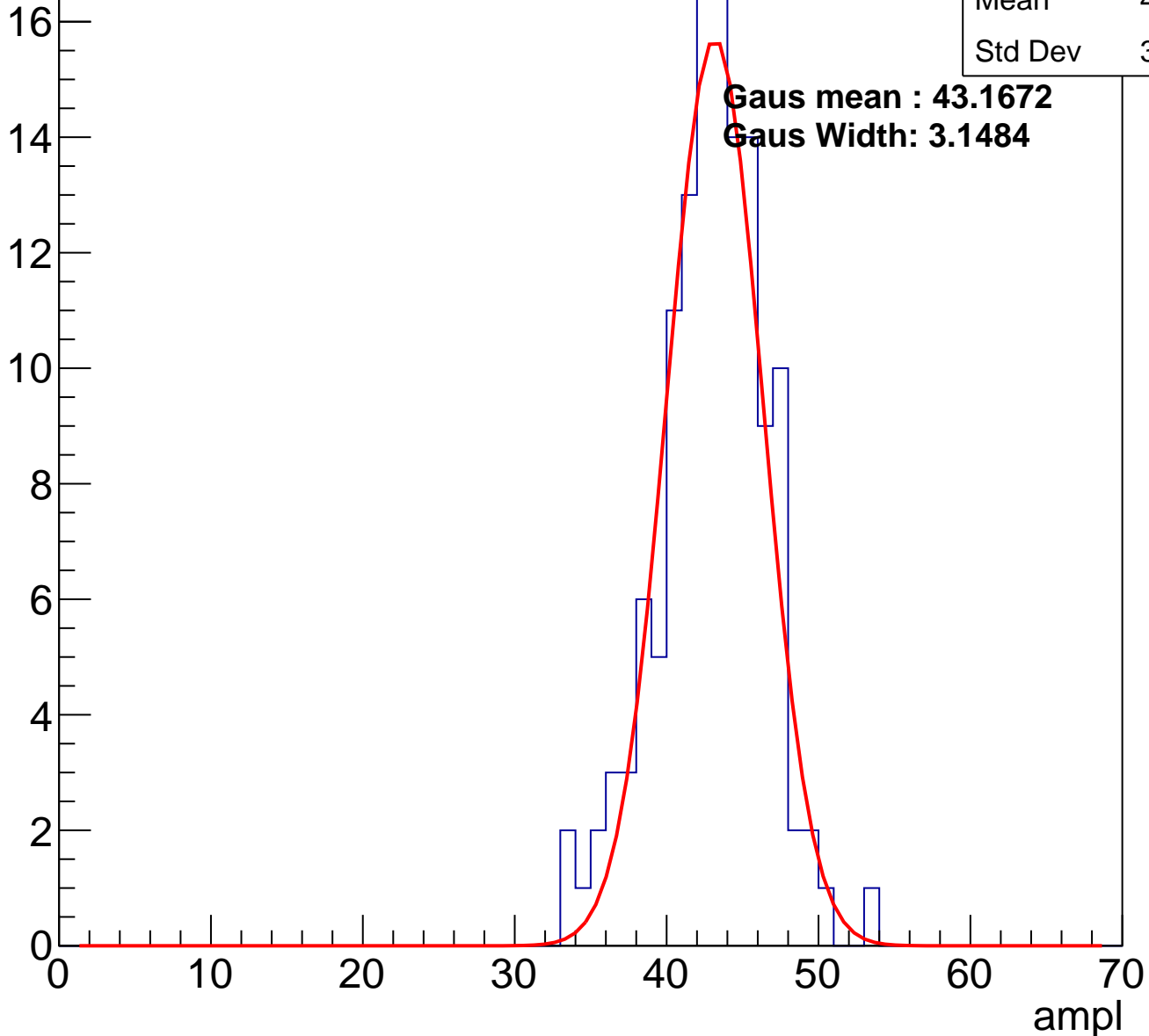
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	133
Mean	42.53
Std Dev	3.485

Gaus mean : 43.1672

Gaus Width: 3.1484



B1L001S, U21-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	49.16
Std Dev	3.224

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

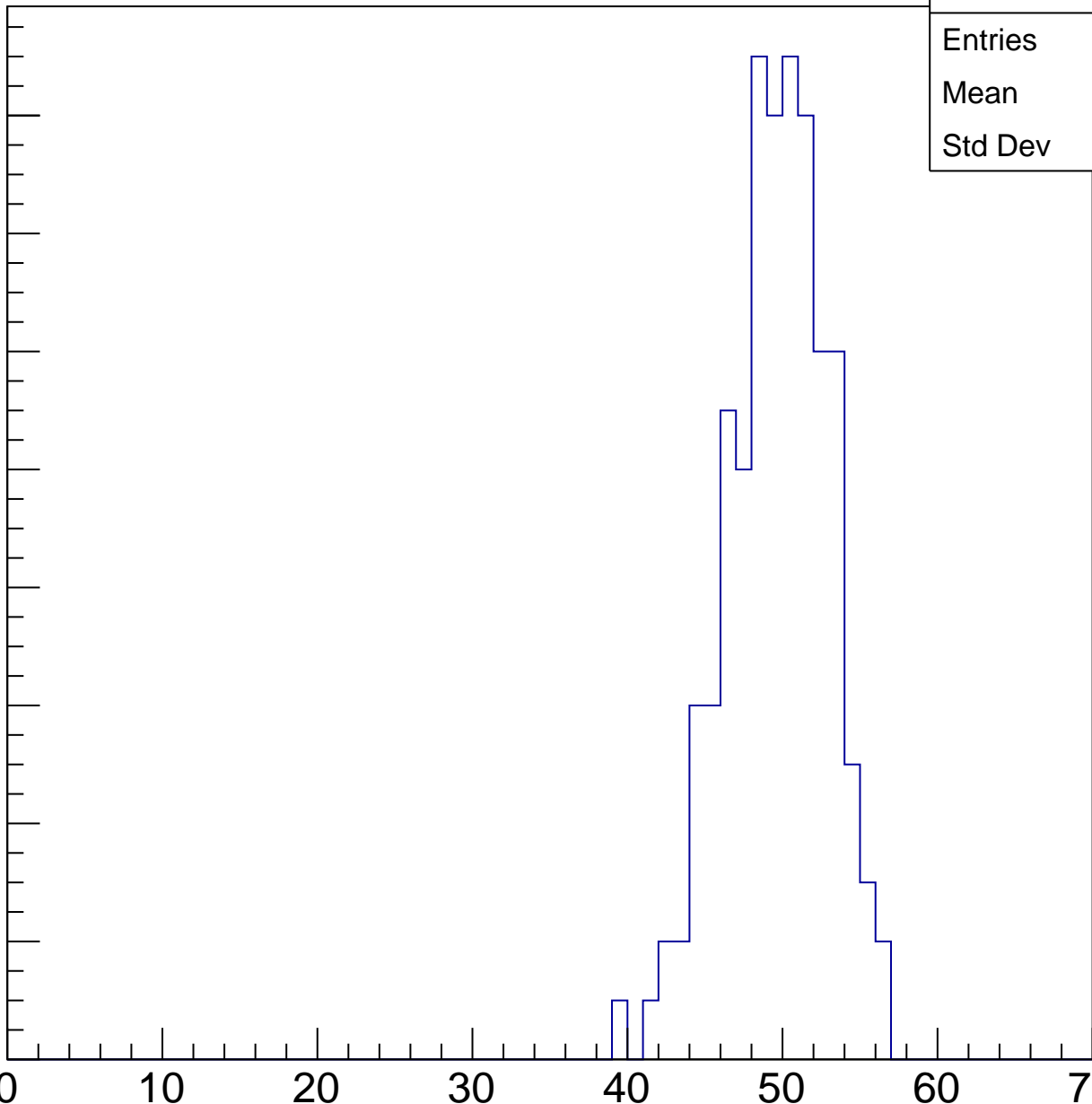
40

50

60

70

ampl



B1L001S, U21-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

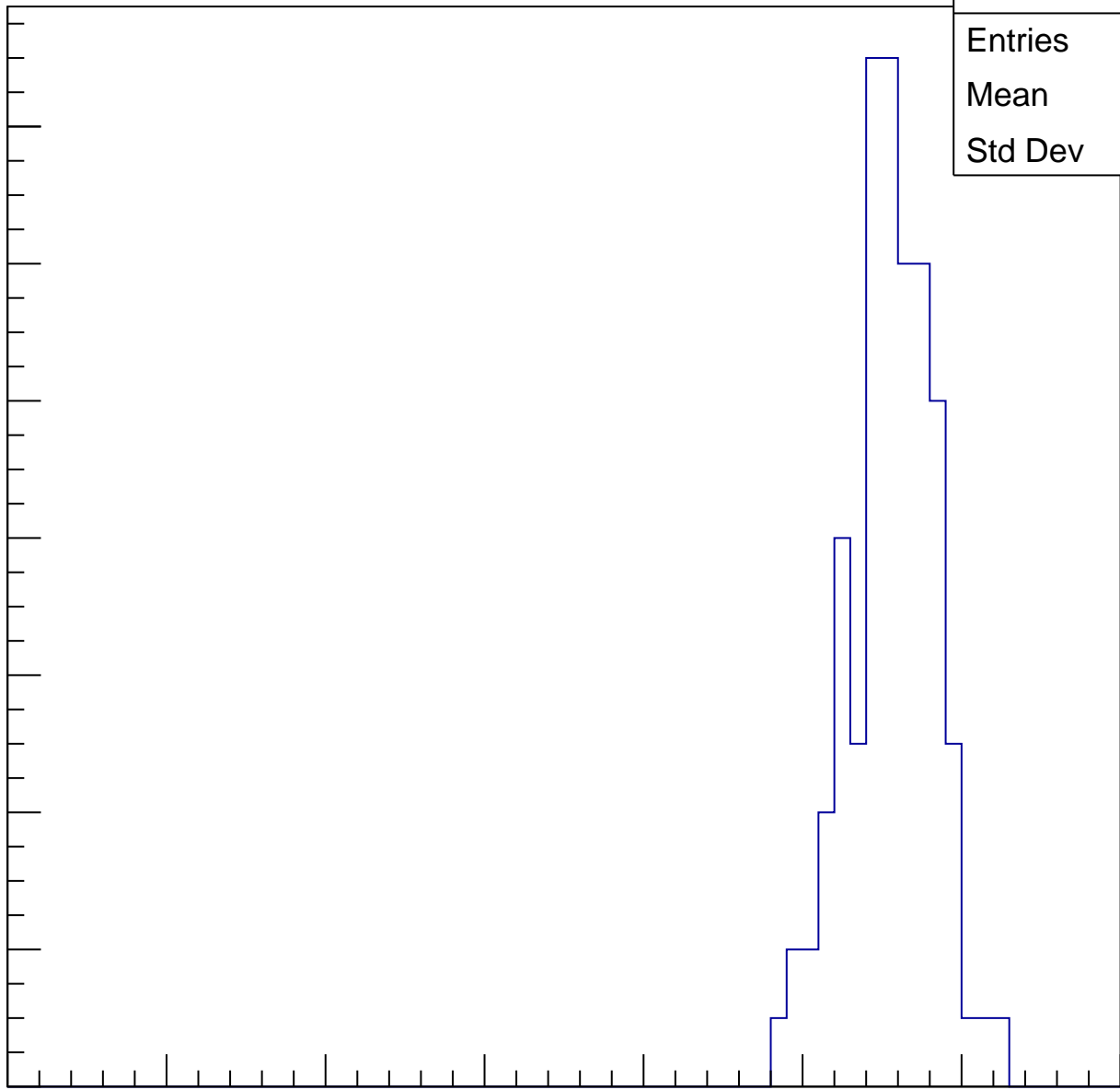
2

0

Entries	94
Mean	55.11
Std Dev	2.691

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch25, adc5

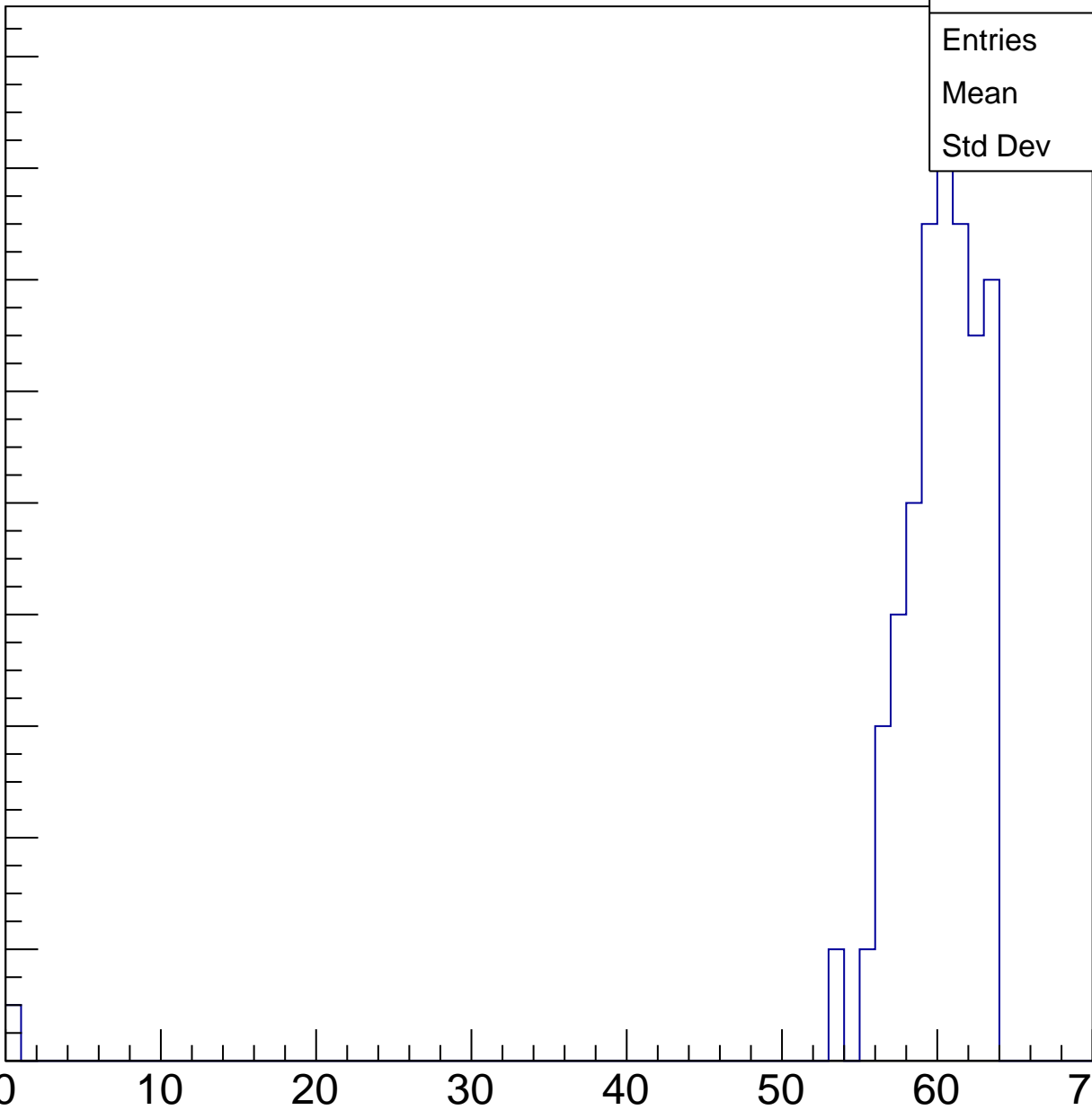
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	104
Mean	59.19
Std Dev	6.276

ampl

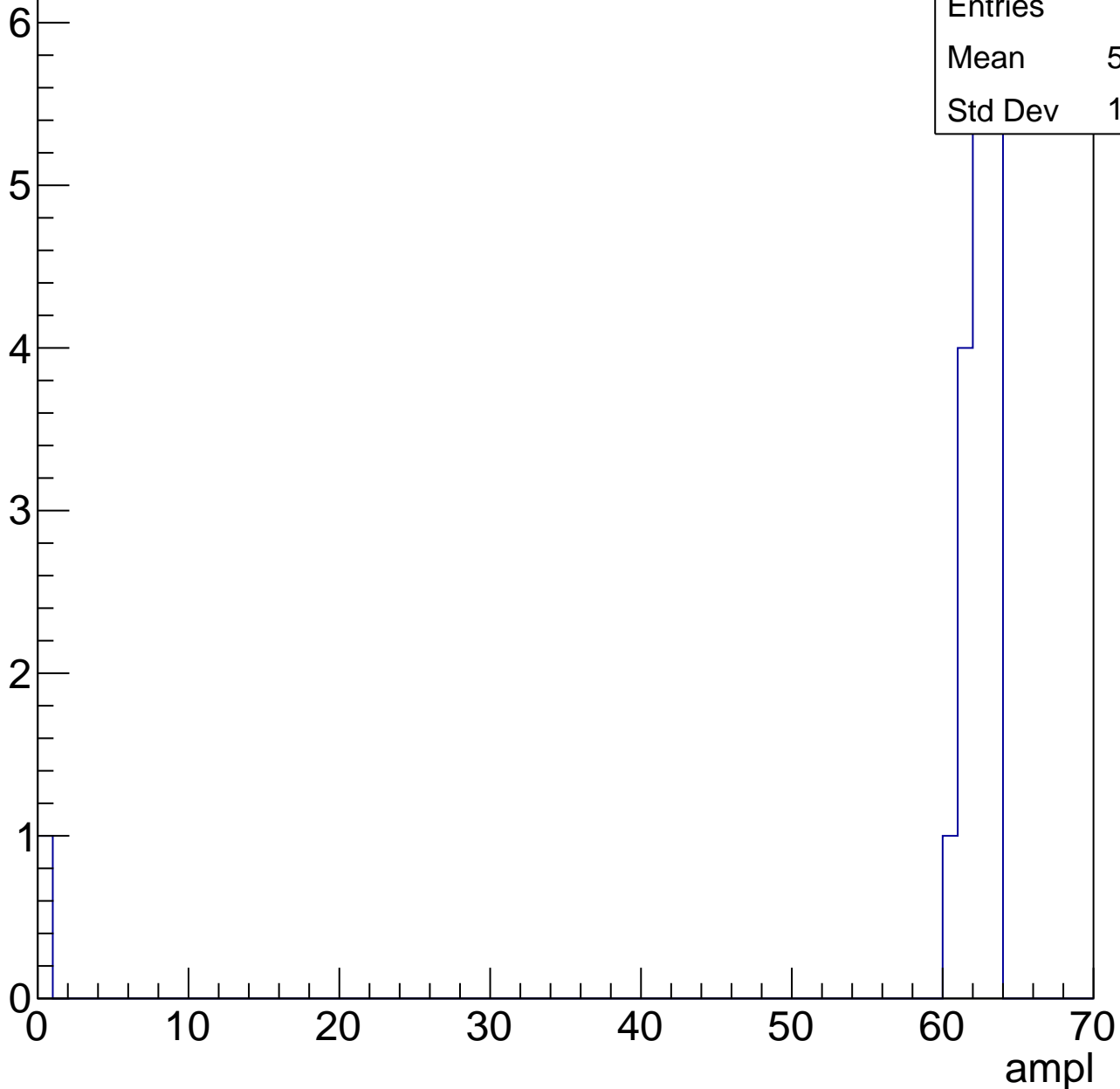


B1L001S, U21-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

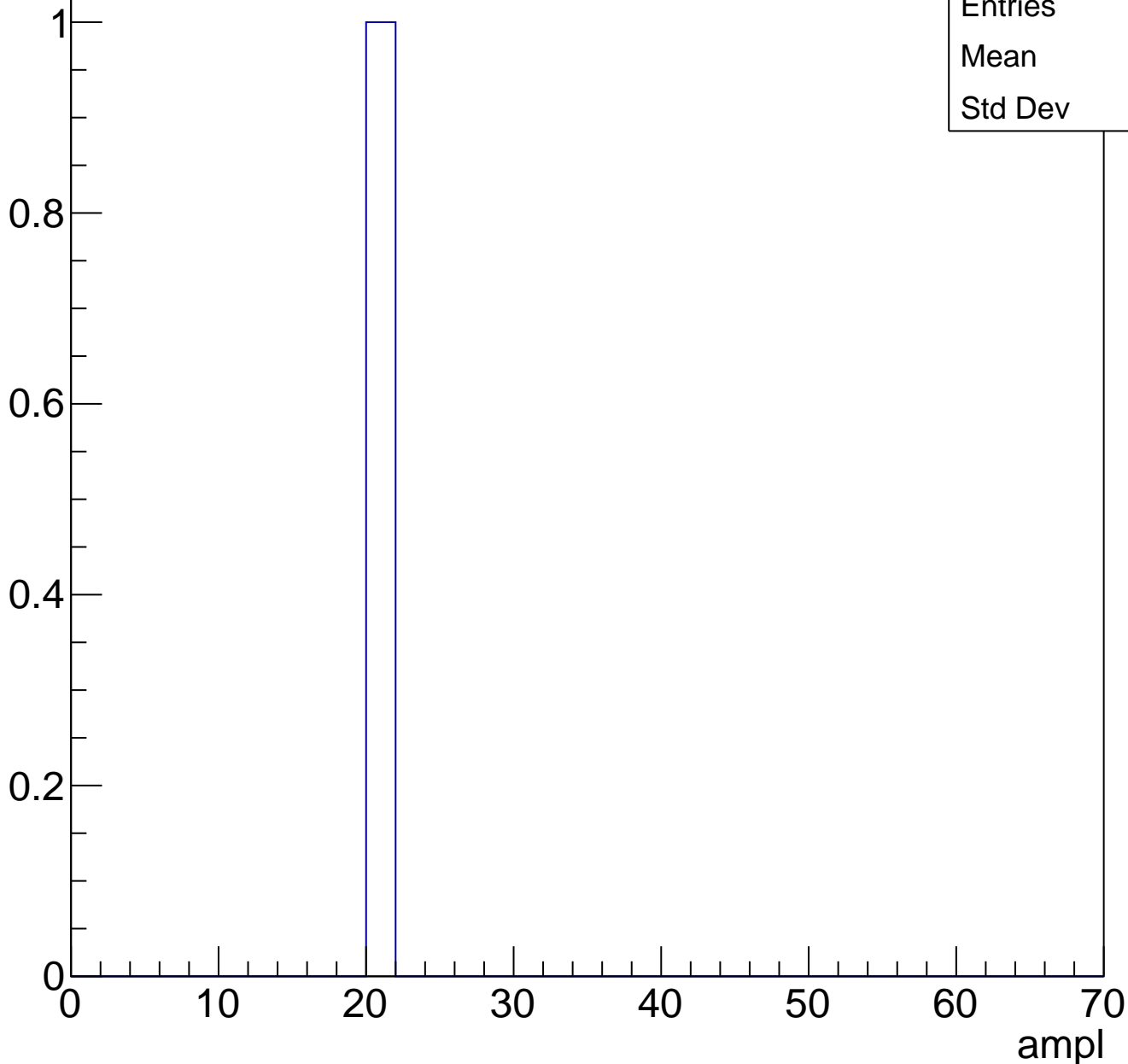
Entries	18
Mean	58.56
Std Dev	14.23



B1L001S, U21-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch26, adc0

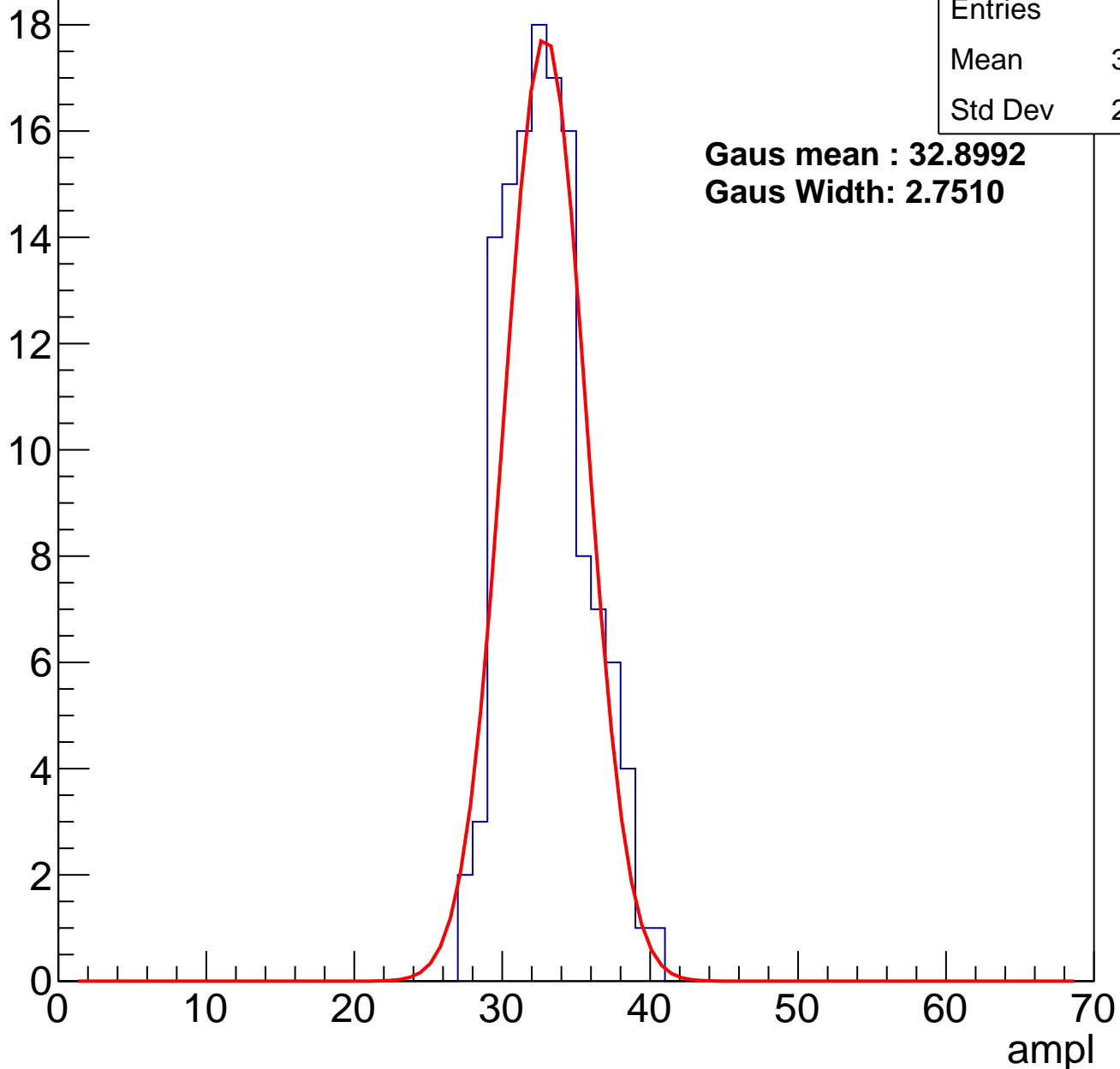
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	32.47
Std Dev	2.716

Gaus mean : 32.8992

Gaus Width: 2.7510

Entry

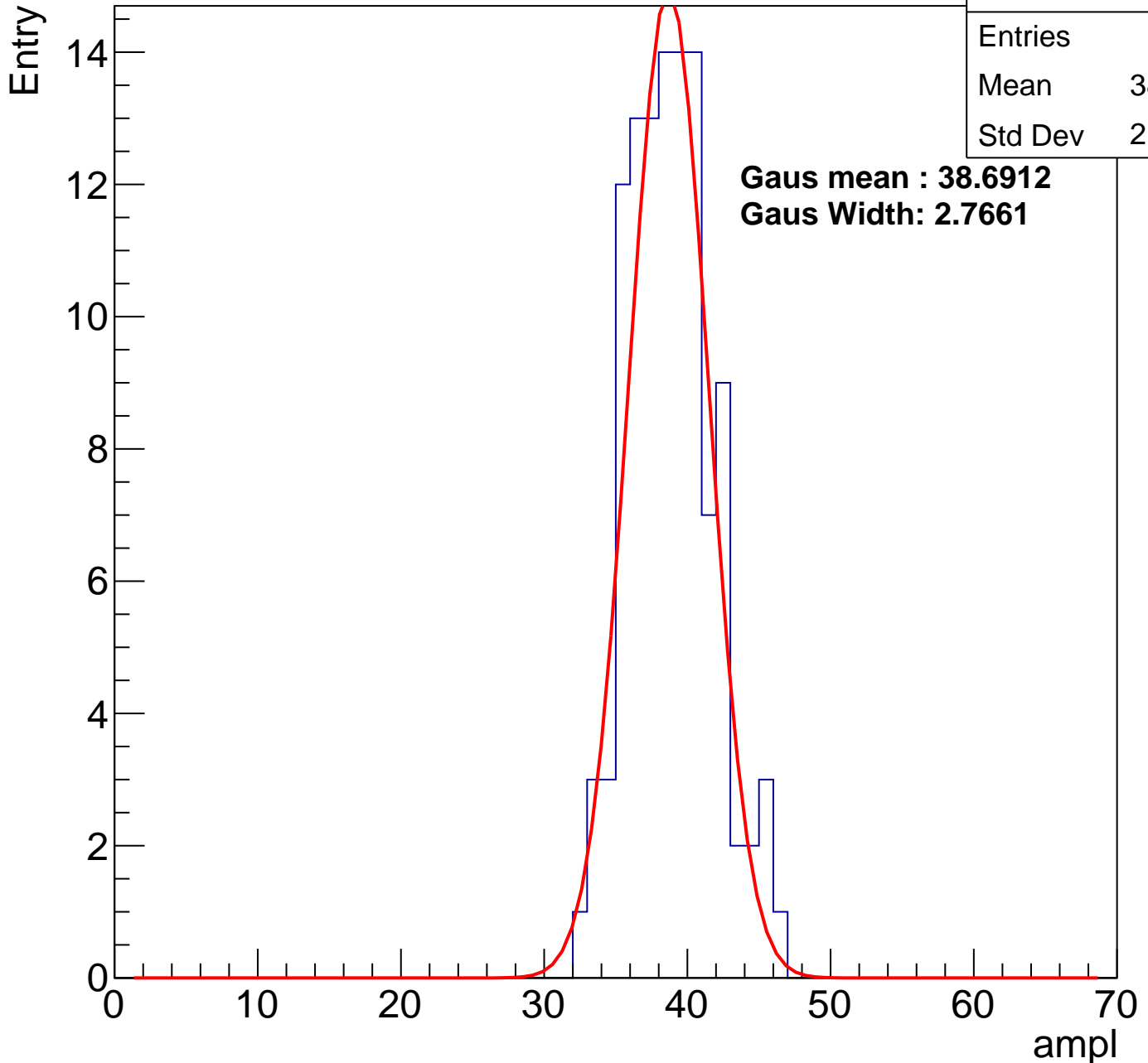


B1L001S, U21-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	38.38
Std Dev	2.888

Gaus mean : 38.6912
Gaus Width: 2.7661



B1L001S, U21-ch26, adc2

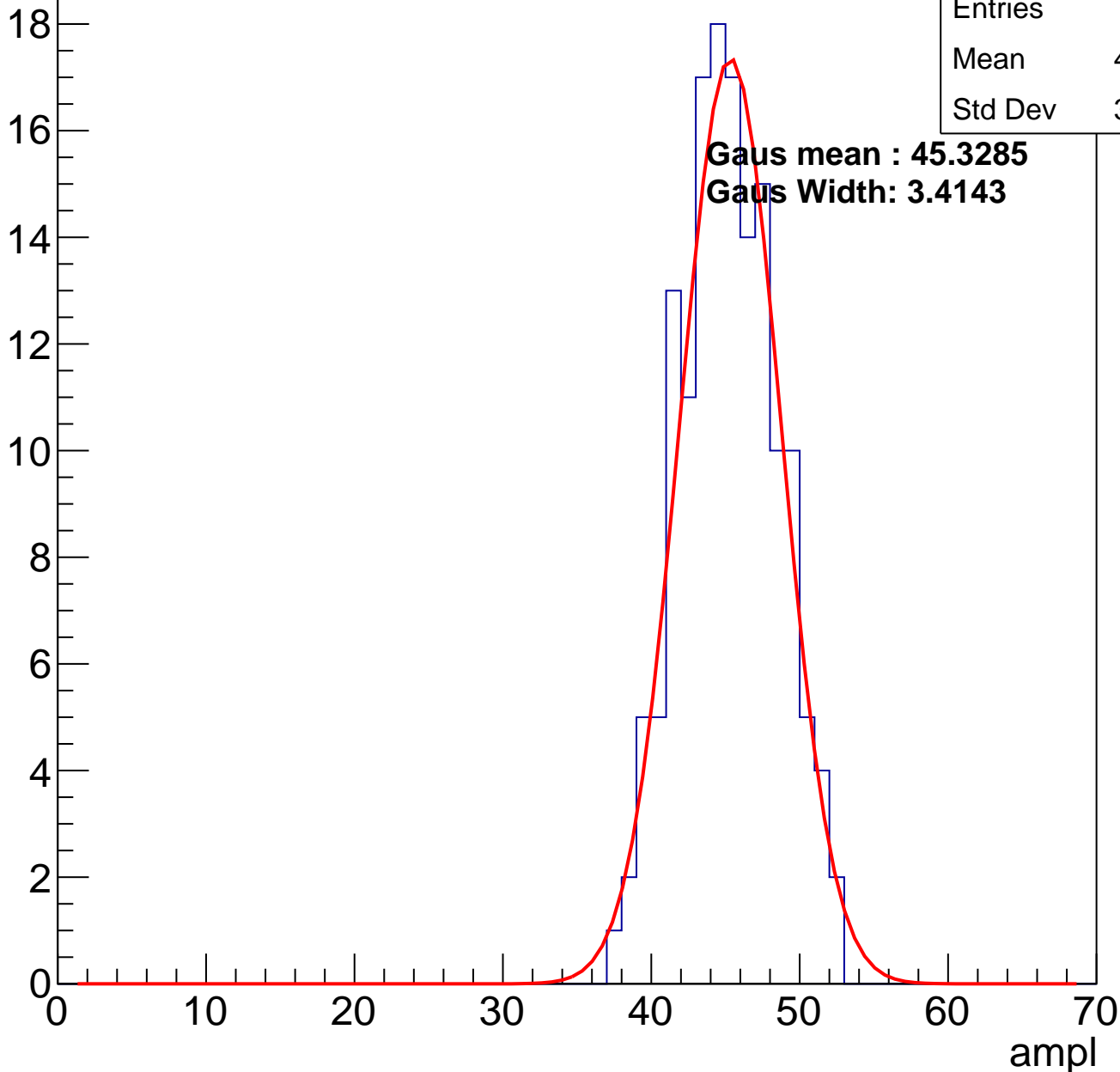
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	44.75
Std Dev	3.217

Gaus mean : 45.3285

Gaus Width: 3.4143

Entry



B1L001S, U21-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	51.42
Std Dev	2.988

Entry

14

12

10

8

6

4

2

0

0

10

20

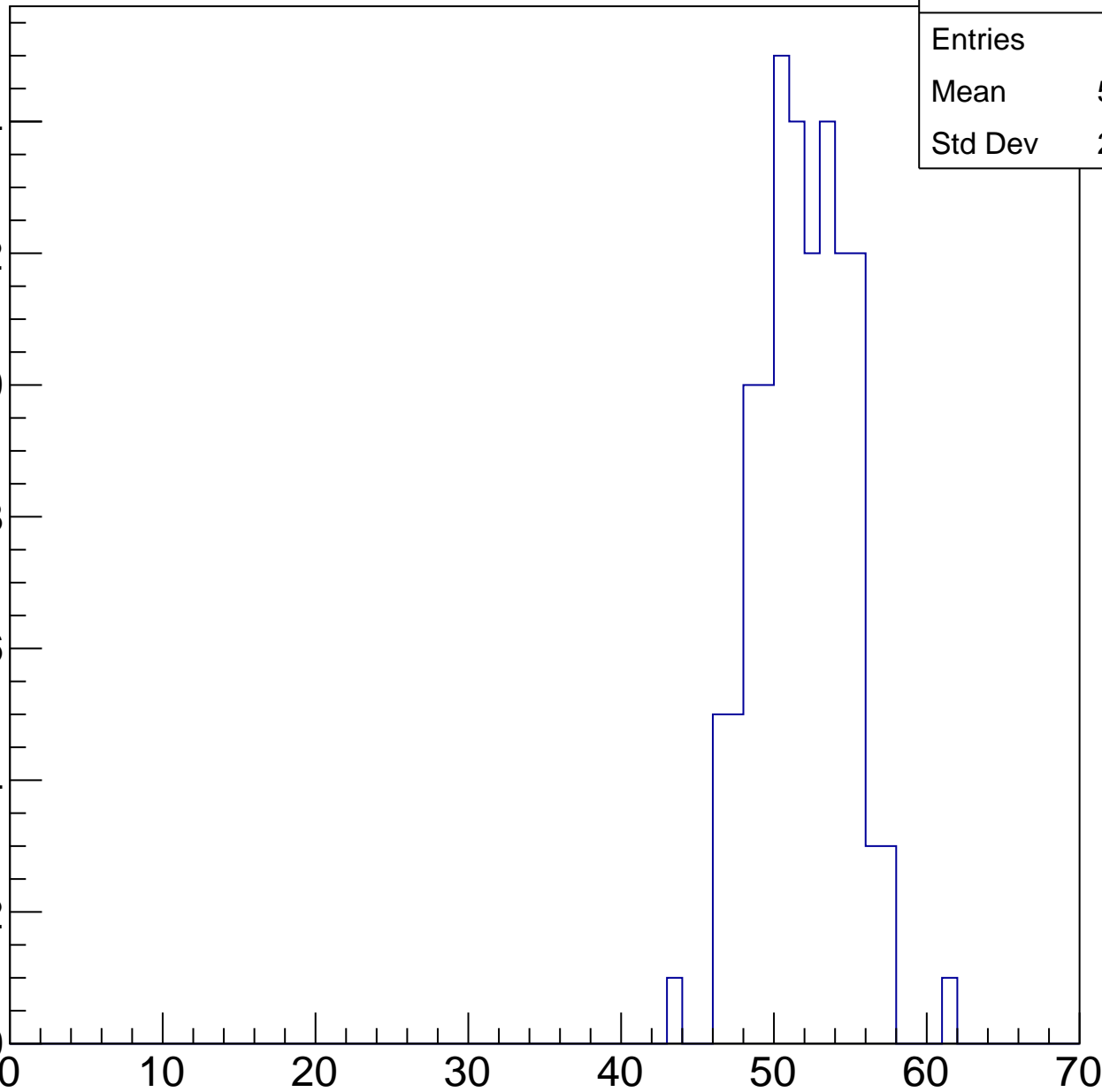
30

40

50

60

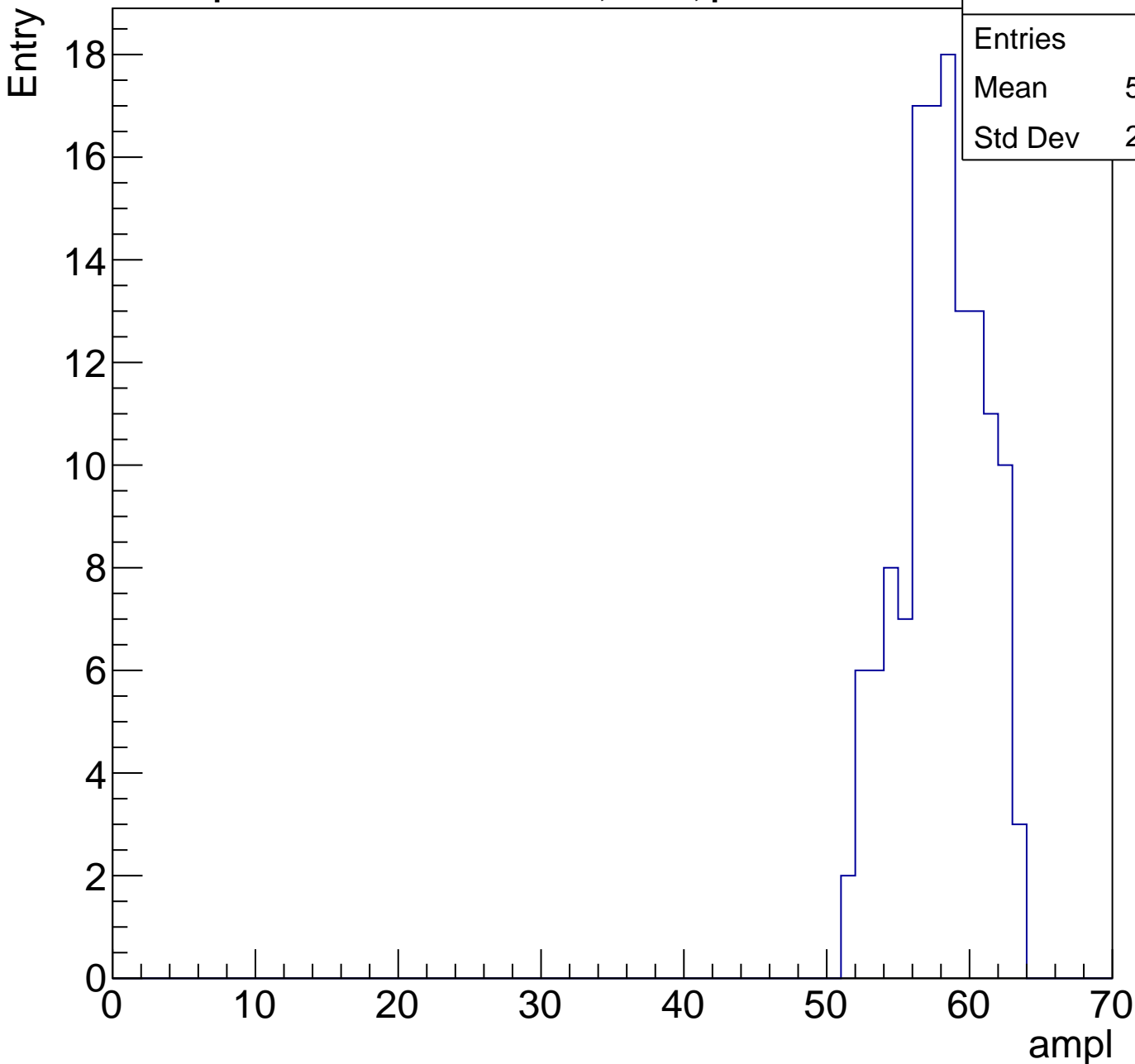
ampl



B1L001S, U21-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	57.56
Std Dev	2.916



B1L001S, U21-ch26, adc5

calib_packv5_042523_0143.root, FC#2, port C2

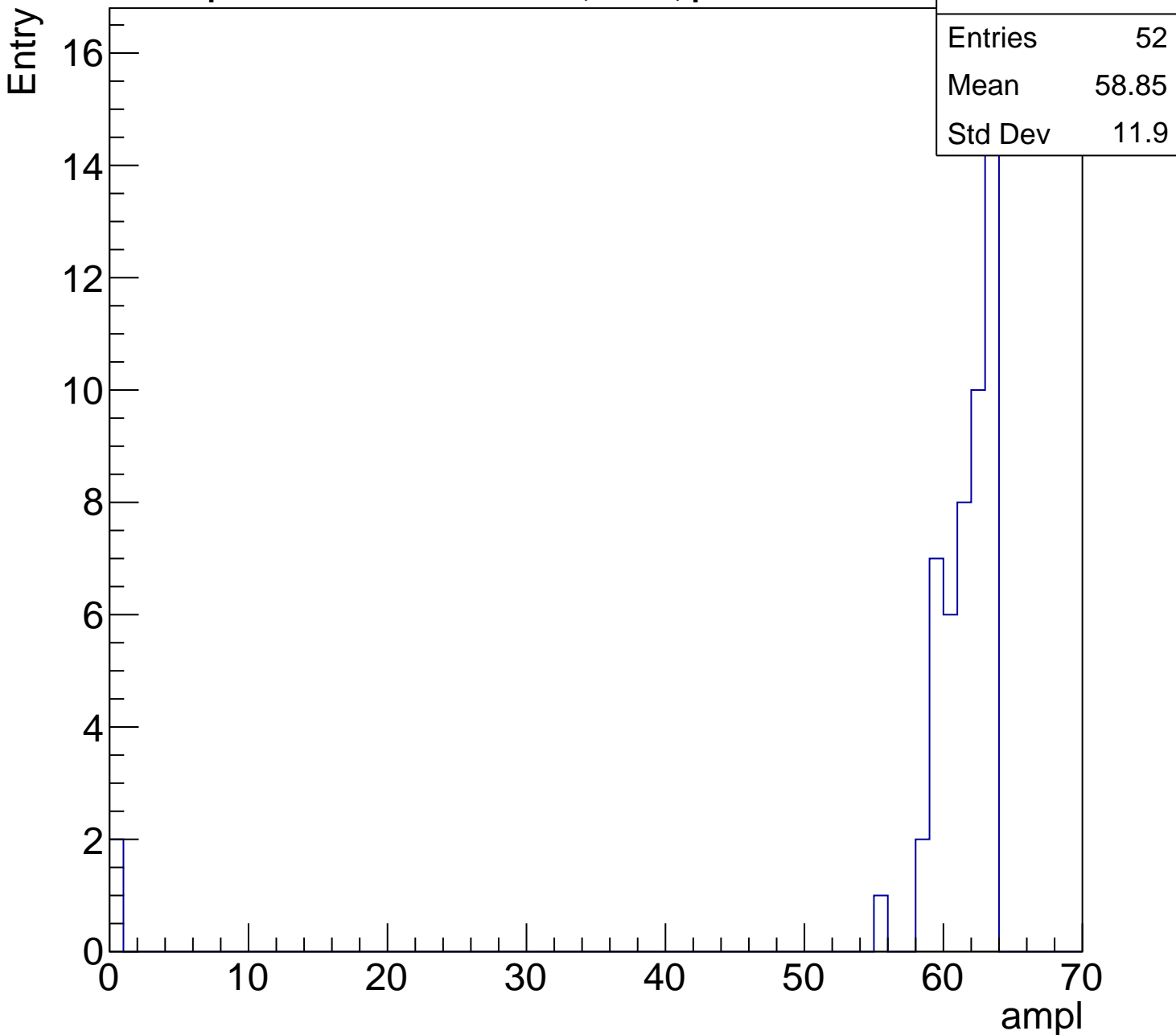
Entries	52
Mean	58.85
Std Dev	11.9

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

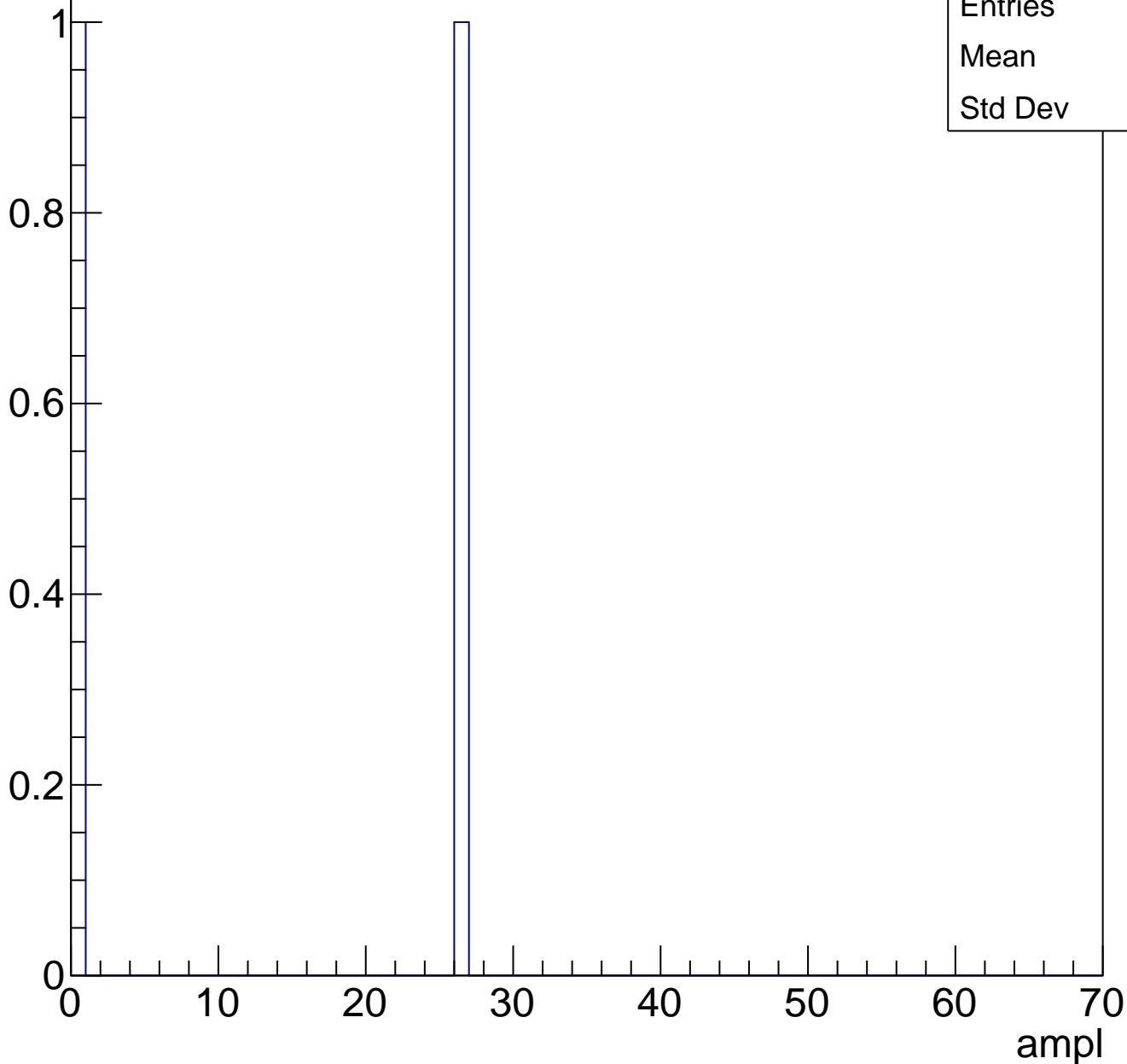
Entry



B1L001S, U21-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch27, adc0

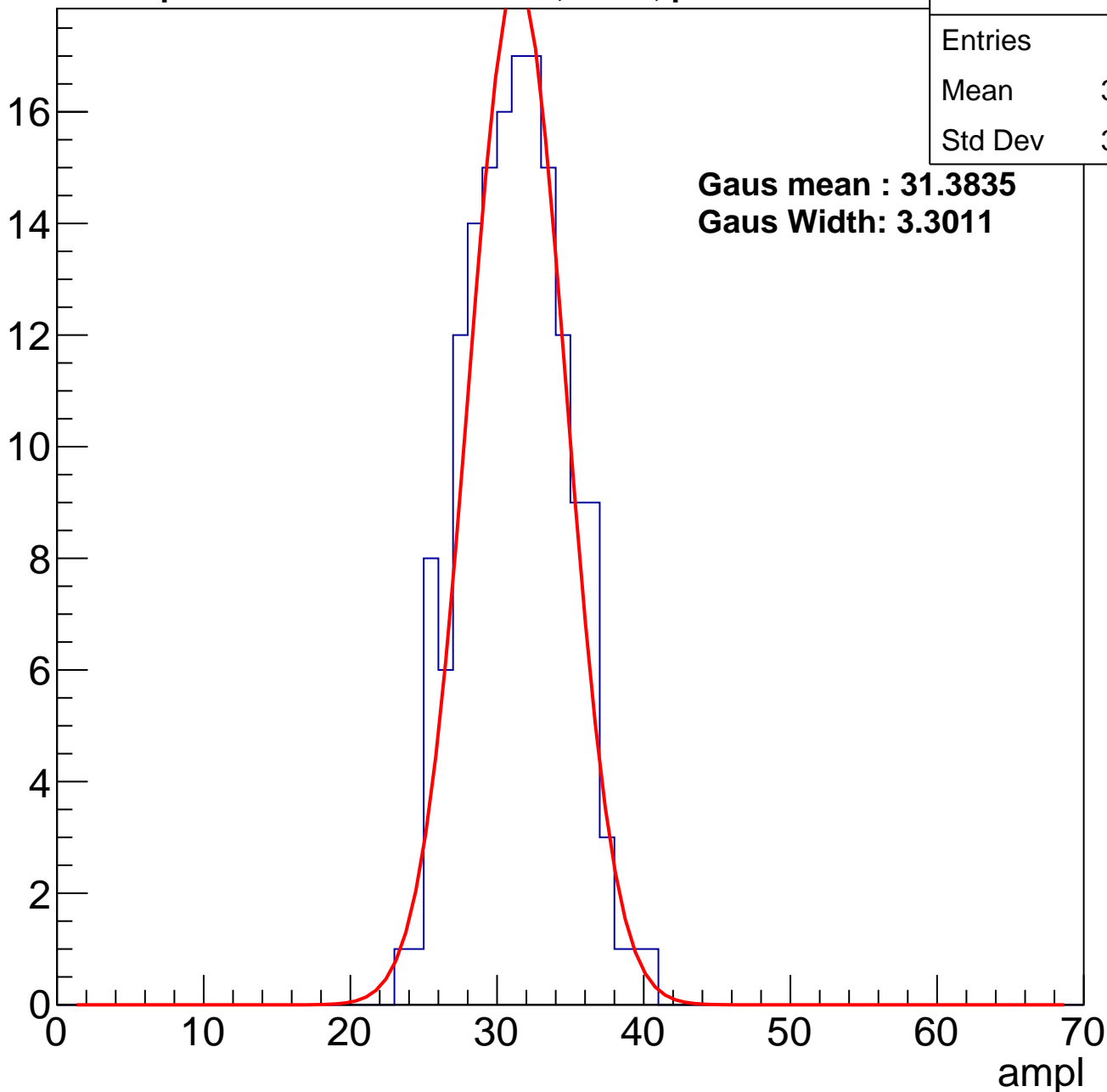
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	30.85
Std Dev	3.373

Gaus mean : 31.3835

Gaus Width: 3.3011

Entry

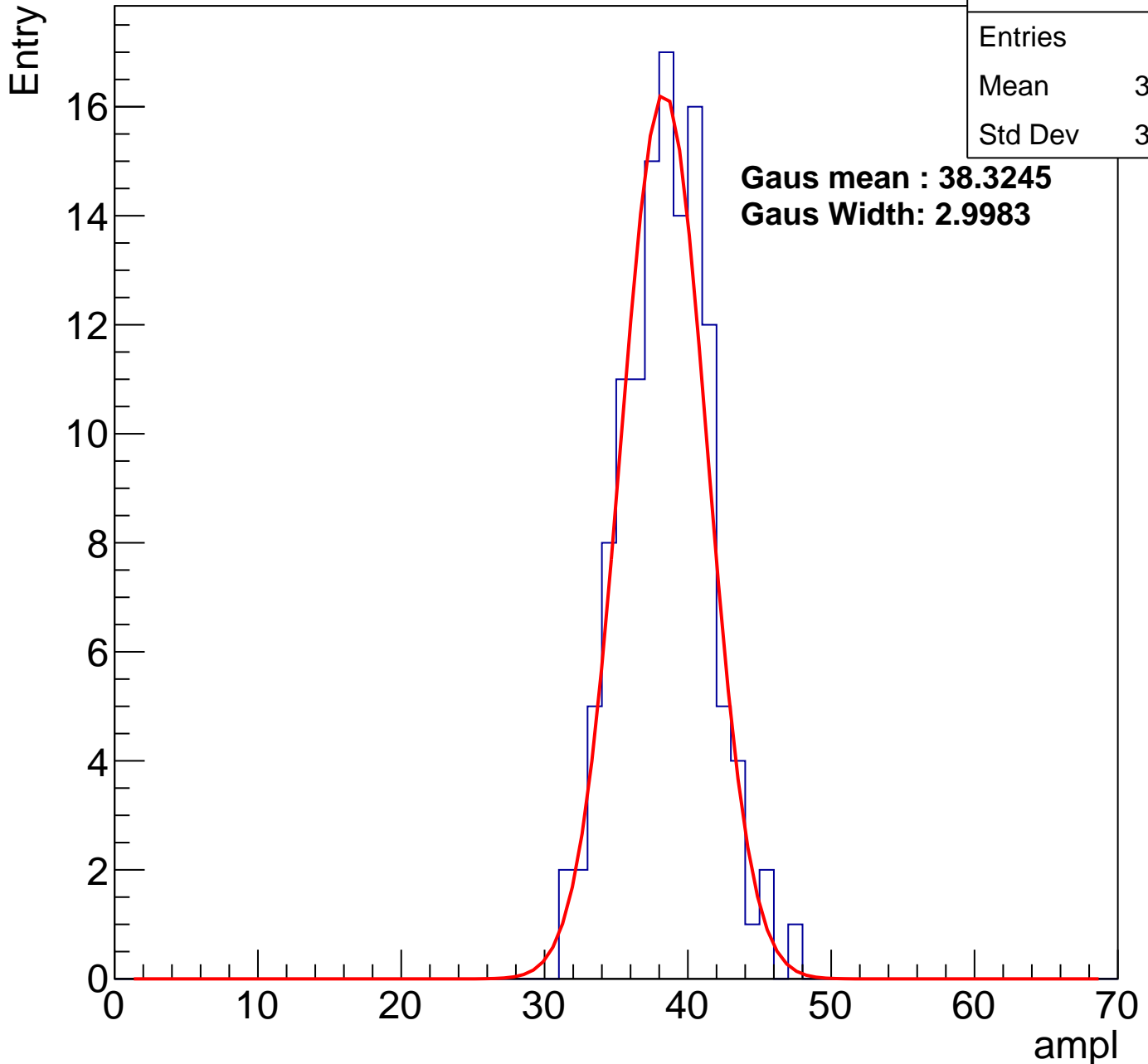


B1L001S, U21-ch27, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	37.98
Std Dev	3.037

Gaus mean : 38.3245
Gaus Width: 2.9983



B1L001S, U21-ch27, adc2

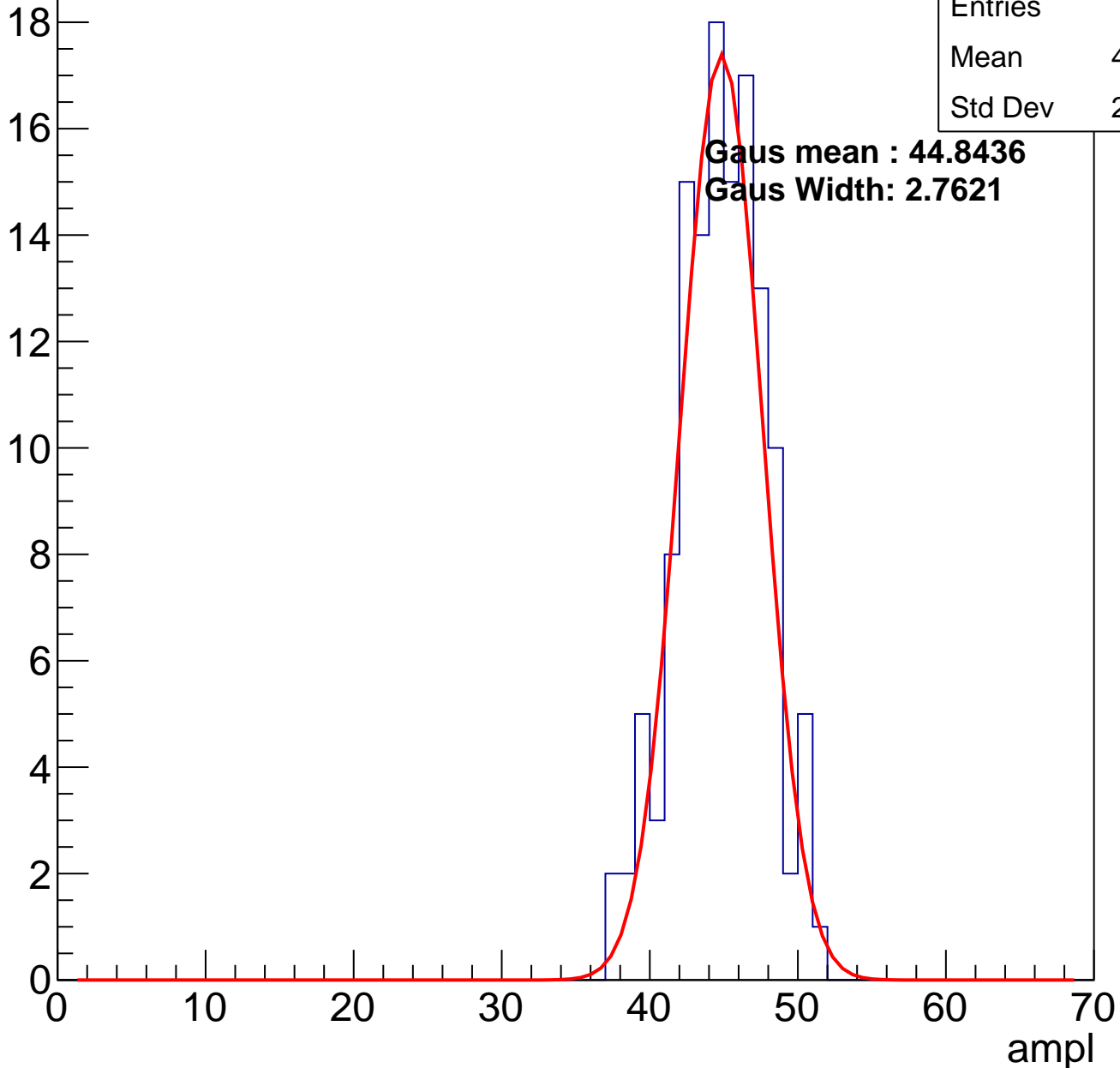
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.34
Std Dev	2.926

Gaus mean : 44.8436

Gaus Width: 2.7621

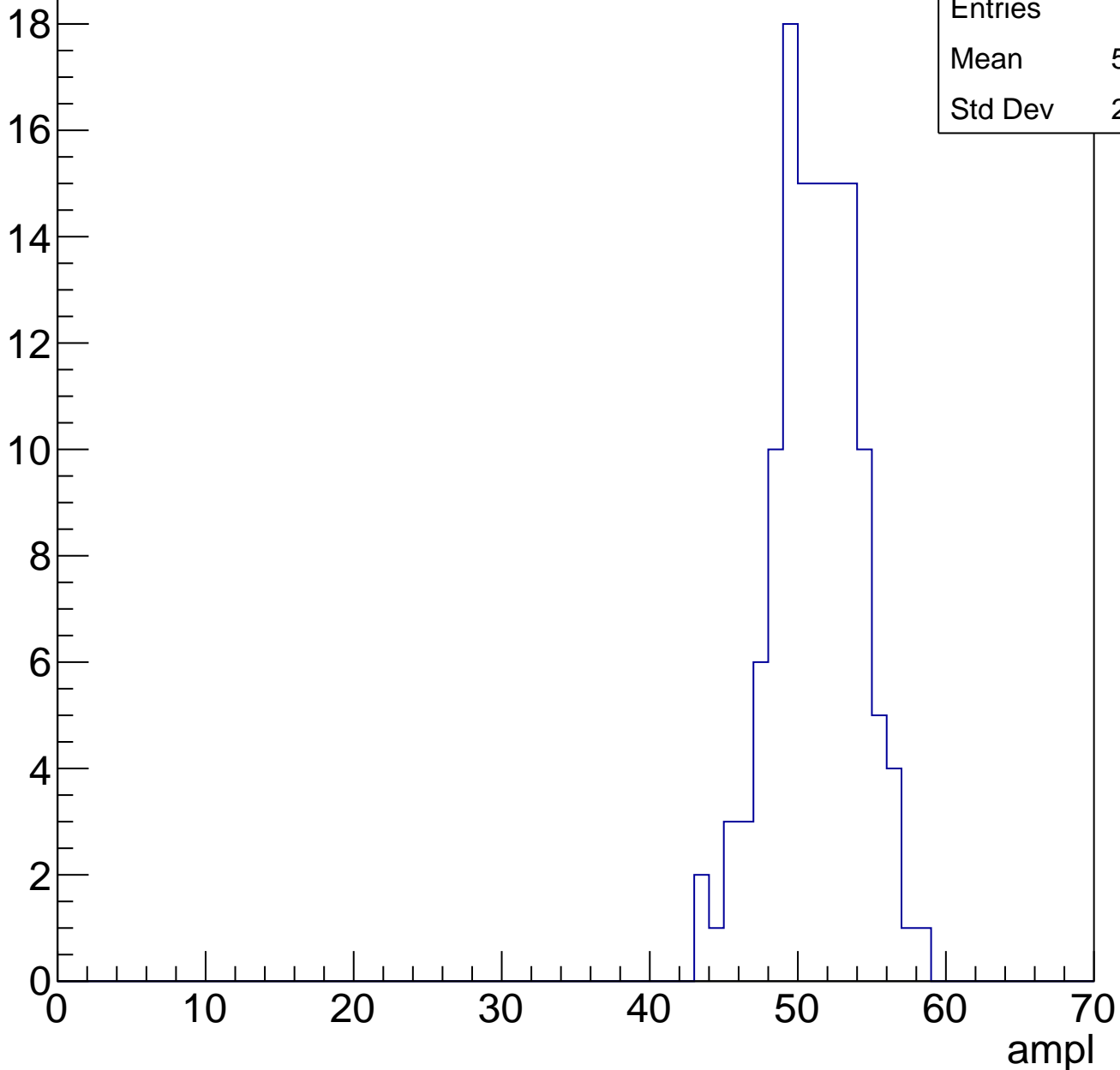
Entry



B1L001S, U21-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	124
Mean	50.73
Std Dev	2.916

B1L001S, U21-ch27, adc4

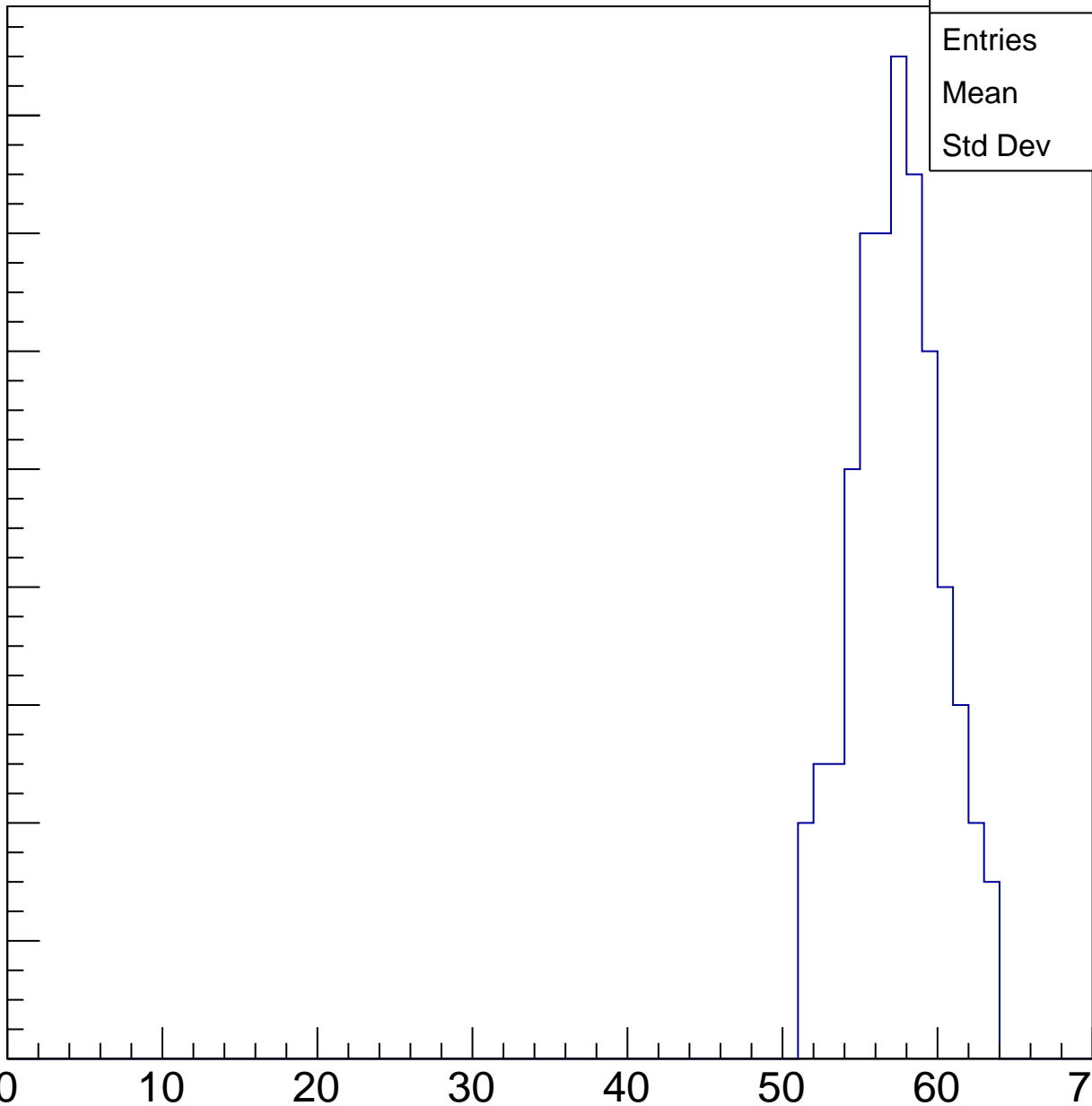
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	56.86
Std Dev	2.843

ampl



B1L001S, U21-ch27, adc5

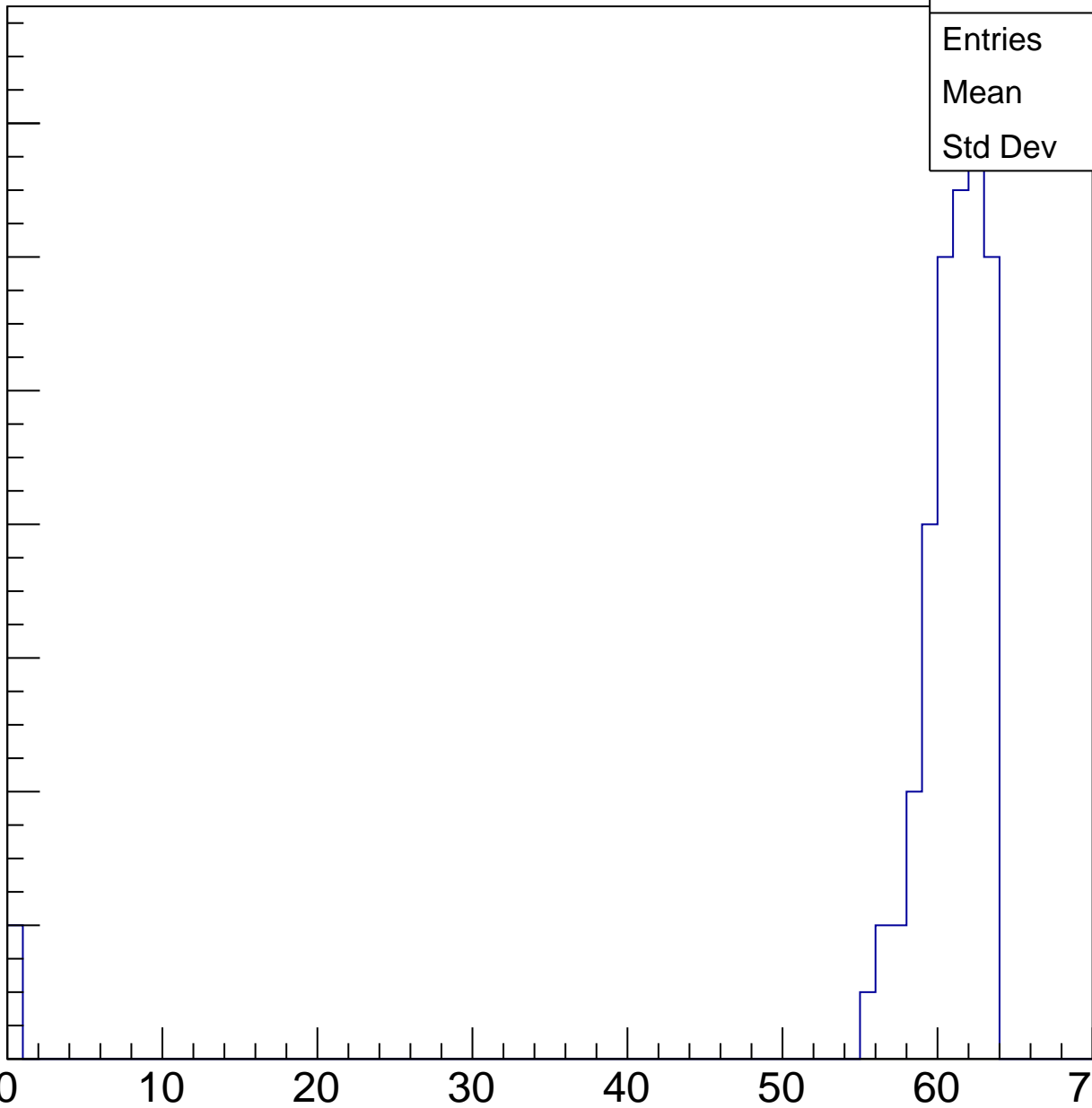
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.93
Std Dev	10.21

ampl

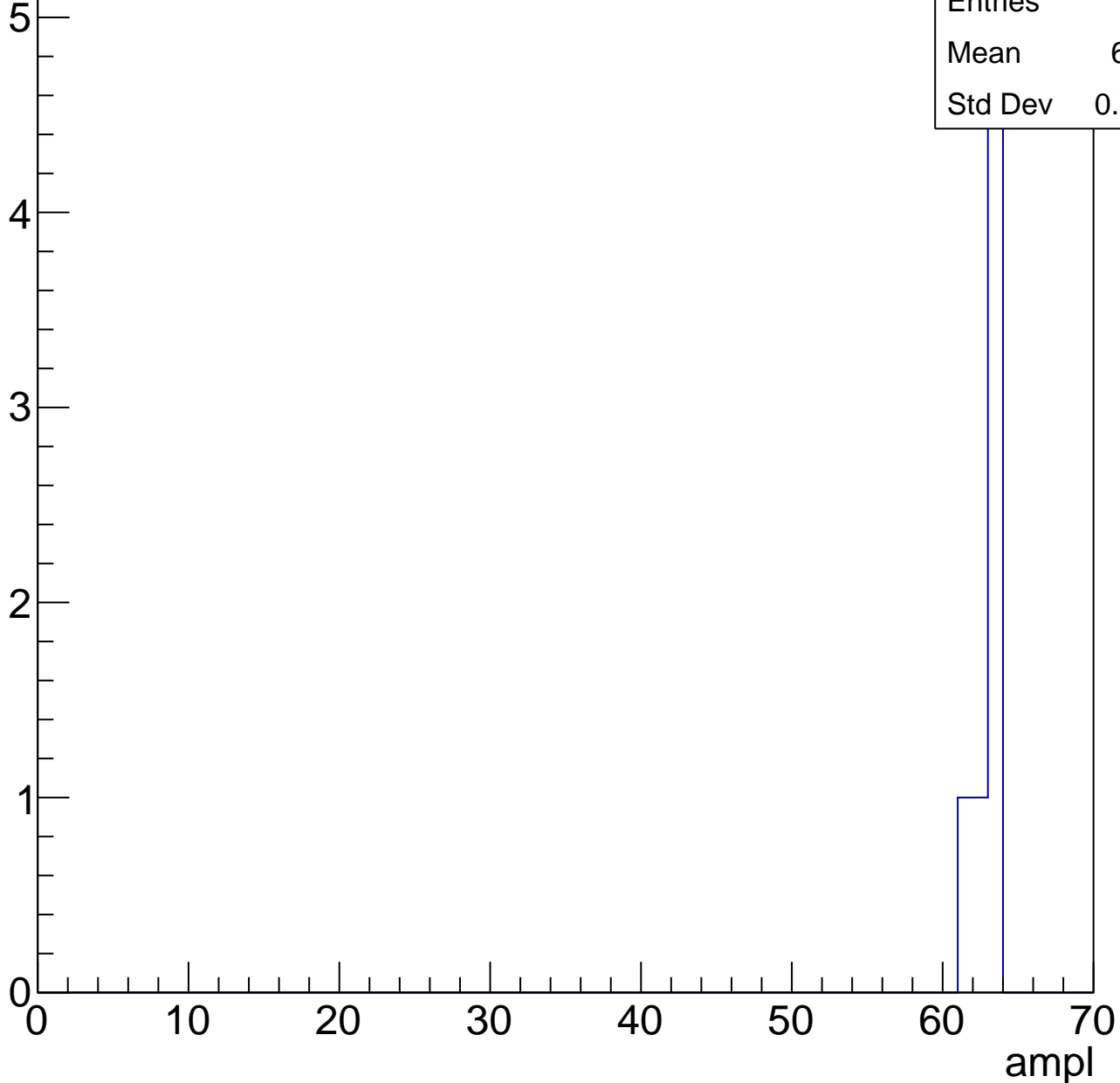


B1L001S, U21-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

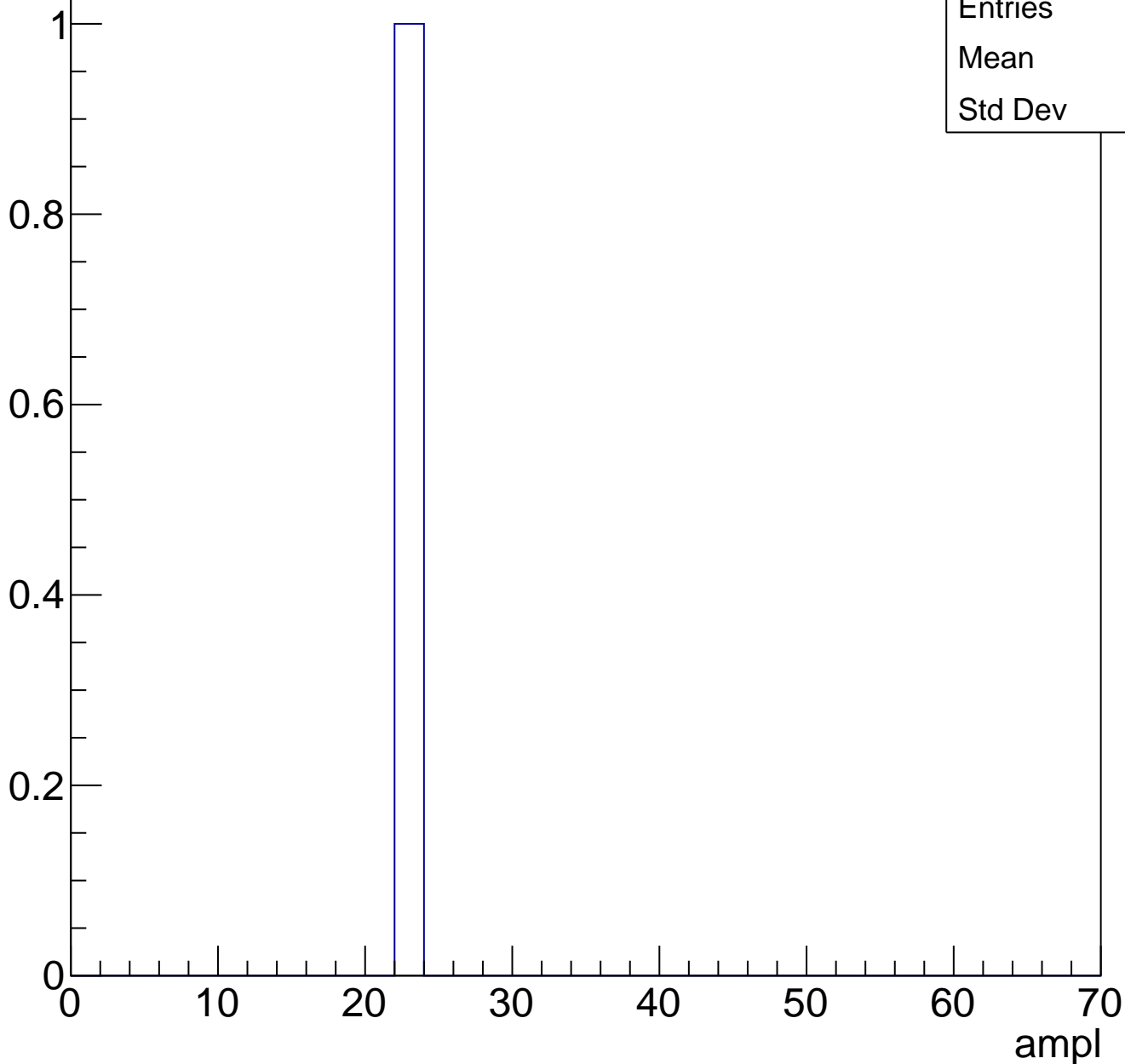
Entries	7
Mean	62.57
Std Dev	0.7284



B1L001S, U21-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch28, adc0

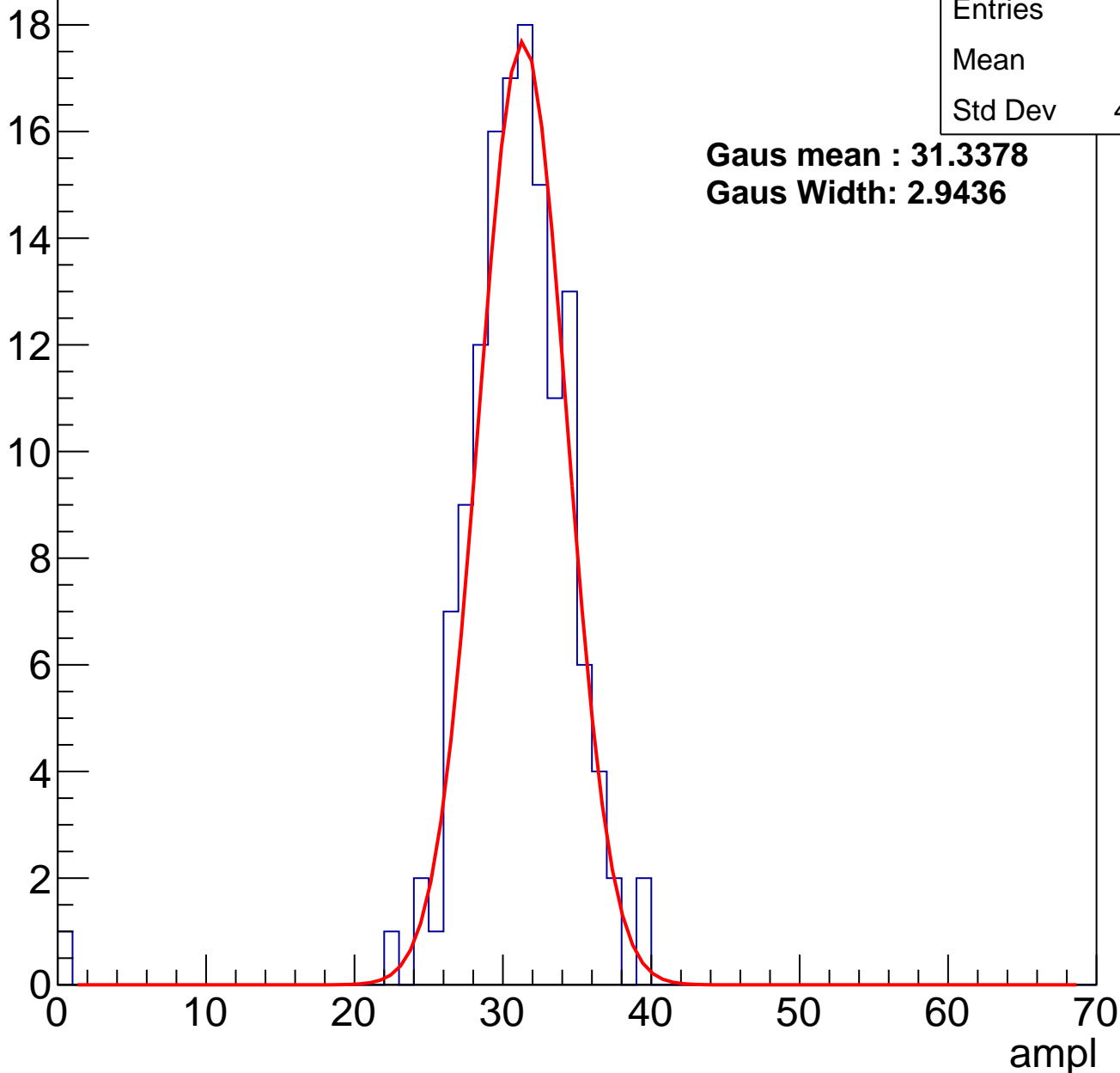
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.5
Std Dev	4.028

Gaus mean : 31.3378

Gaus Width: 2.9436

Entry



B1L001S, U21-ch28, adc1

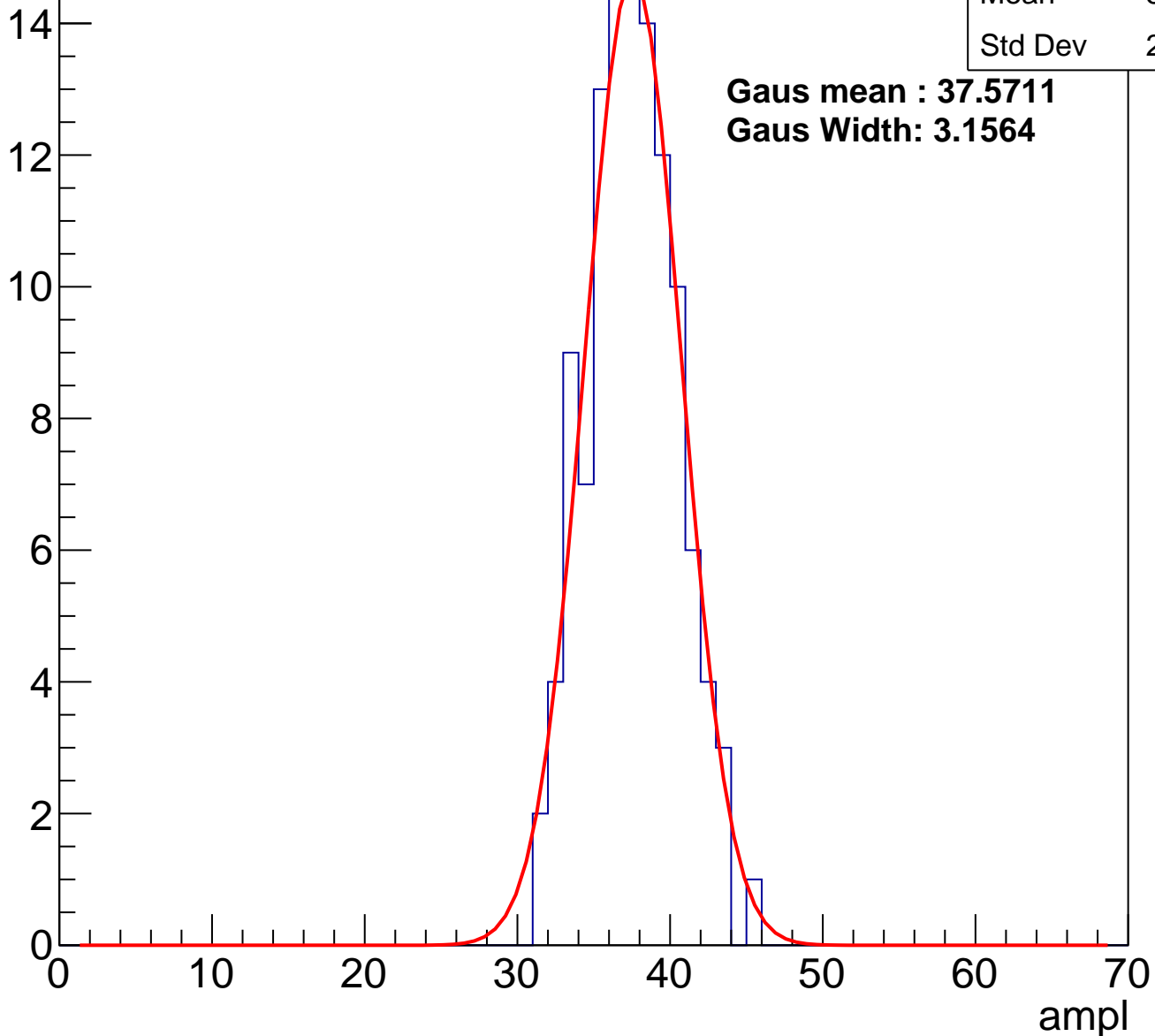
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	37.07
Std Dev	2.897

Gaus mean : 37.5711

Gaus Width: 3.1564

Entry



B1L001S, U21-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

145

Mean

43.69

Std Dev

3.526

Gaus mean : 44.3432

Gaus Width: 3.8351

0

10

20

30

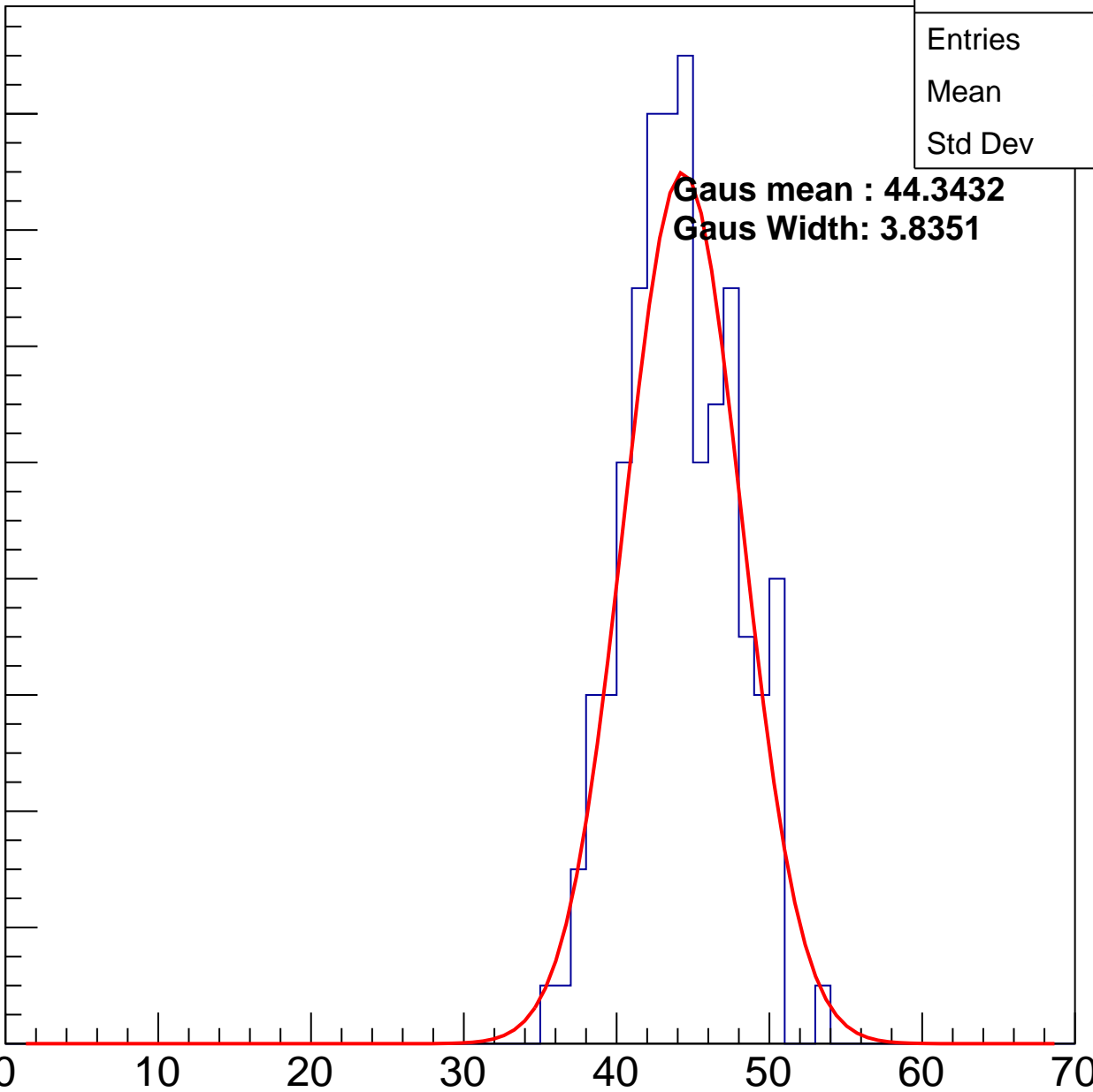
40

50

60

70

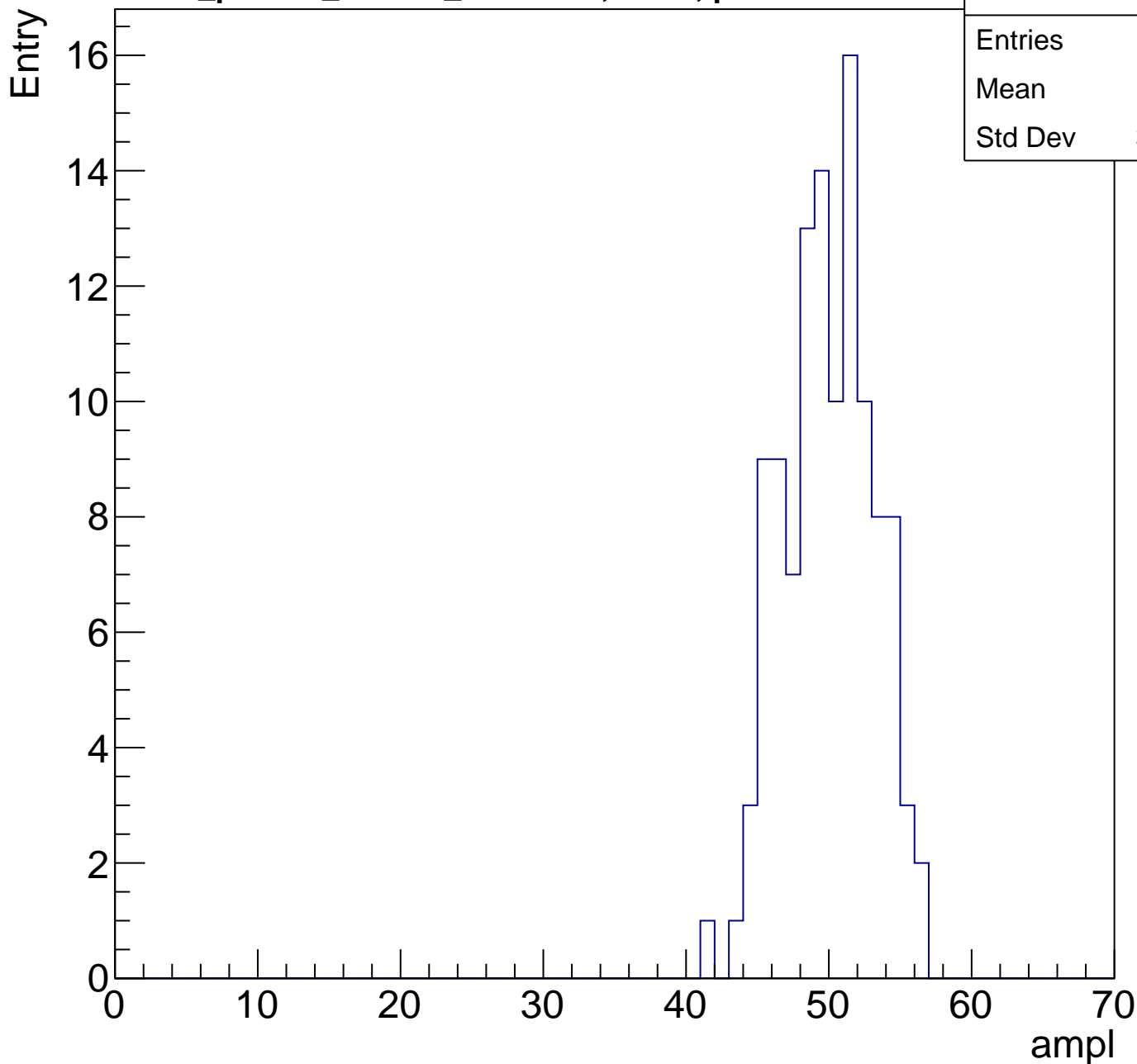
ampl



B1L001S, U21-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	49.5
Std Dev	3.121



B1L001S, U21-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

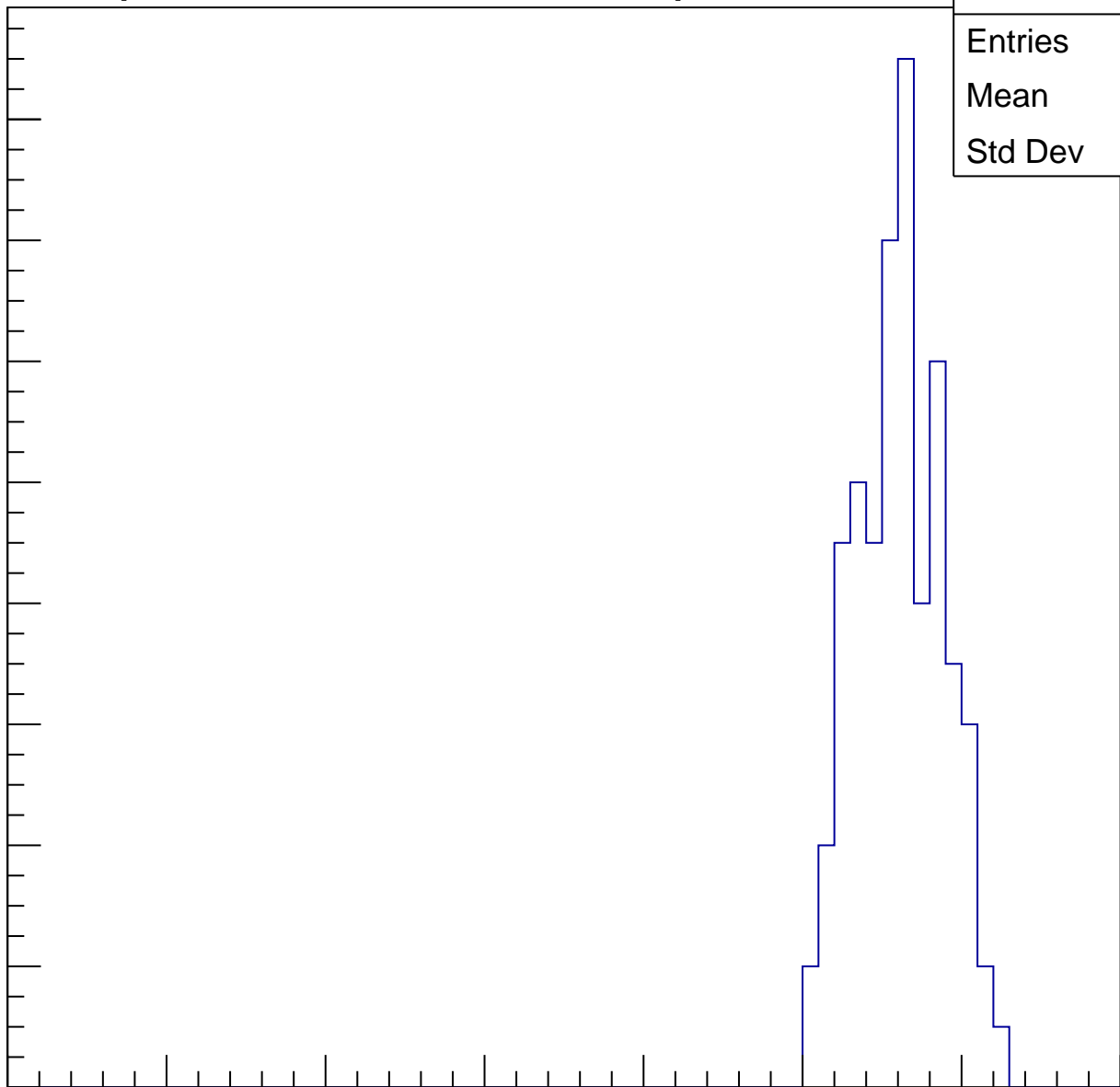
Entries	101
Mean	55.63
Std Dev	2.713

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

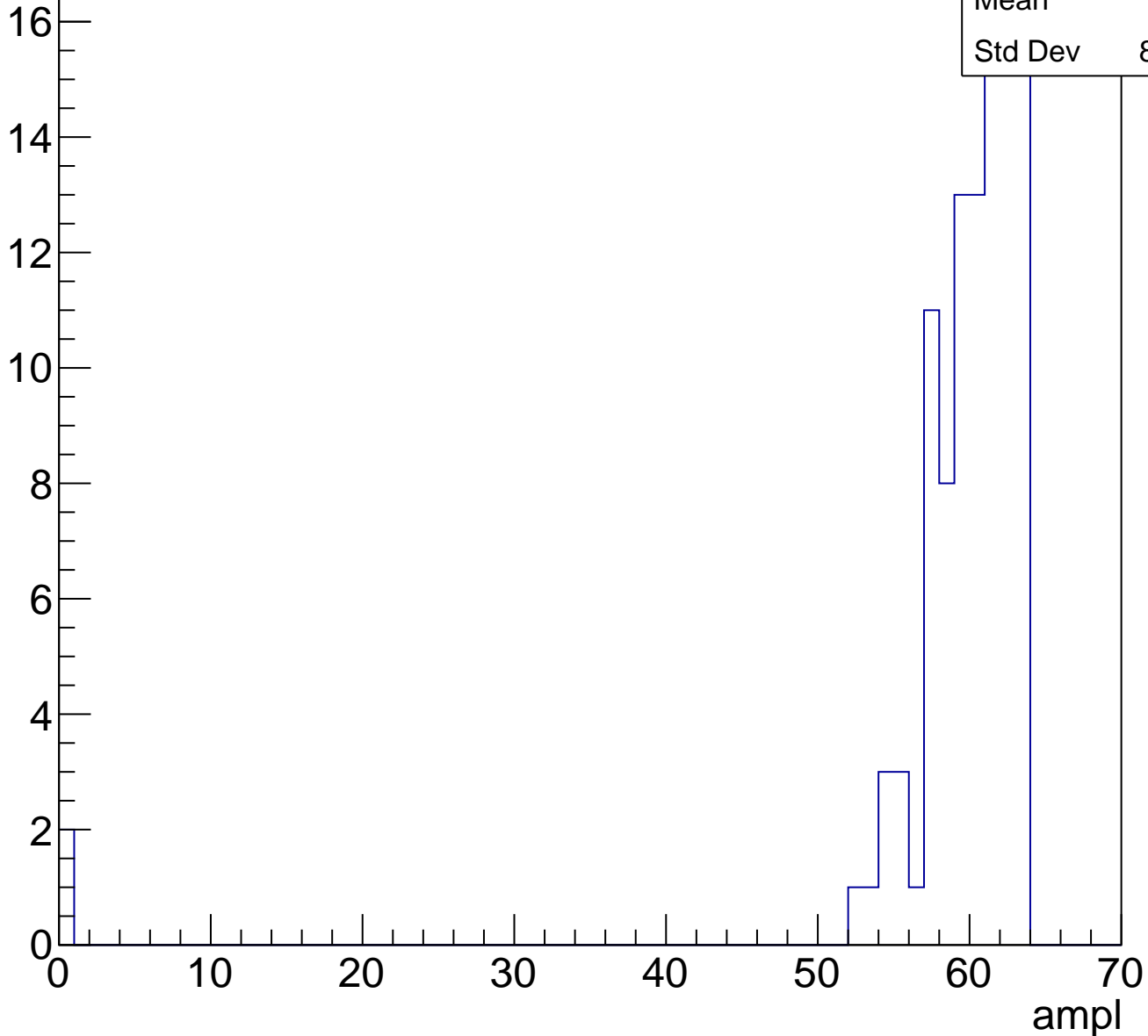


B1L001S, U21-ch28, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	58.7
Std Dev	8.568

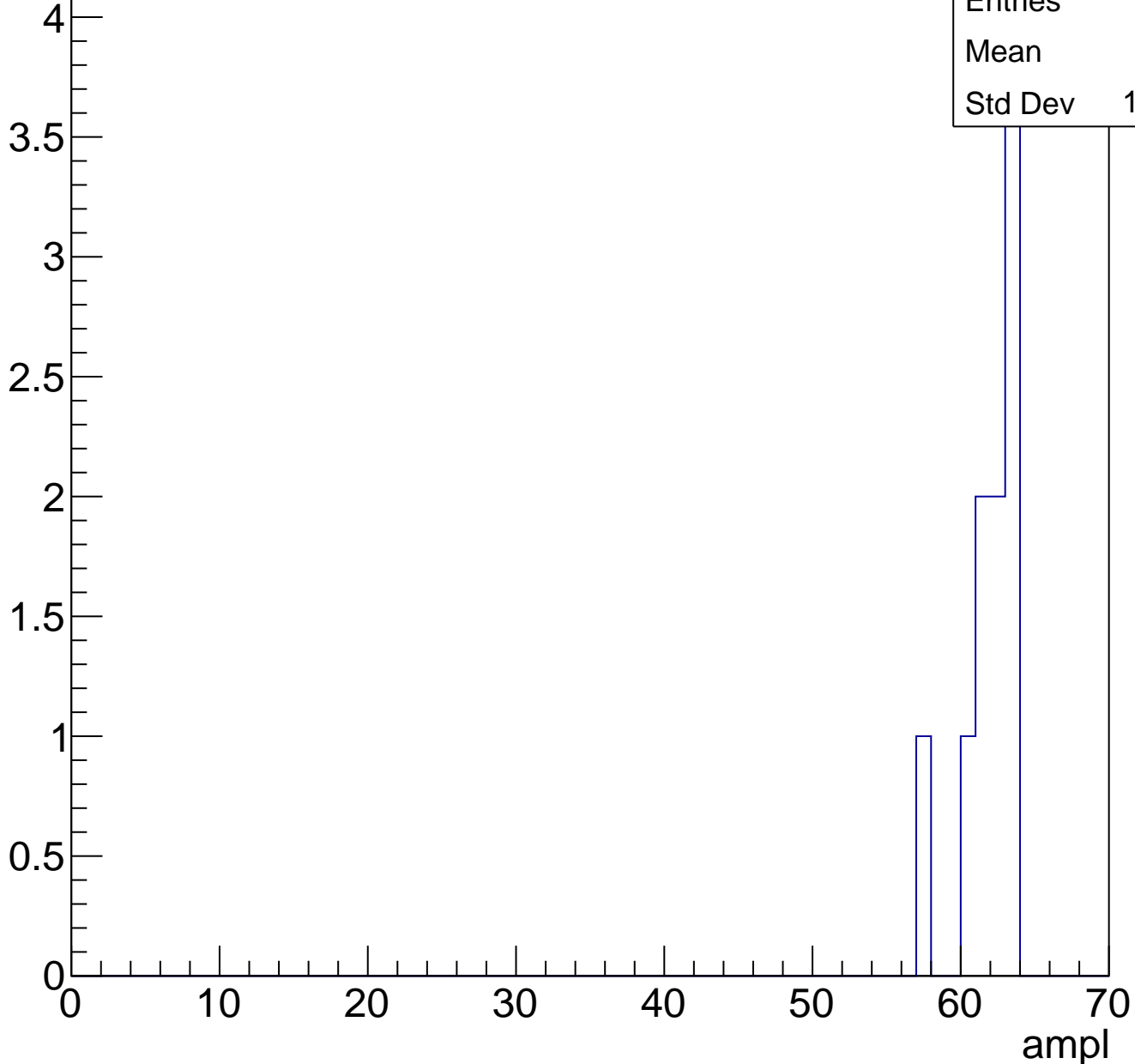
Entry



B1L001S, U21-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	10
Mean	61.5
Std Dev	1.803

B1L001S, U21-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U21-ch29, adc0

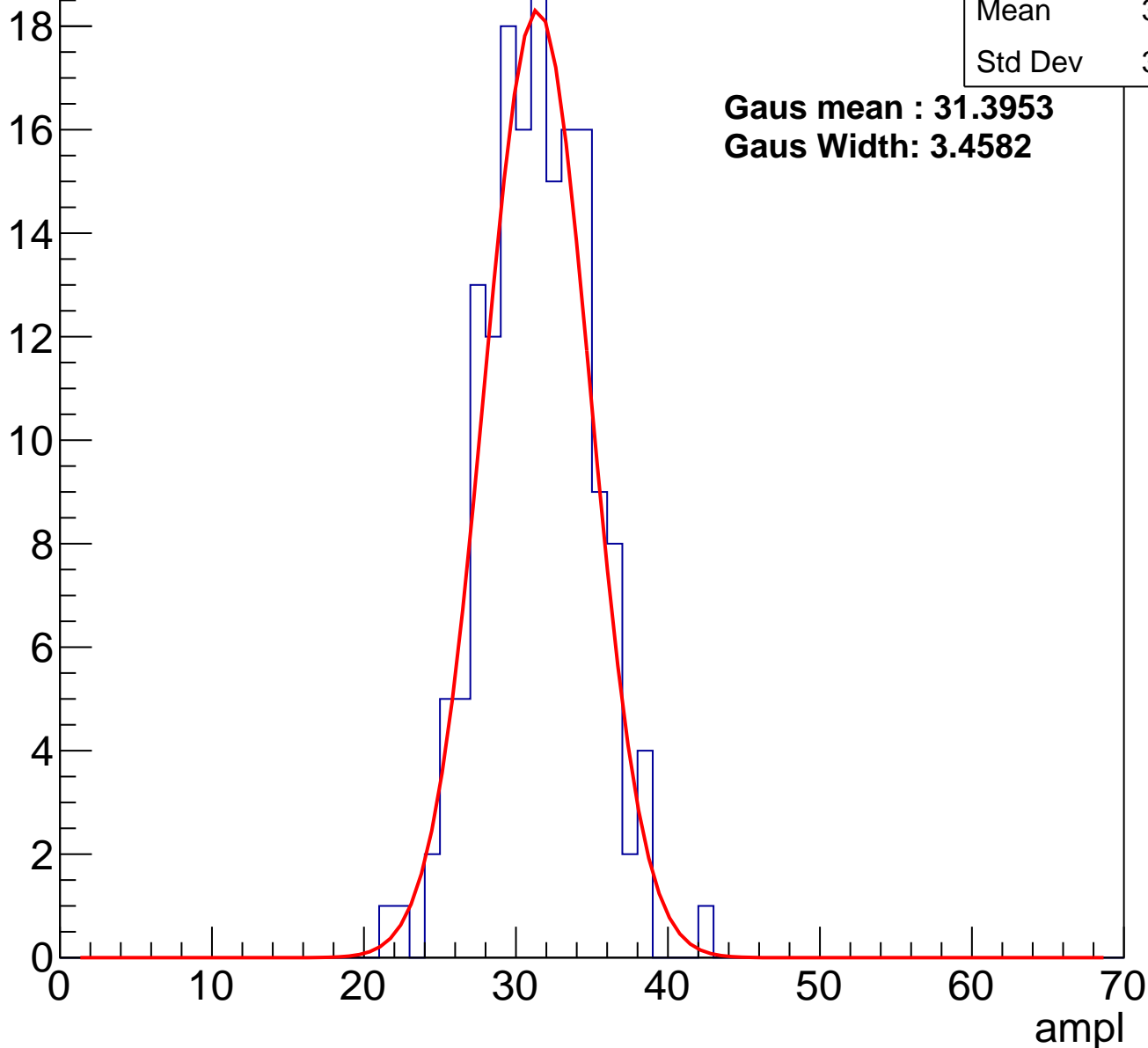
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	30.96
Std Dev	3.453

Gaus mean : 31.3953

Gaus Width: 3.4582

Entry



B1L001S, U21-ch29, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	38.02
Std Dev	3.02

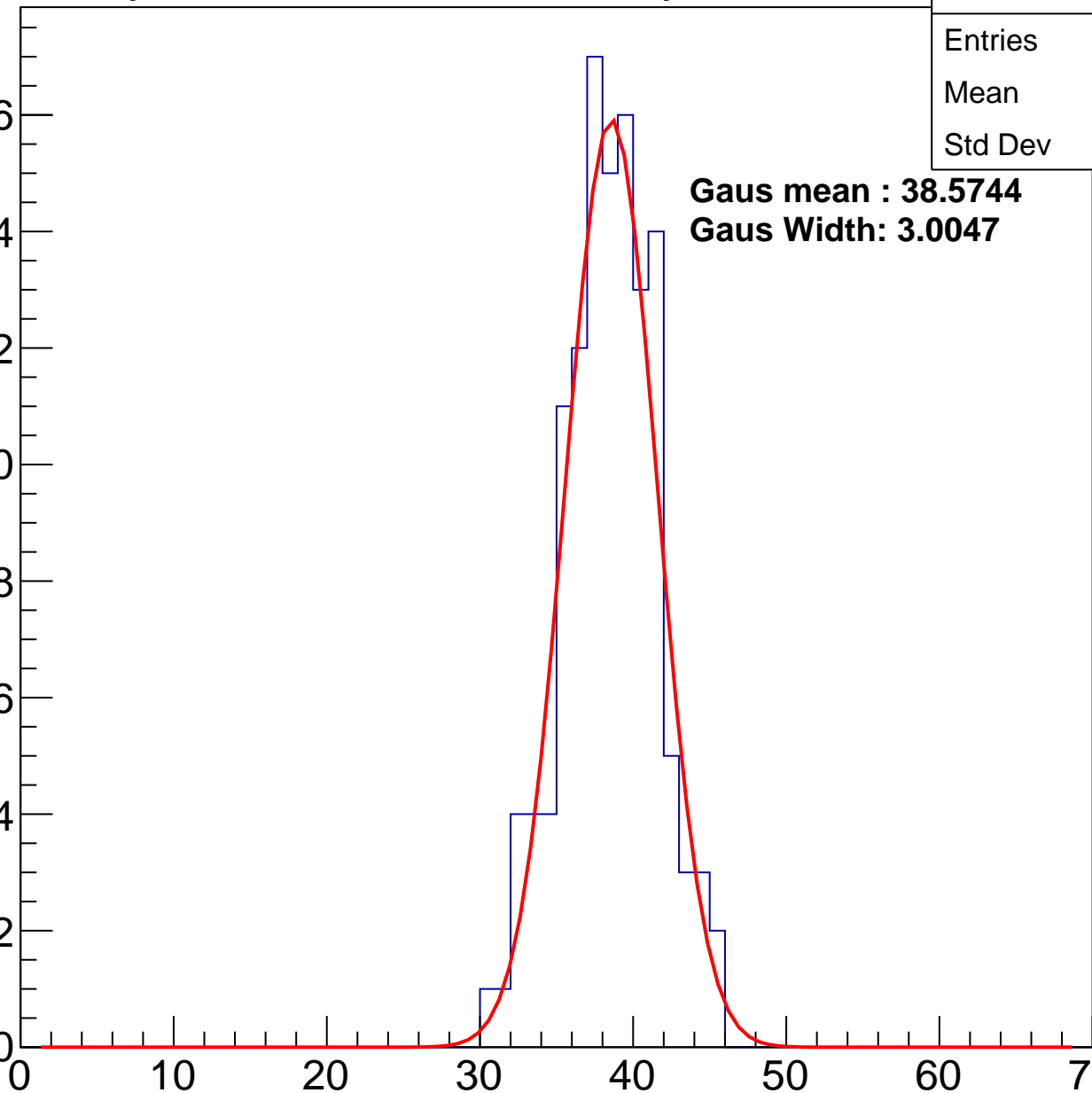
Gaus mean : 38.5744

Gaus Width: 3.0047

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch29, adc2

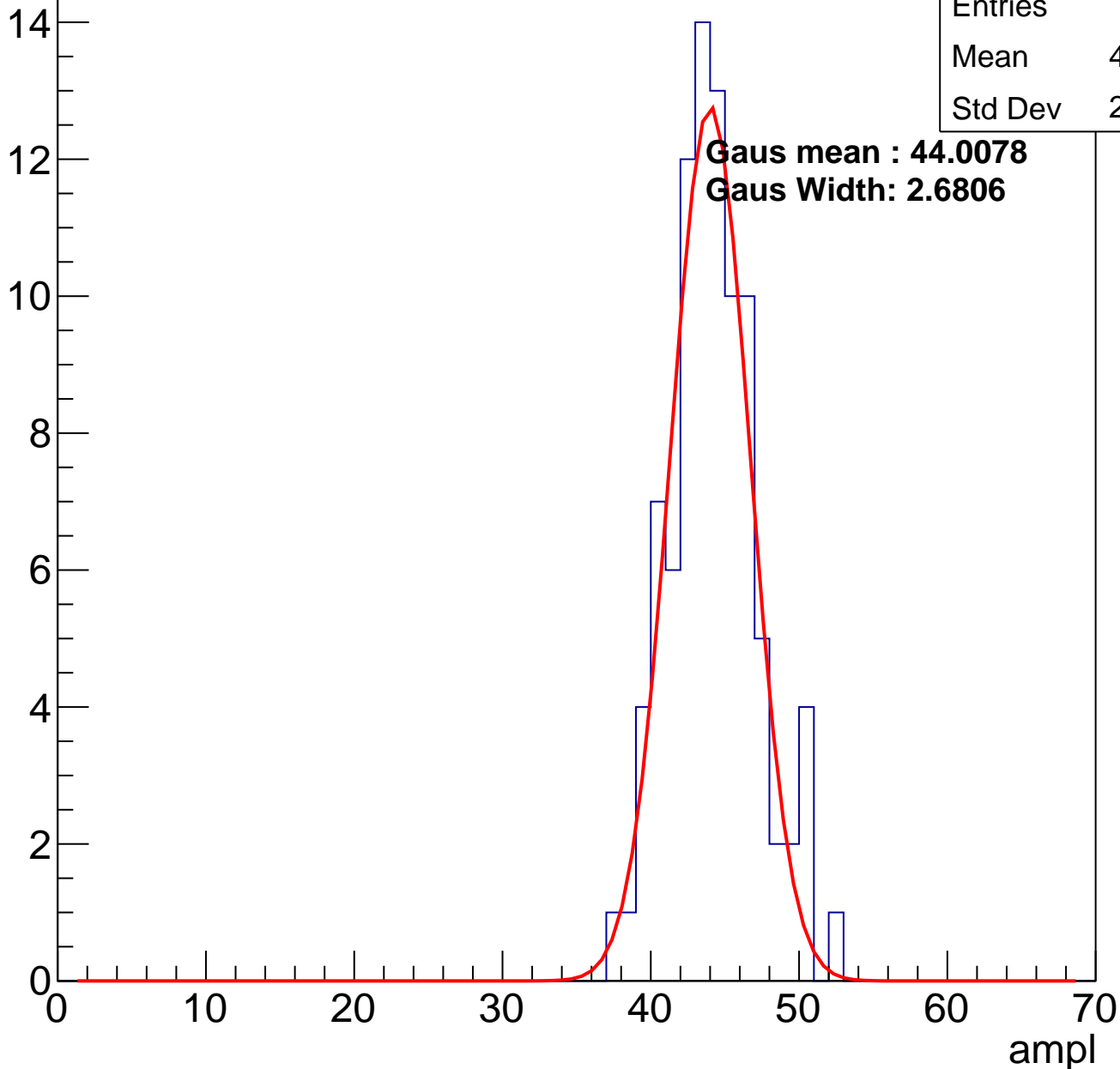
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	92
Mean	43.76
Std Dev	2.935

Gaus mean : 44.0078

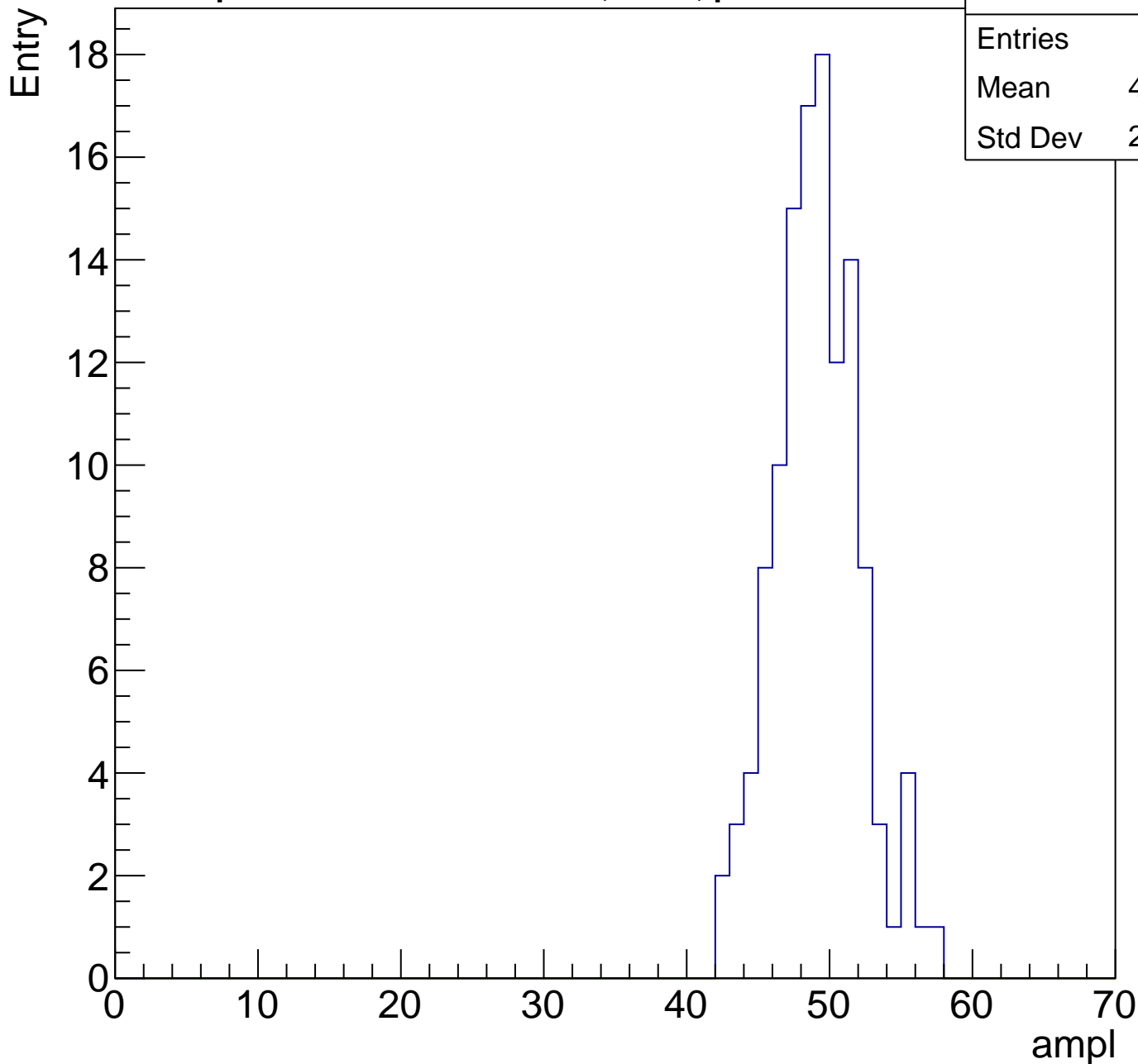
Gaus Width: 2.6806



B1L001S, U21-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

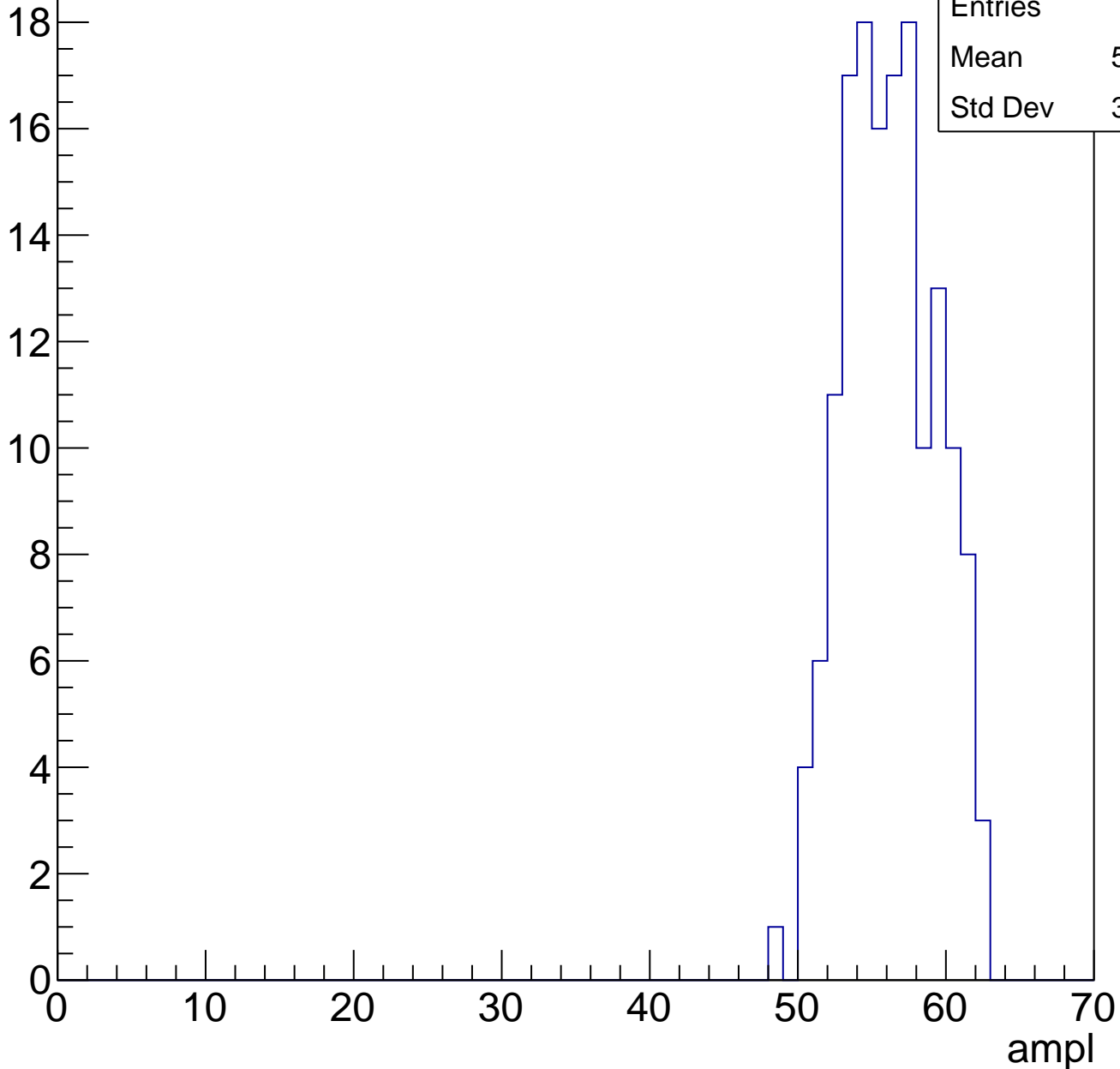
Entries	121
Mean	48.66
Std Dev	2.956



B1L001S, U21-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

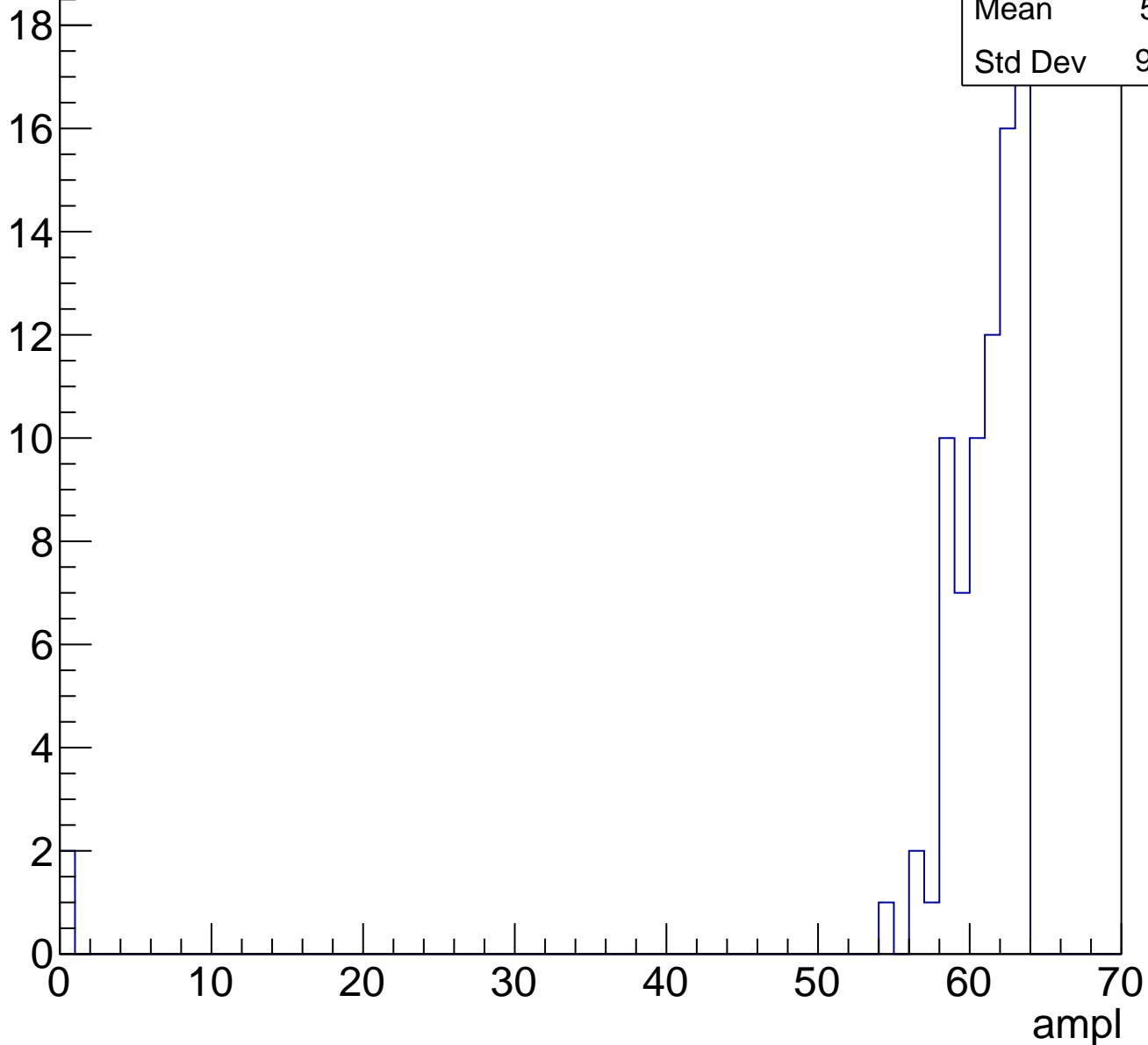


Entries	152
Mean	55.78
Std Dev	3.046

B1L001S, U21-ch29, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

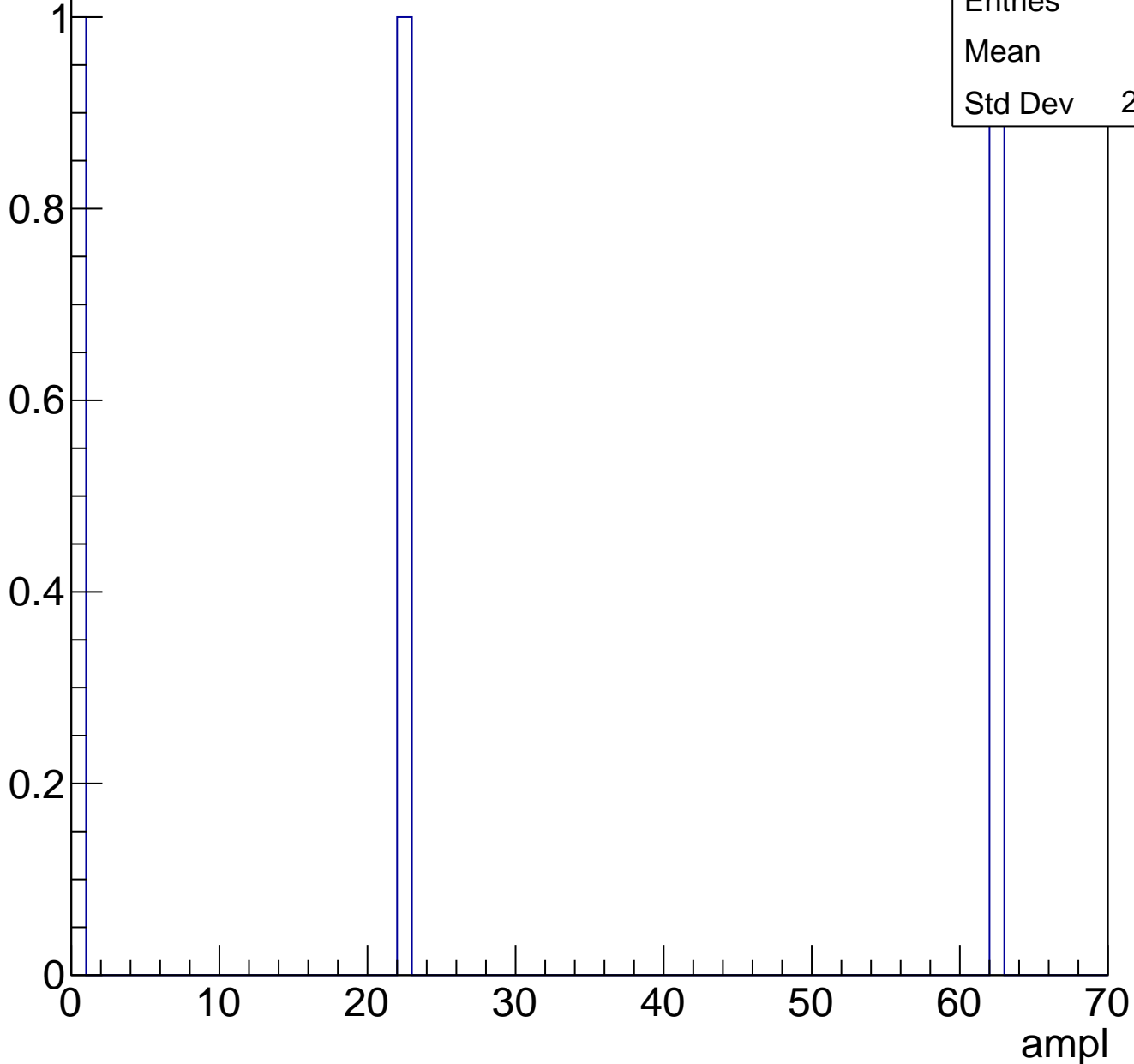
Entry



B1L001S, U21-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	25.66

B1L001S, U21-ch30, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	29.69
Std Dev	3.511

Gaus mean : 30.3737

Gaus Width: 3.7218

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

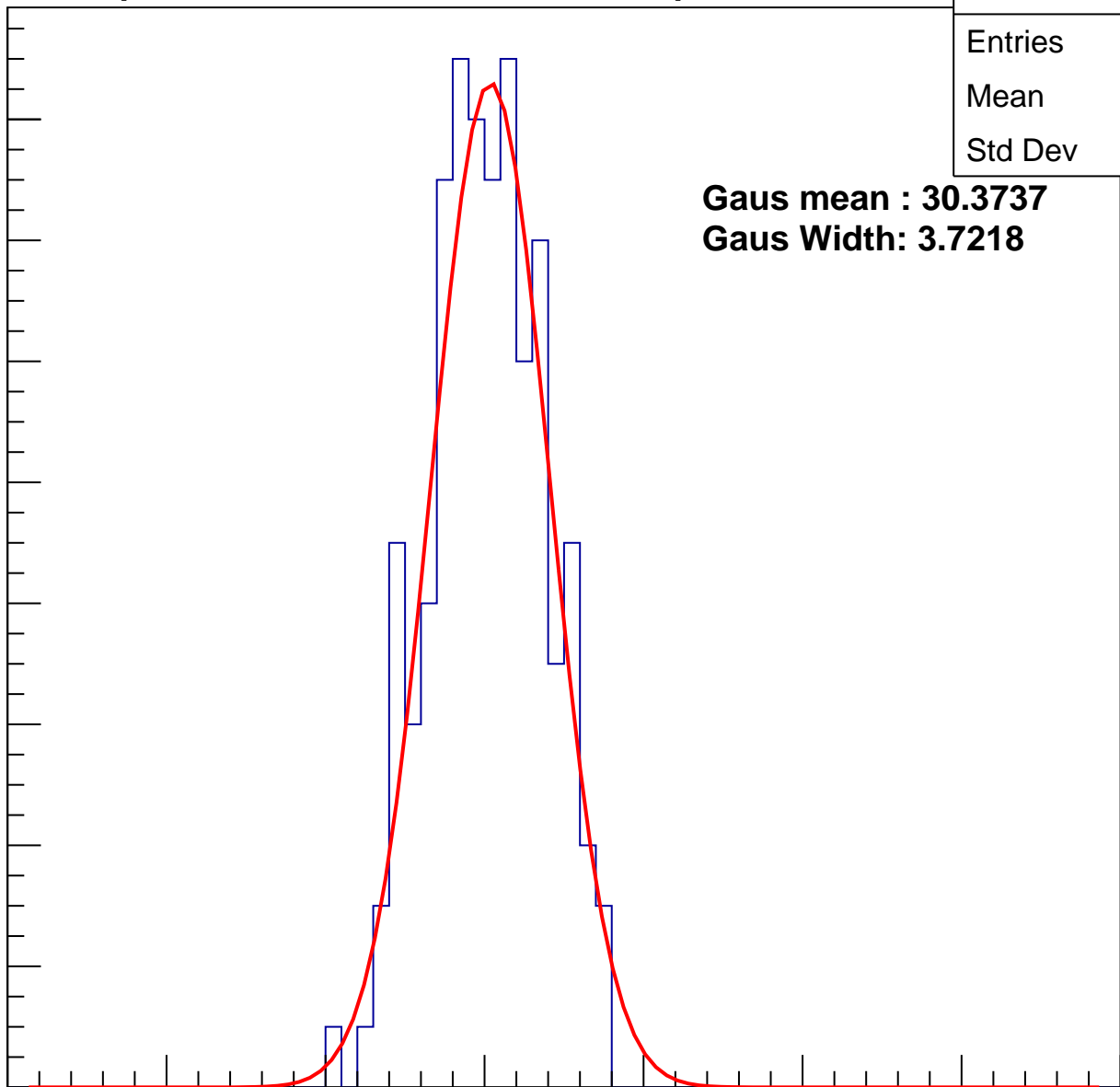
40

50

60

70

ampl



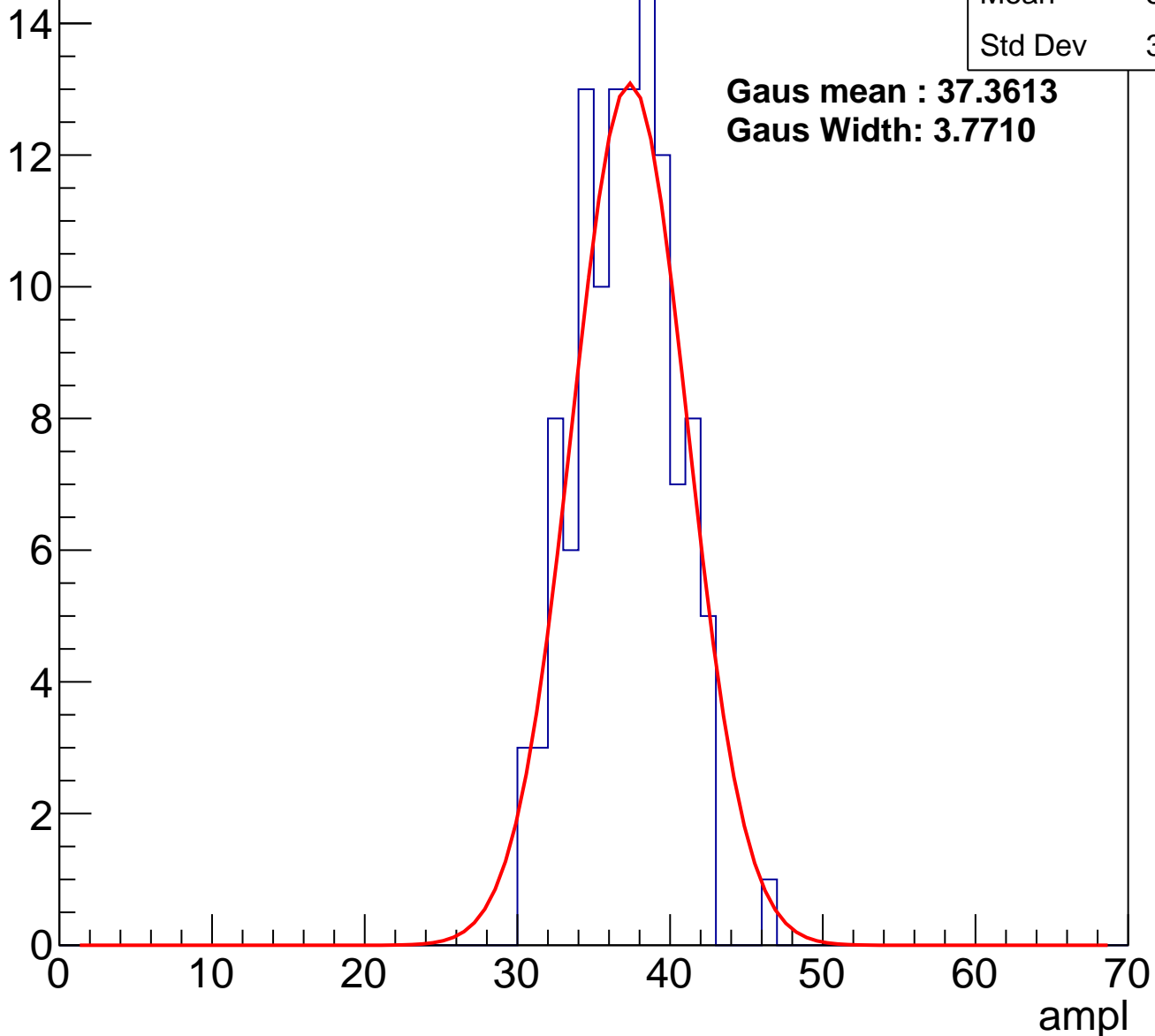
B1L001S, U21-ch30, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	36.58
Std Dev	3.149

Gaus mean : 37.3613
Gaus Width: 3.7710

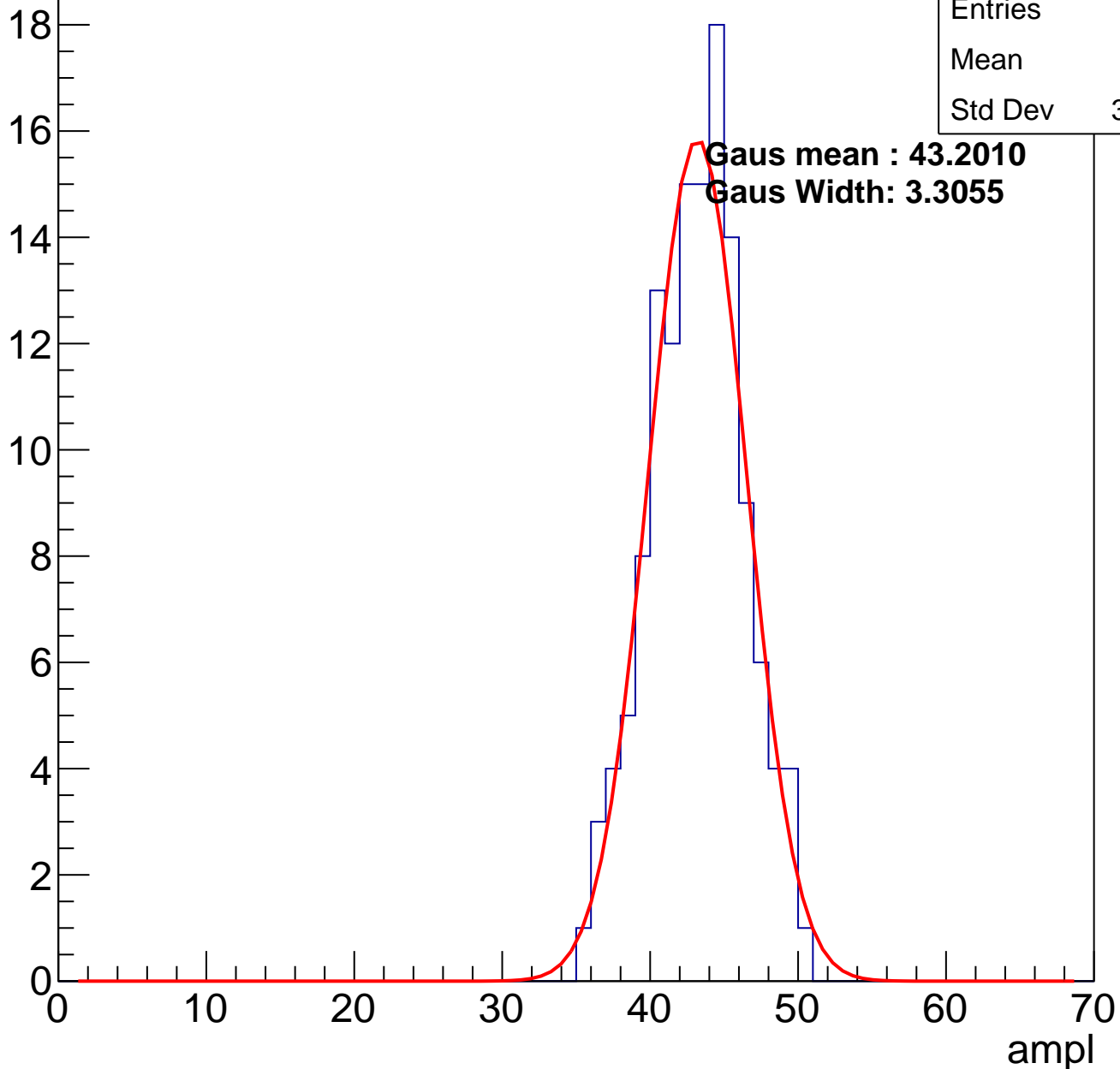
Entry



B1L001S, U21-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

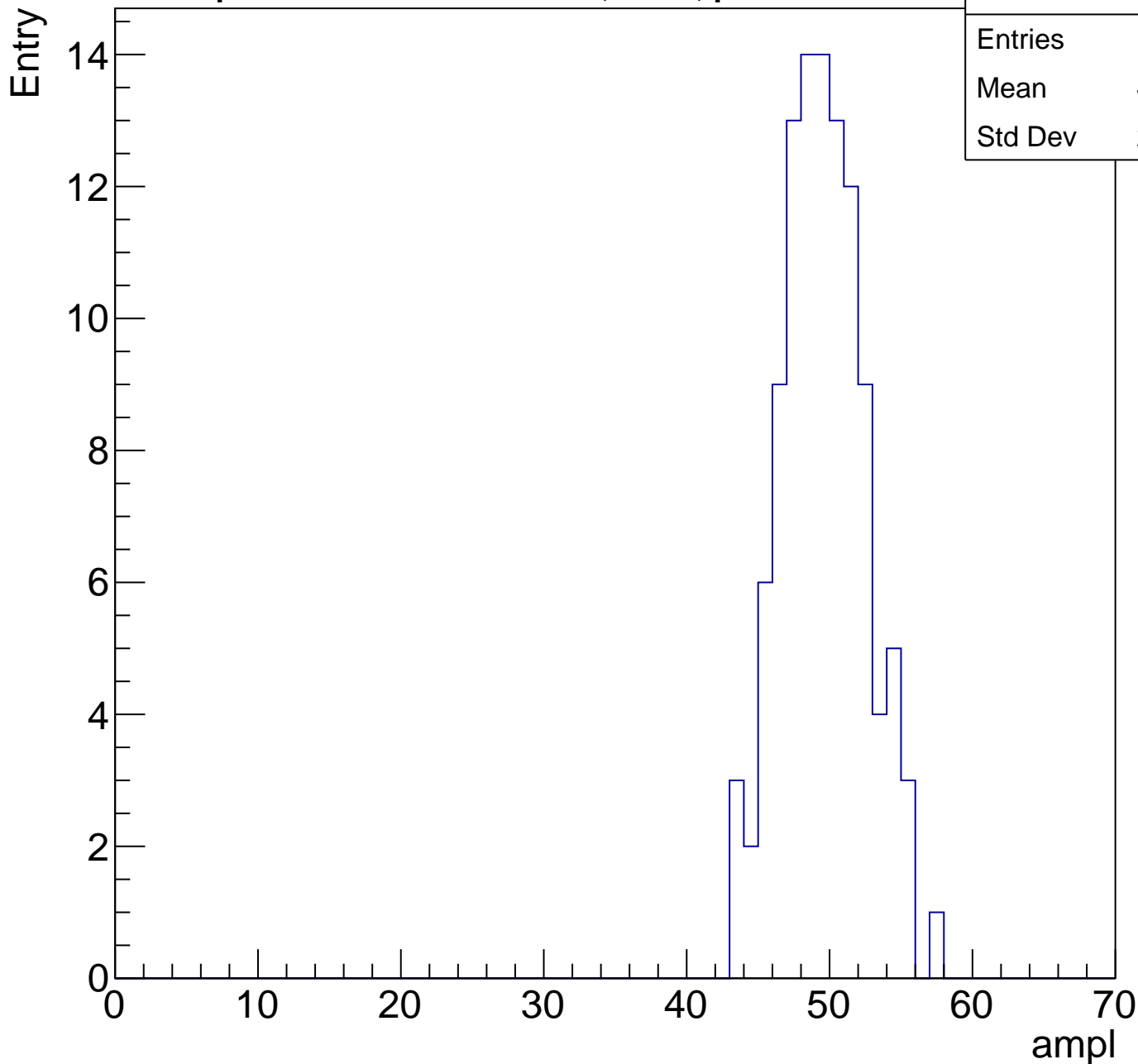
Entry



B1L001S, U21-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

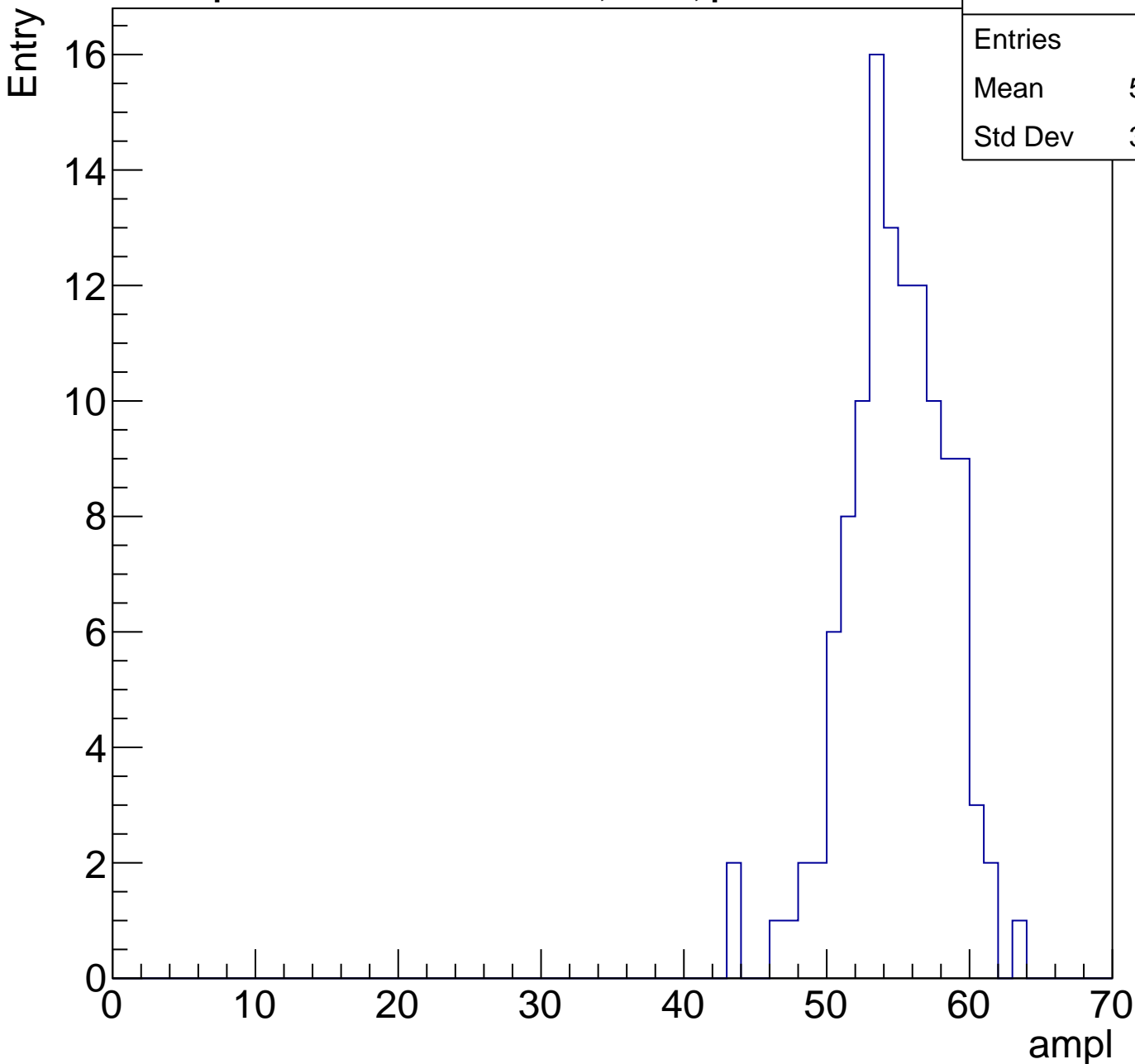
Entries	108
Mean	49.11
Std Dev	2.891



B1L001S, U21-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.38
Std Dev	3.538



B1L001S, U21-ch30, adc5

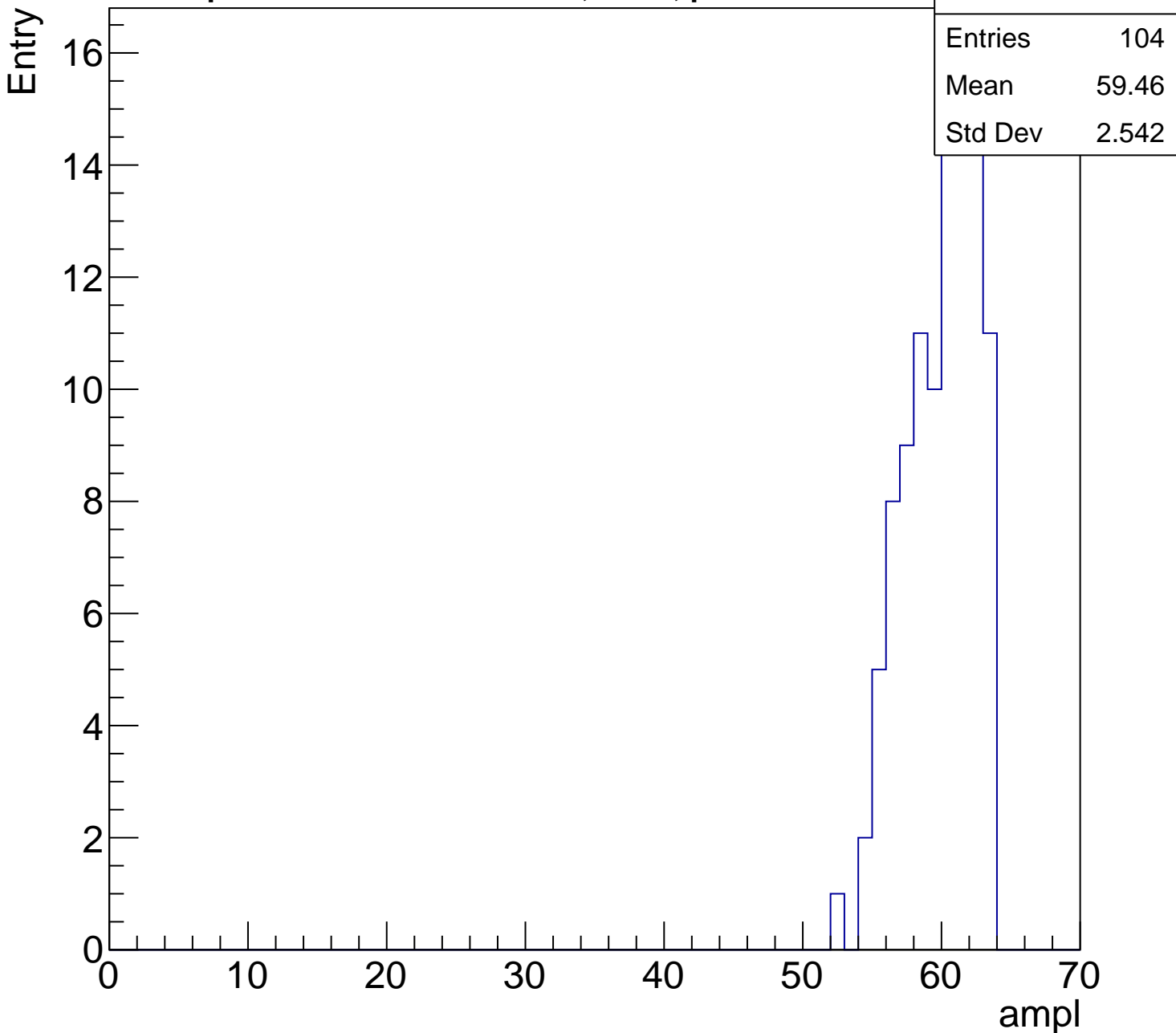
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	59.46
Std Dev	2.542

ampl

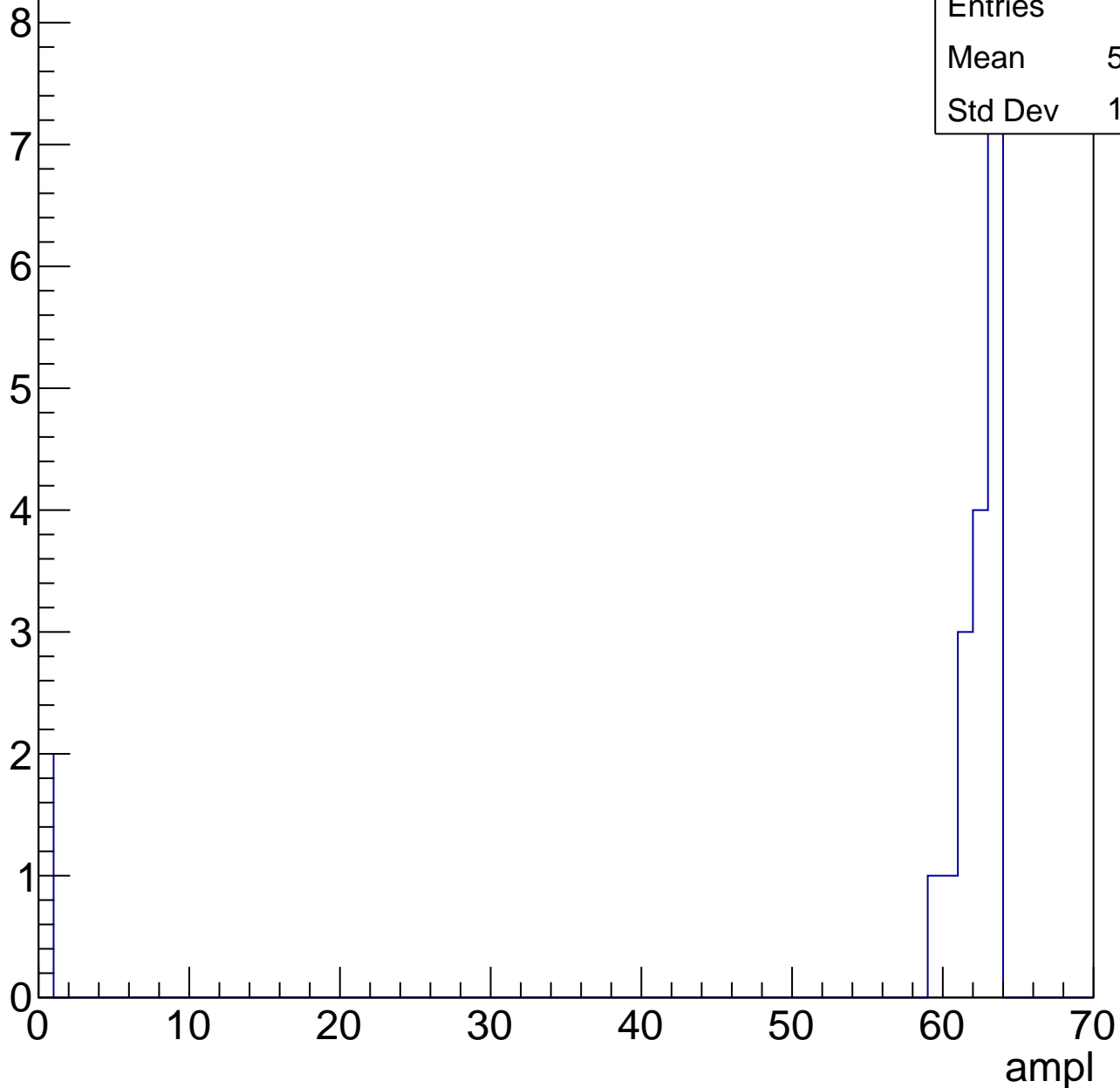


B1L001S, U21-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	55.47
Std Dev	19.06



B1L001S, U21-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

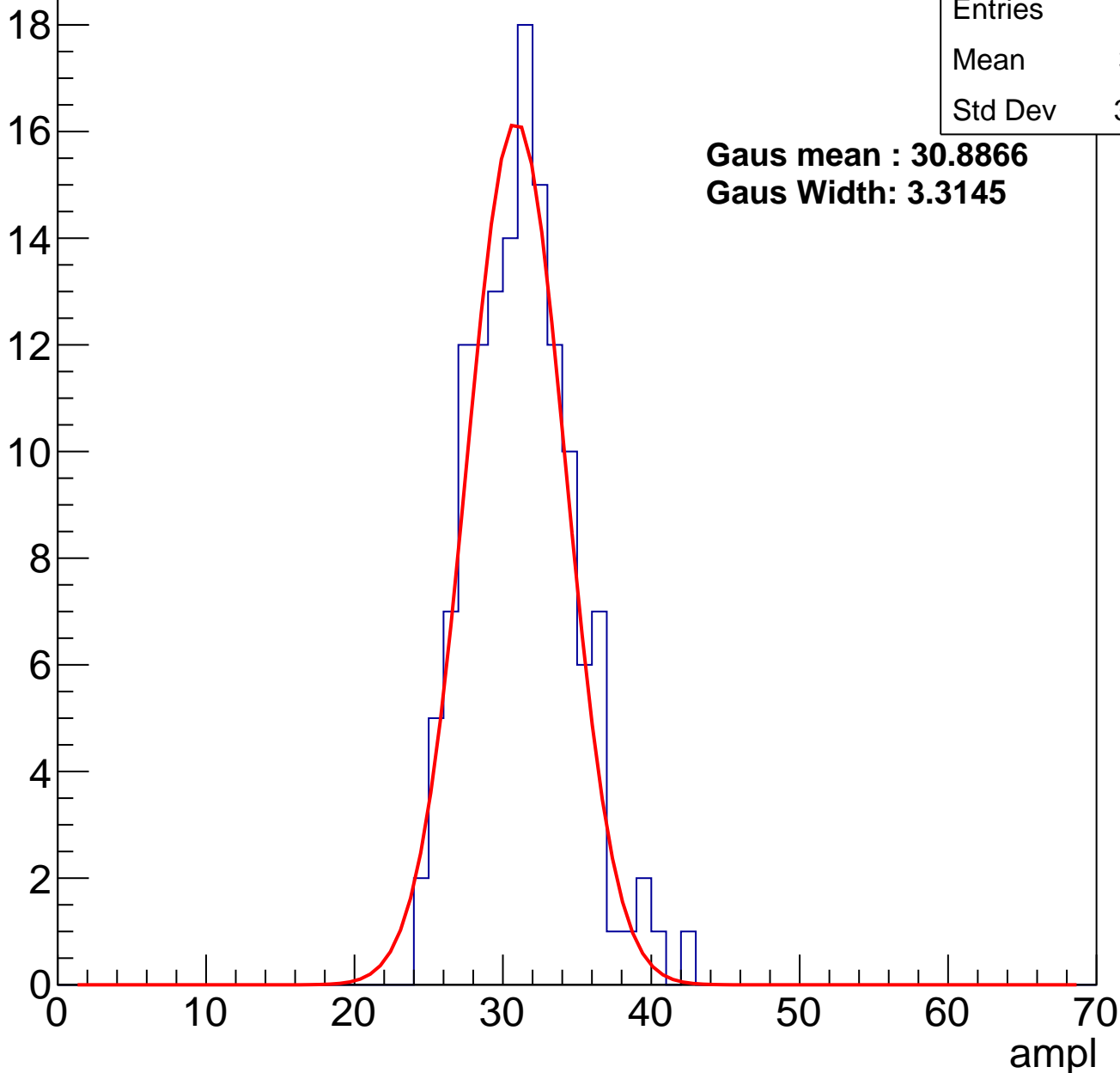


Entries	2
Mean	11
Std Dev	11

B1L001S, U21-ch31, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch31, adc1

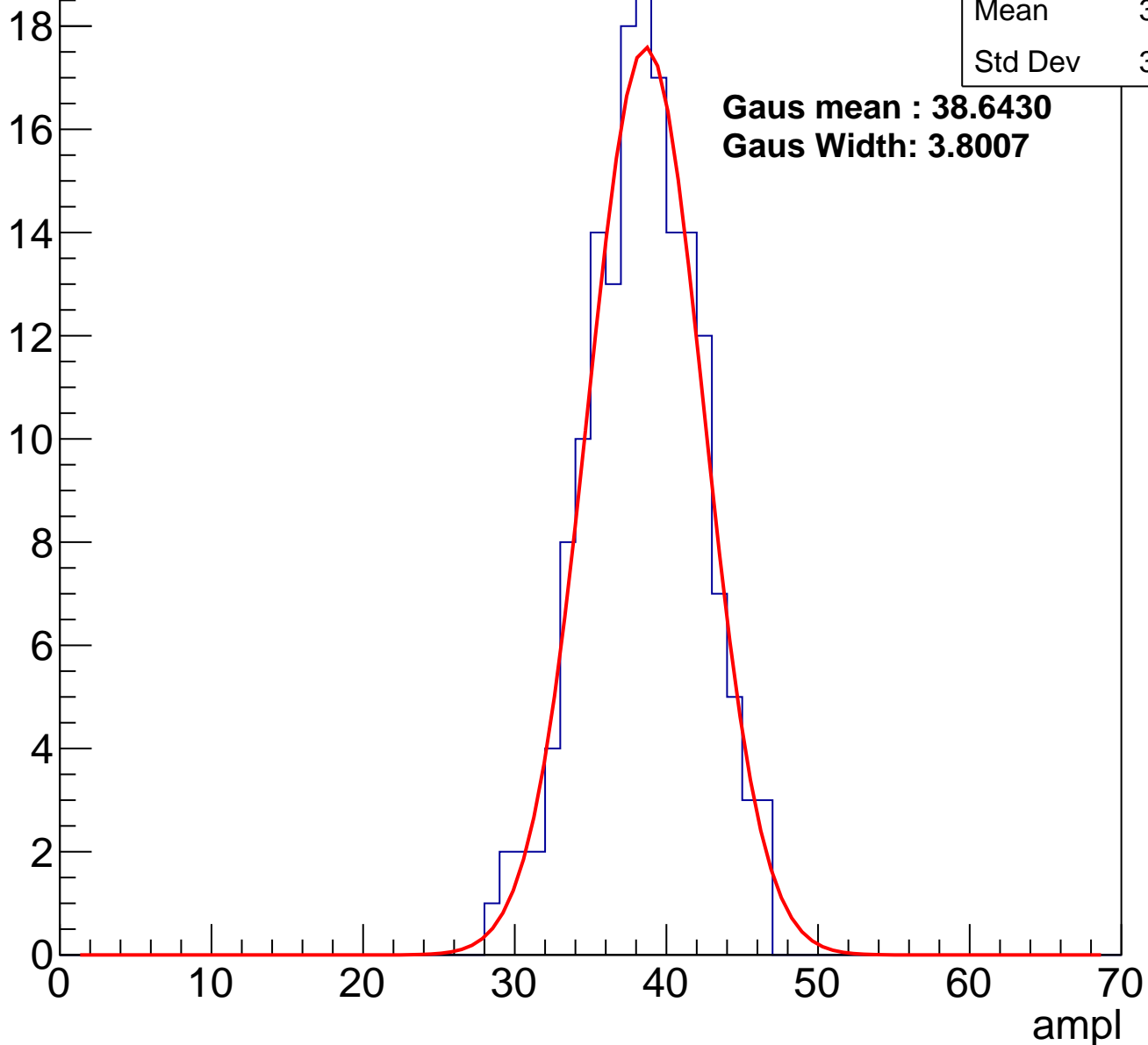
calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	37.98
Std Dev	3.678

Gaus mean : 38.6430

Gaus Width: 3.8007

Entry

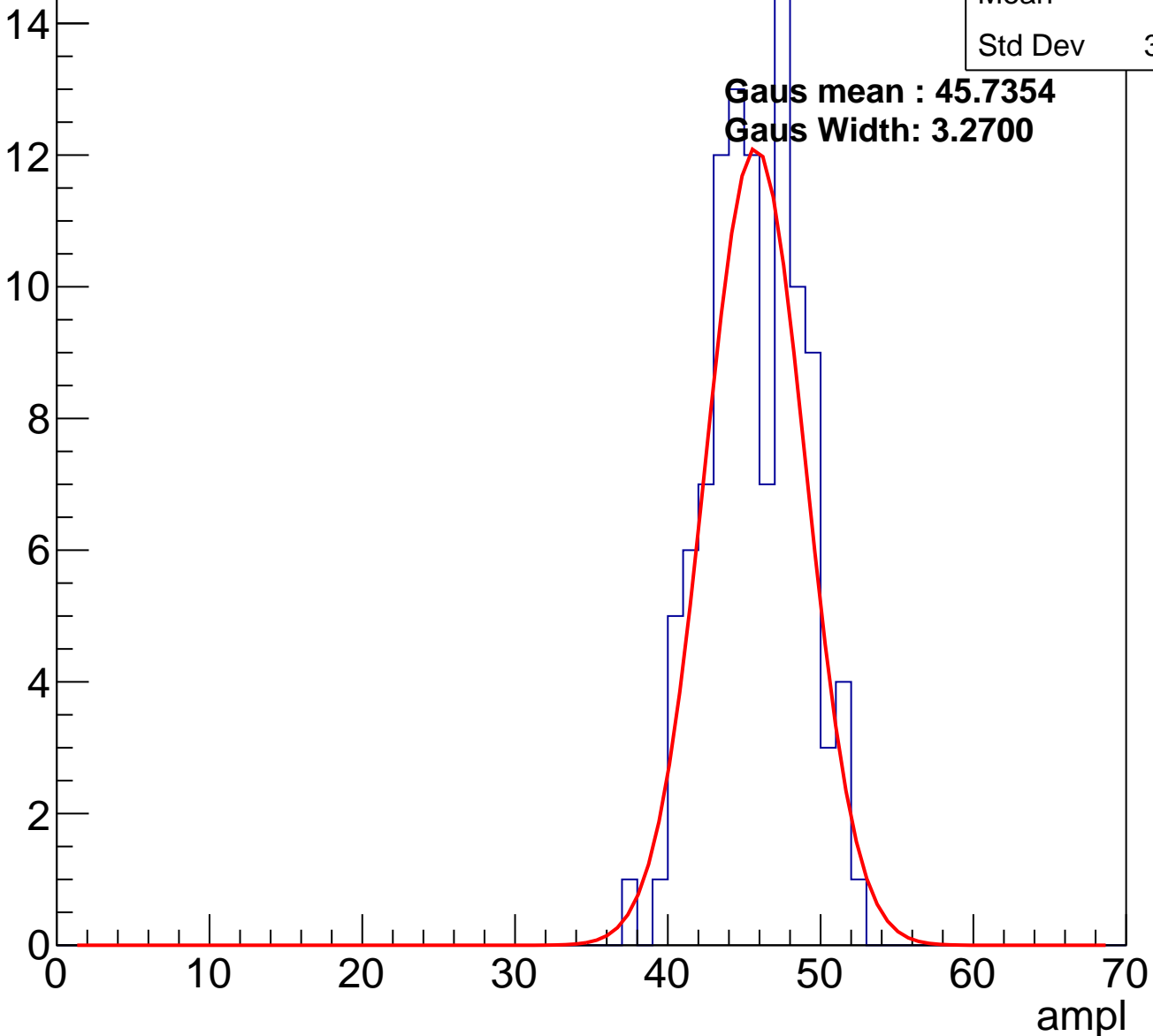


B1L001S, U21-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	45.26
Std Dev	3.082

Entry



B1L001S, U21-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

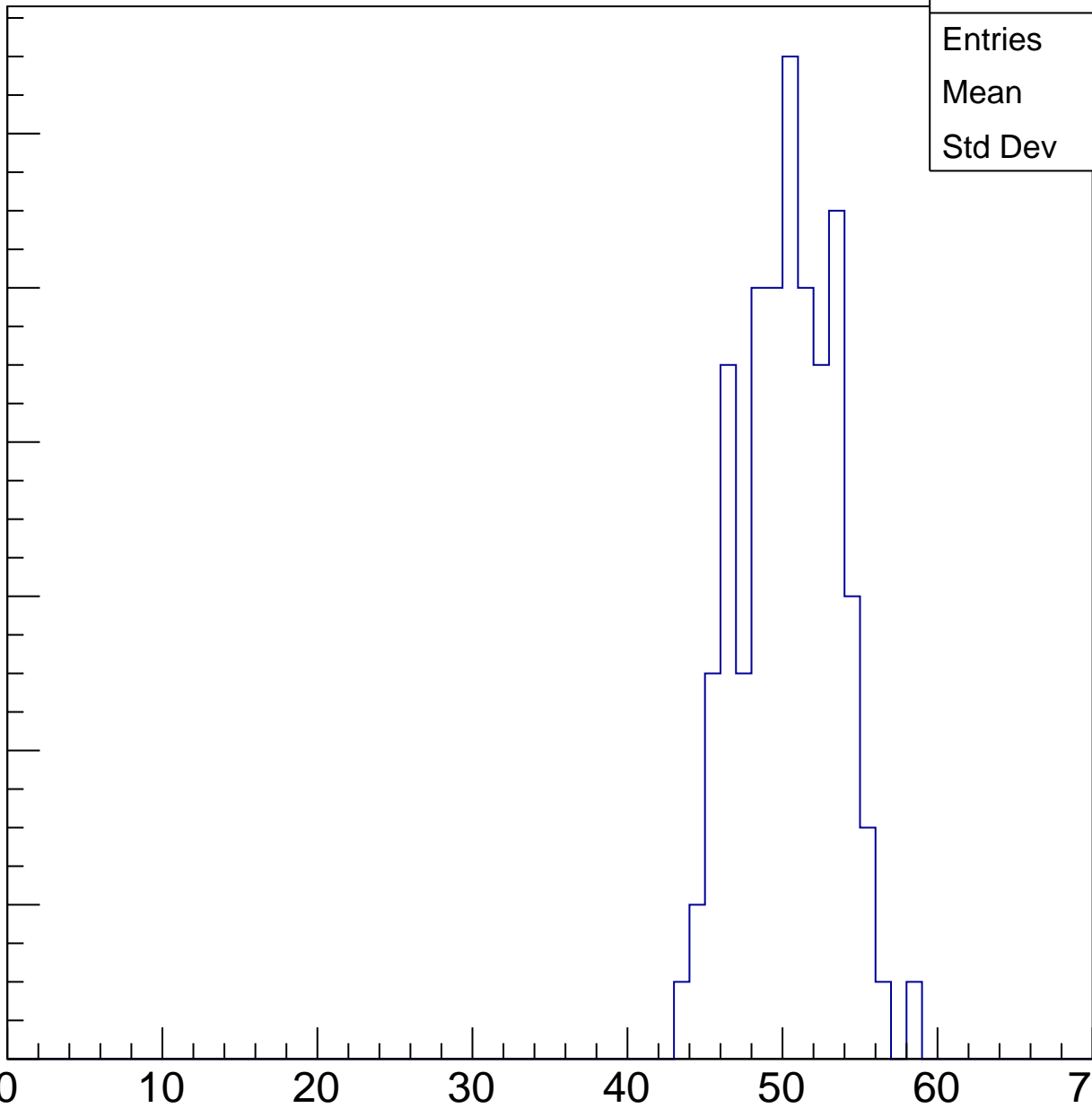
2

0

Entries	96
Mean	49.89
Std Dev	3.061

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch31, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	56.2
Std Dev	3.36

Entry

16

14

12

10

8

6

4

2

0

0

10

20

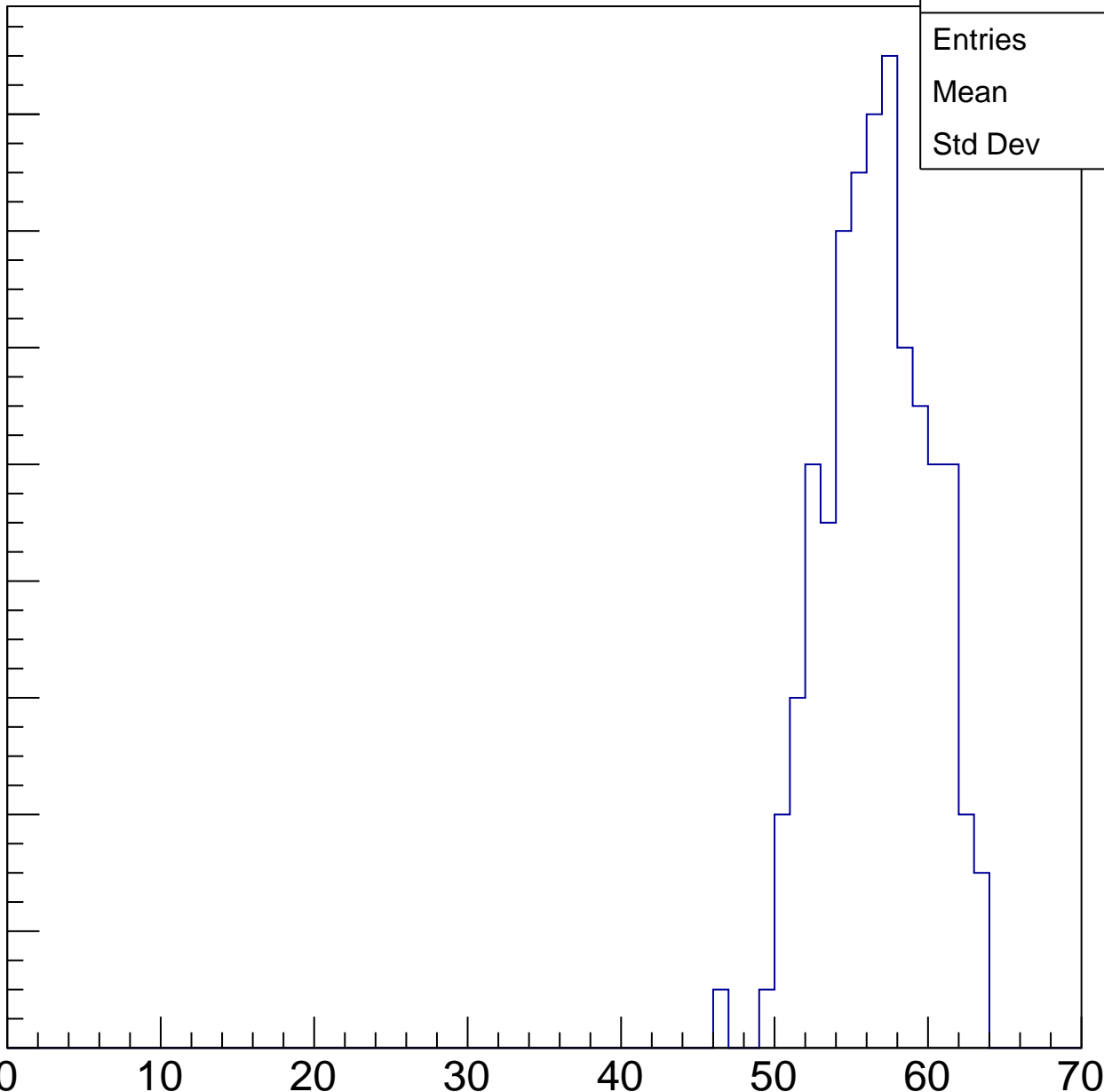
30

40

50

60

ampl



B1L001S, U21-ch31, adc5

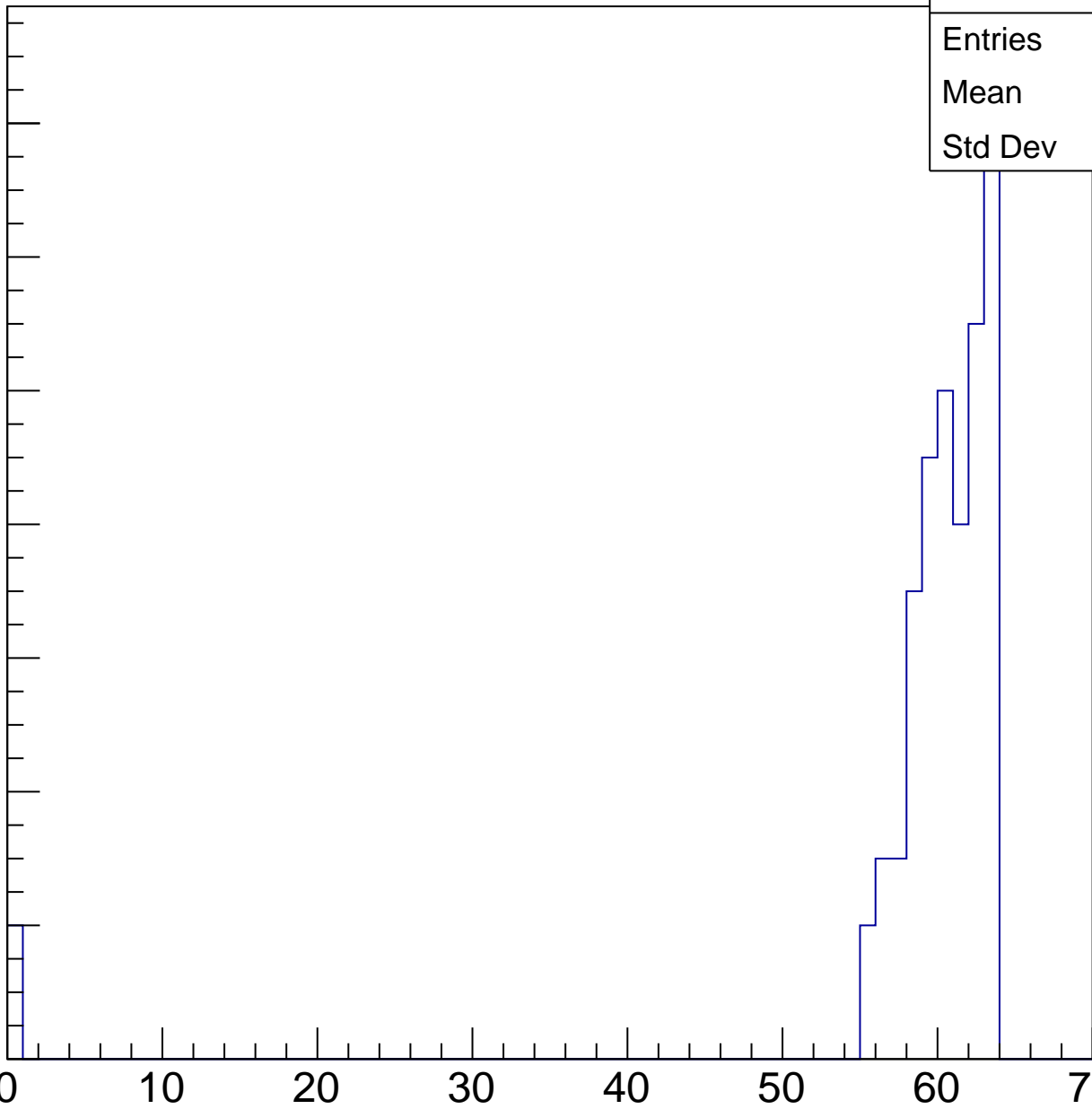
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	58.59
Std Dev	10.29

ampl

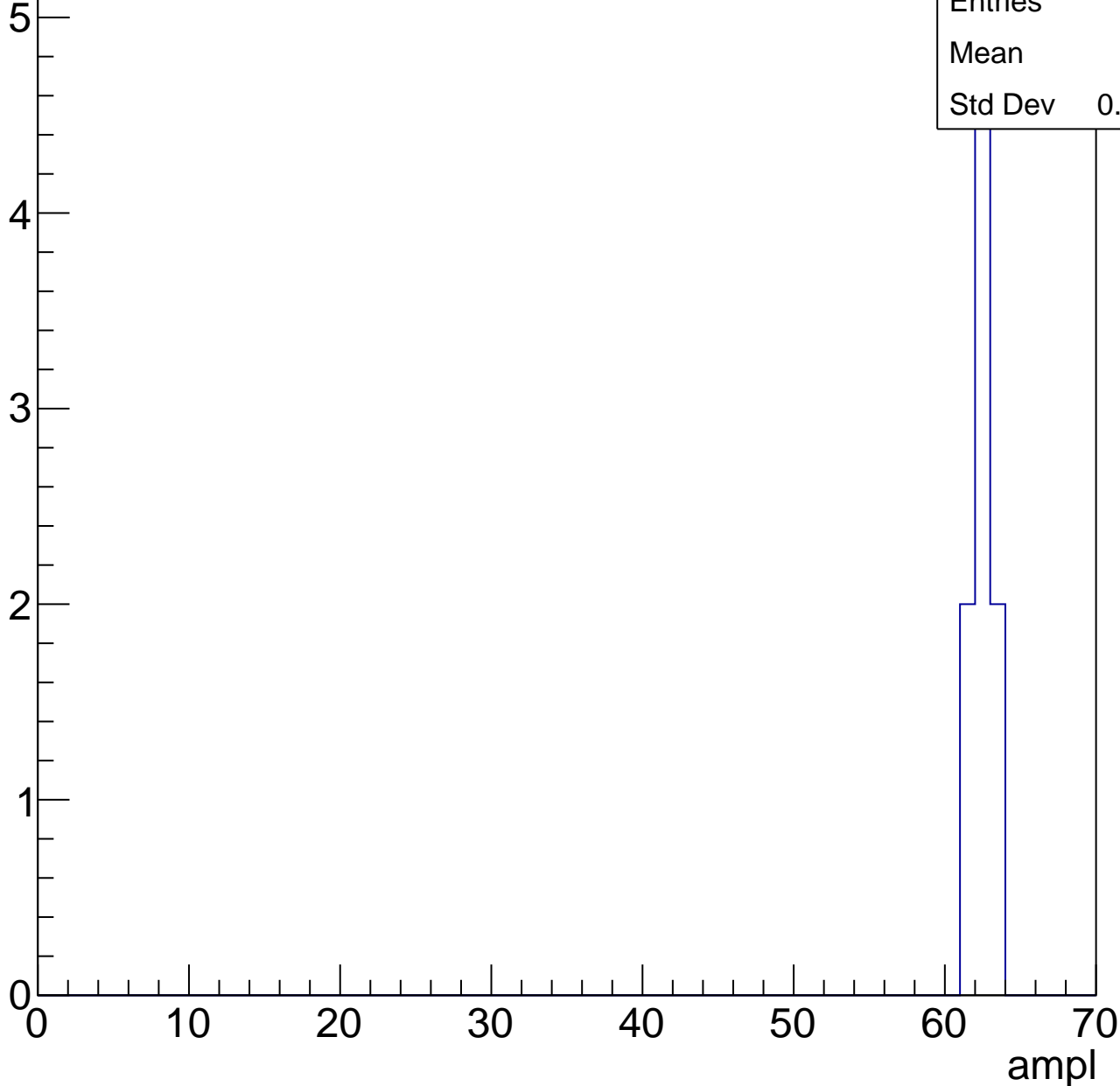


B1L001S, U21-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

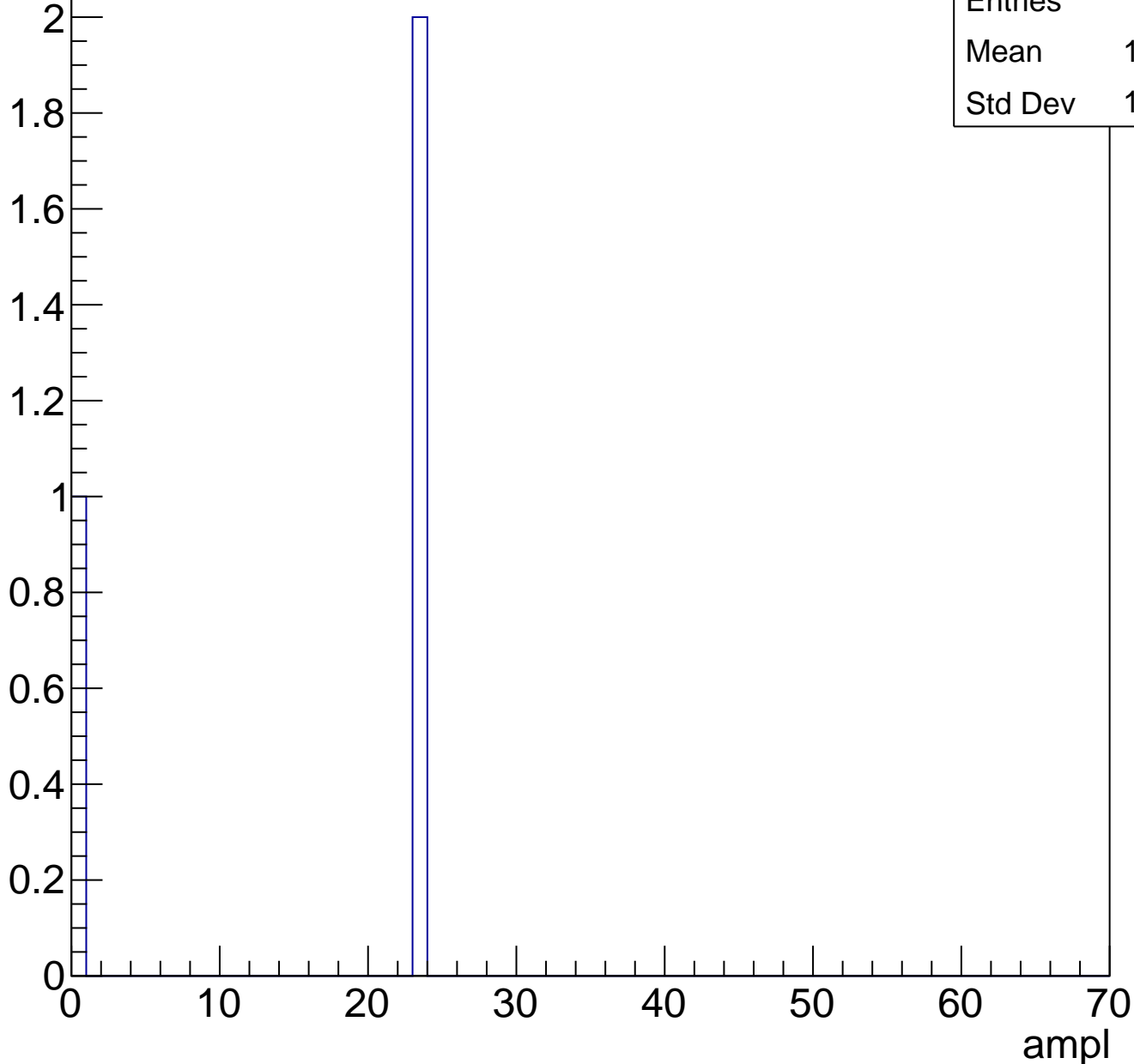
Entries	9
Mean	62
Std Dev	0.6667



B1L001S, U21-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	15.33
Std Dev	10.84

B1L001S, U21-ch32, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	28.25
Std Dev	3.054

Gaus mean : 28.6163

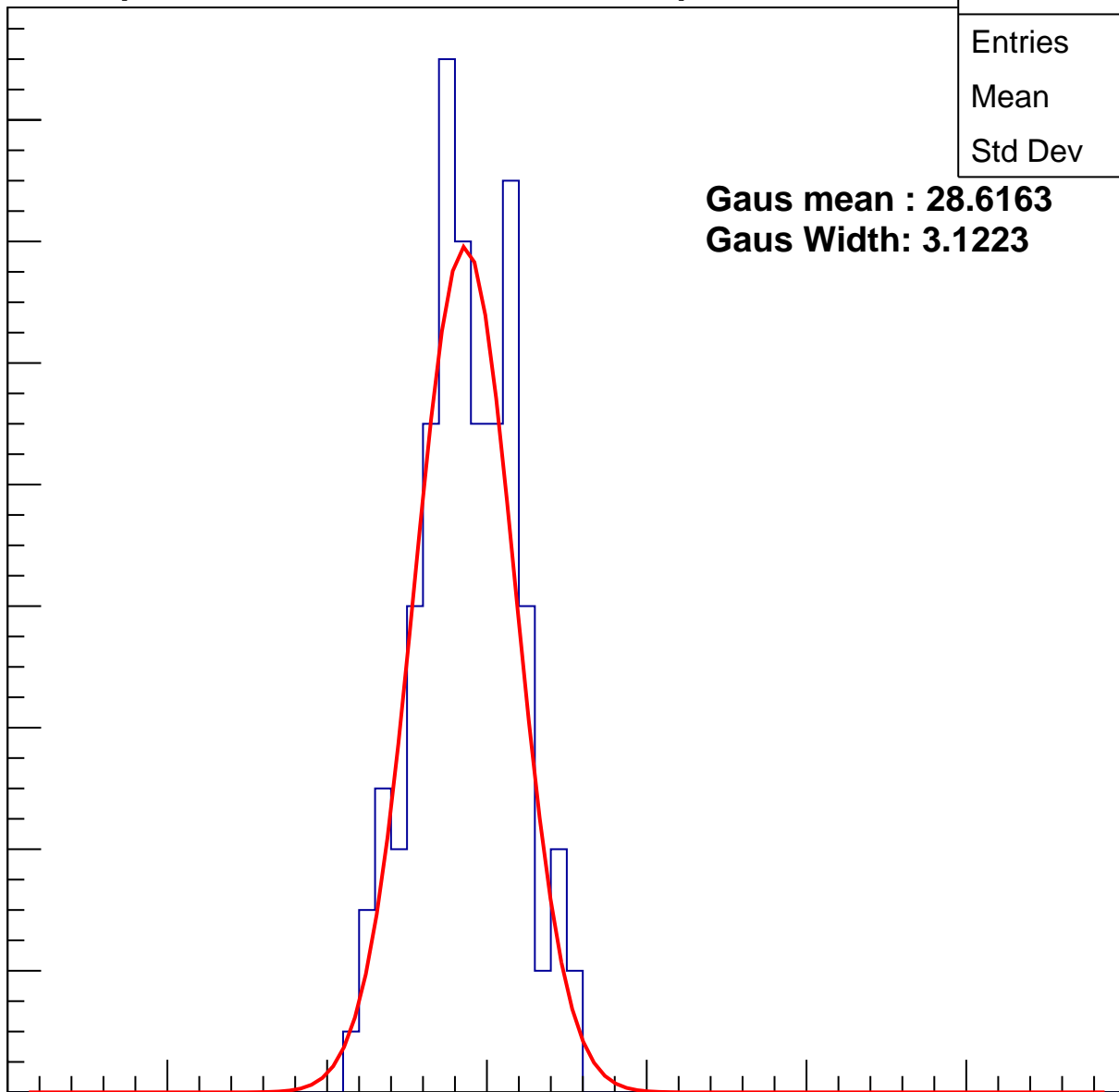
Gaus Width: 3.1223

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



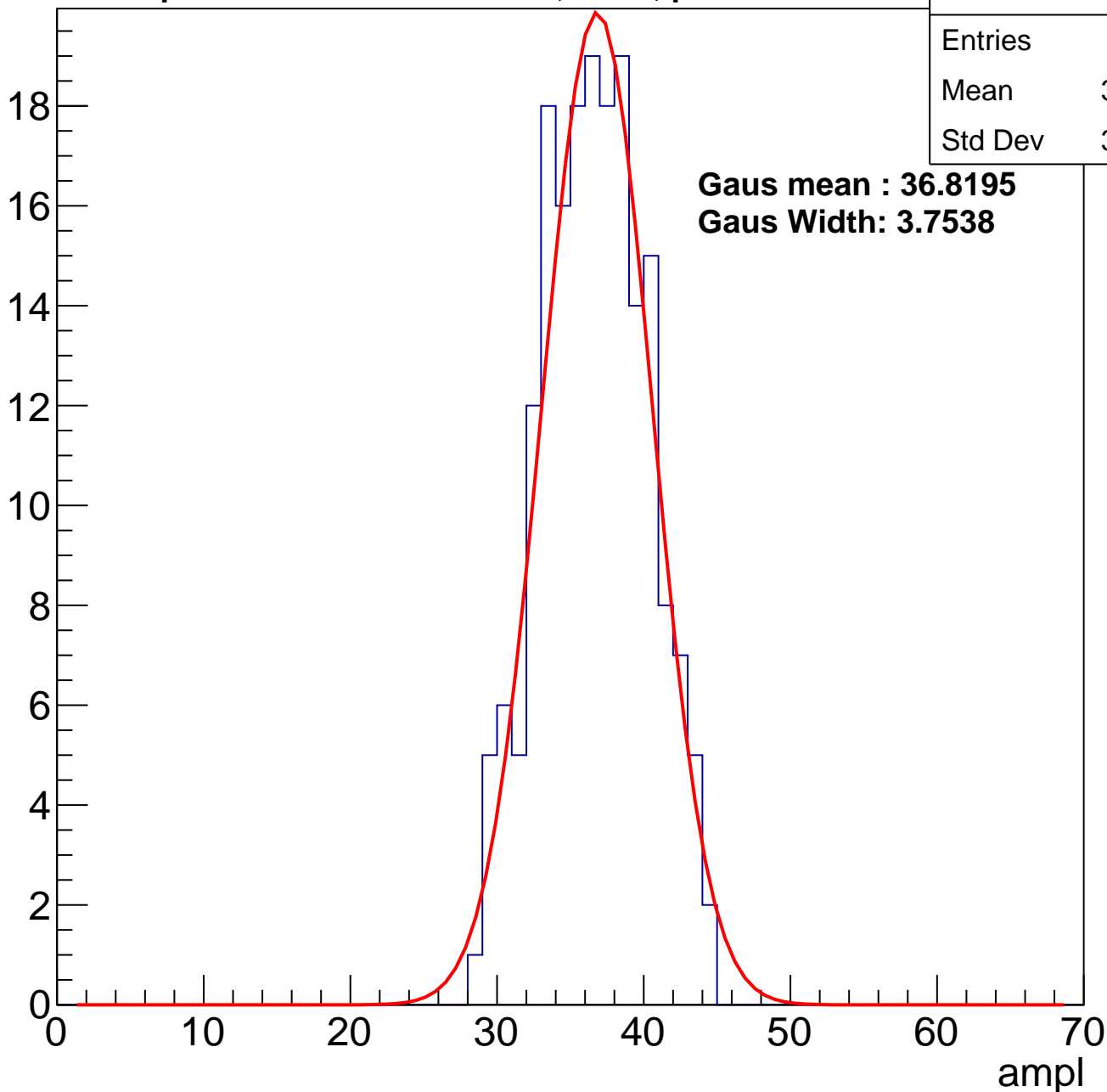
B1L001S, U21-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	188
Mean	36.19
Std Dev	3.546

Gaus mean : 36.8195
Gaus Width: 3.7538

Entry



B1L001S, U21-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	43.62
Std Dev	2.823

Gaus mean : 44.4374

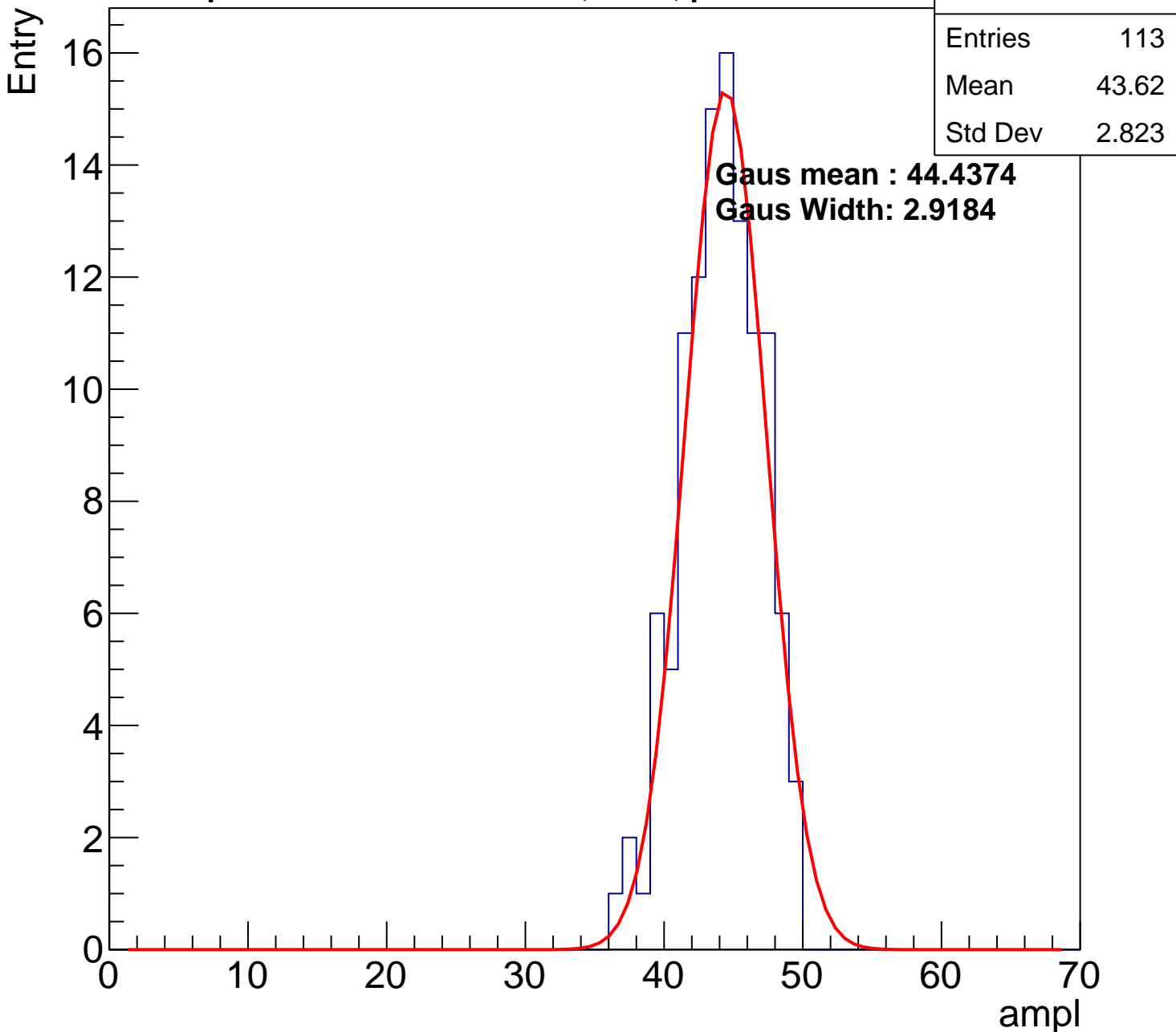
Gaus Width: 2.9184

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

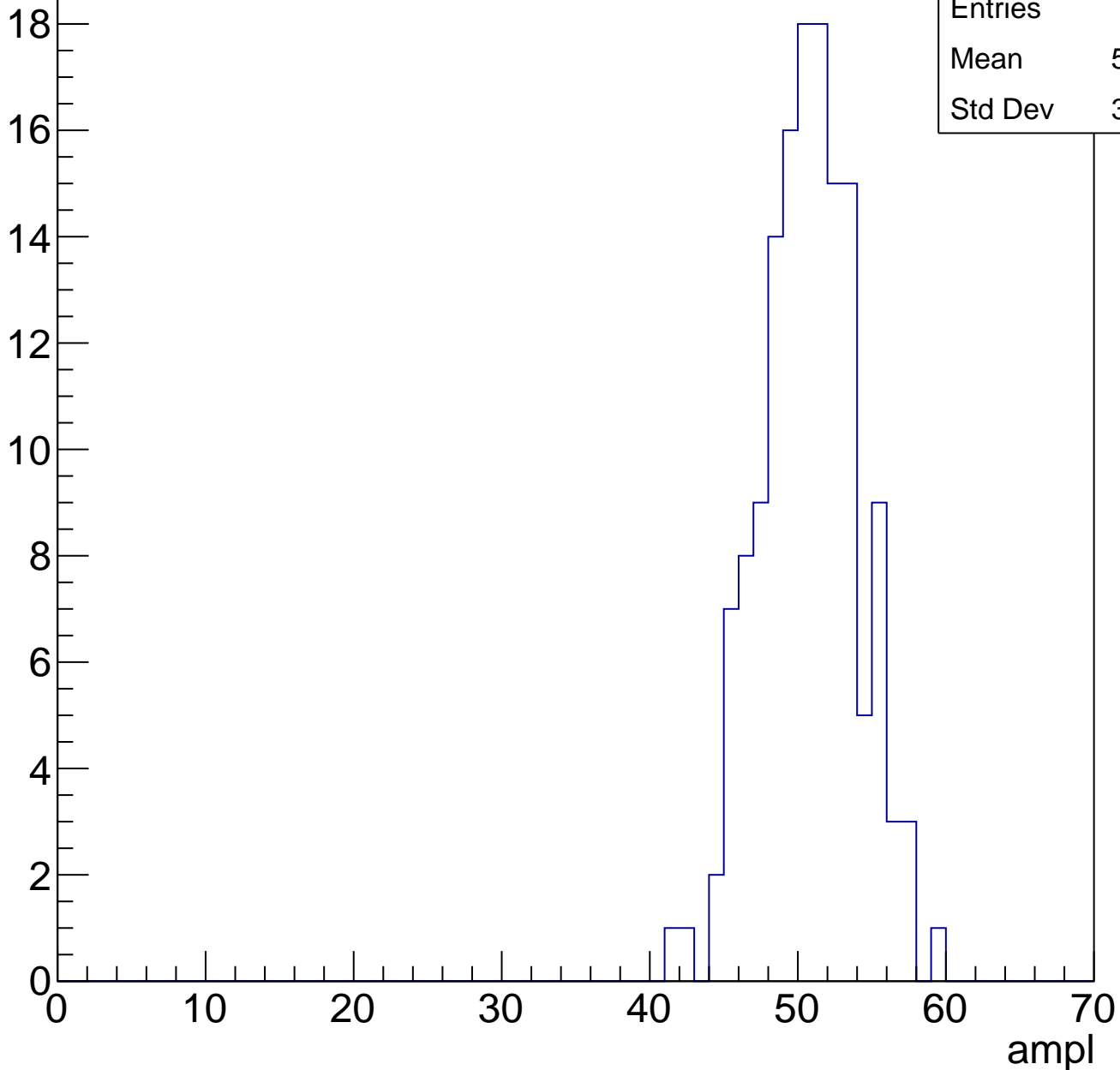


B1L001S, U21-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	50.27
Std Dev	3.234

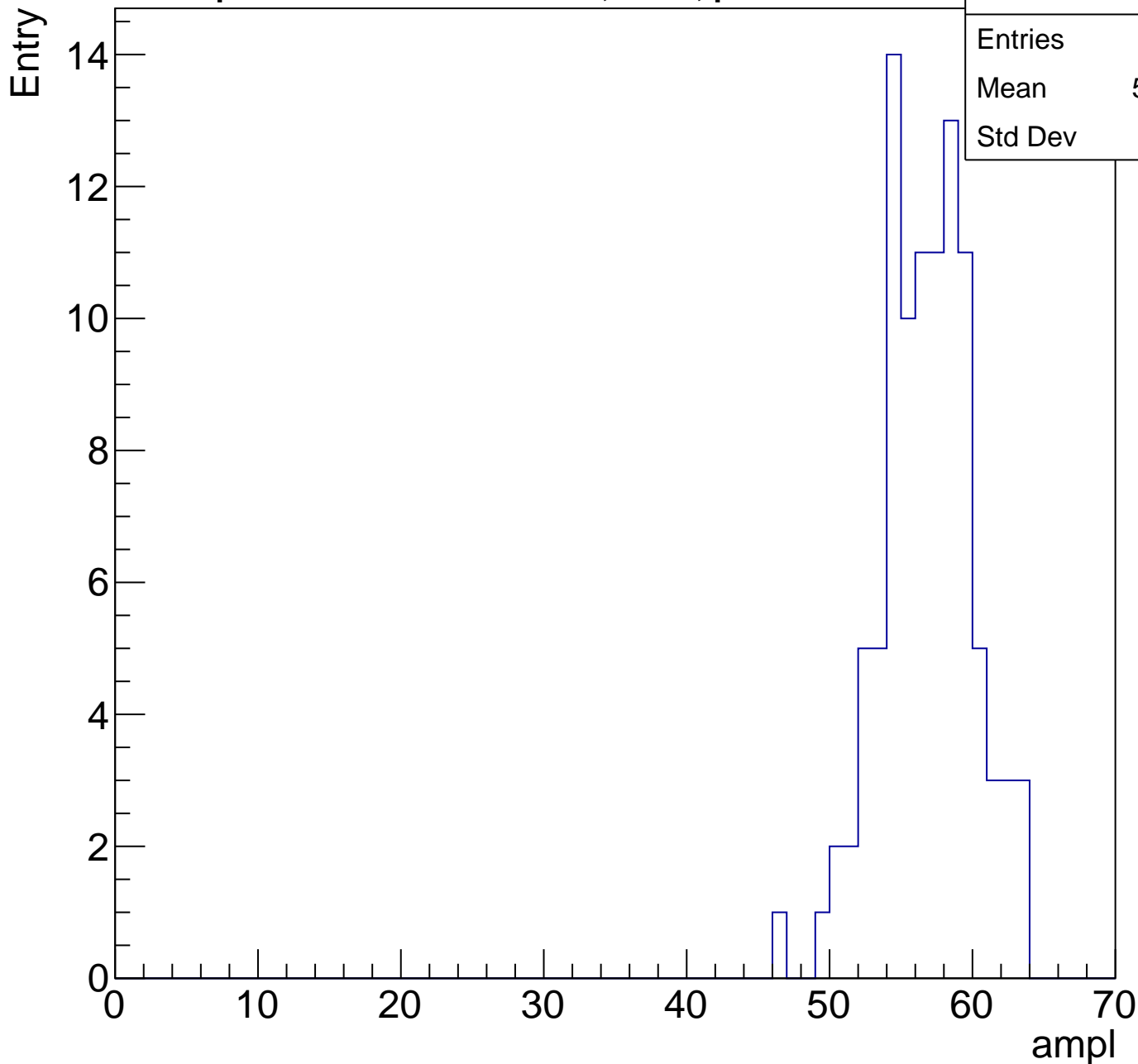
Entry



B1L001S, U21-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

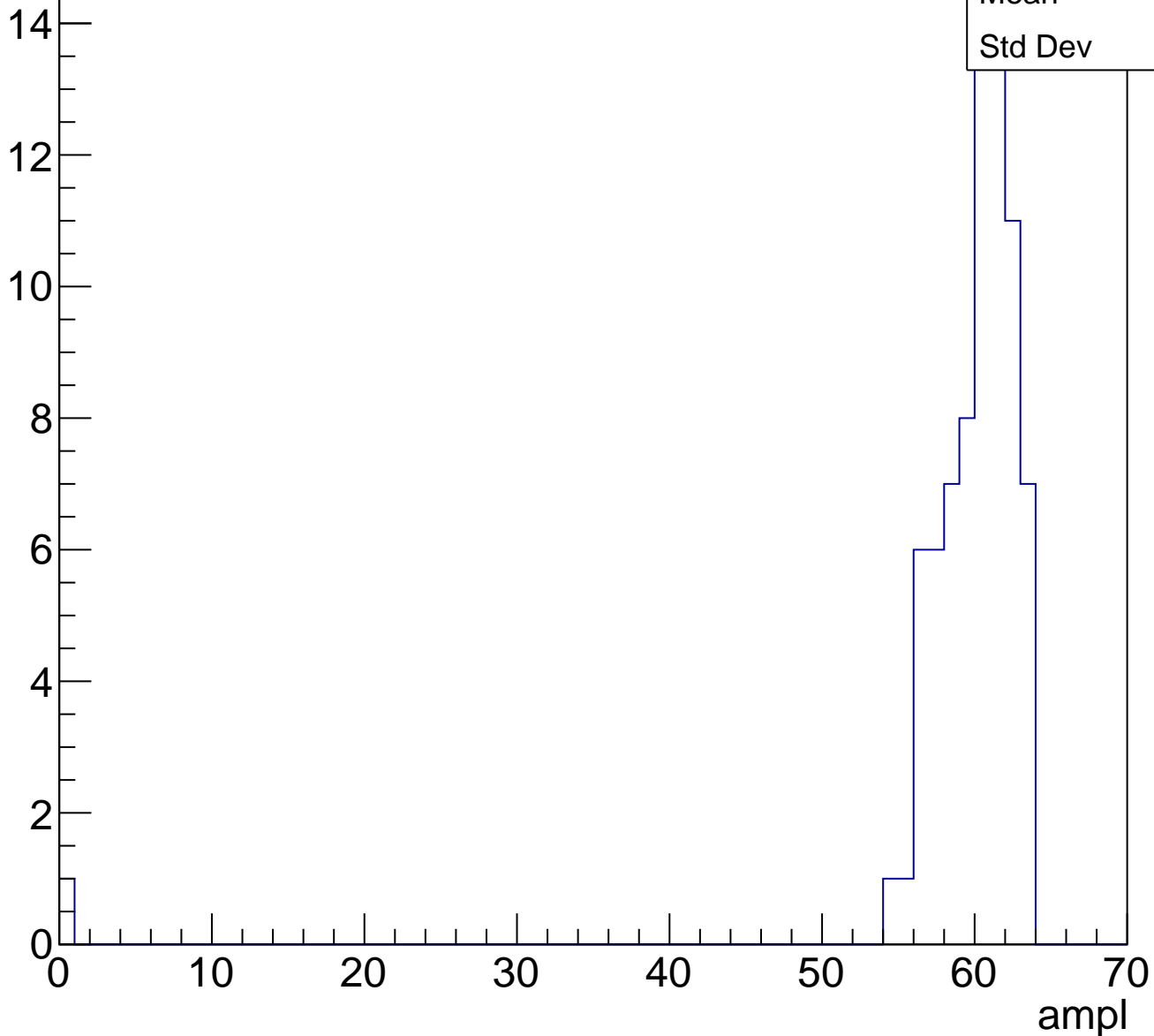
Entries	100
Mean	56.32
Std Dev	3.2



B1L001S, U21-ch32, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

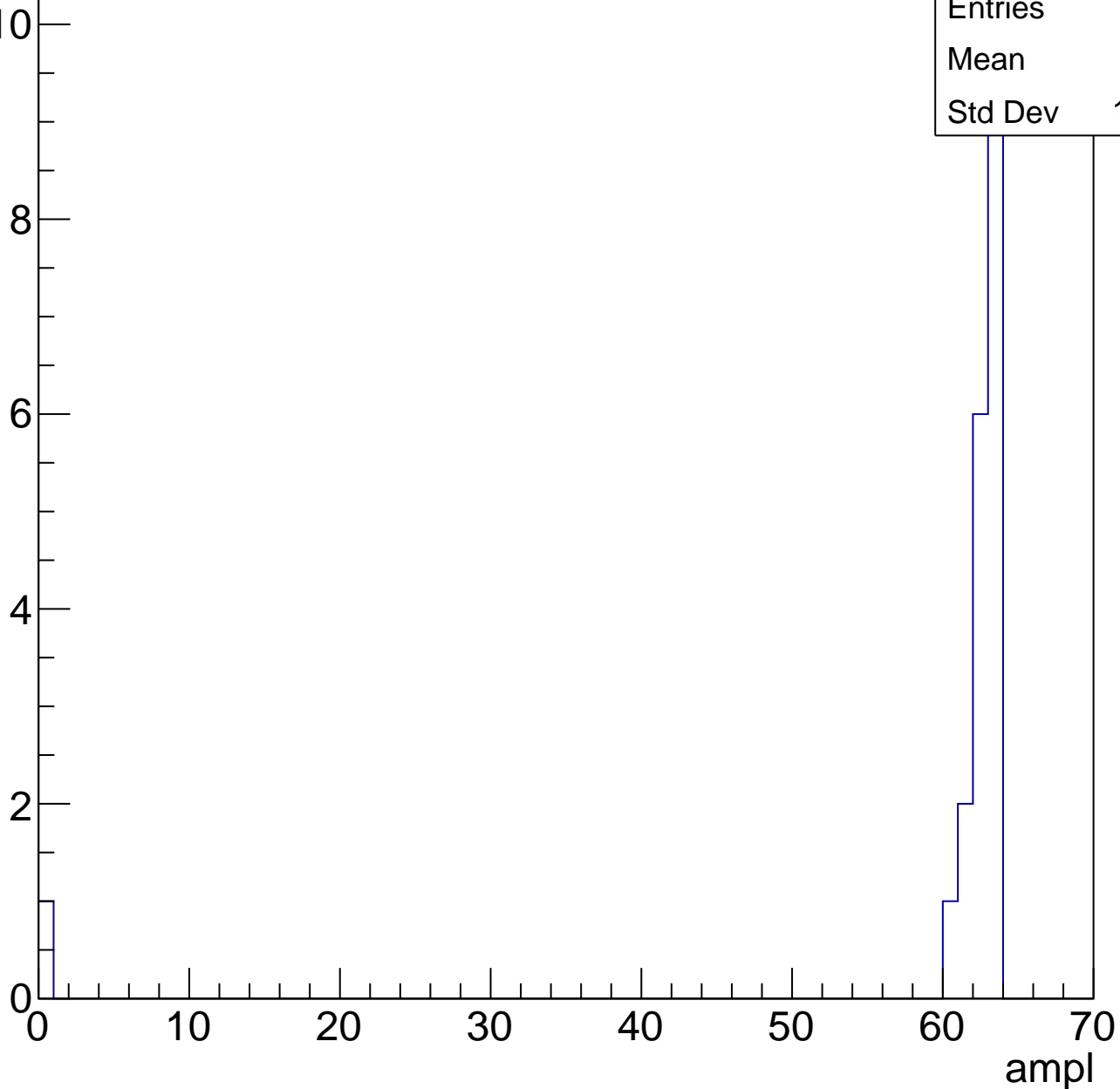


B1L001S, U21-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	59.2
Std Dev	13.61



B1L001S, U21-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch33, adc0

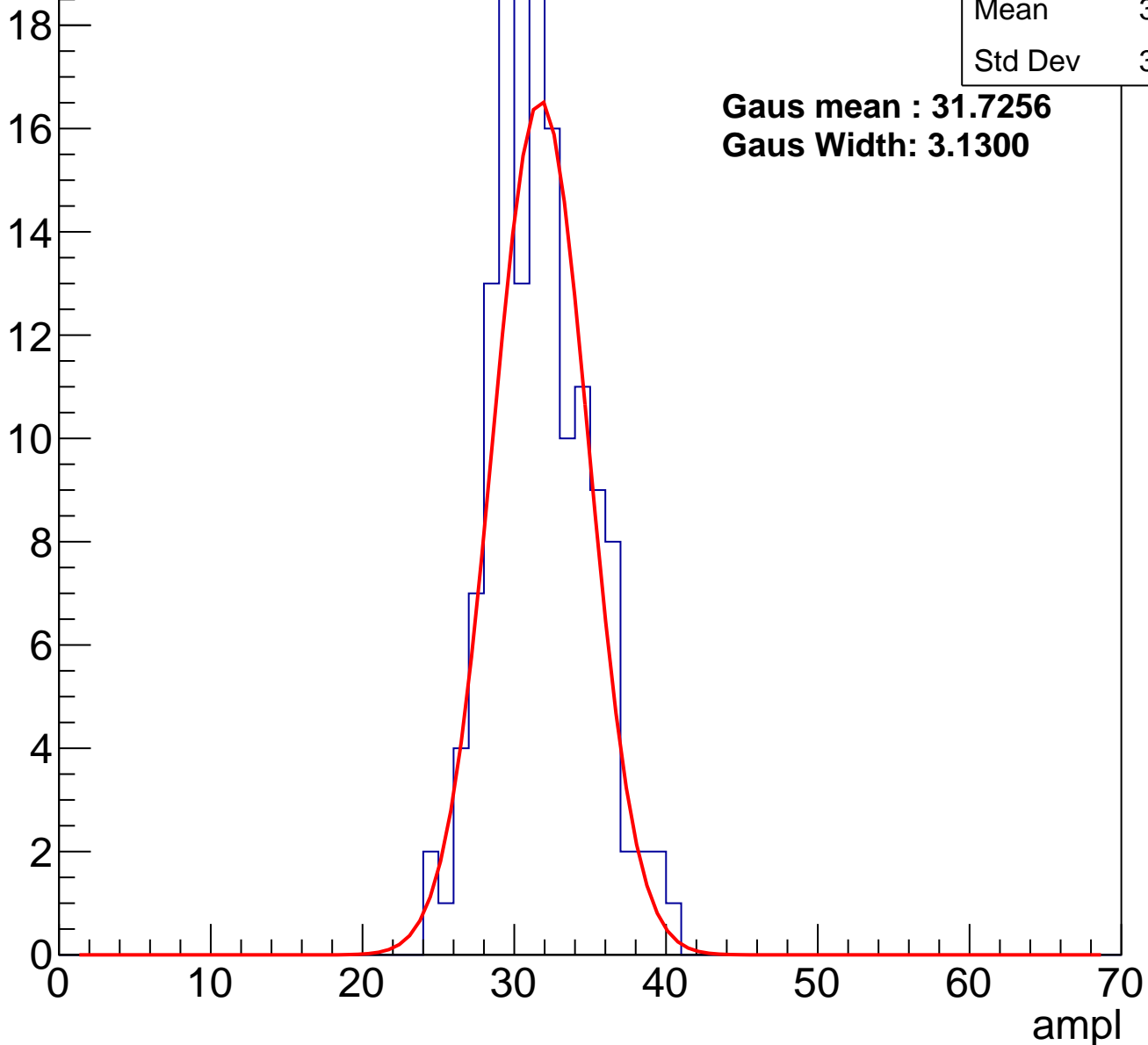
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	31.27
Std Dev	3.196

Gaus mean : 31.7256

Gaus Width: 3.1300

Entry



B1L001S, U21-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	37.95
Std Dev	3.077

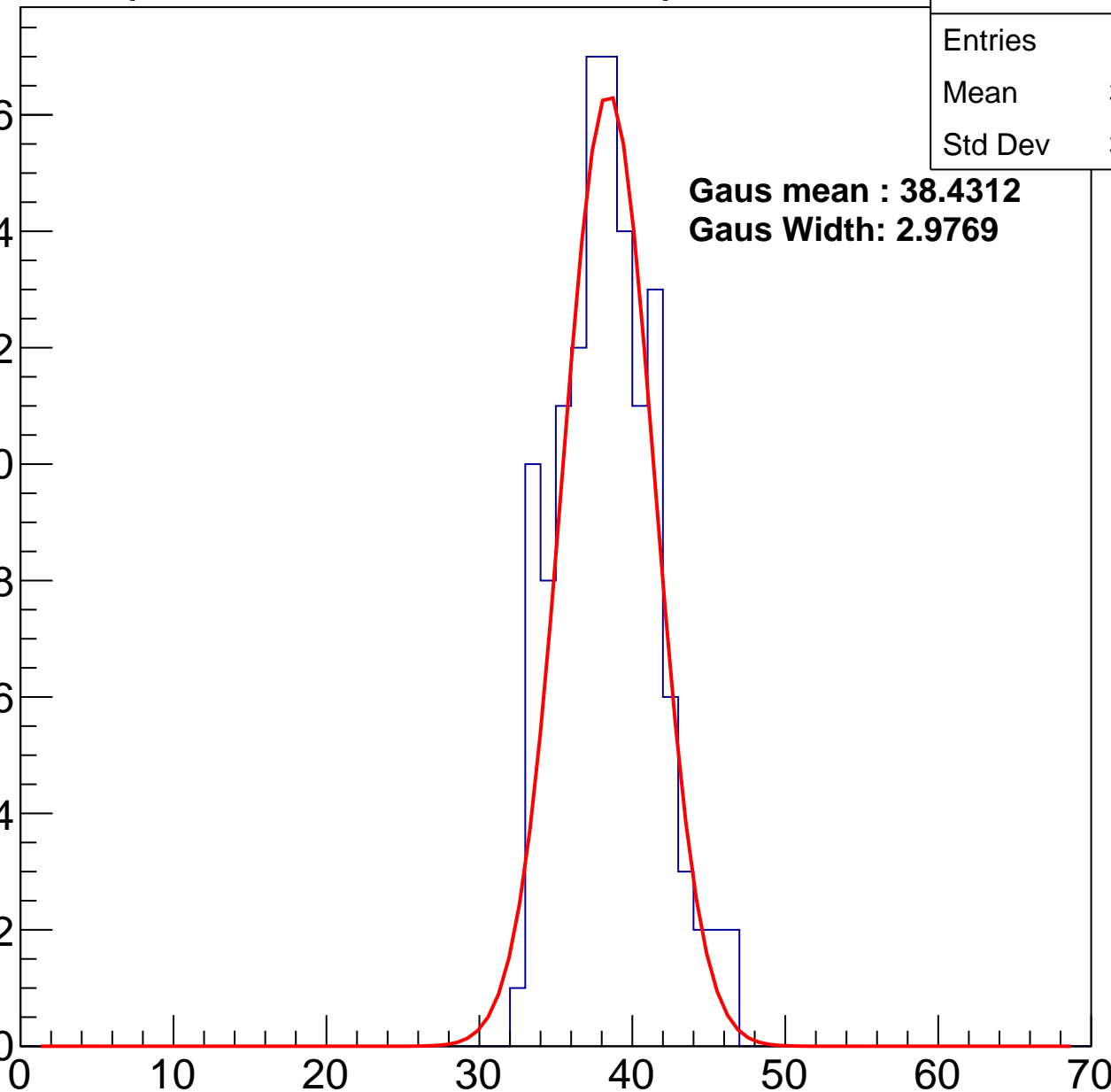
Gaus mean : 38.4312

Gaus Width: 2.9769

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

152

Mean

45.09

Std Dev

3.303

Gaus mean : 45.5588

Gaus Width: 3.3076

0

10

20

30

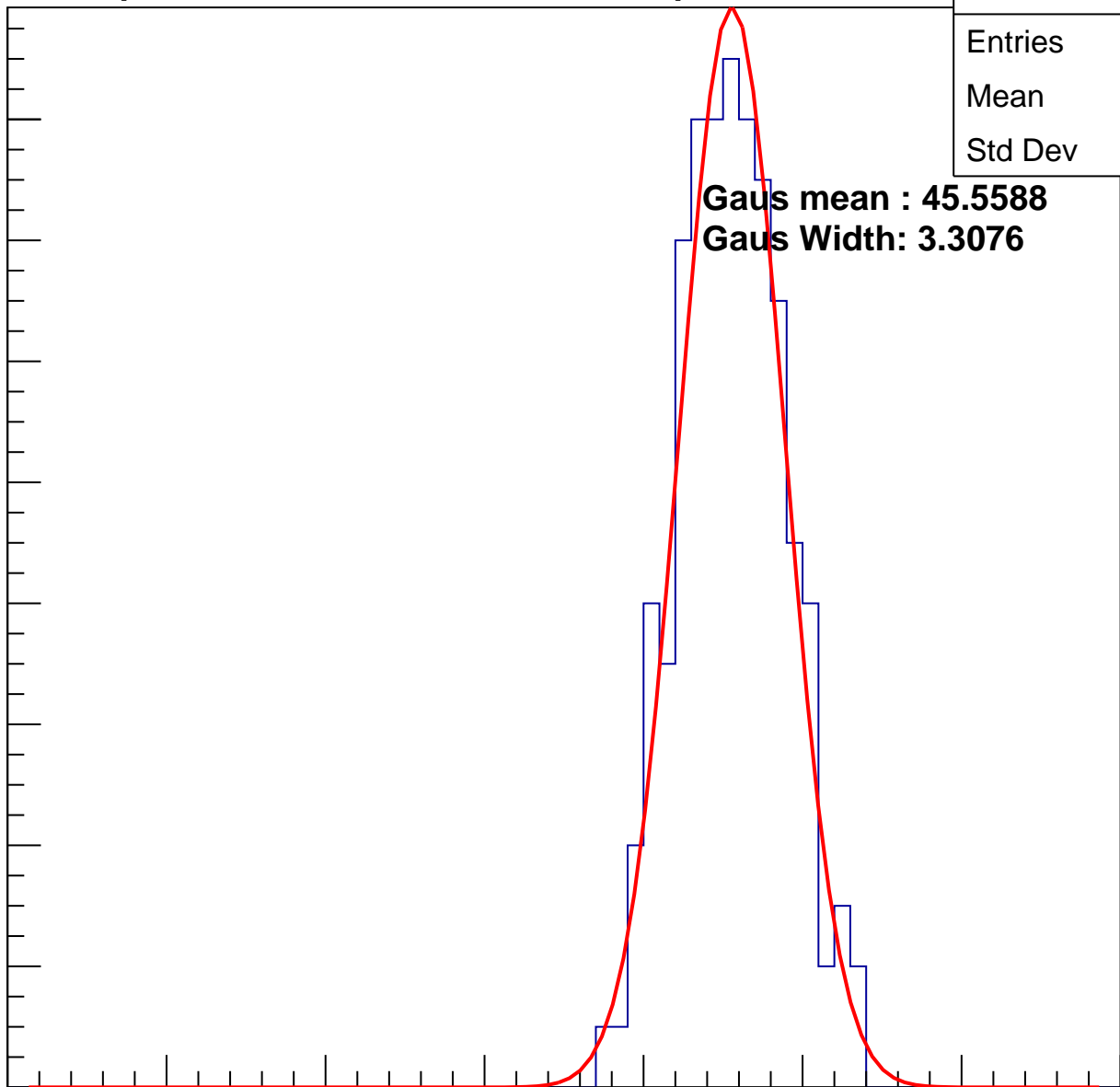
40

50

60

70

ampl



B1L001S, U21-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

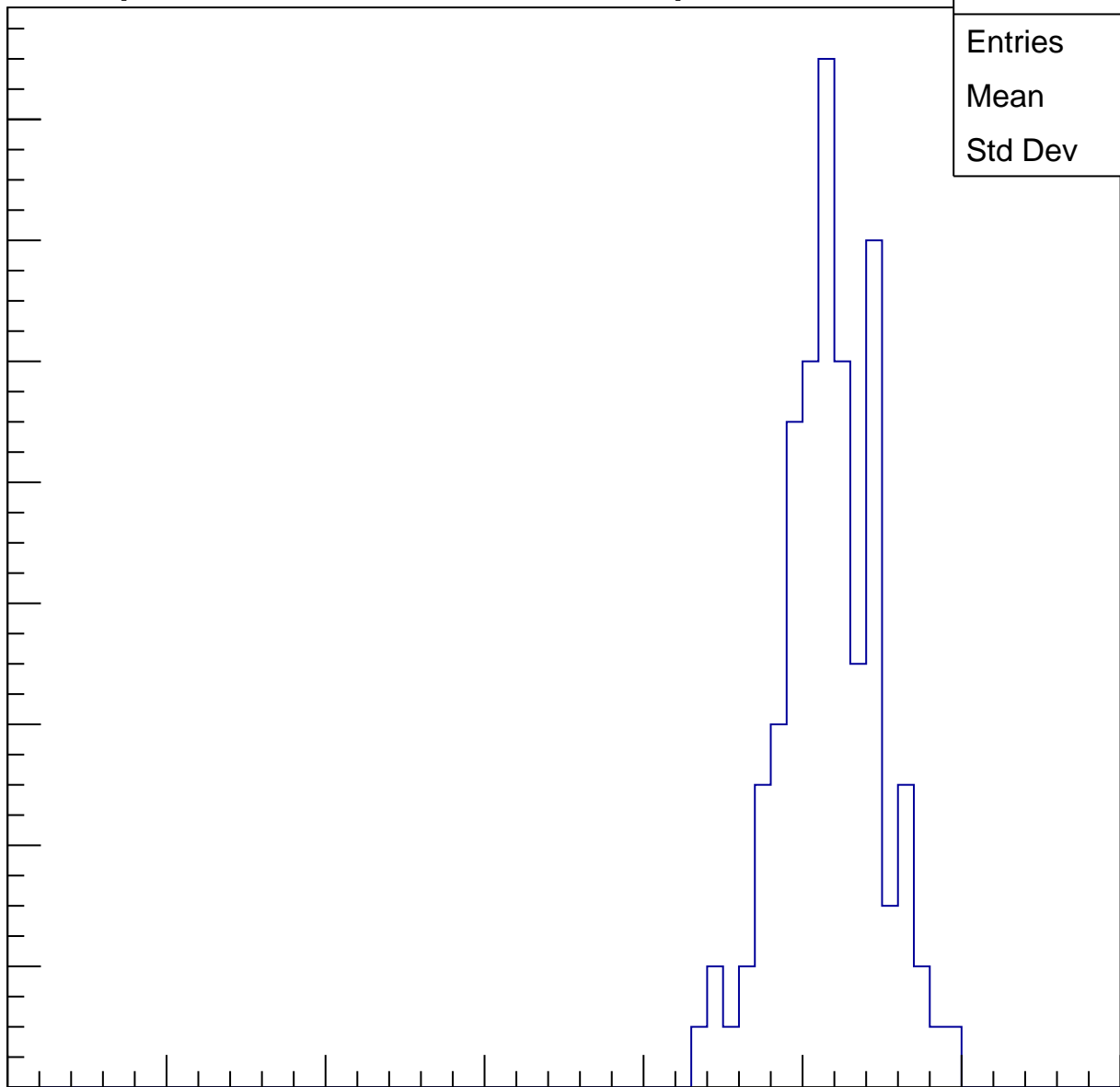
Entries	102
Mean	51.22
Std Dev	3.063

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.52
Std Dev	5.803

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

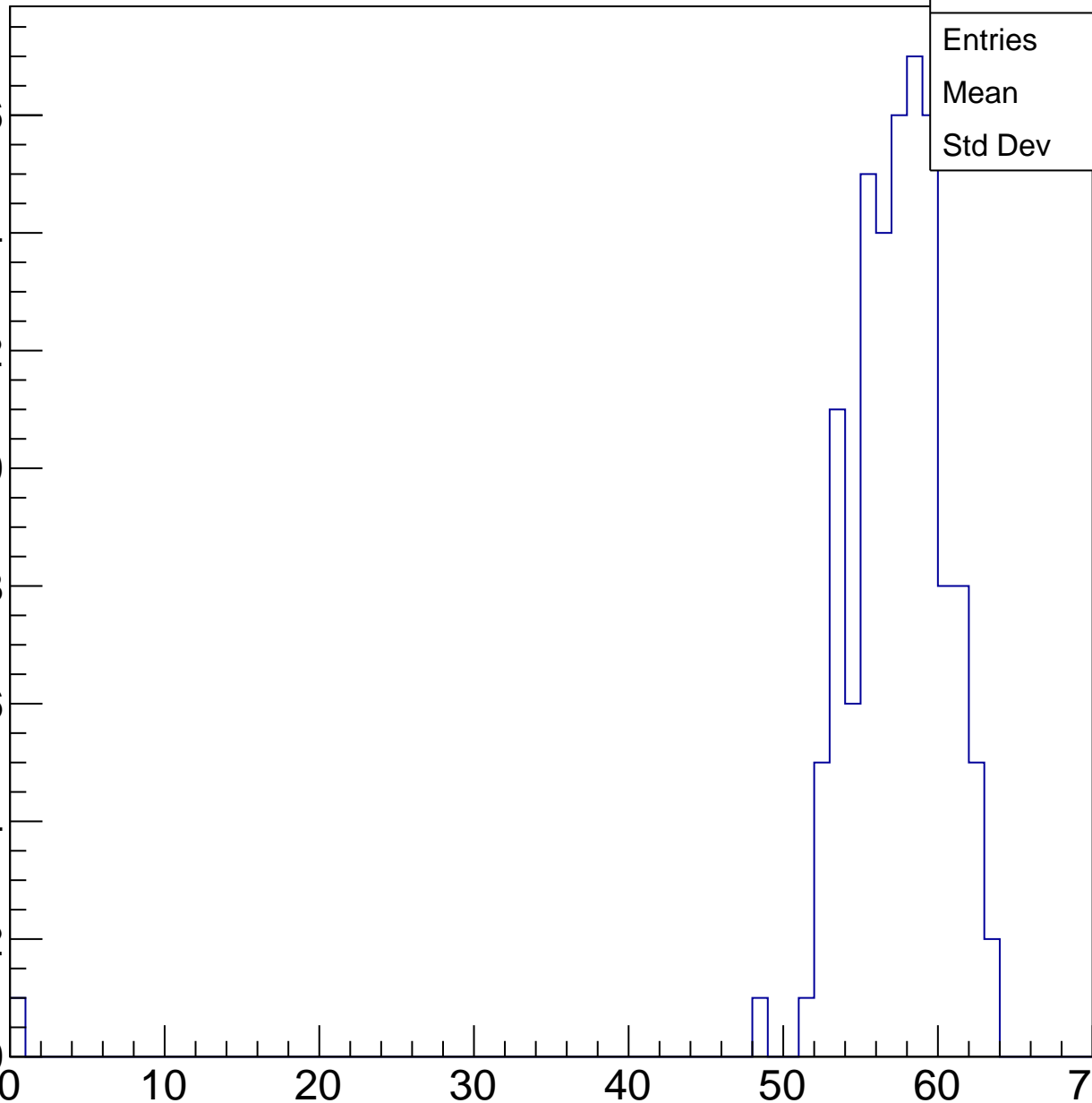
40

50

60

70

ampl



B1L001S, U21-ch33, adc5

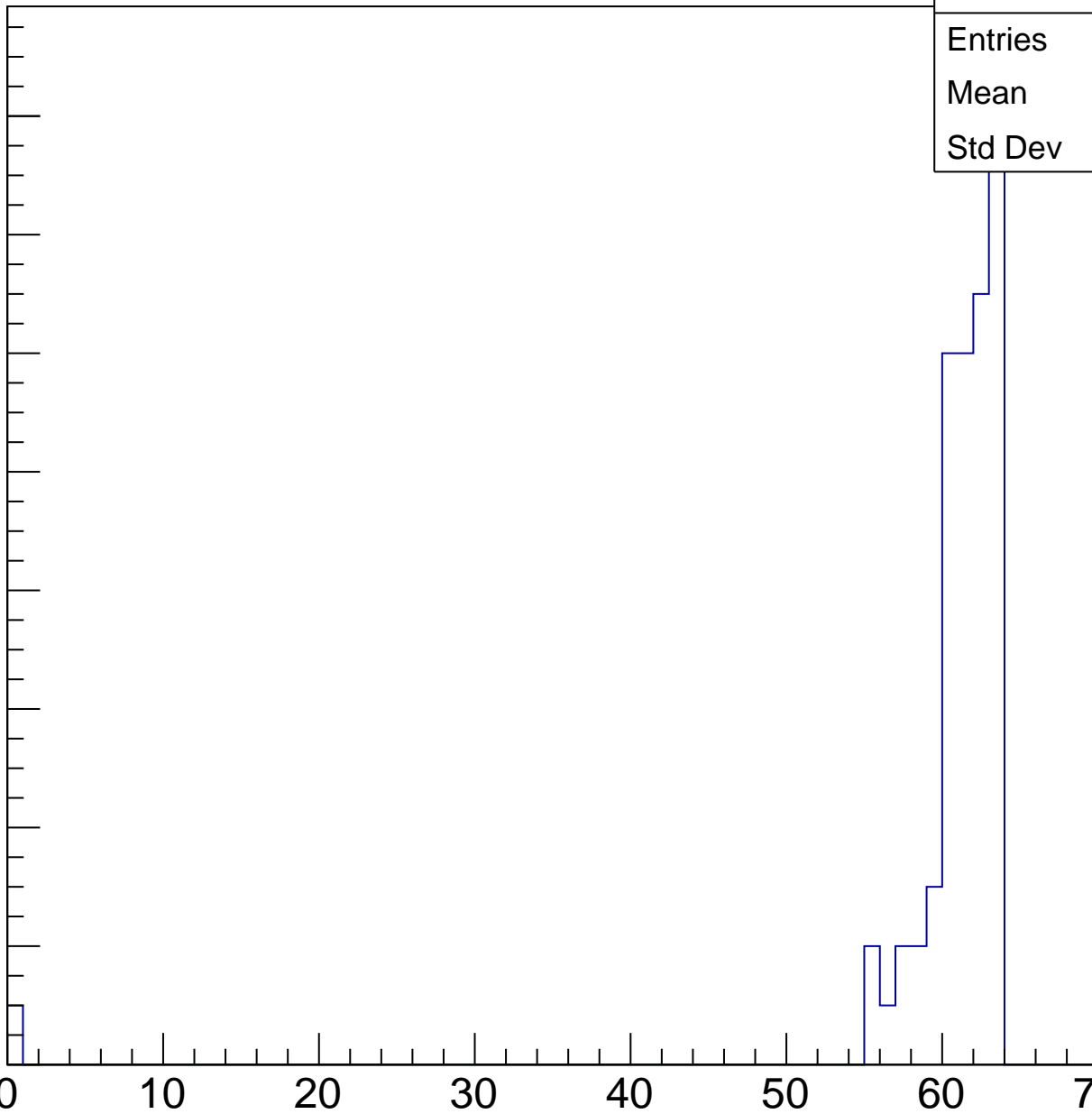
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	65
Mean	60.03
Std Dev	7.758

ampl



B1L001S, U21-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L001S, U21-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch34, adc0

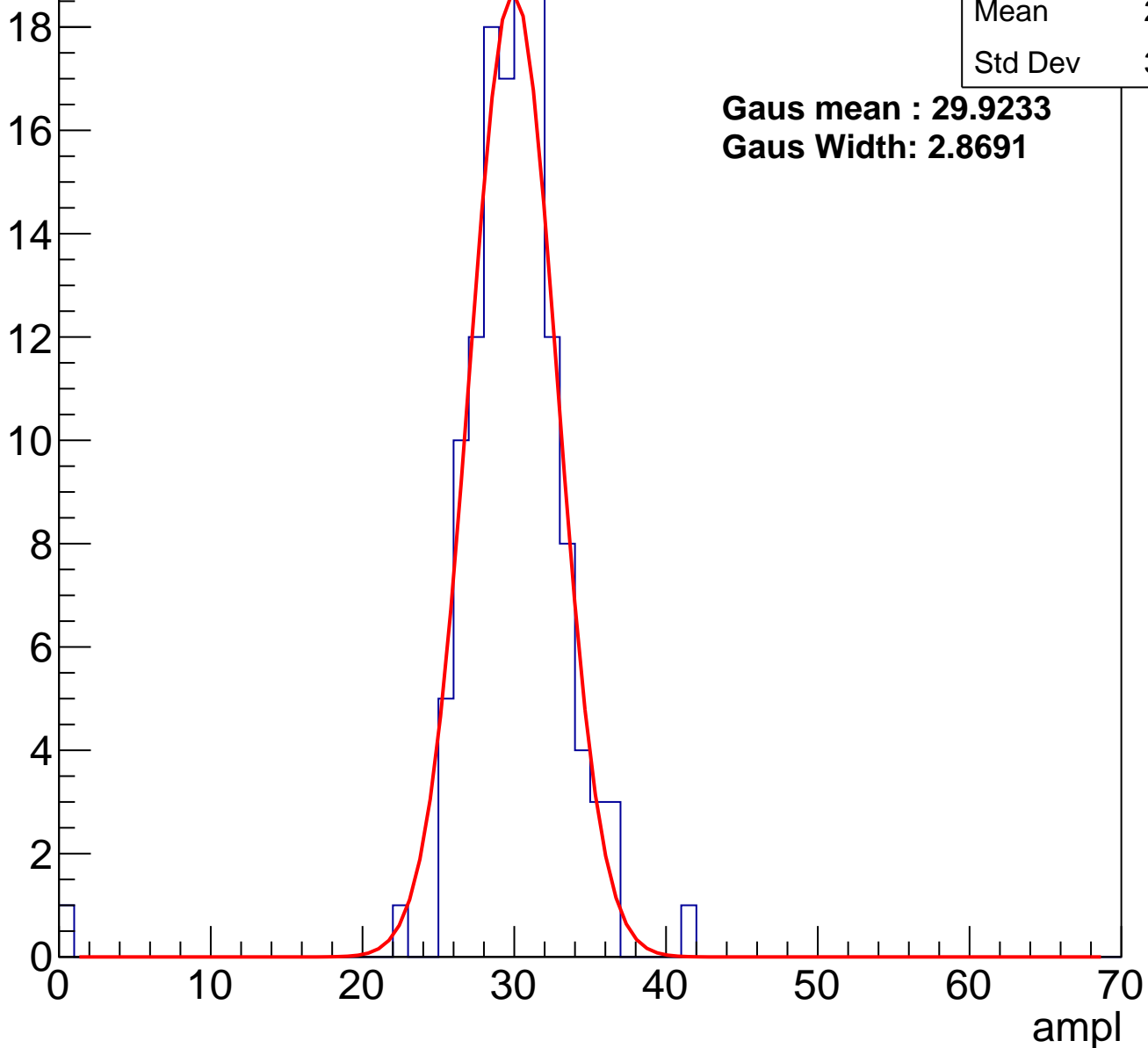
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	29.51
Std Dev	3.801

Gaus mean : 29.9233

Gaus Width: 2.8691

Entry

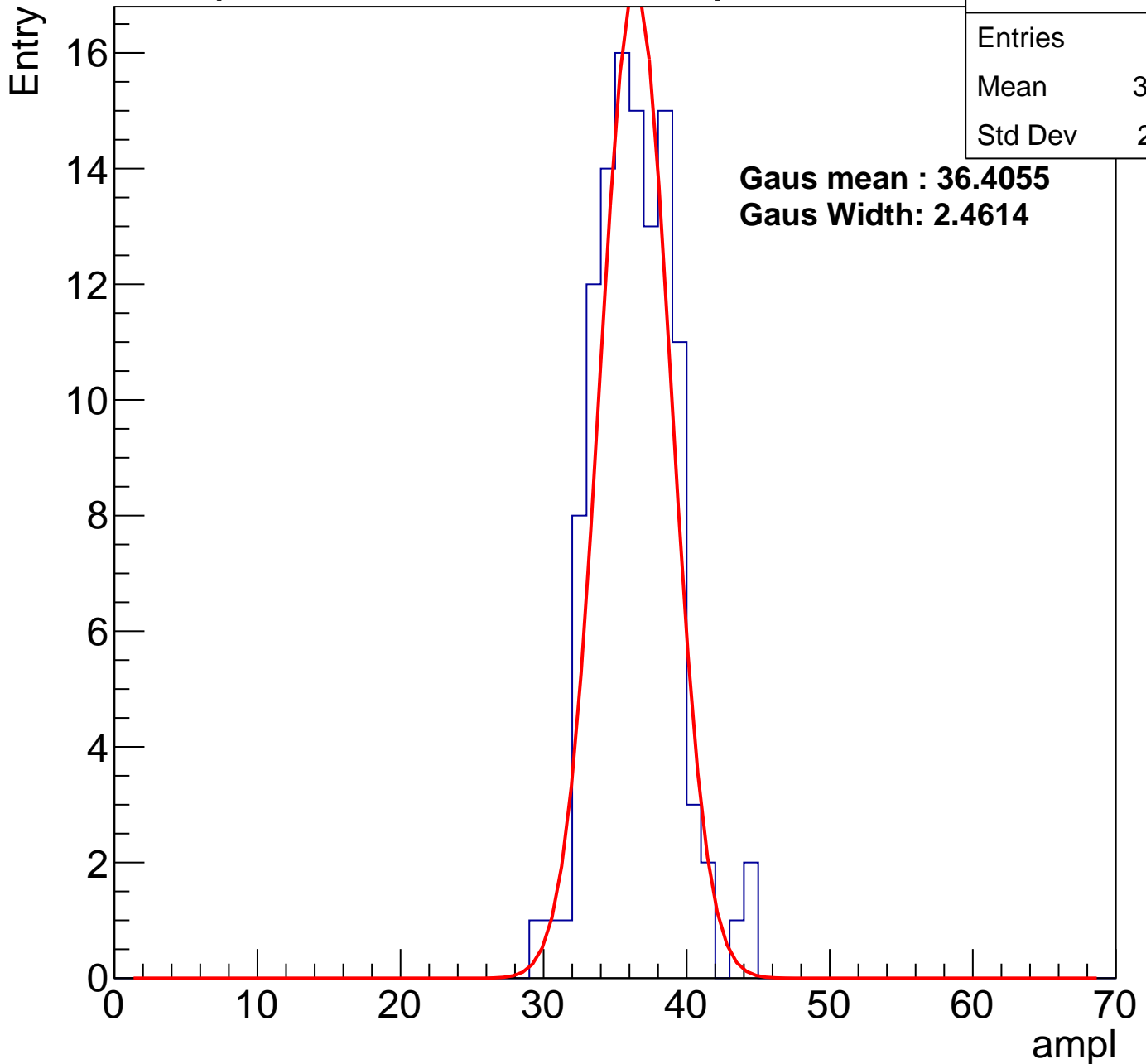


B1L001S, U21-ch34, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	35.92
Std Dev	2.741

Gaus mean : 36.4055
Gaus Width: 2.4614



B1L001S, U21-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	42.53
Std Dev	3.456

Gaus mean : 43.0942

Gaus Width: 3.3820

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

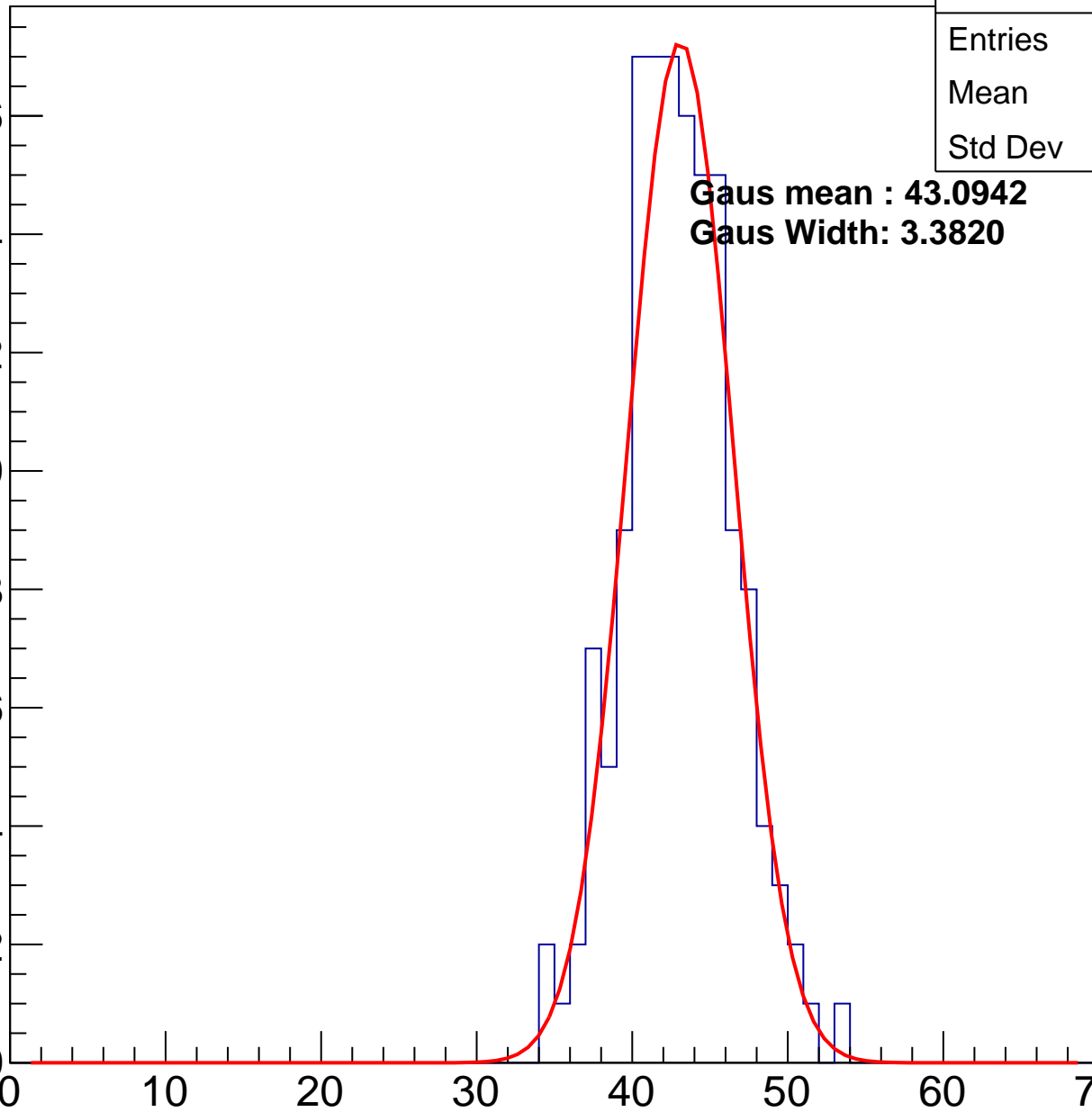
40

50

60

70

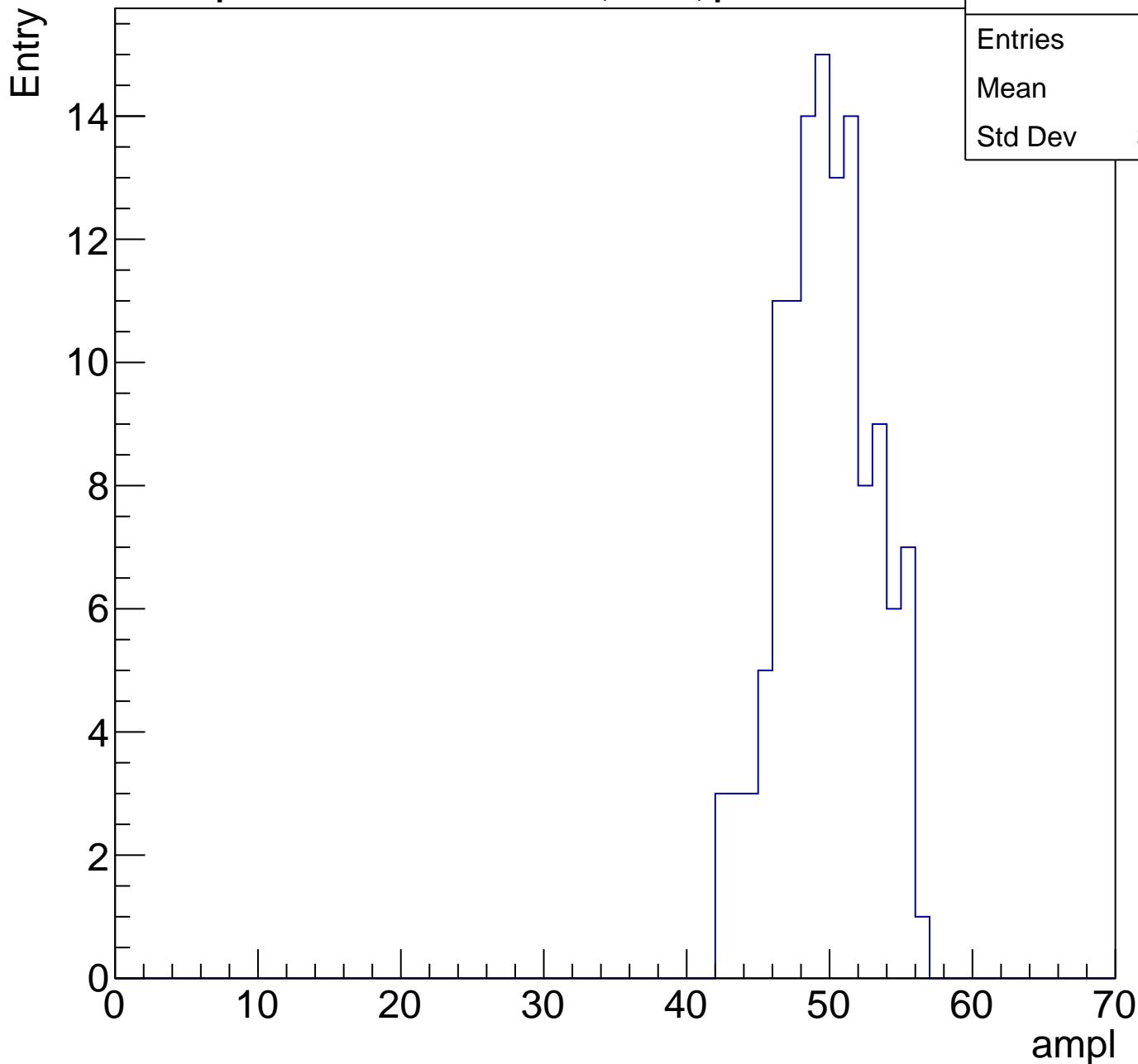
ampl



B1L001S, U21-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

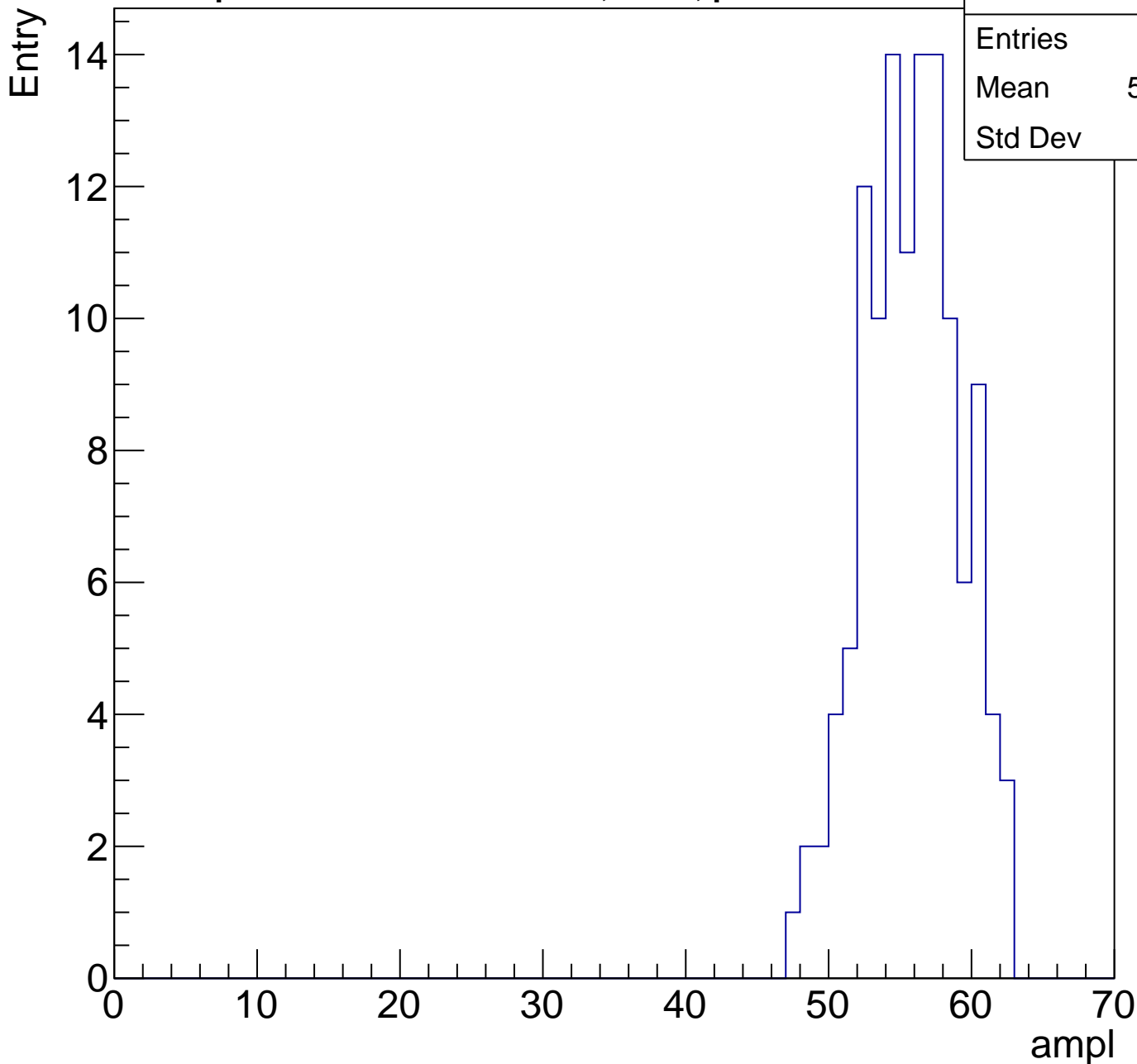
Entries	123
Mean	49.3
Std Dev	3.241



B1L001S, U21-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	55.35
Std Dev	3.32



B1L001S, U21-ch34, adc5

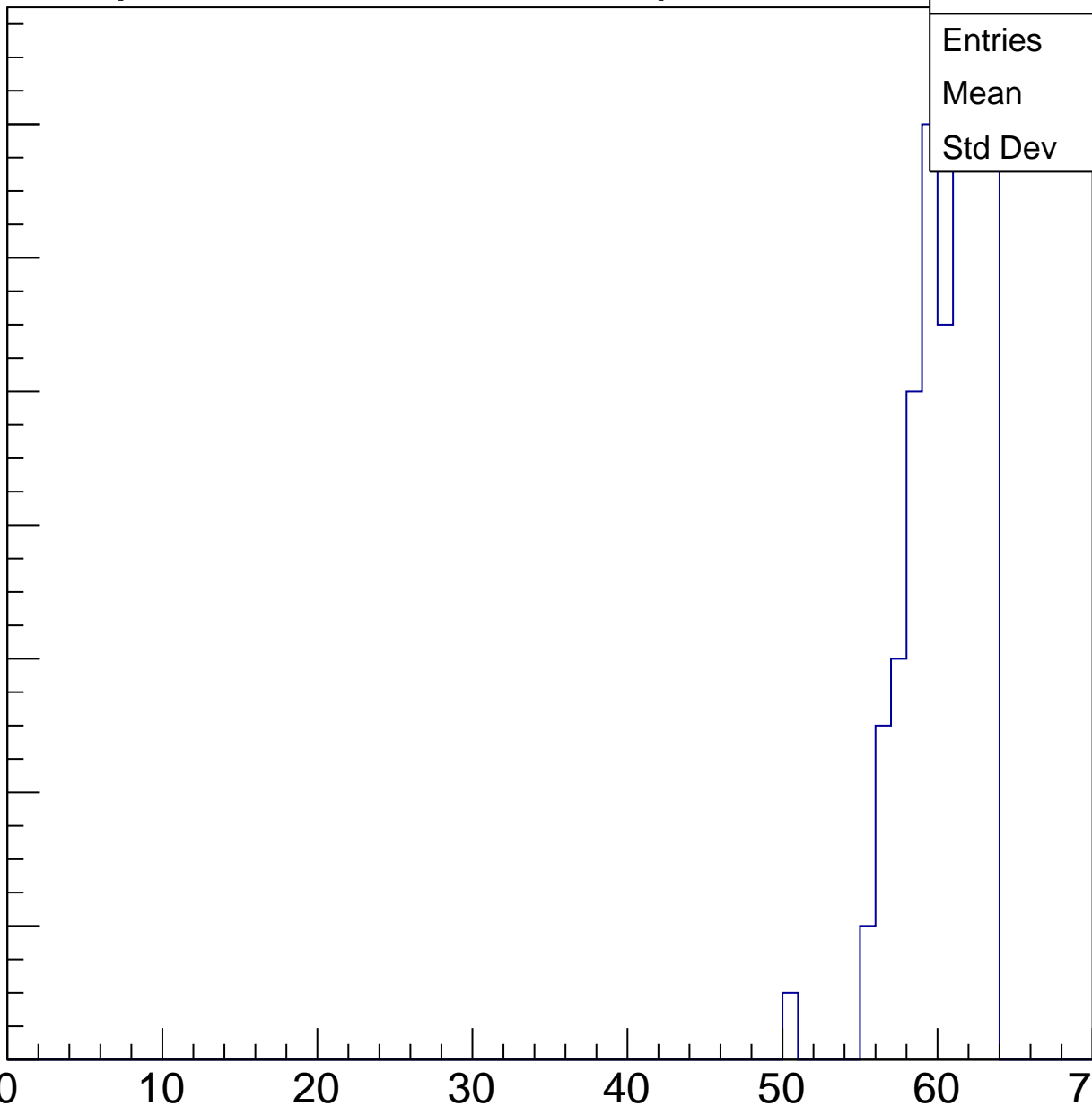
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	59.93
Std Dev	2.417

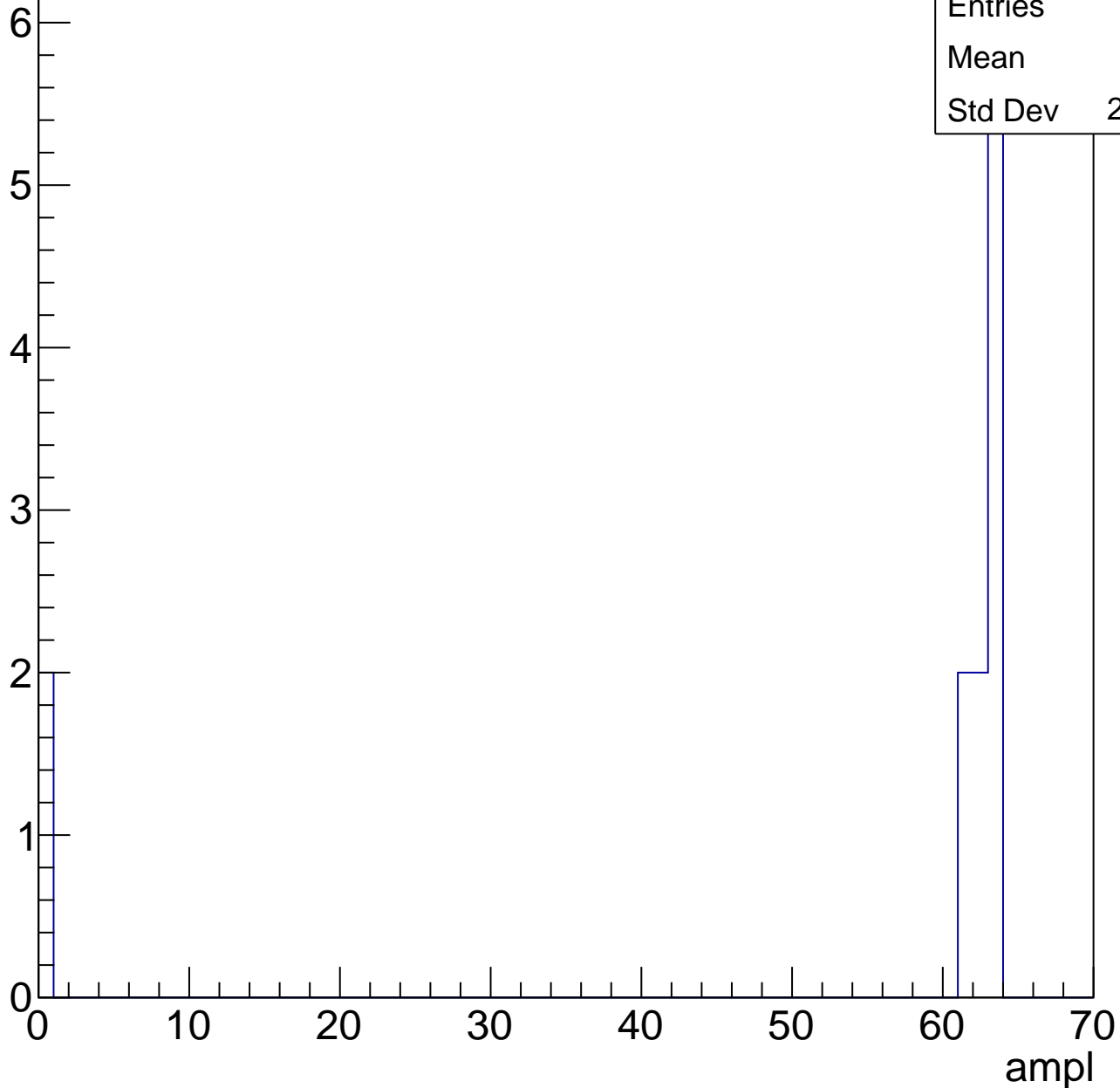
ampl



B1L001S, U21-ch34, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	12
Mean	52
Std Dev	23.27

B1L001S, U21-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch35, adc0

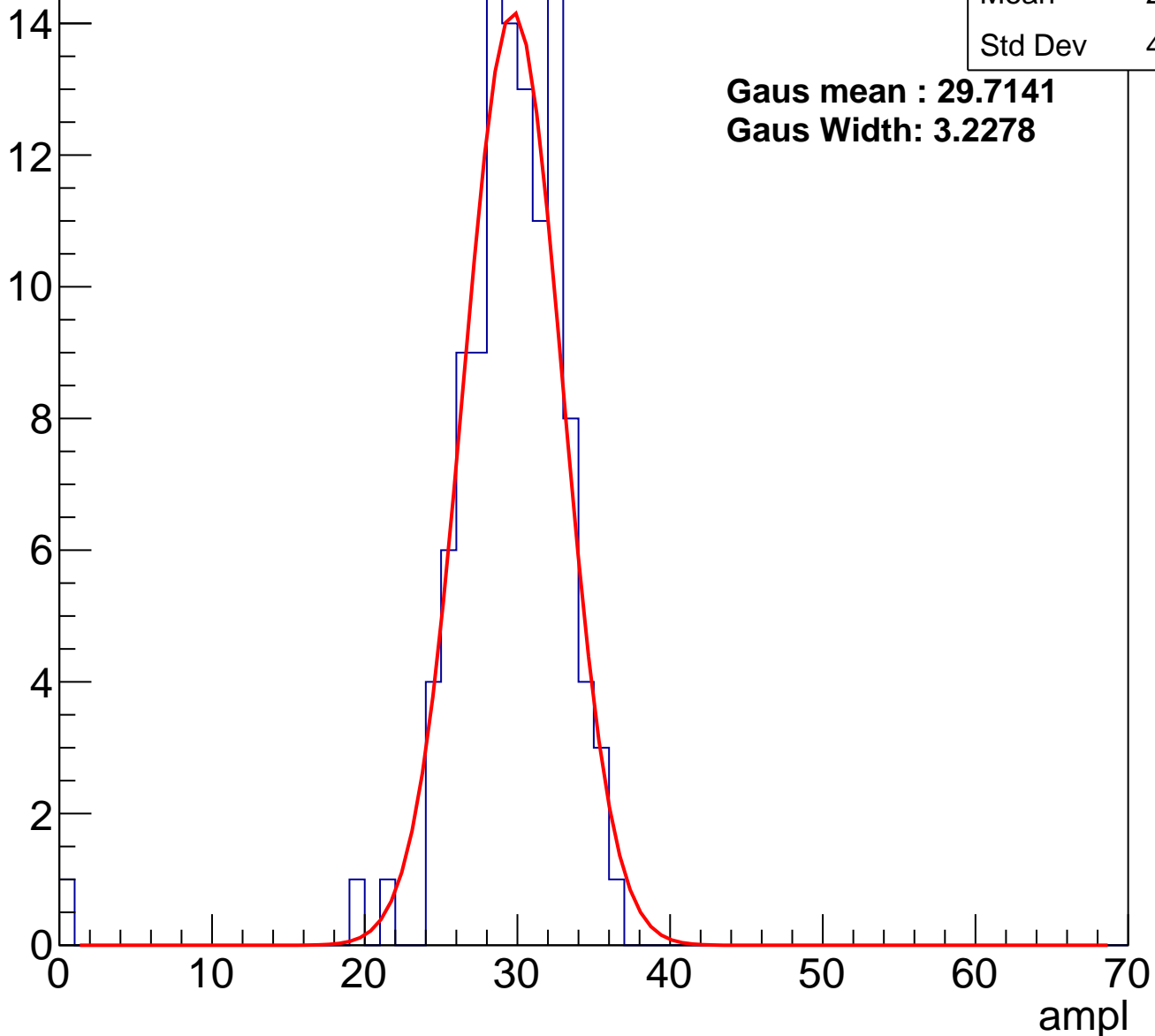
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	29.05
Std Dev	4.075

Gaus mean : 29.7141

Gaus Width: 3.2278

Entry



B1L001S, U21-ch35, adc1

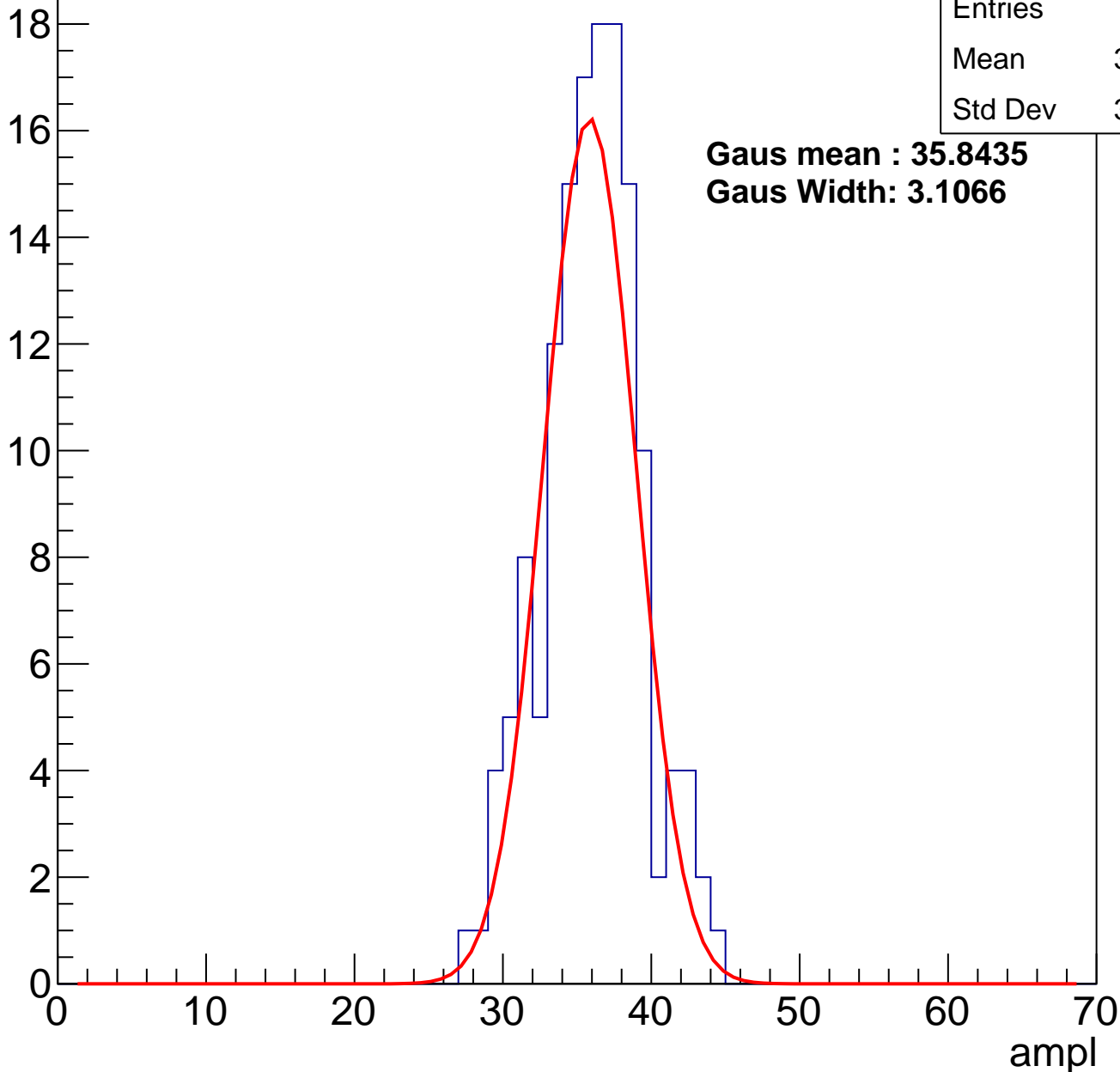
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	35.54
Std Dev	3.316

Gaus mean : 35.8435

Gaus Width: 3.1066

Entry



B1L001S, U21-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	42.24
Std Dev	2.82

Gaus mean : 42.5109

Gaus Width: 2.6412

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

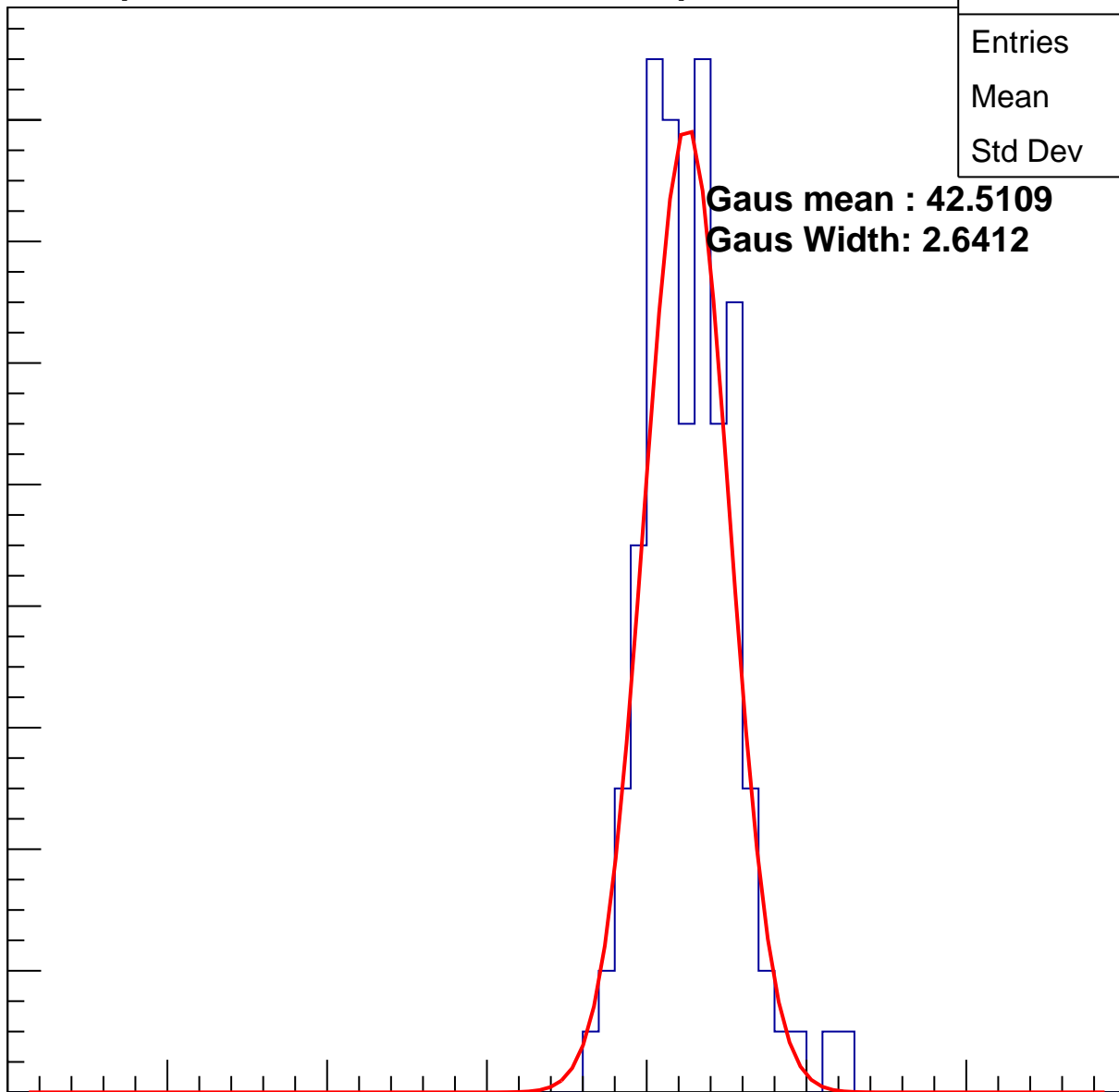
40

50

60

70

ampl

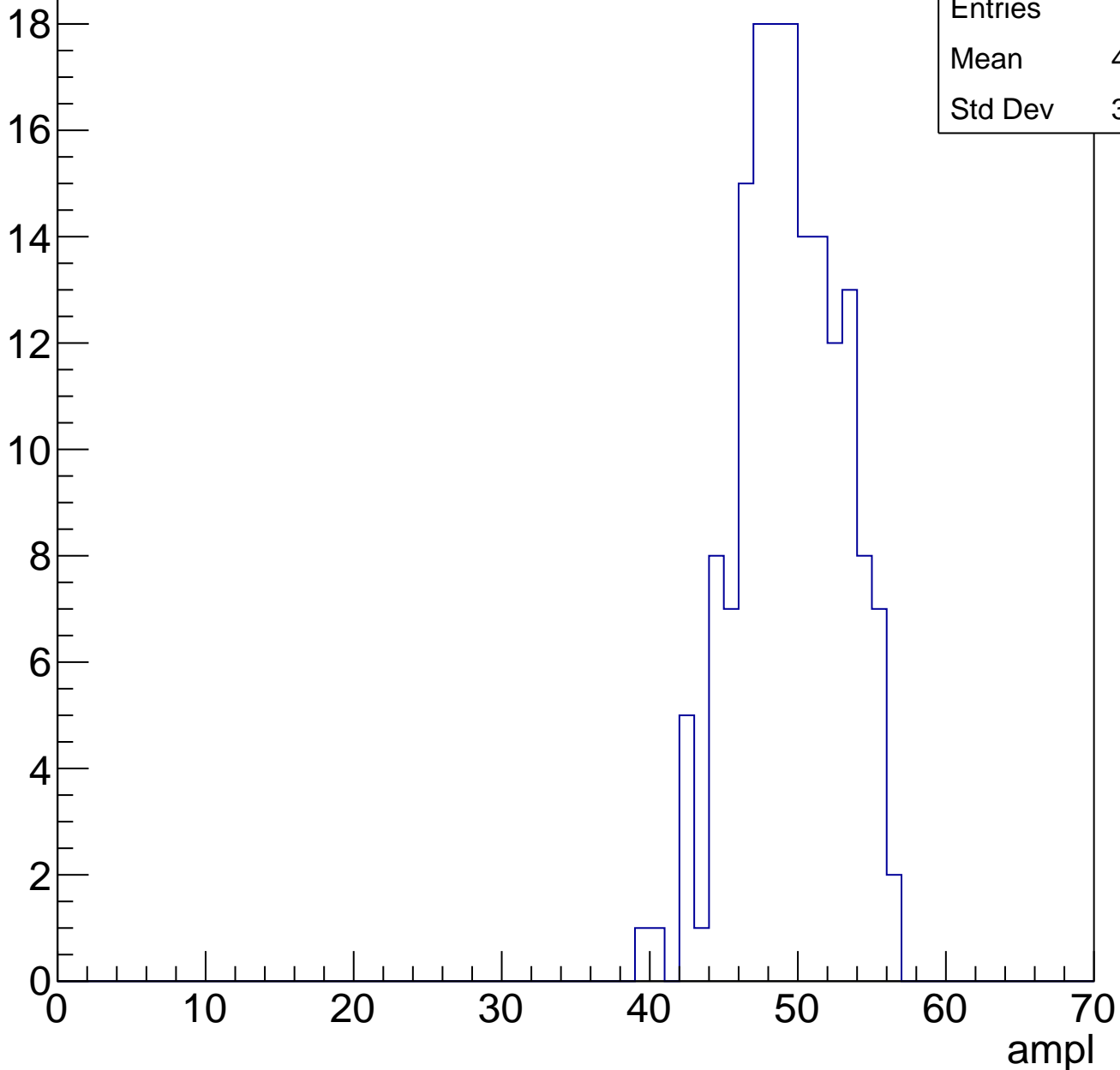


B1L001S, U21-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	48.99
Std Dev	3.456

Entry



B1L001S, U21-ch35, adc4

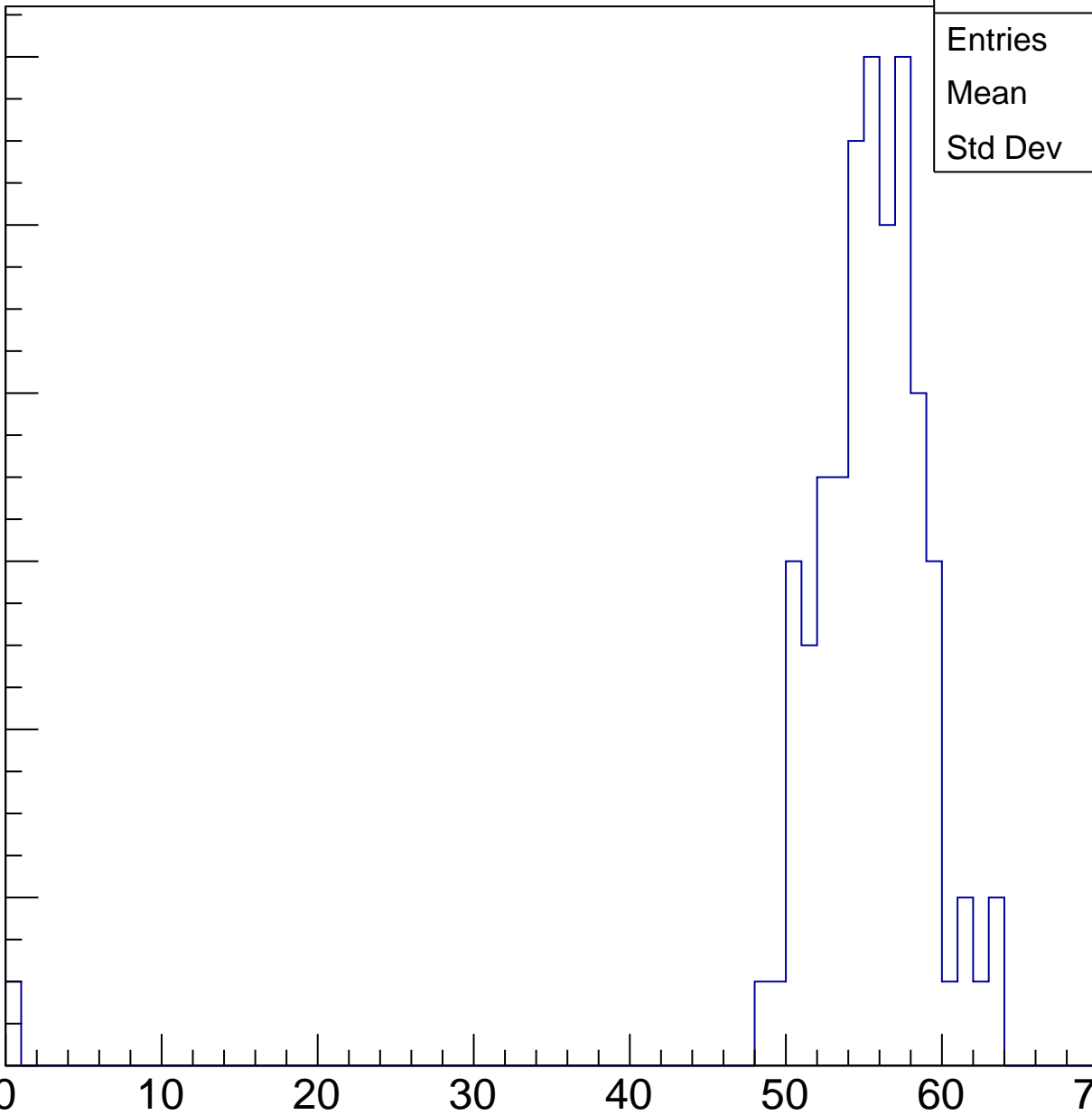
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	93
Mean	54.55
Std Dev	6.495

ampl



B1L001S, U21-ch35, adc5

calib_packv5_042523_0143.root, FC#2, port C2

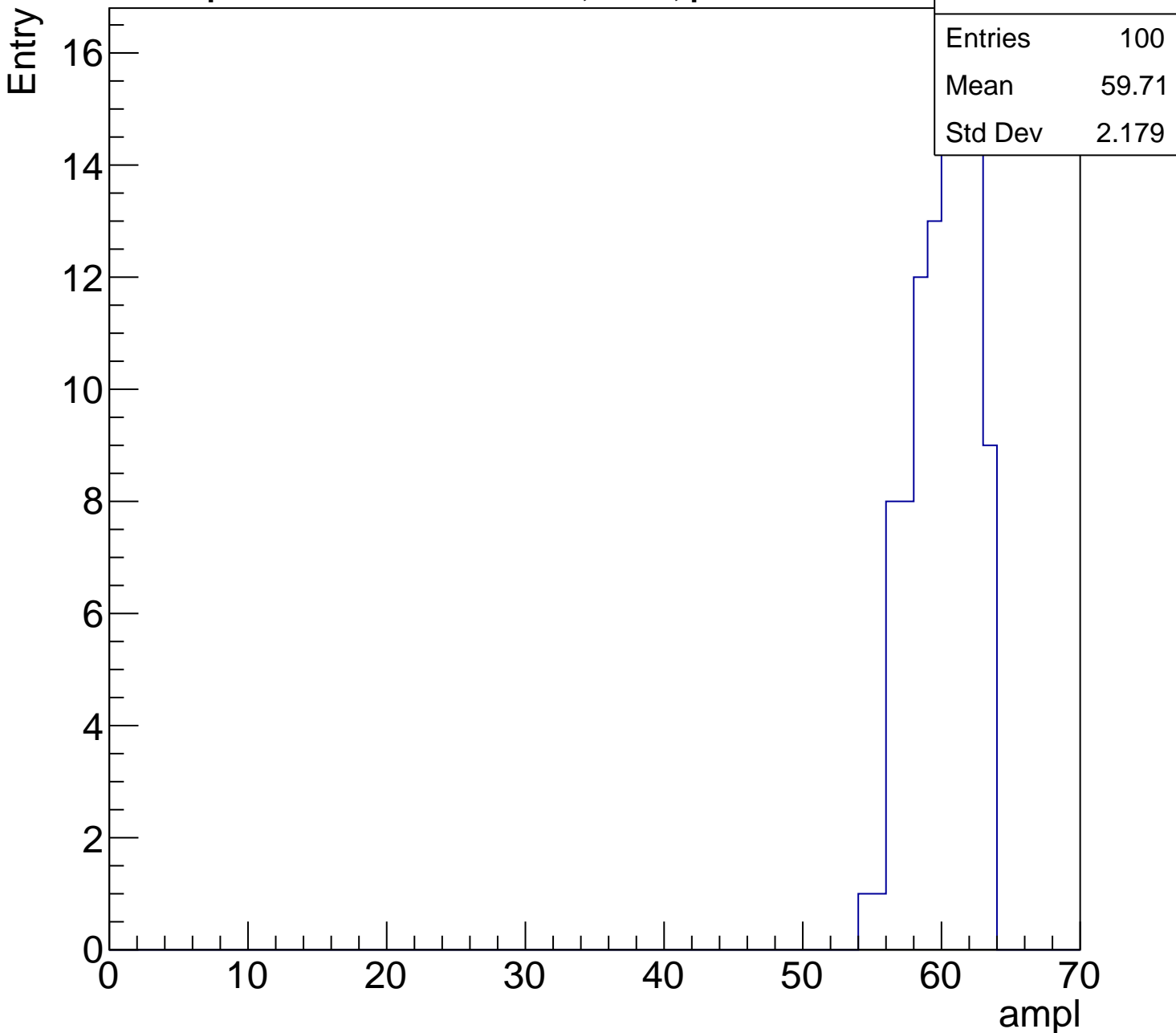
Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.71
Std Dev	2.179

ampl

0 10 20 30 40 50 60 70

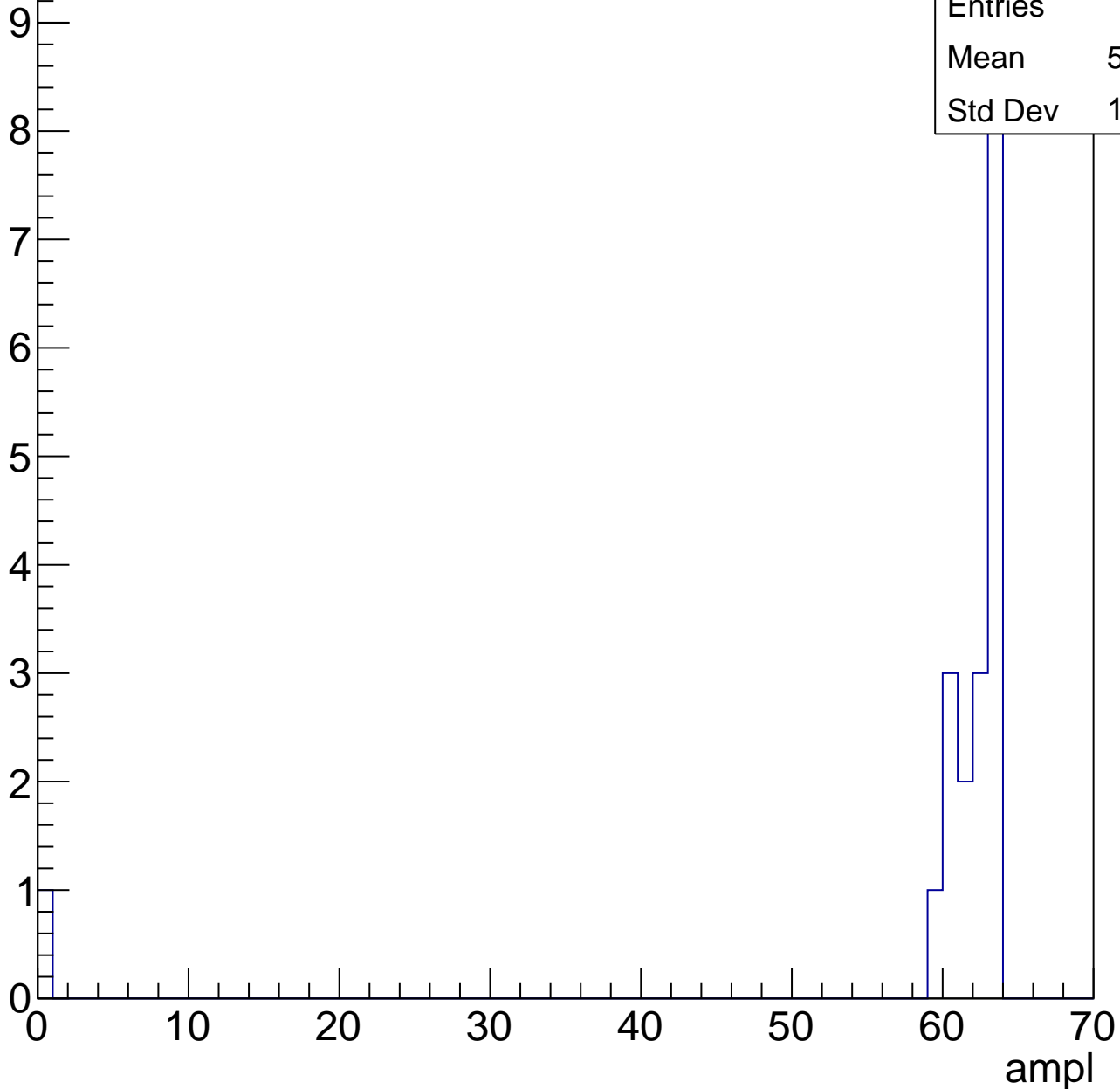


B1L001S, U21-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	58.63
Std Dev	13.88



B1L001S, U21-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U21-ch36, adc0

calib_packv5_042523_0143.root, FC#2, port C2

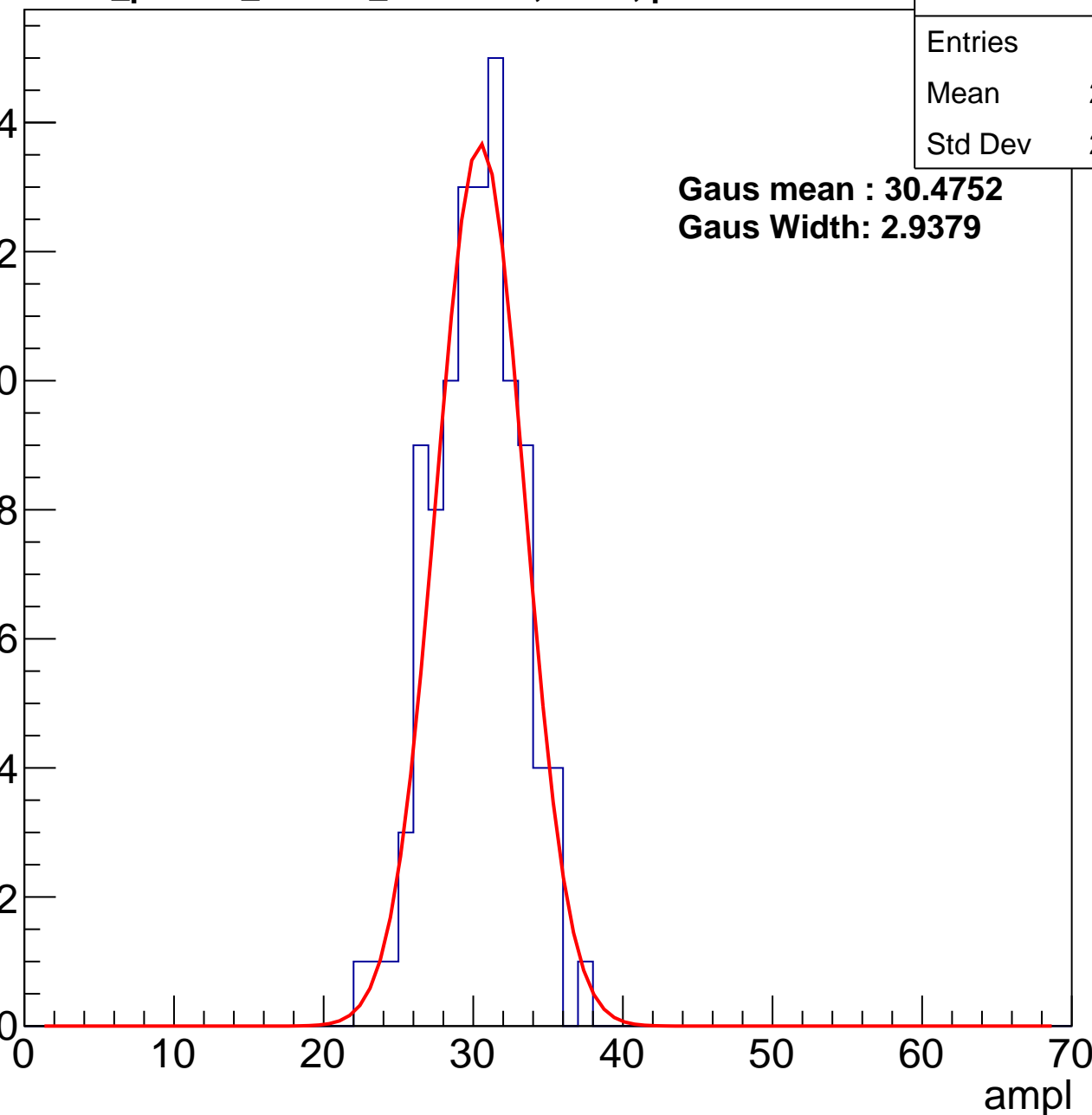
Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	29.76
Std Dev	2.863

Gaus mean : 30.4752

Gaus Width: 2.9379



B1L001S, U21-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

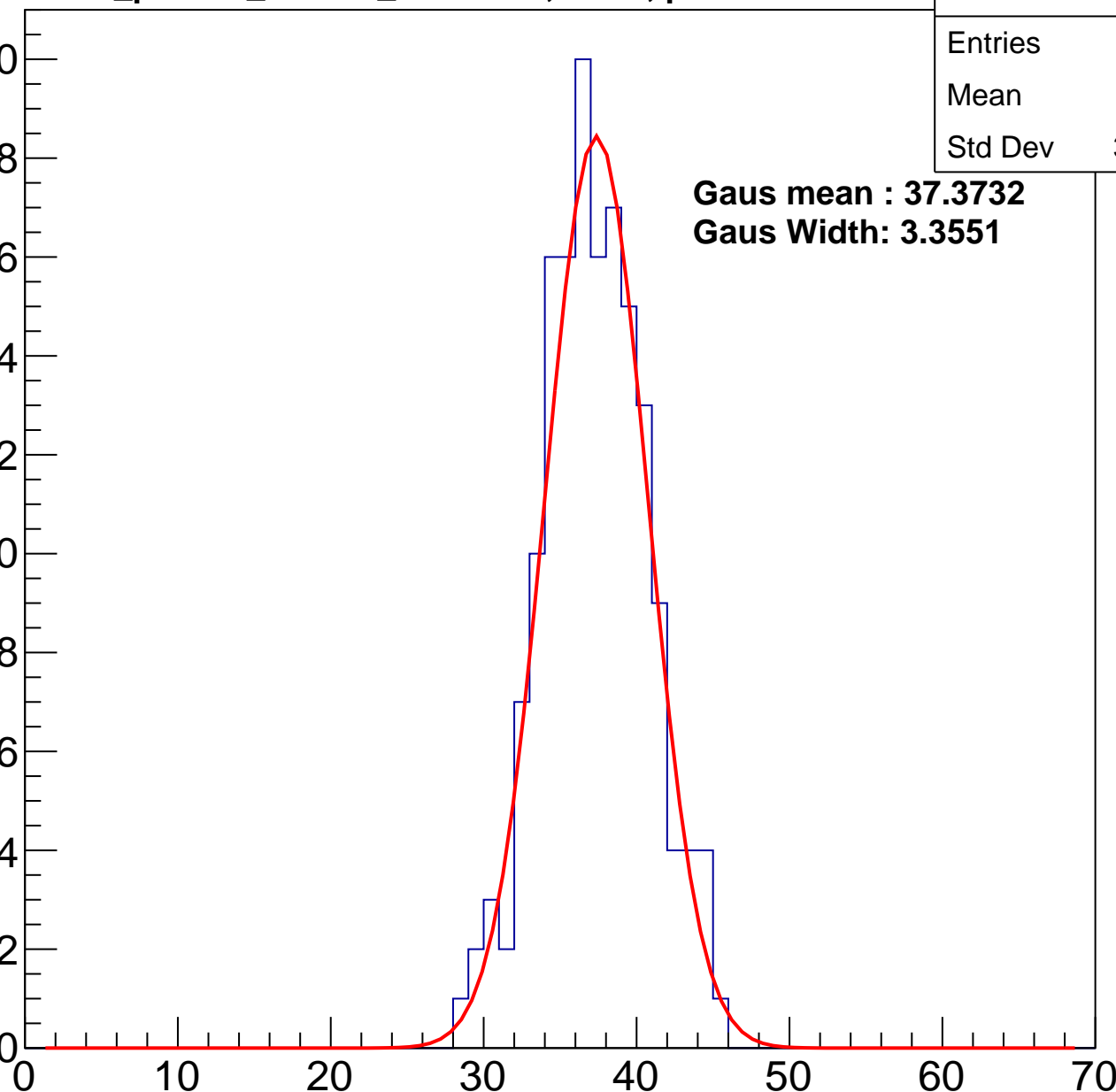
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	160
Mean	36.81
Std Dev	3.379

Gaus mean : 37.3732

Gaus Width: 3.3551



ampl

B1L001S, U21-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

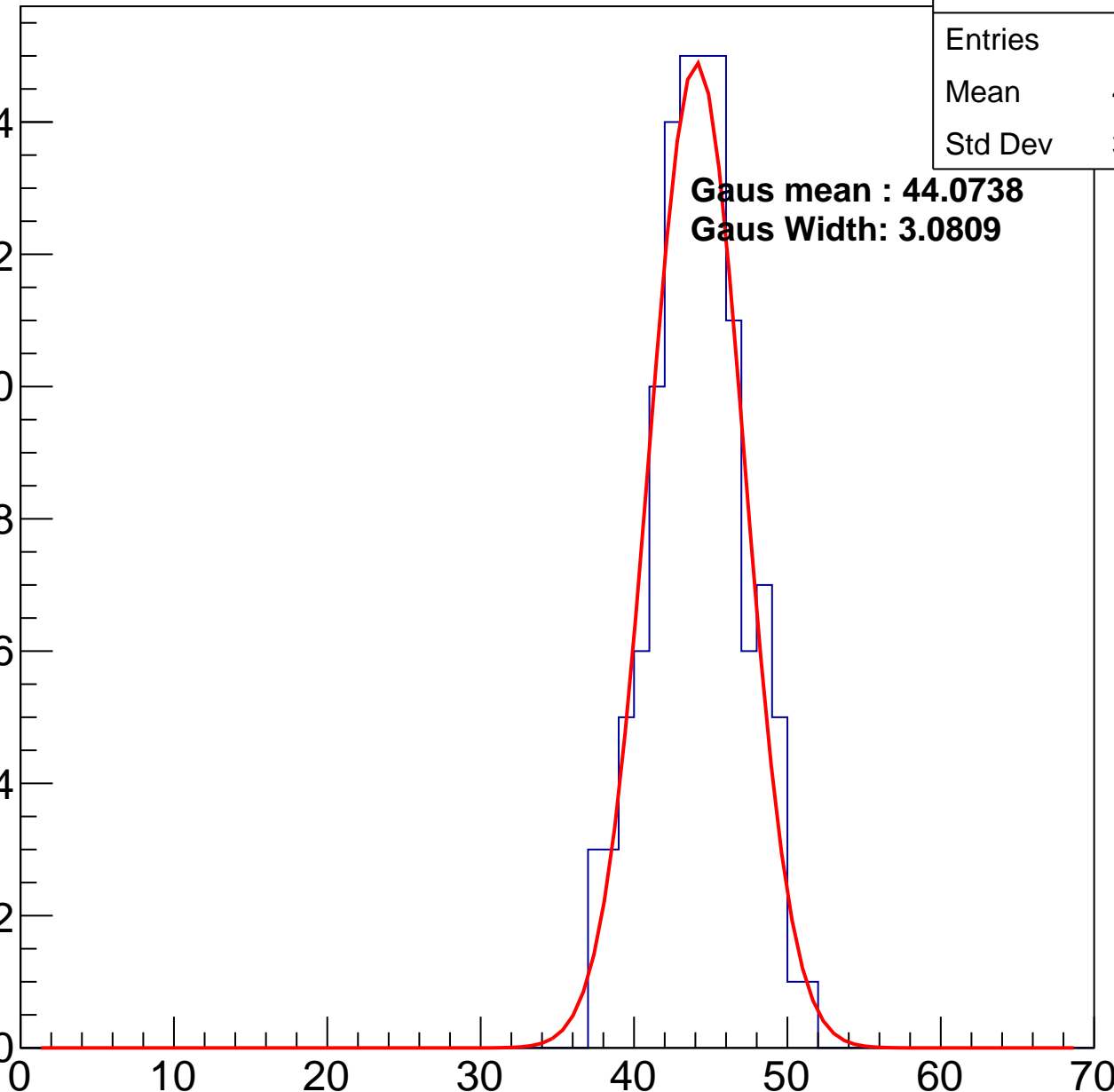
14
12
10
8
6
4
2
0

Entries	117
Mean	43.66
Std Dev	3.008

Gaus mean : 44.0738

Gaus Width: 3.0809

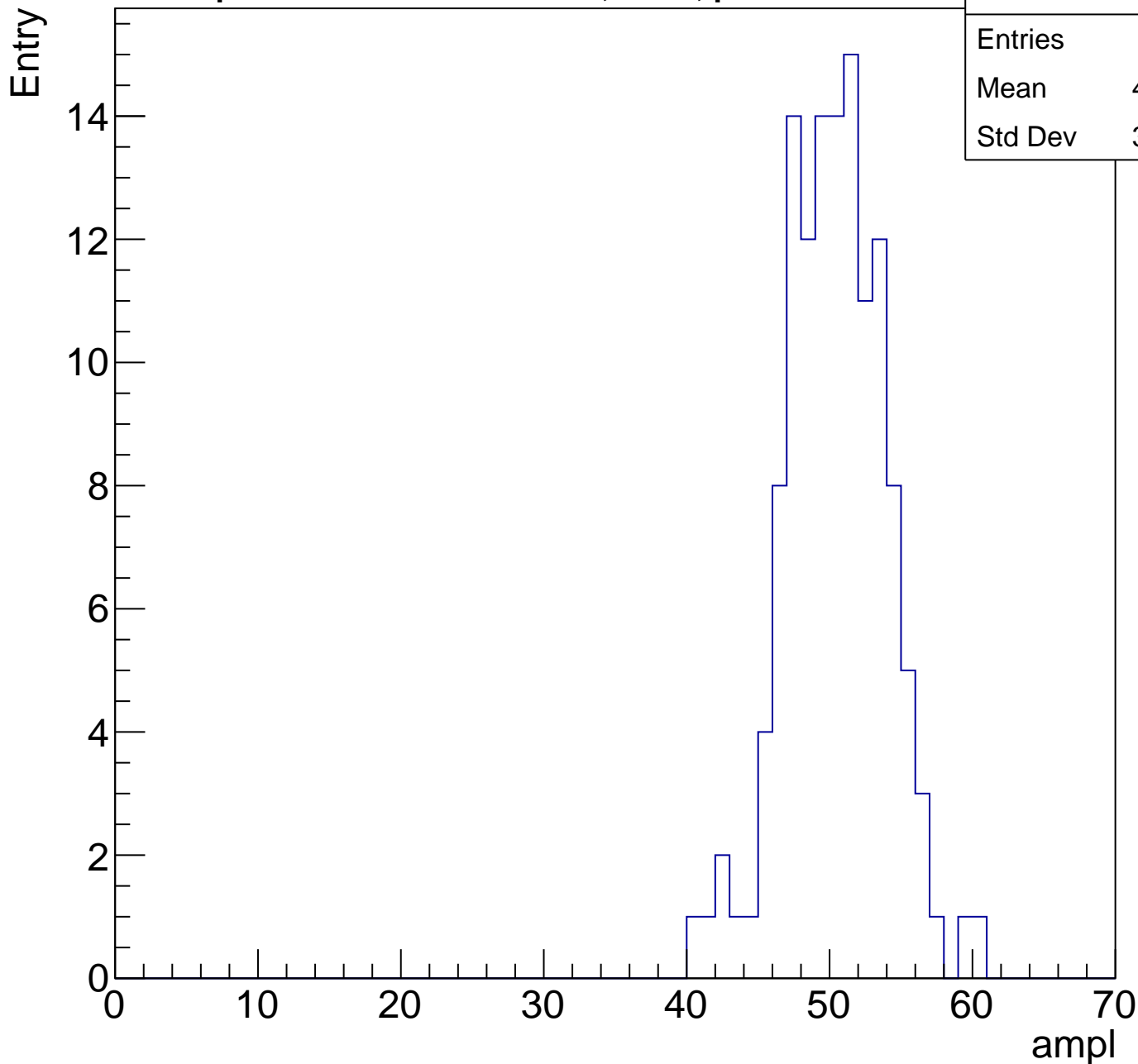
ampl



B1L001S, U21-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

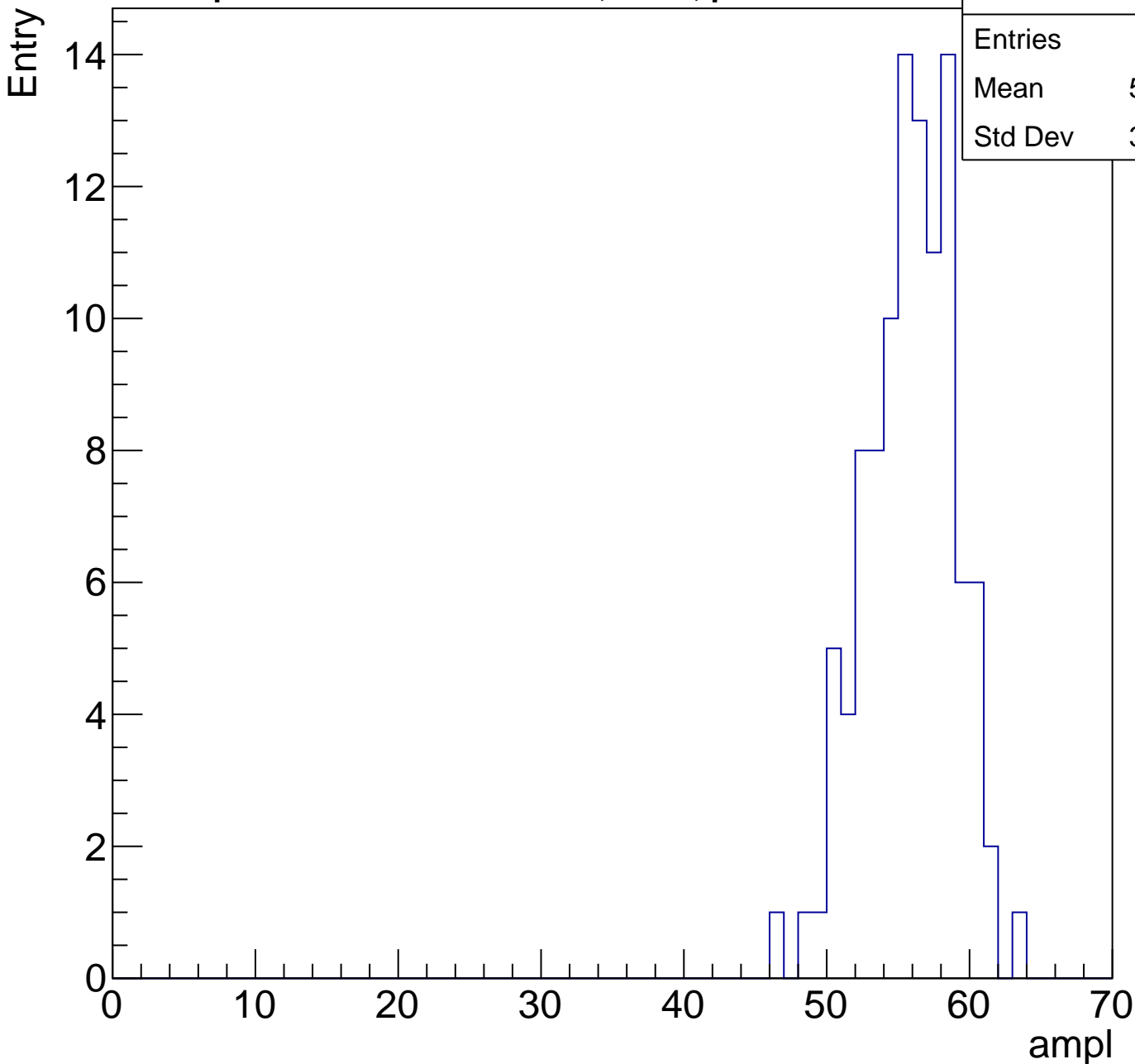
Entries	129
Mean	49.95
Std Dev	3.486



B1L001S, U21-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	55.36
Std Dev	3.117



B1L001S, U21-ch36, adc5

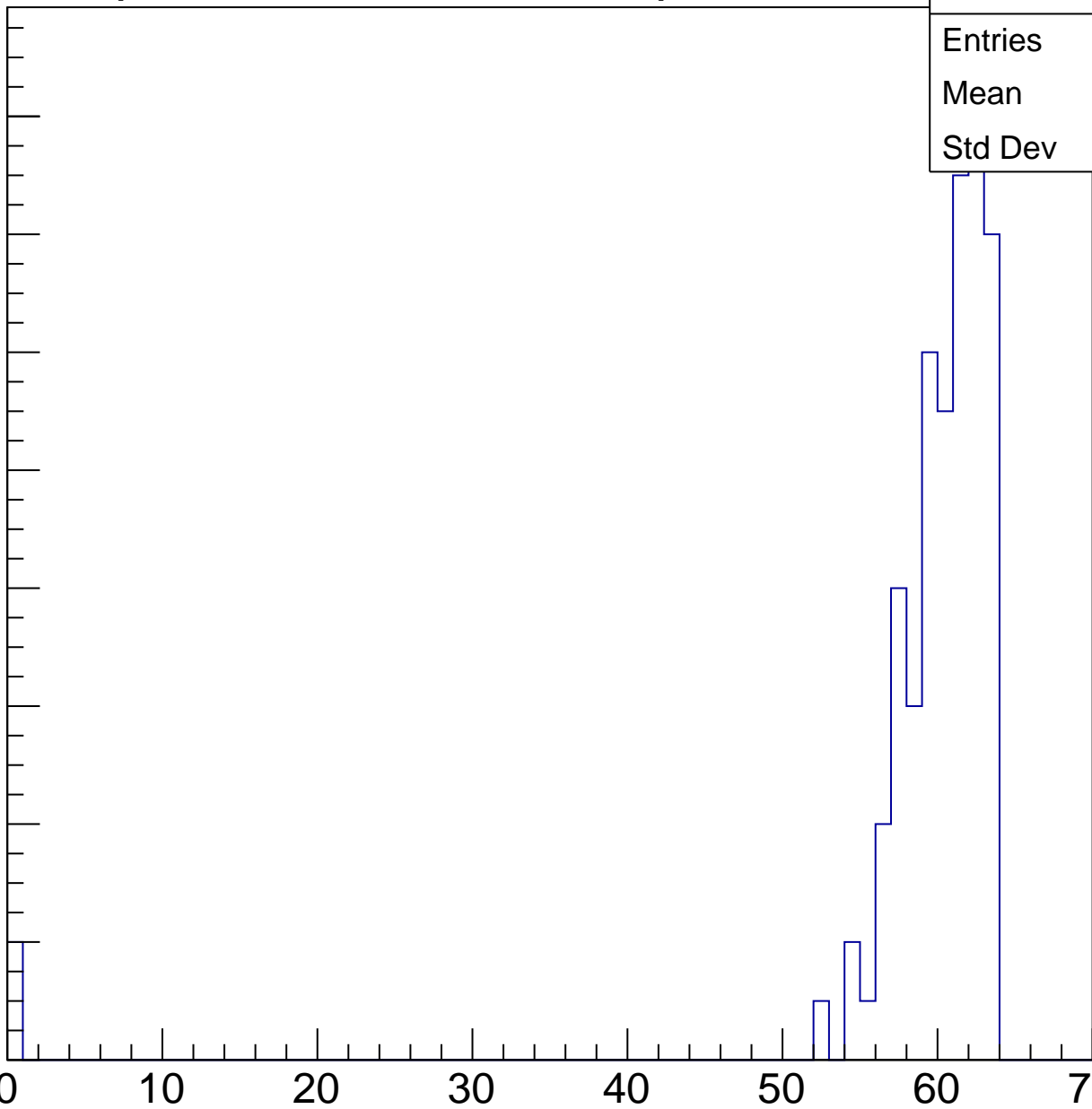
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.73
Std Dev	9.035

ampl

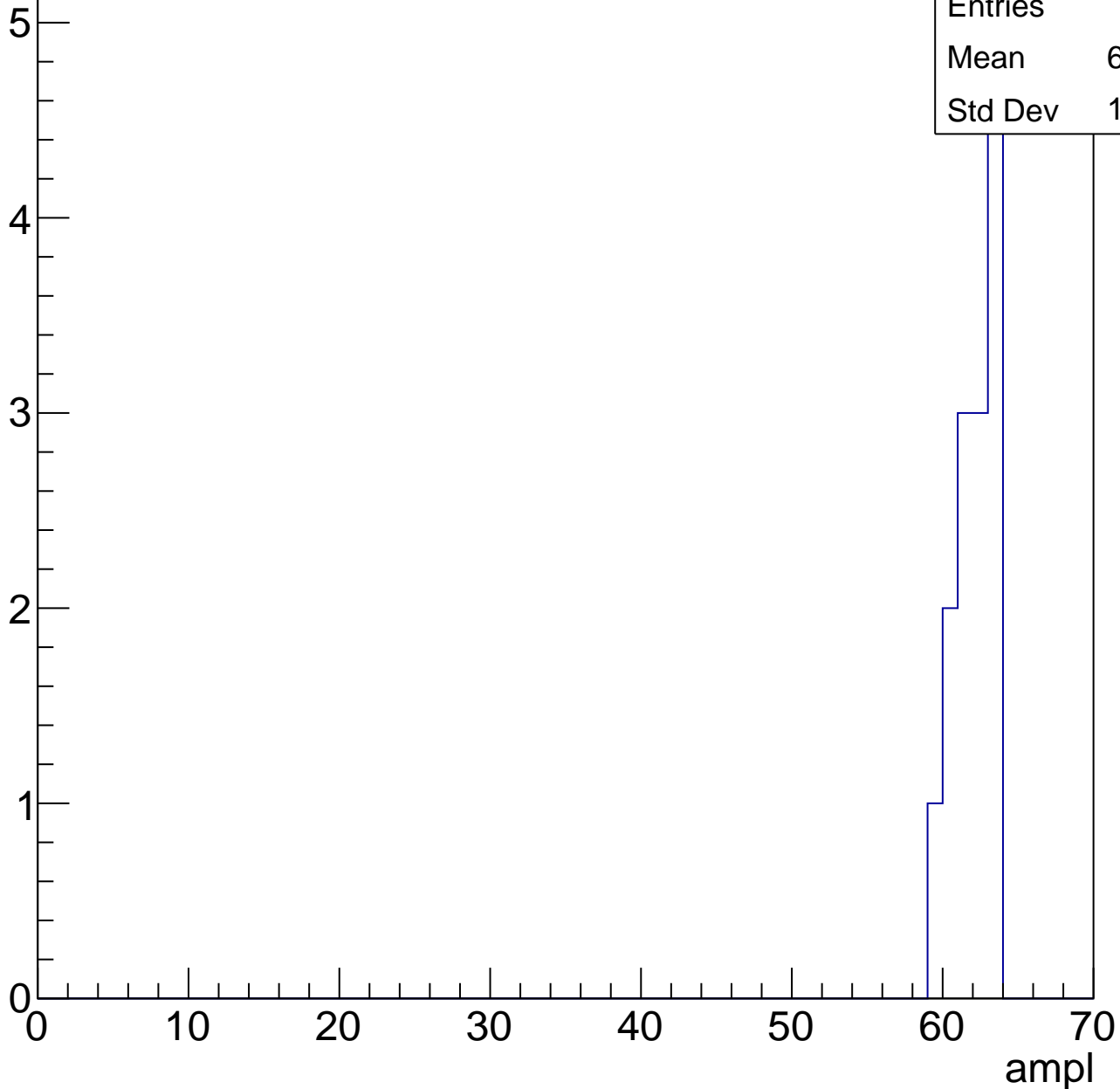


B1L001S, U21-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	61.64
Std Dev	1.288



B1L001S, U21-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch37, adc0

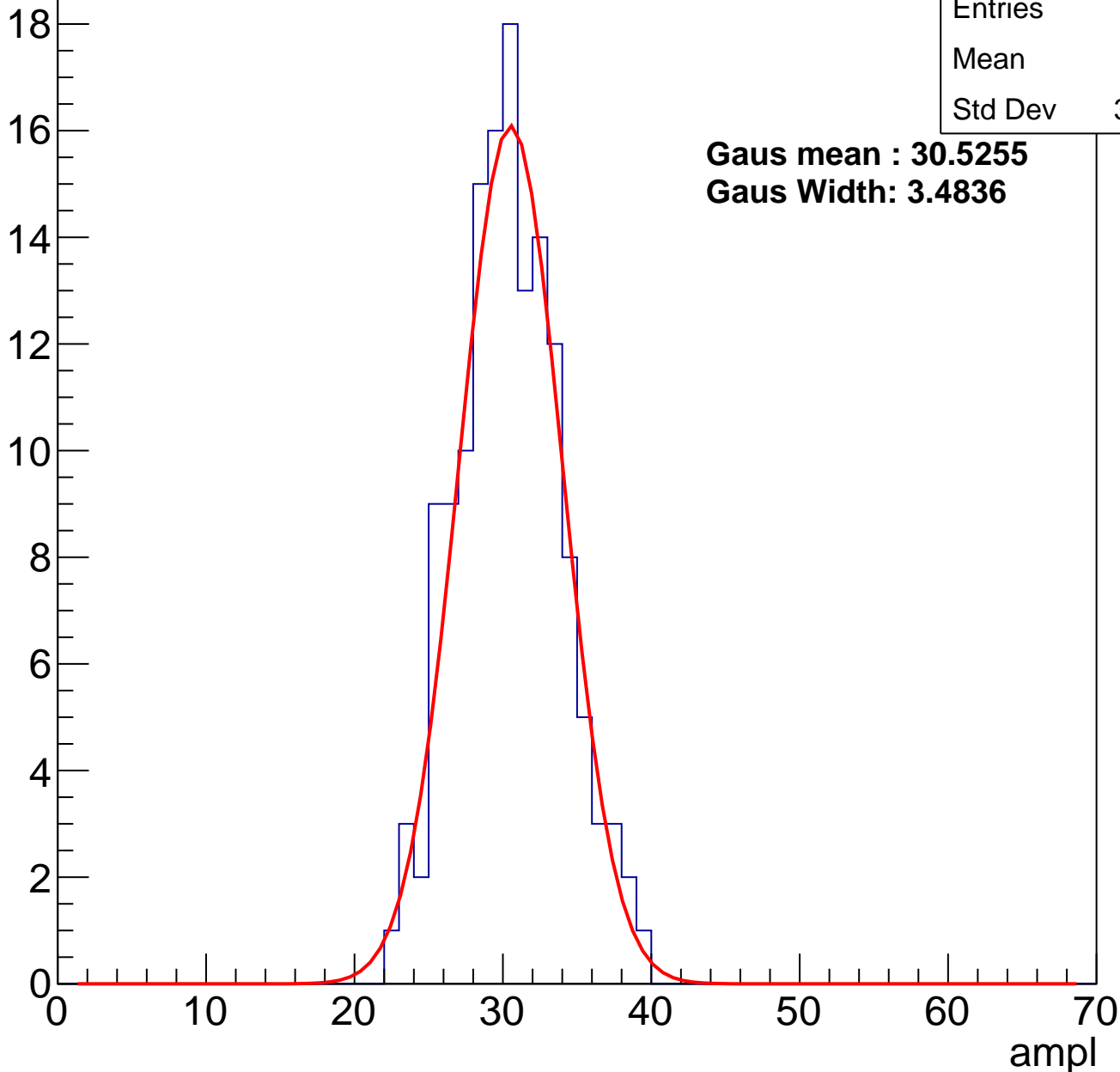
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	30
Std Dev	3.448

Gaus mean : 30.5255

Gaus Width: 3.4836

Entry

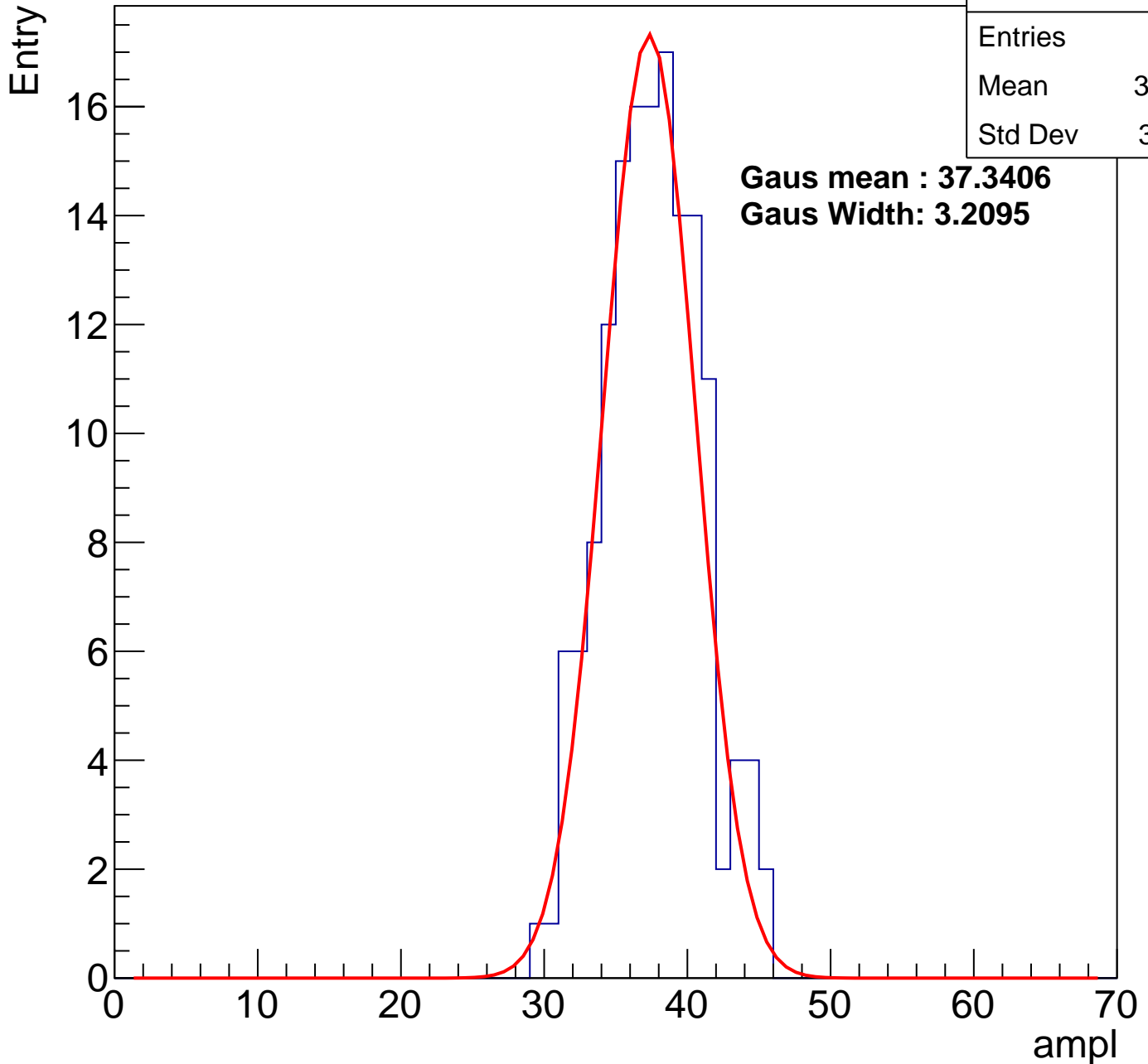


B1L001S, U21-ch37, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.09
Std Dev	3.361

Gaus mean : 37.3406
Gaus Width: 3.2095



B1L001S, U21-ch37, adc2

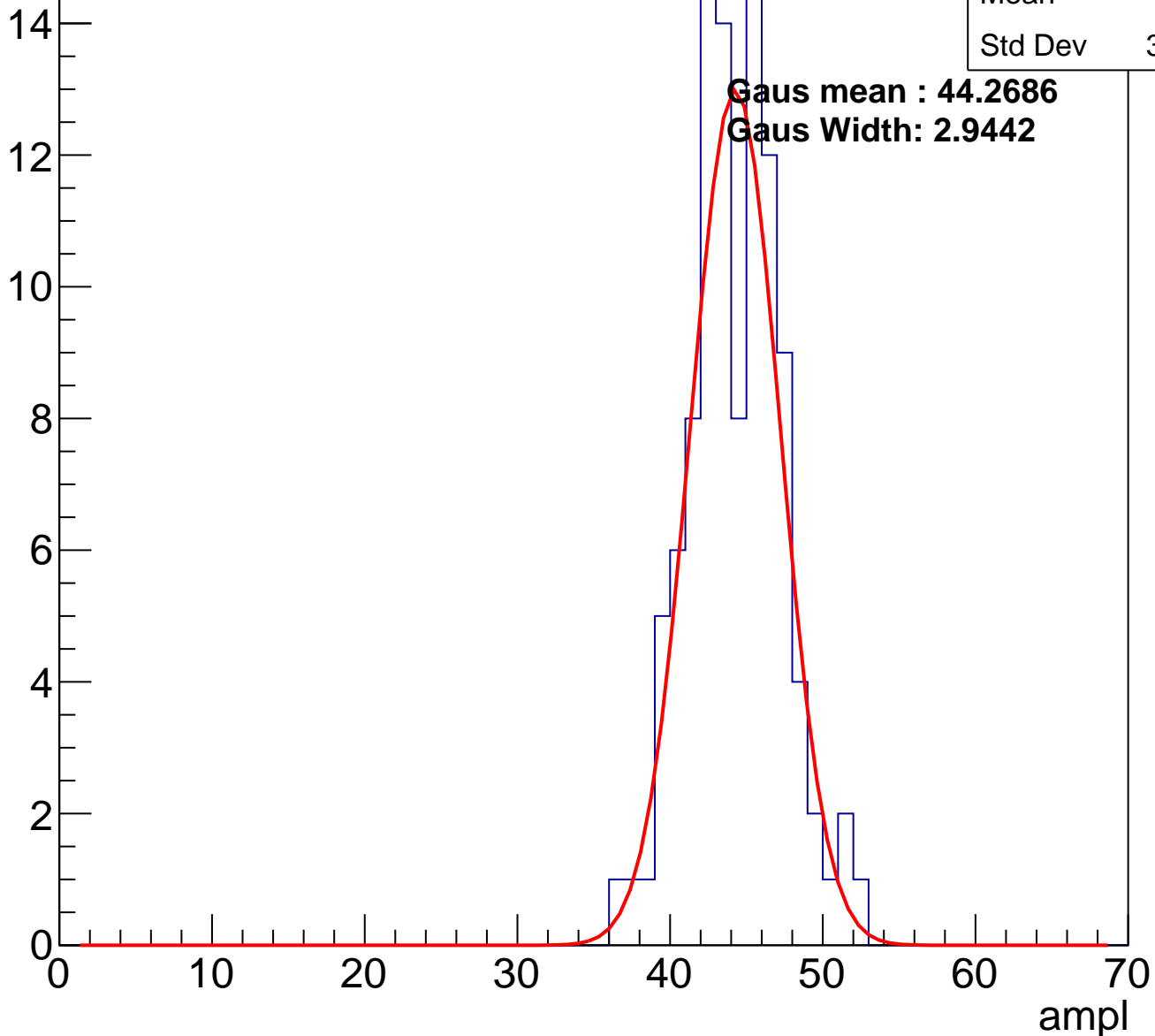
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	43.83
Std Dev	3.038

Gaus mean : 44.2686

Gaus Width: 2.9442

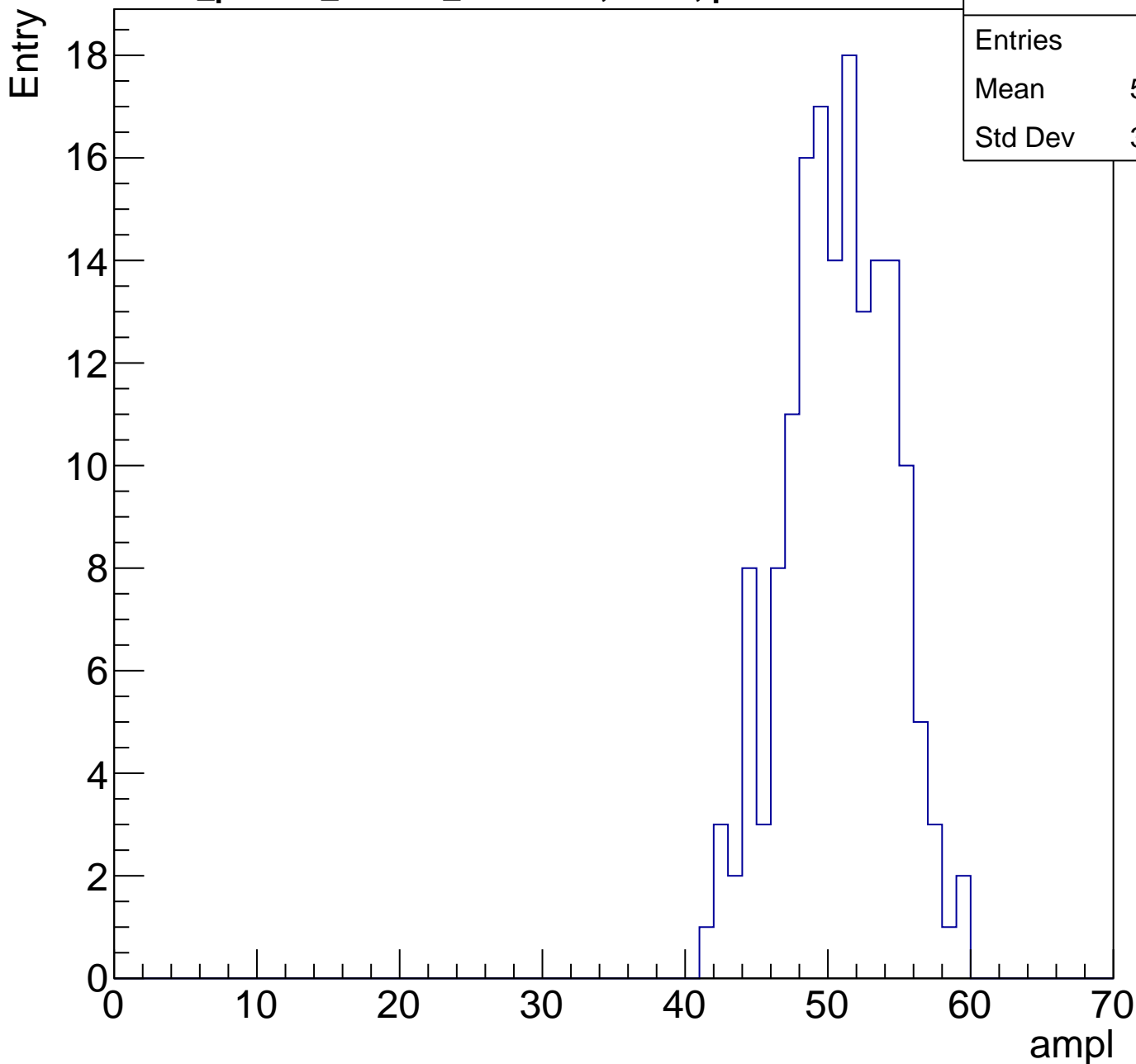
Entry



B1L001S, U21-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	50.28
Std Dev	3.717



B1L001S, U21-ch37, adc4

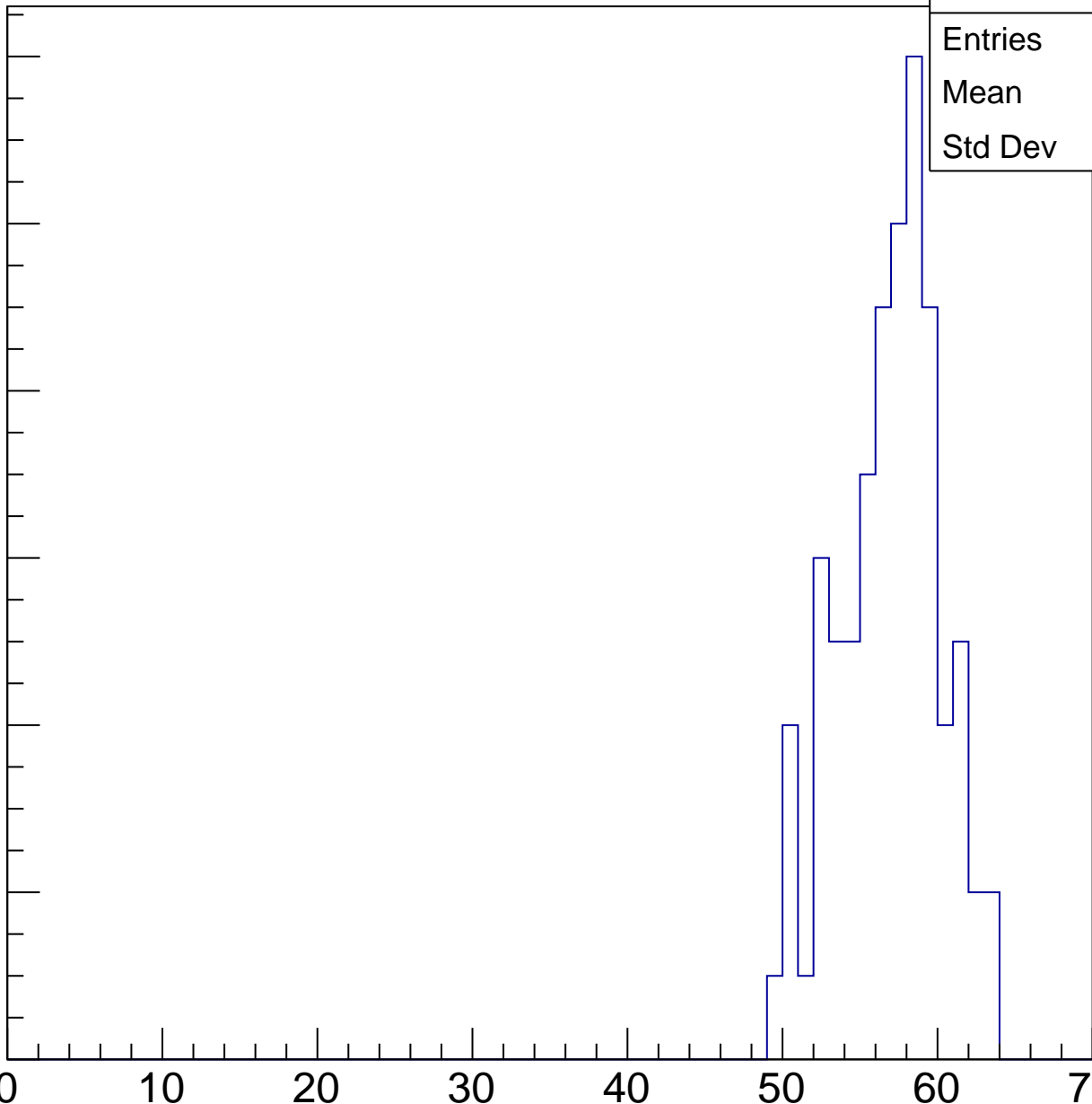
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	82
Mean	56.44
Std Dev	3.254

ampl



B1L001S, U21-ch37, adc5

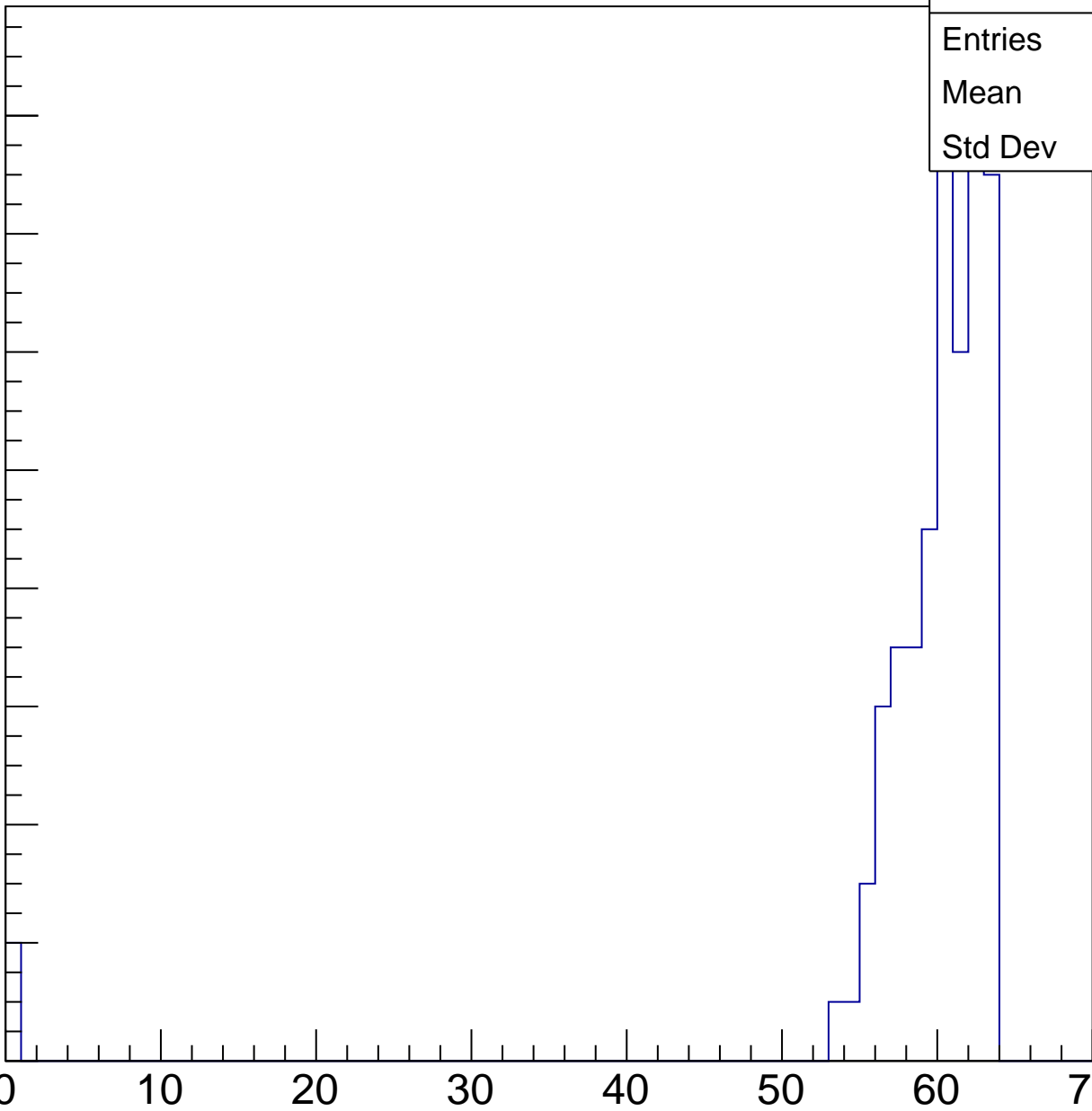
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.7
Std Dev	8.902

ampl



B1L001S, U21-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

7

Mean

62

Std Dev

0.9258

B1L001S, U21-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L001S, U21-ch38, adc0

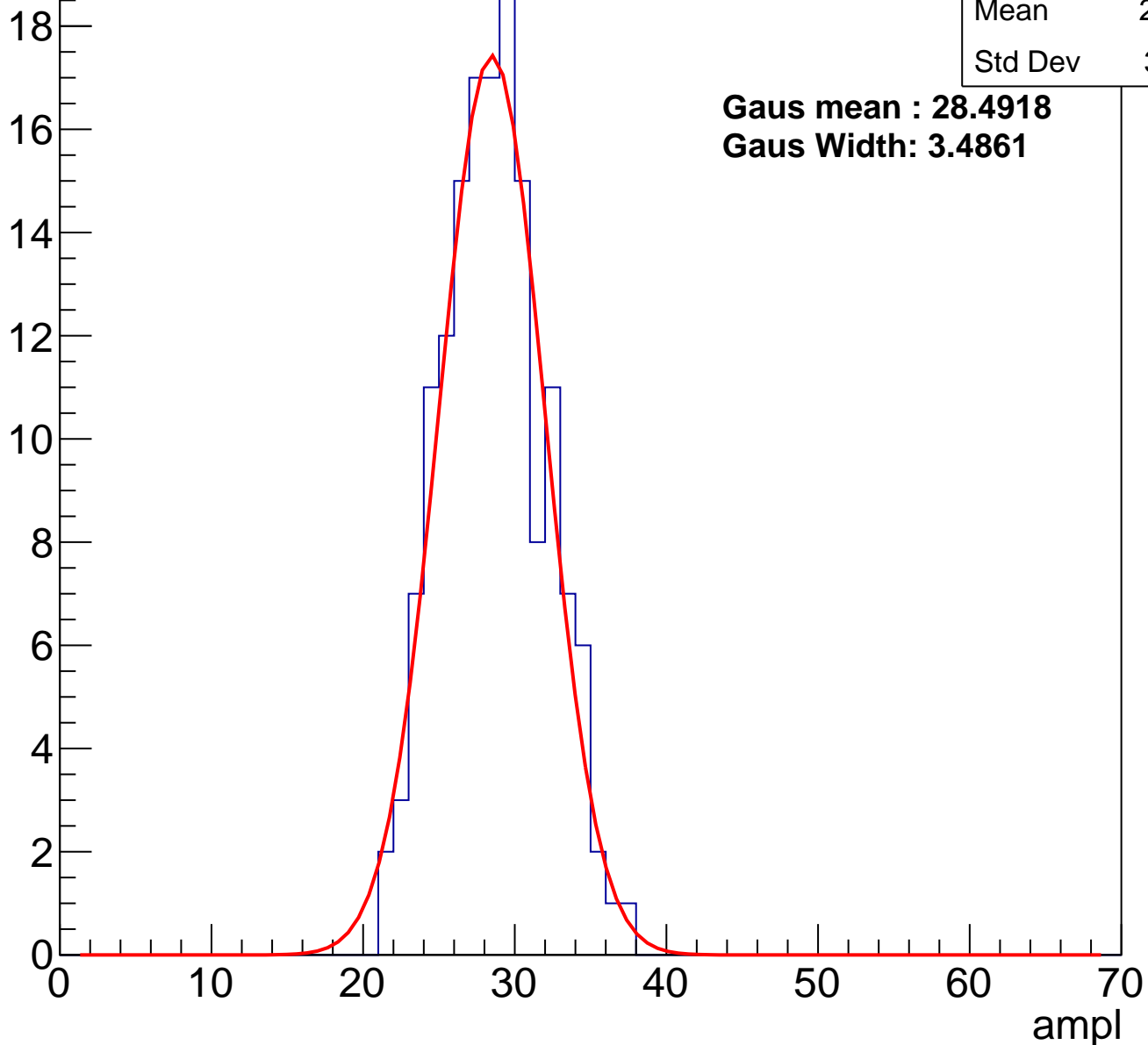
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	28.16
Std Dev	3.321

Gaus mean : 28.4918

Gaus Width: 3.4861

Entry

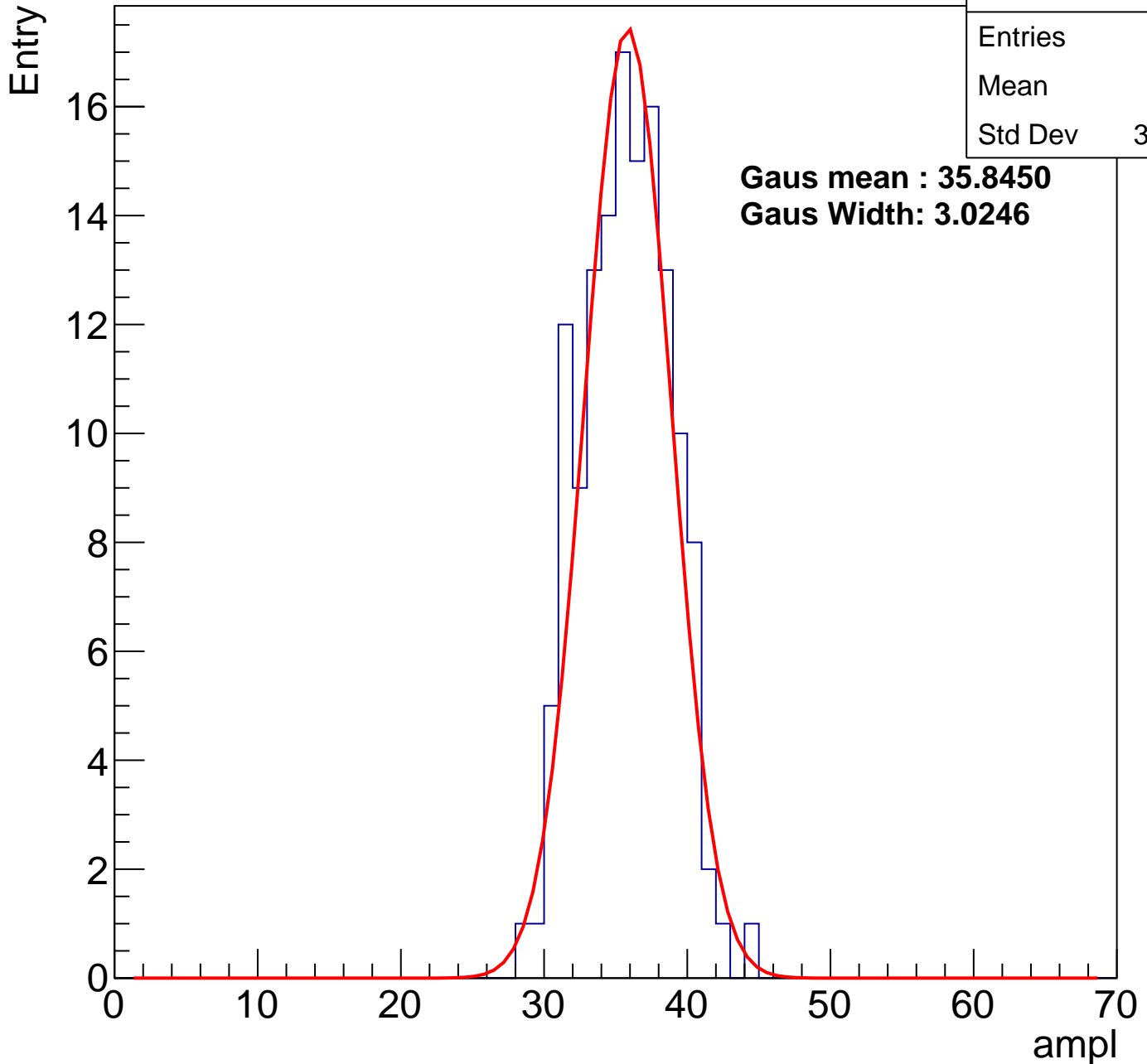


B1L001S, U21-ch38, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.3
Std Dev	3.063

Gaus mean : 35.8450
Gaus Width: 3.0246



B1L001S, U21-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

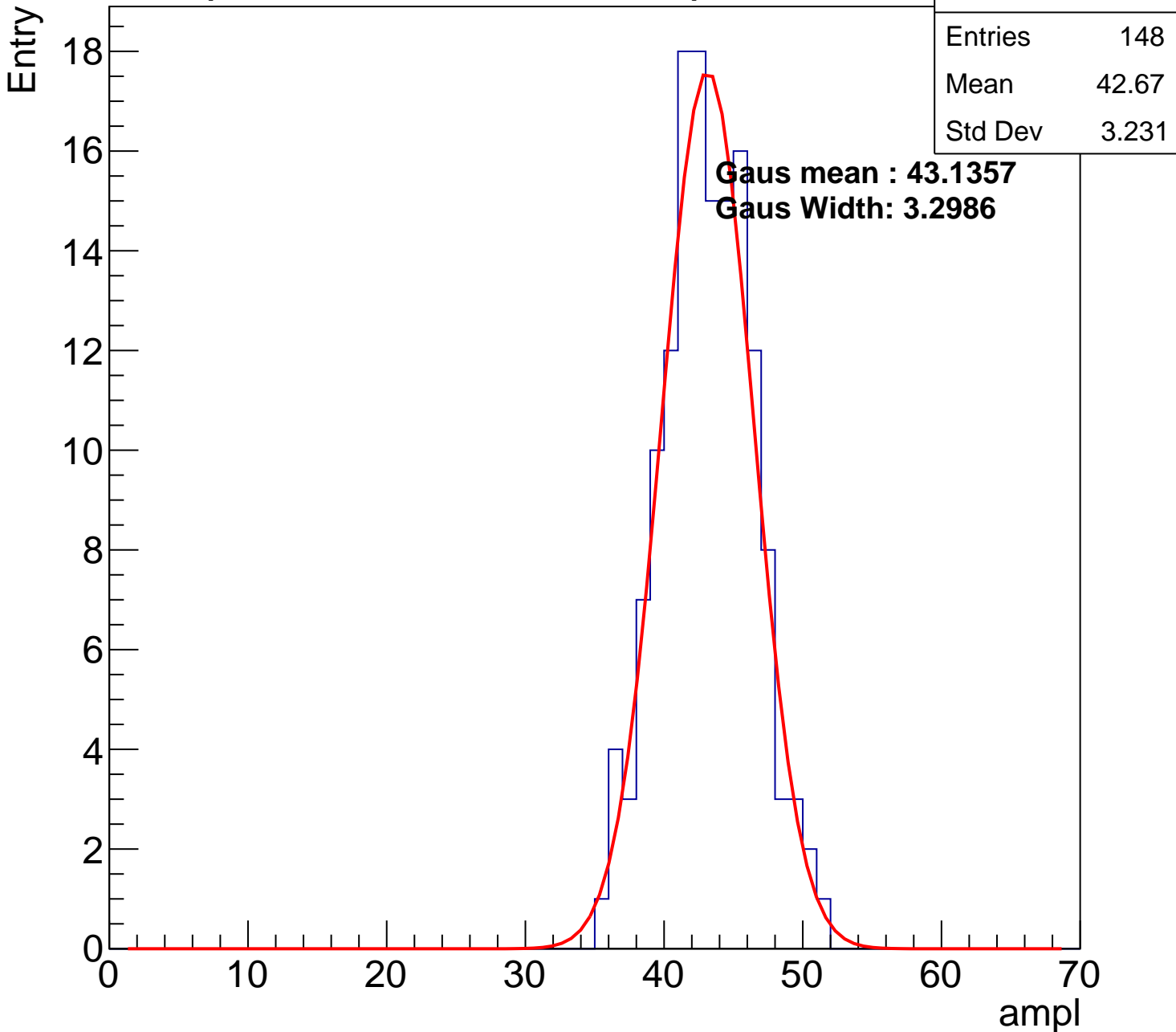
Entries	148
Mean	42.67
Std Dev	3.231

Gaus mean : 43.1357

Gaus Width: 3.2986

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

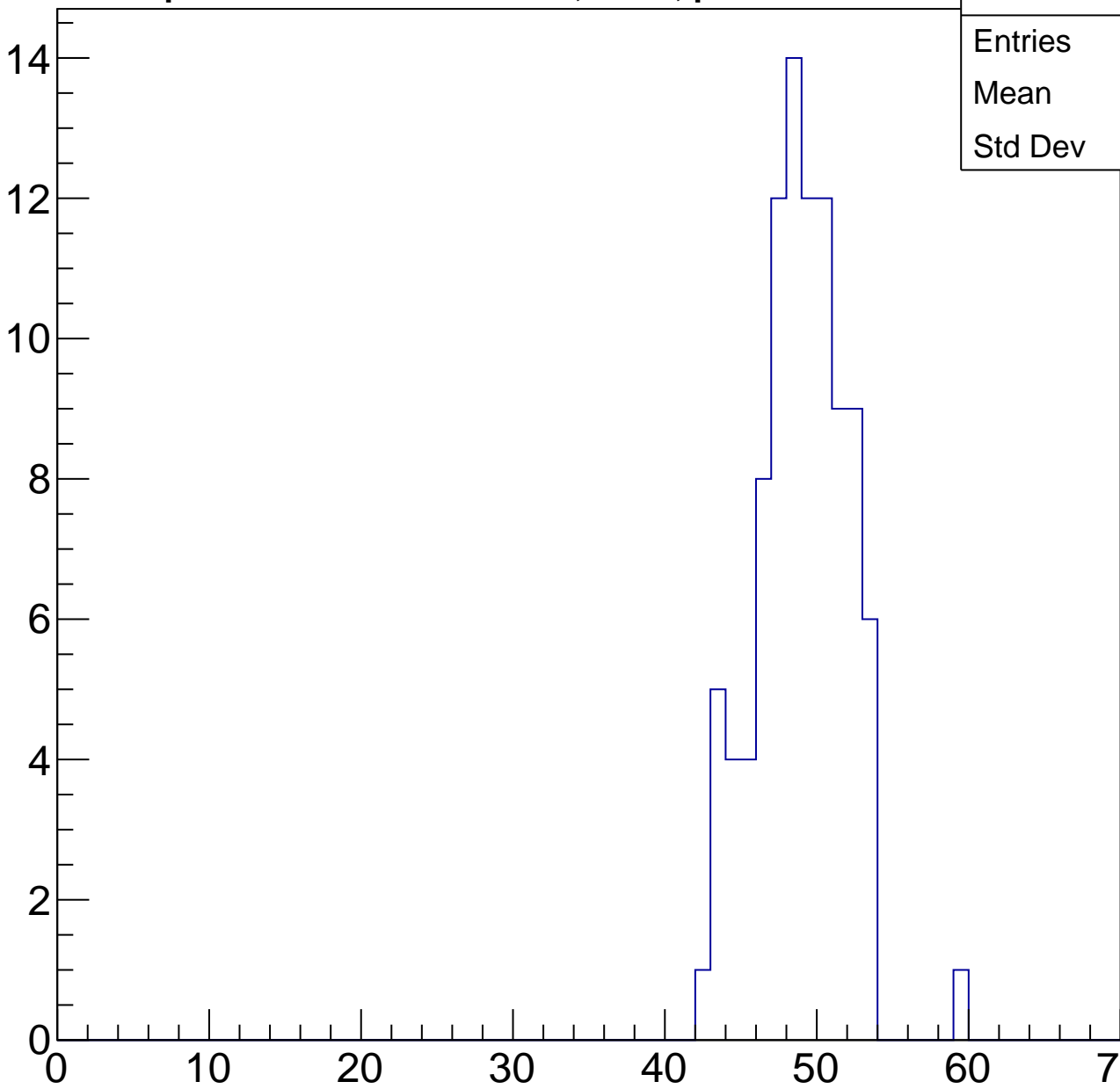
Entries	97
Mean	48.55
Std Dev	2.929

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

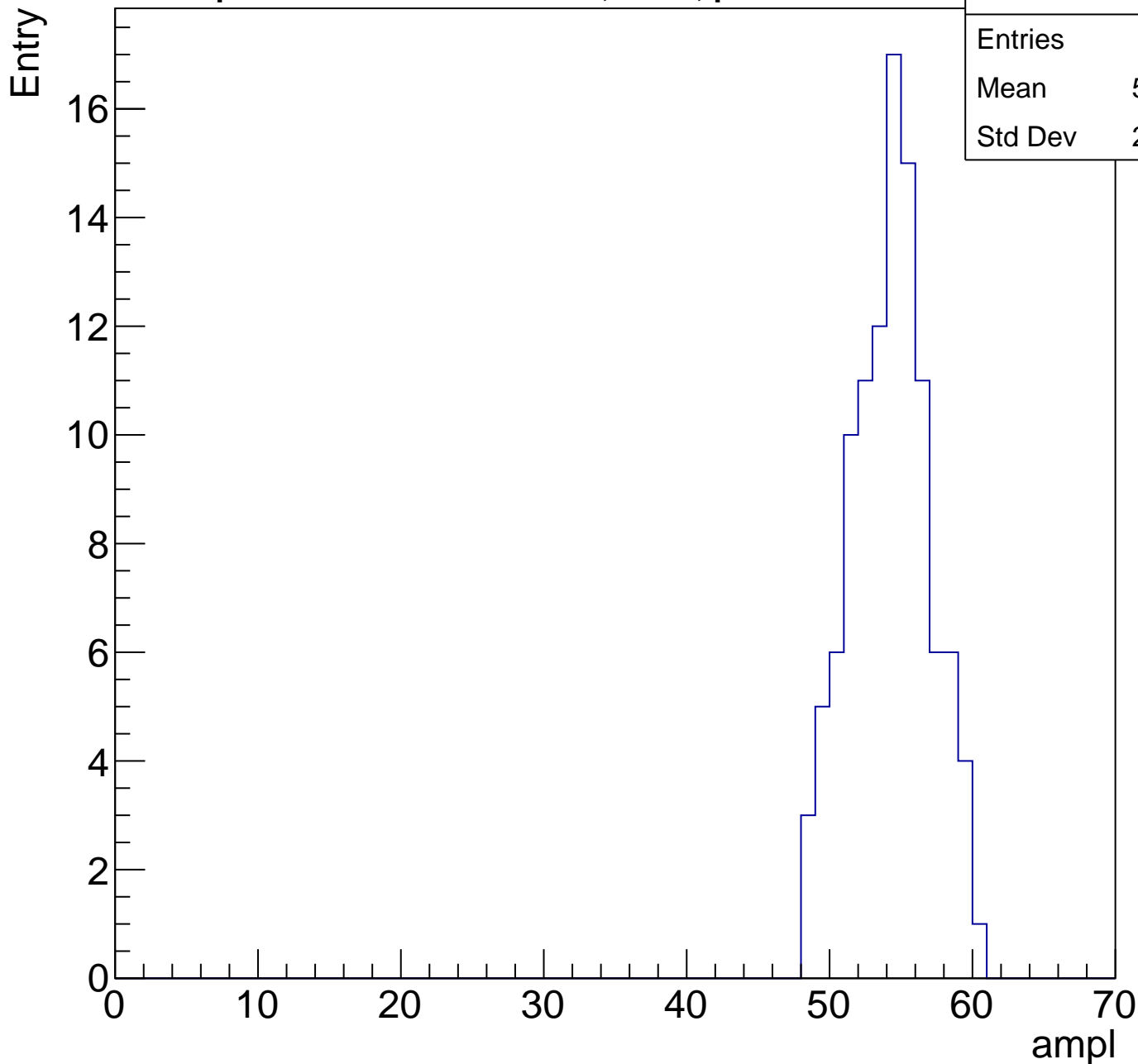
ampl



B1L001S, U21-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	53.76
Std Dev	2.758



B1L001S, U21-ch38, adc5

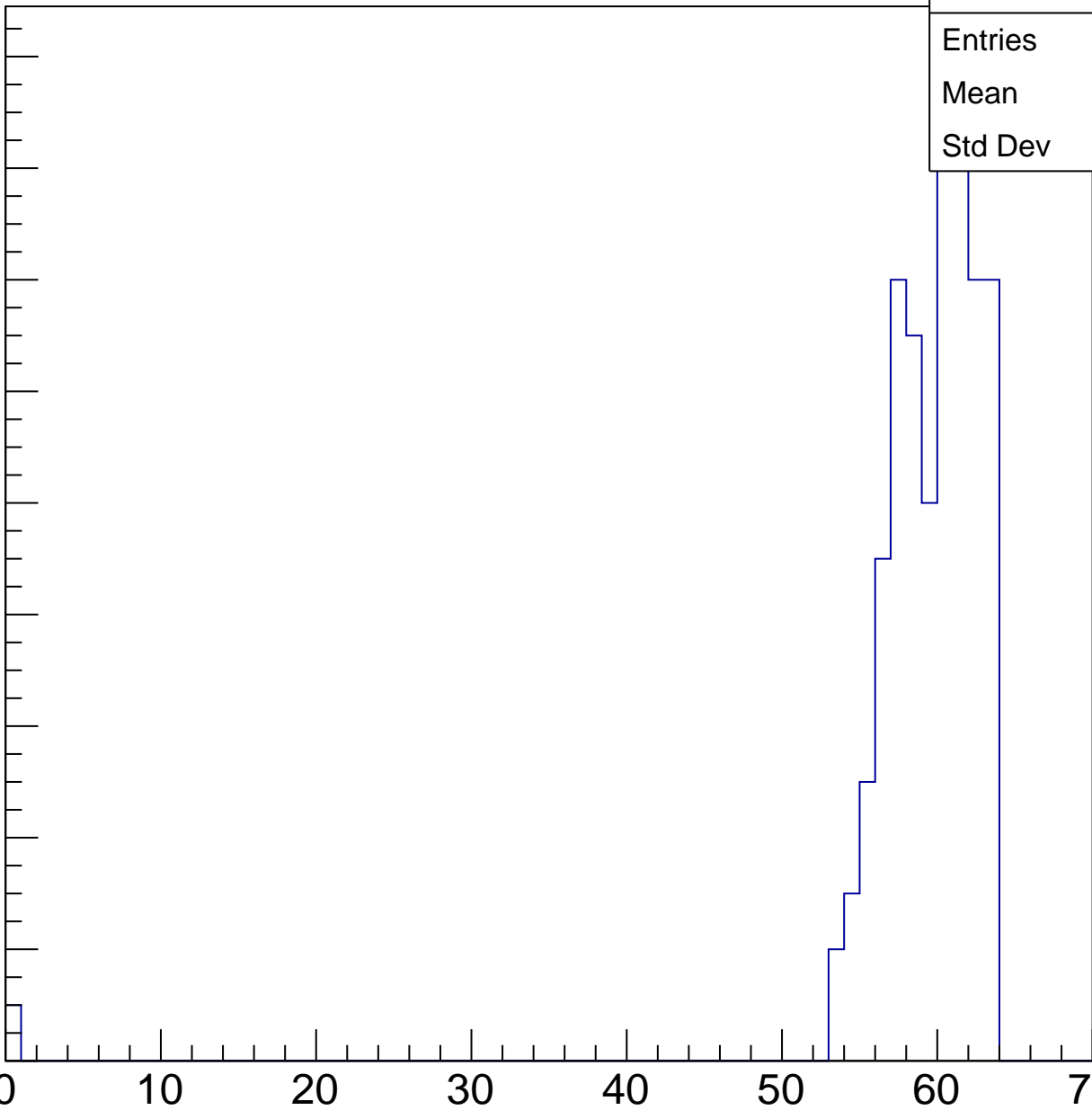
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	120
Mean	58.81
Std Dev	5.981

ampl

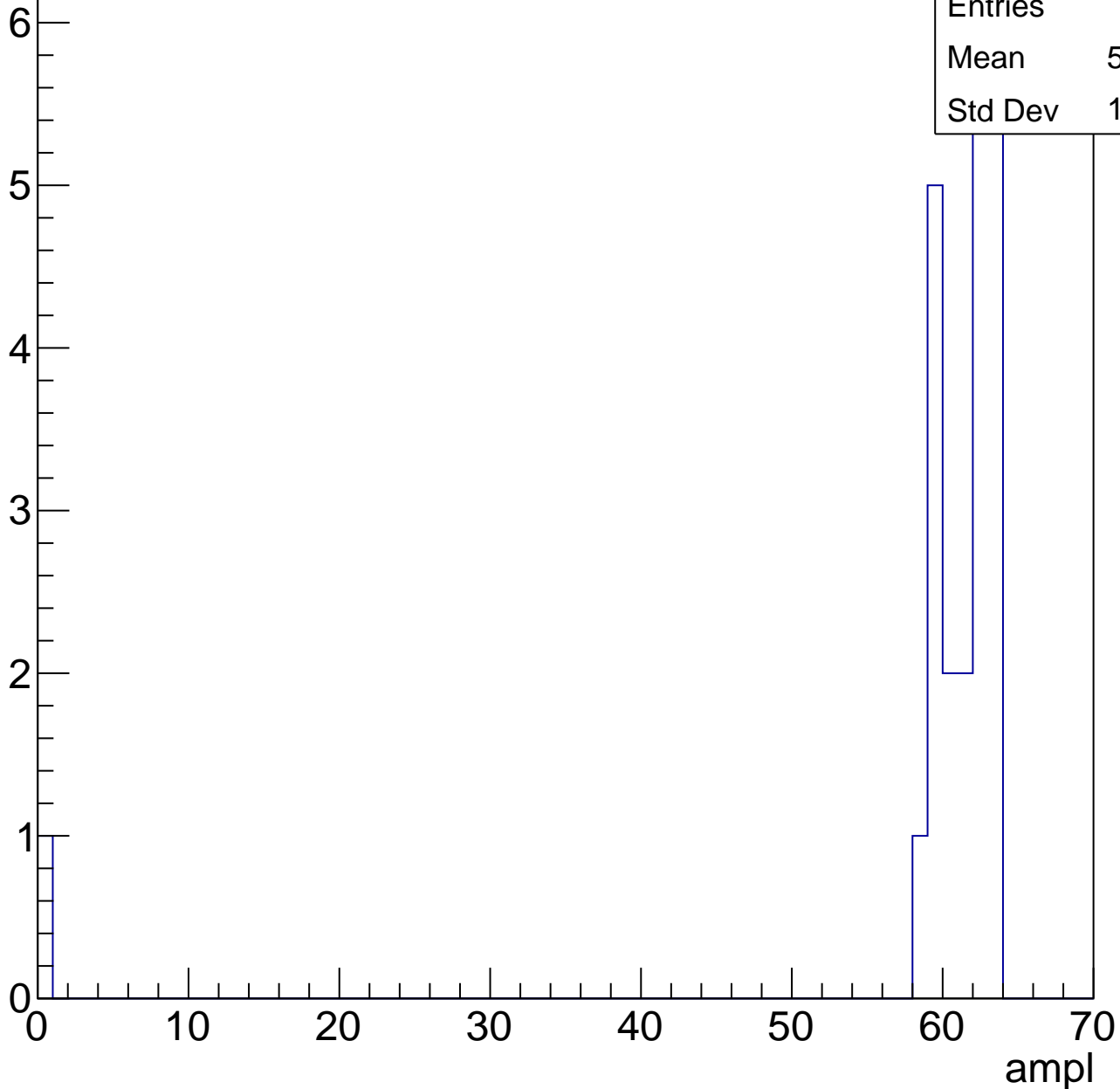


B1L001S, U21-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	58.48
Std Dev	12.57



B1L001S, U21-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



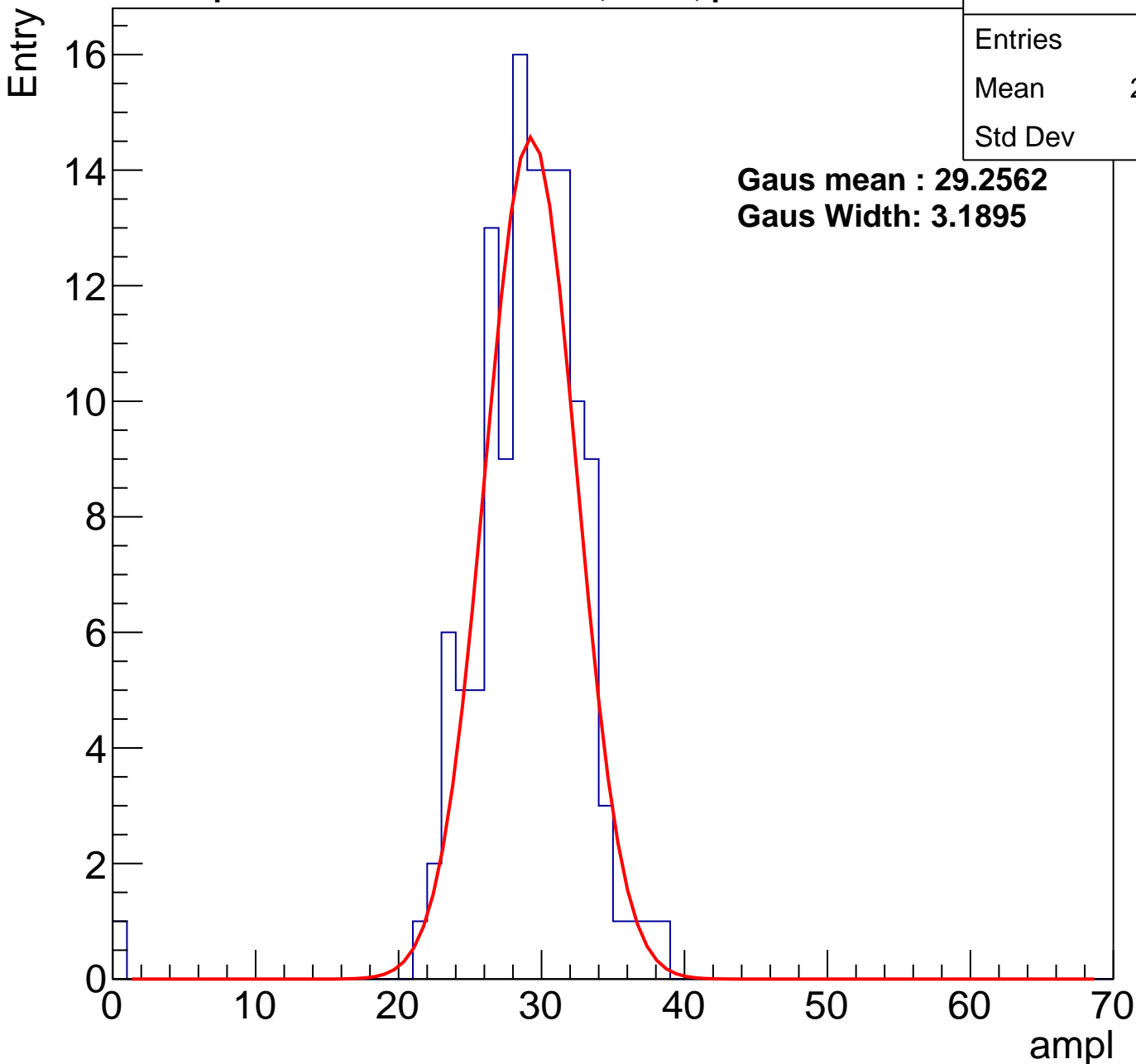
B1L001S, U21-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	28.59
Std Dev	4.16

Gaus mean : 29.2562

Gaus Width: 3.1895



B1L001S, U21-ch39, adc1

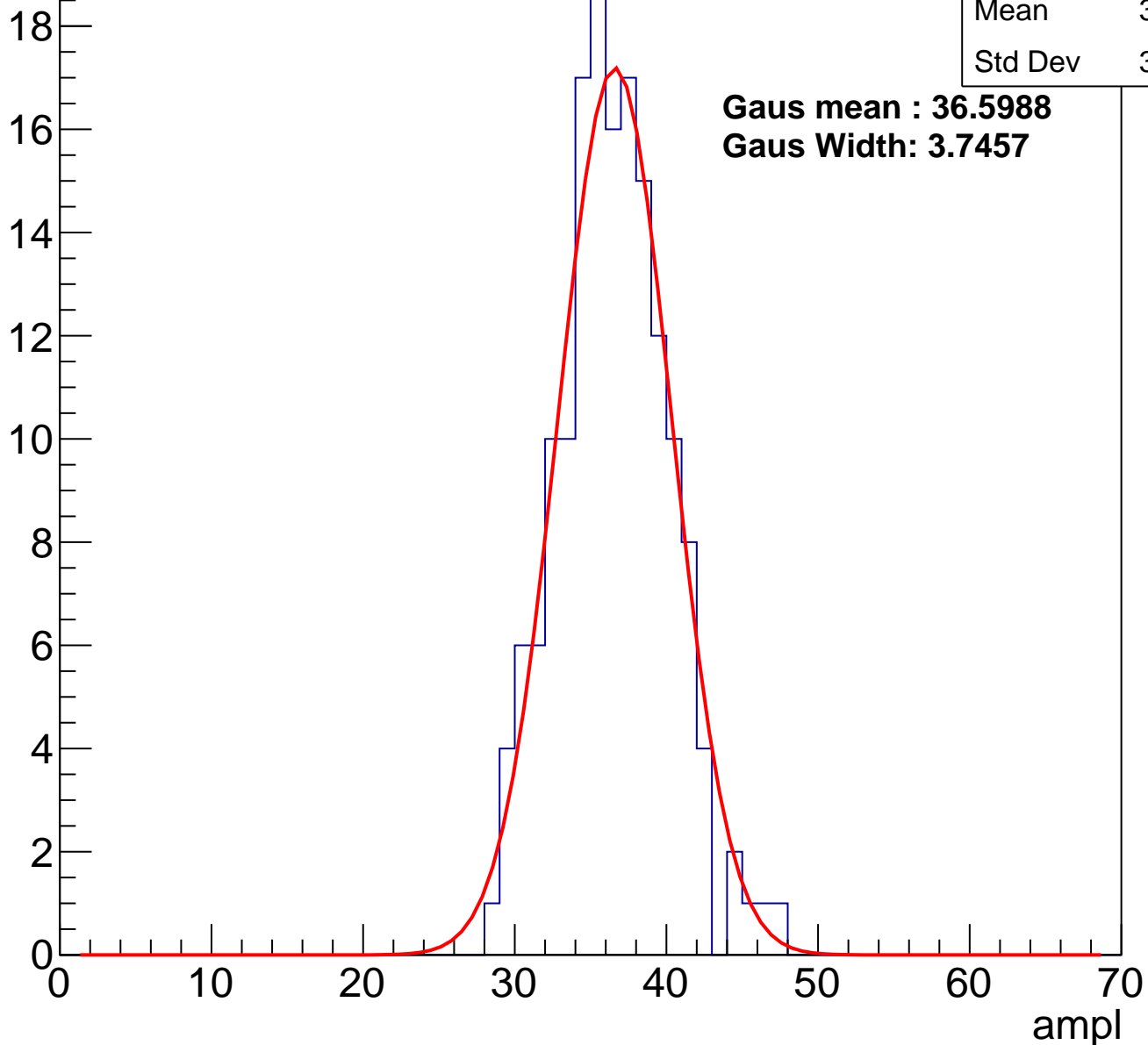
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	36.05
Std Dev	3.607

Gaus mean : 36.5988

Gaus Width: 3.7457

Entry



B1L001S, U21-ch39, adc2

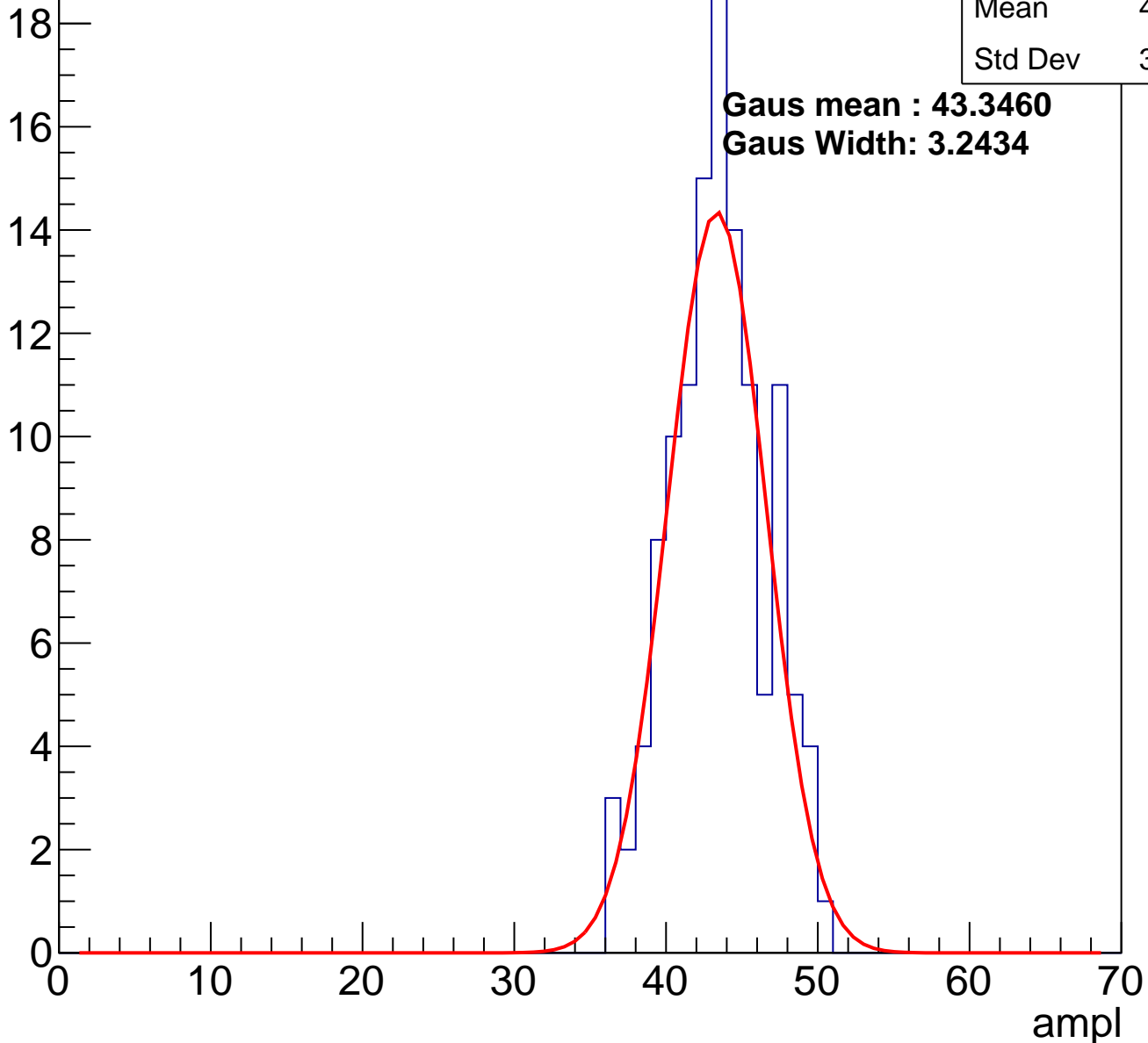
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	42.99
Std Dev	3.114

Gaus mean : 43.3460

Gaus Width: 3.2434

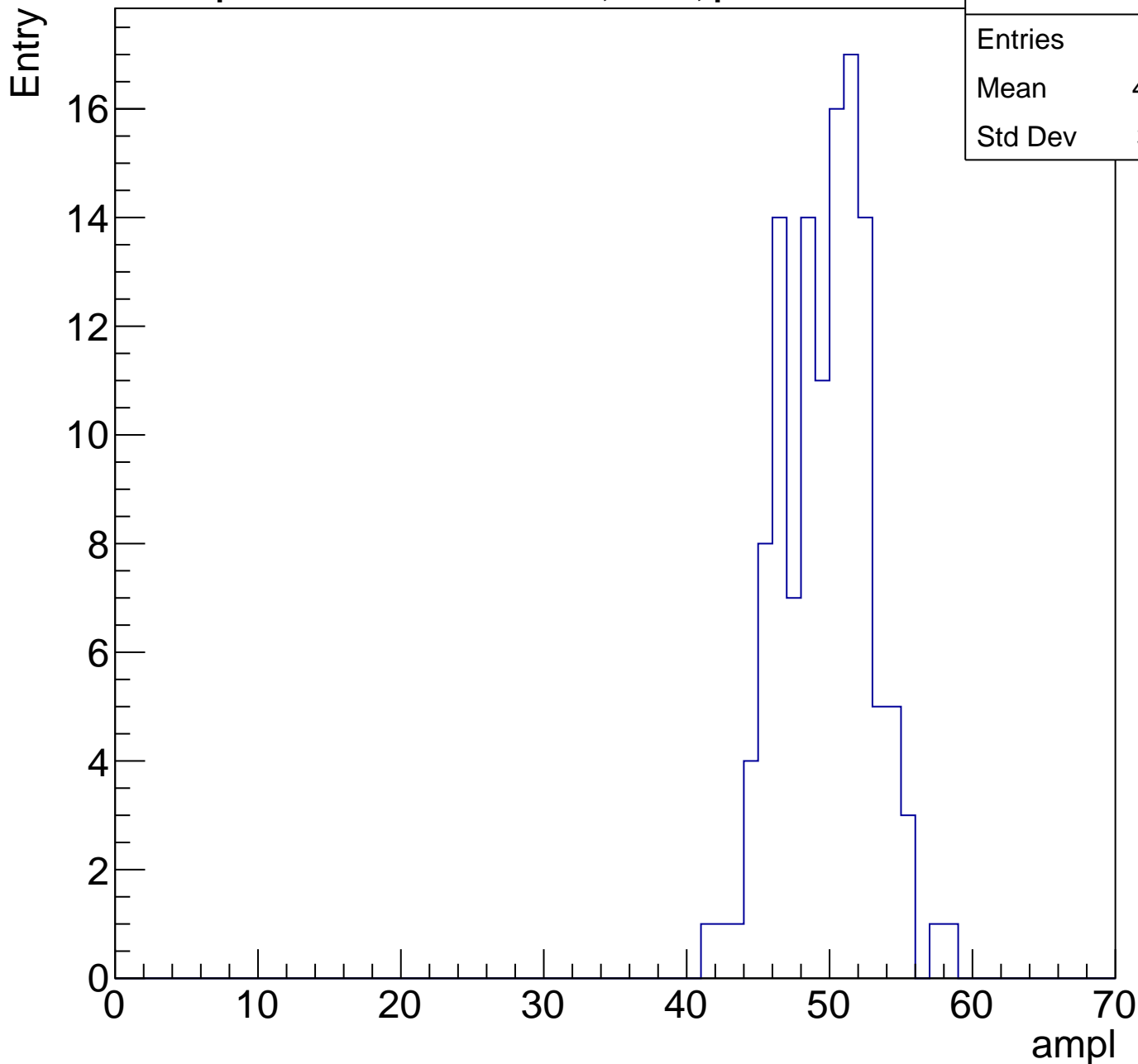
Entry



B1L001S, U21-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	49.24
Std Dev	3.131

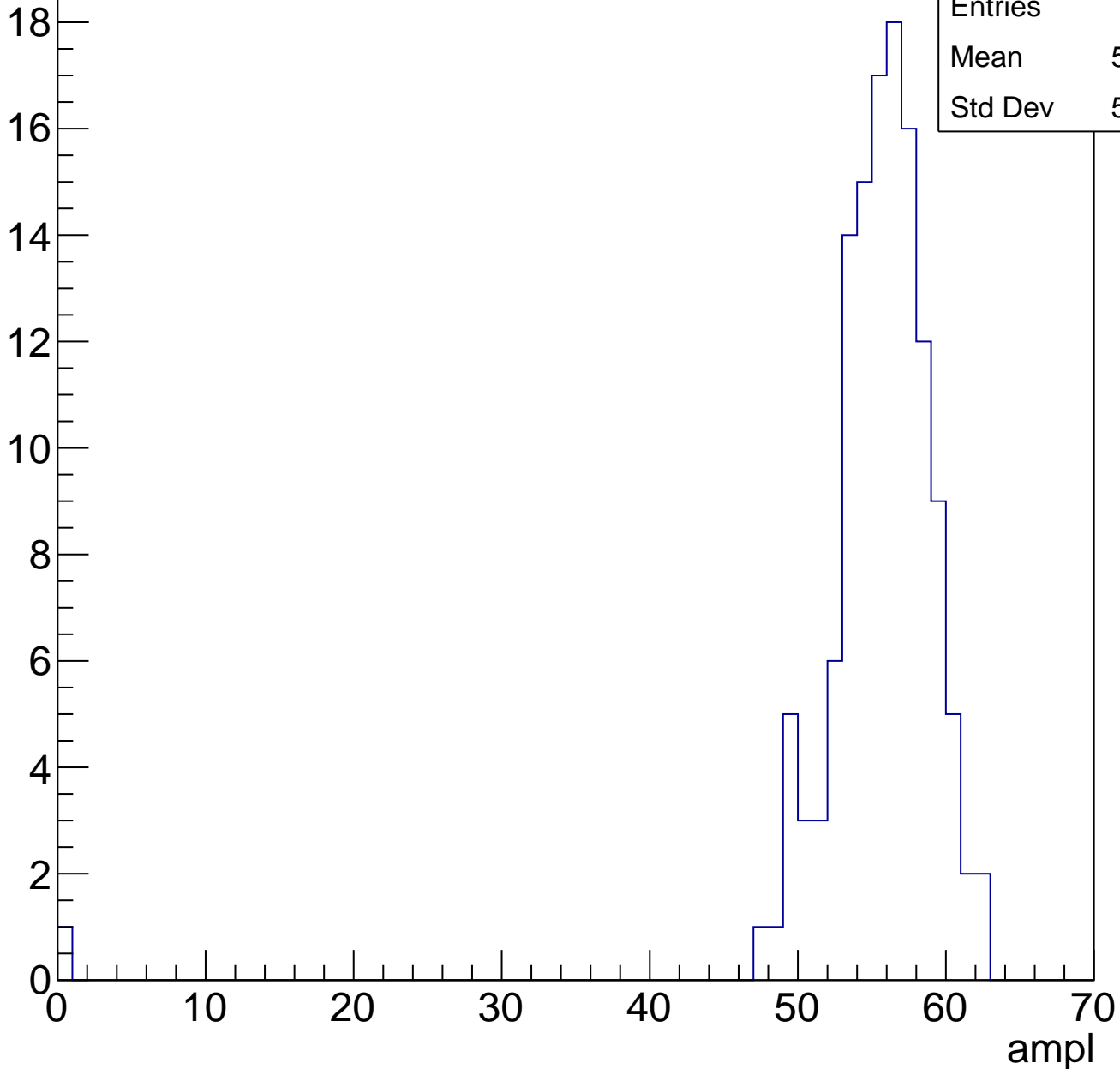


B1L001S, U21-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	54.88
Std Dev	5.679

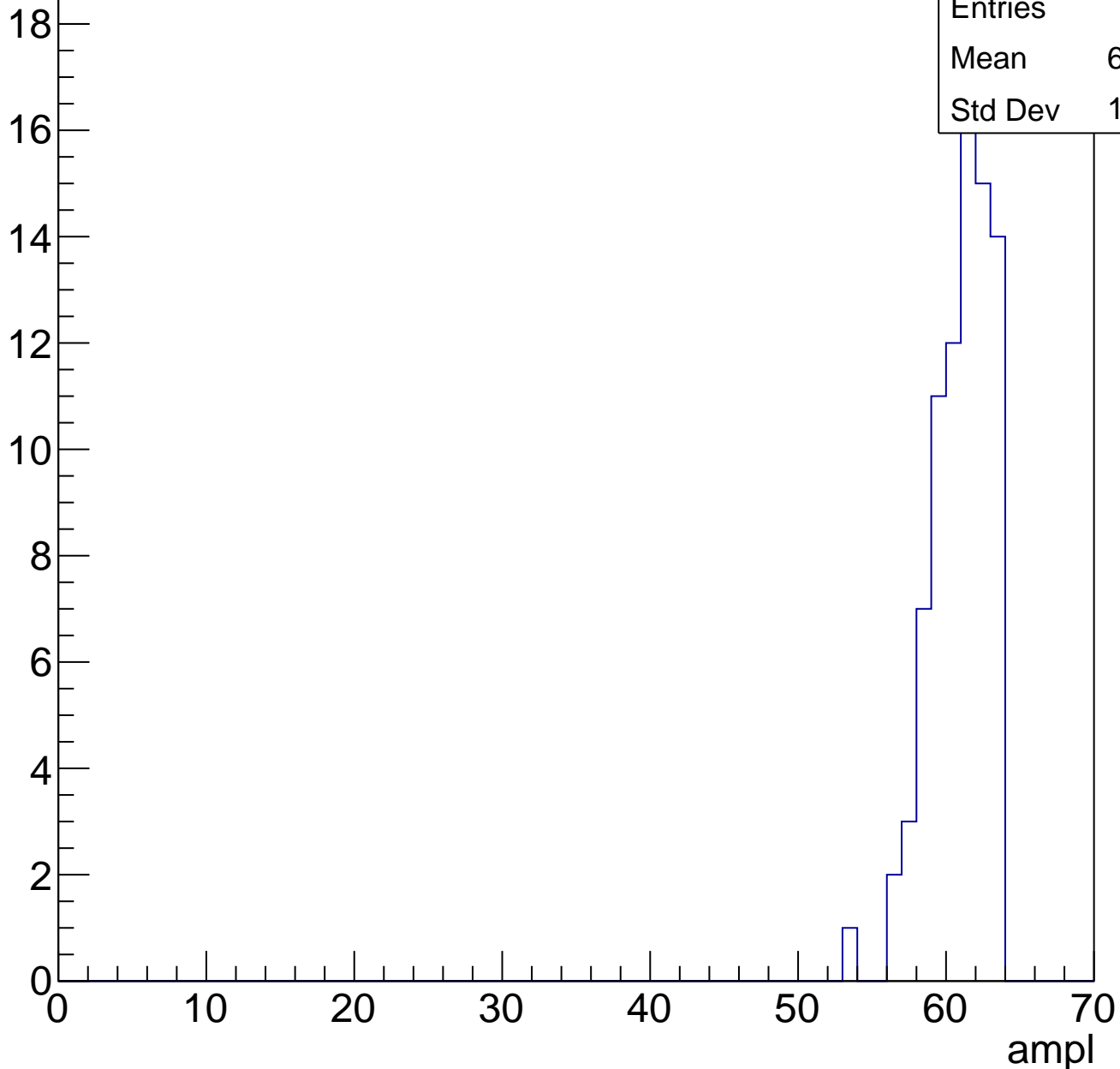
Entry



B1L001S, U21-ch39, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

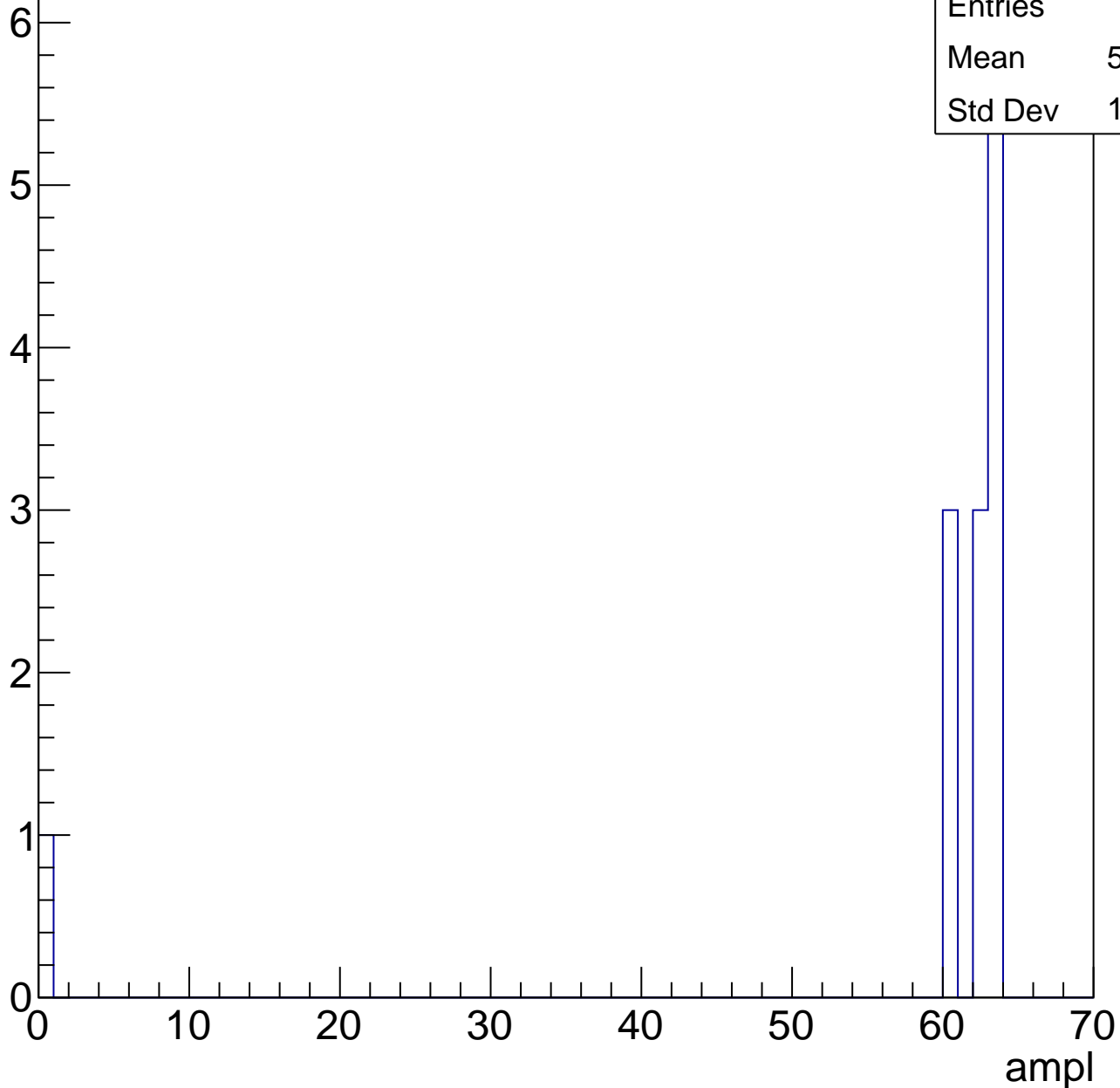


Entries	83
Mean	60.49
Std Dev	1.996

B1L001S, U21-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch40, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	30.19
Std Dev	3.152

Gaus mean : 30.6870

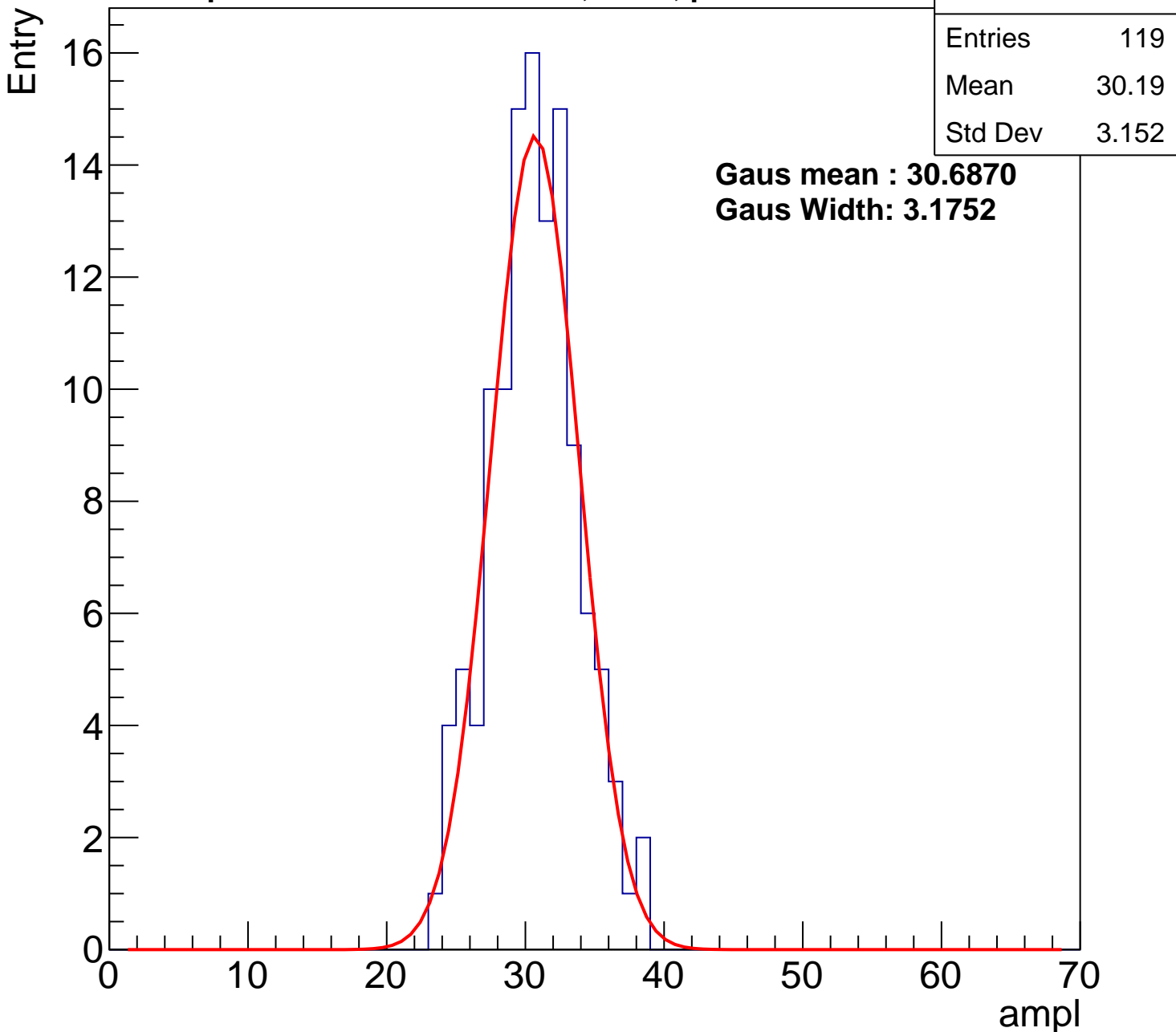
Gaus Width: 3.1752

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



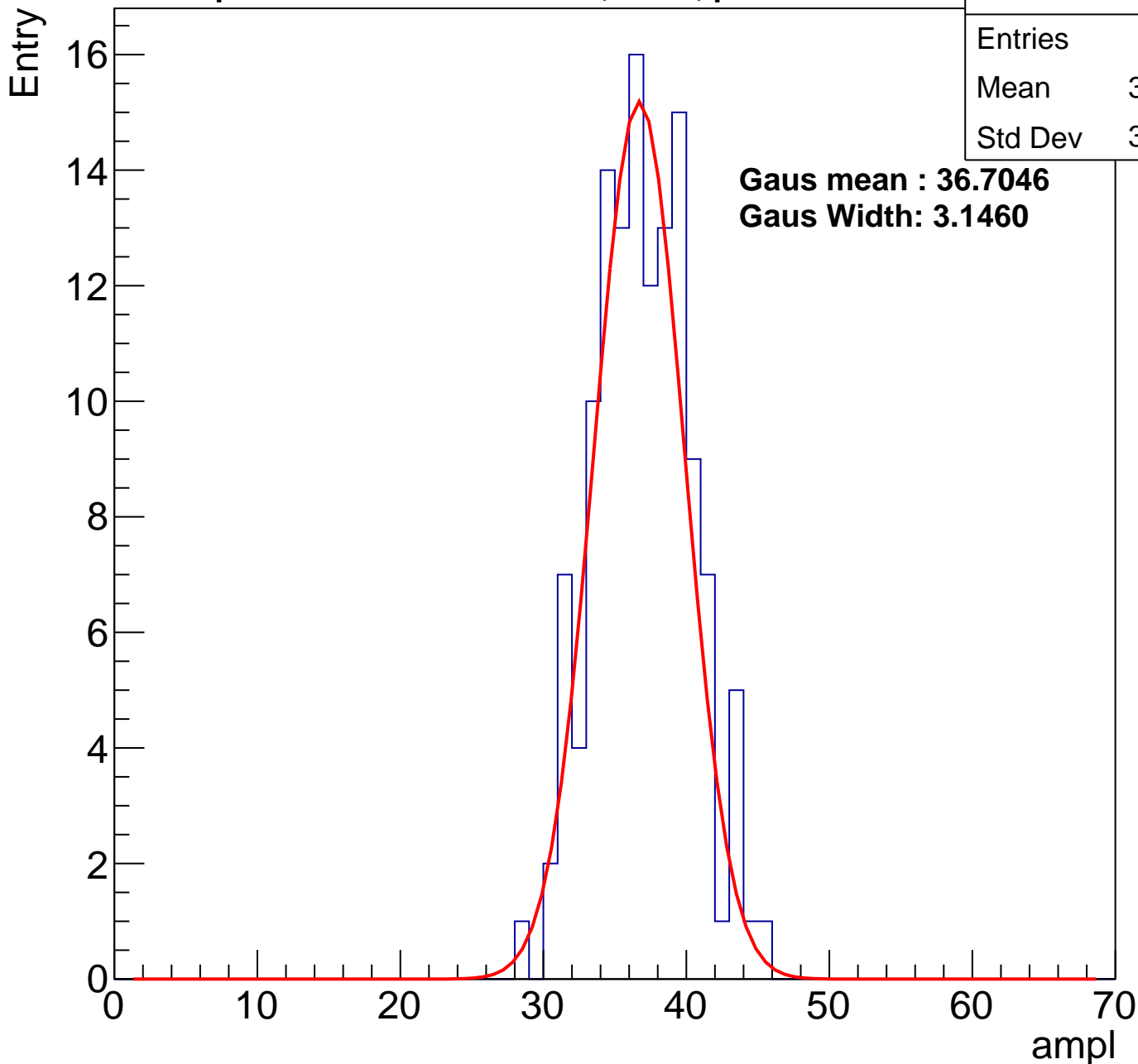
B1L001S, U21-ch40, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.53
Std Dev	3.316

Gaus mean : 36.7046

Gaus Width: 3.1460



B1L001S, U21-ch40, adc2

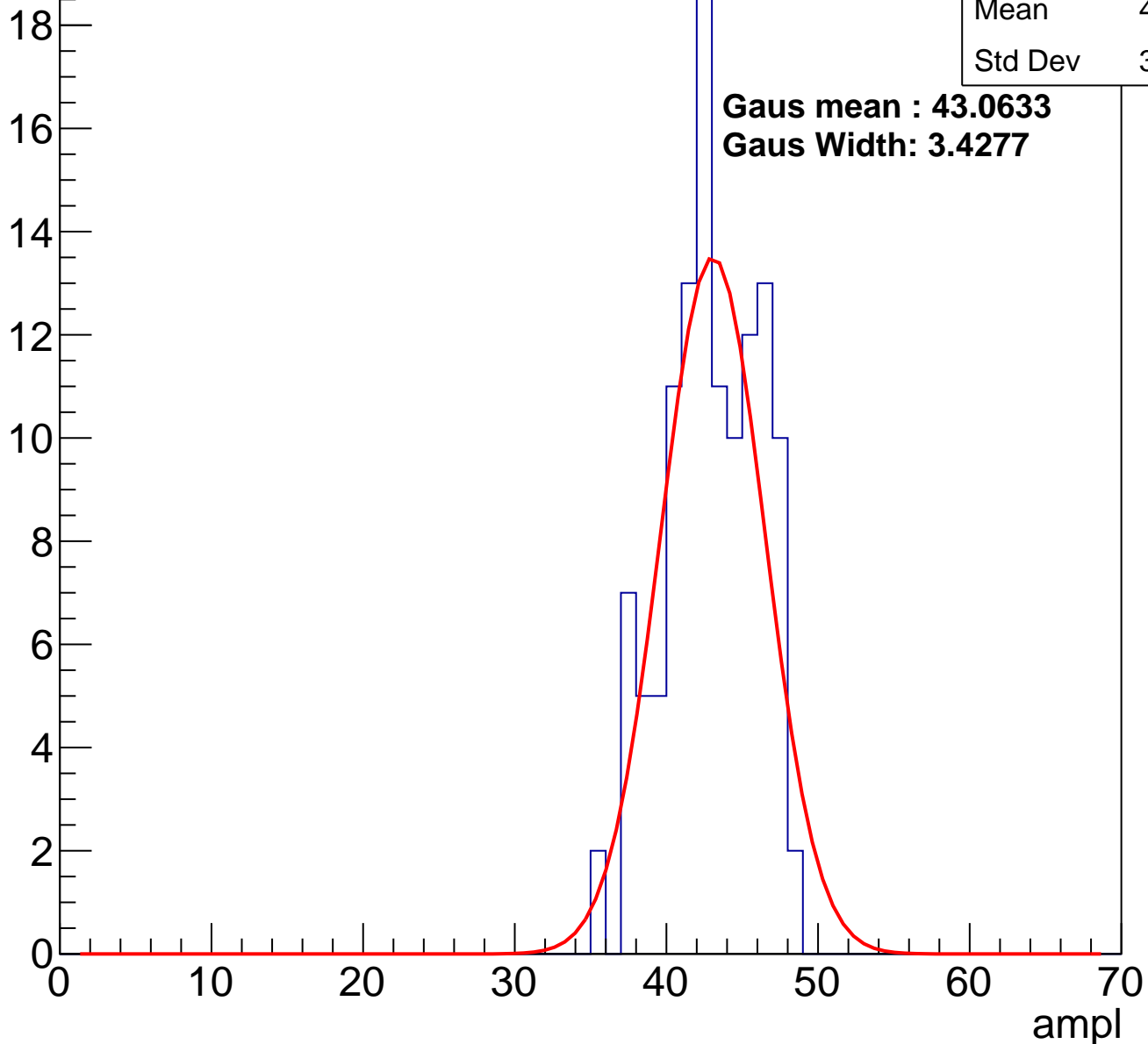
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	42.52
Std Dev	3.044

Gaus mean : 43.0633

Gaus Width: 3.4277

Entry



B1L001S, U21-ch40, adc3

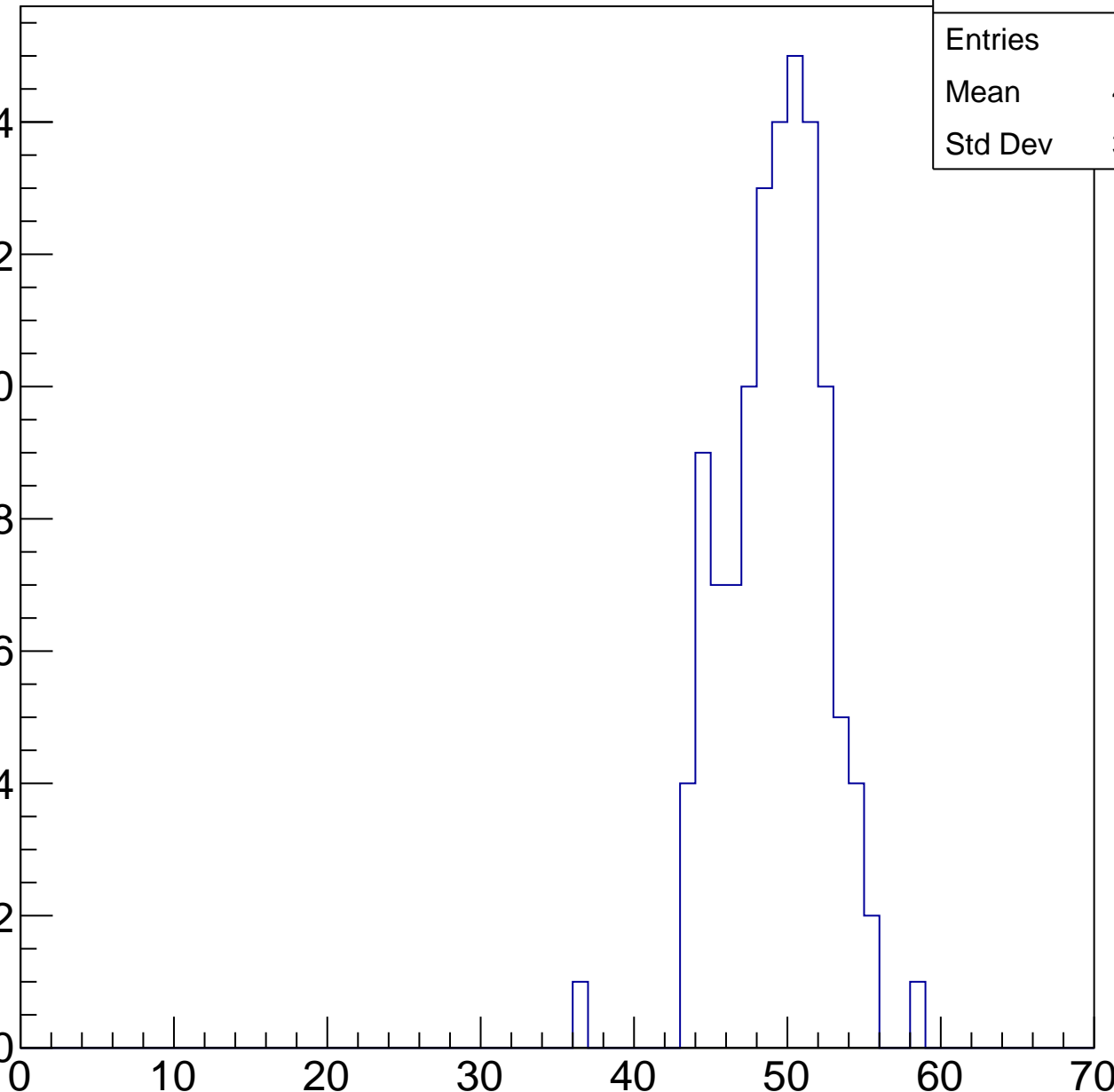
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	116
Mean	48.74
Std Dev	3.286

ampl



B1L001S, U21-ch40, adc4

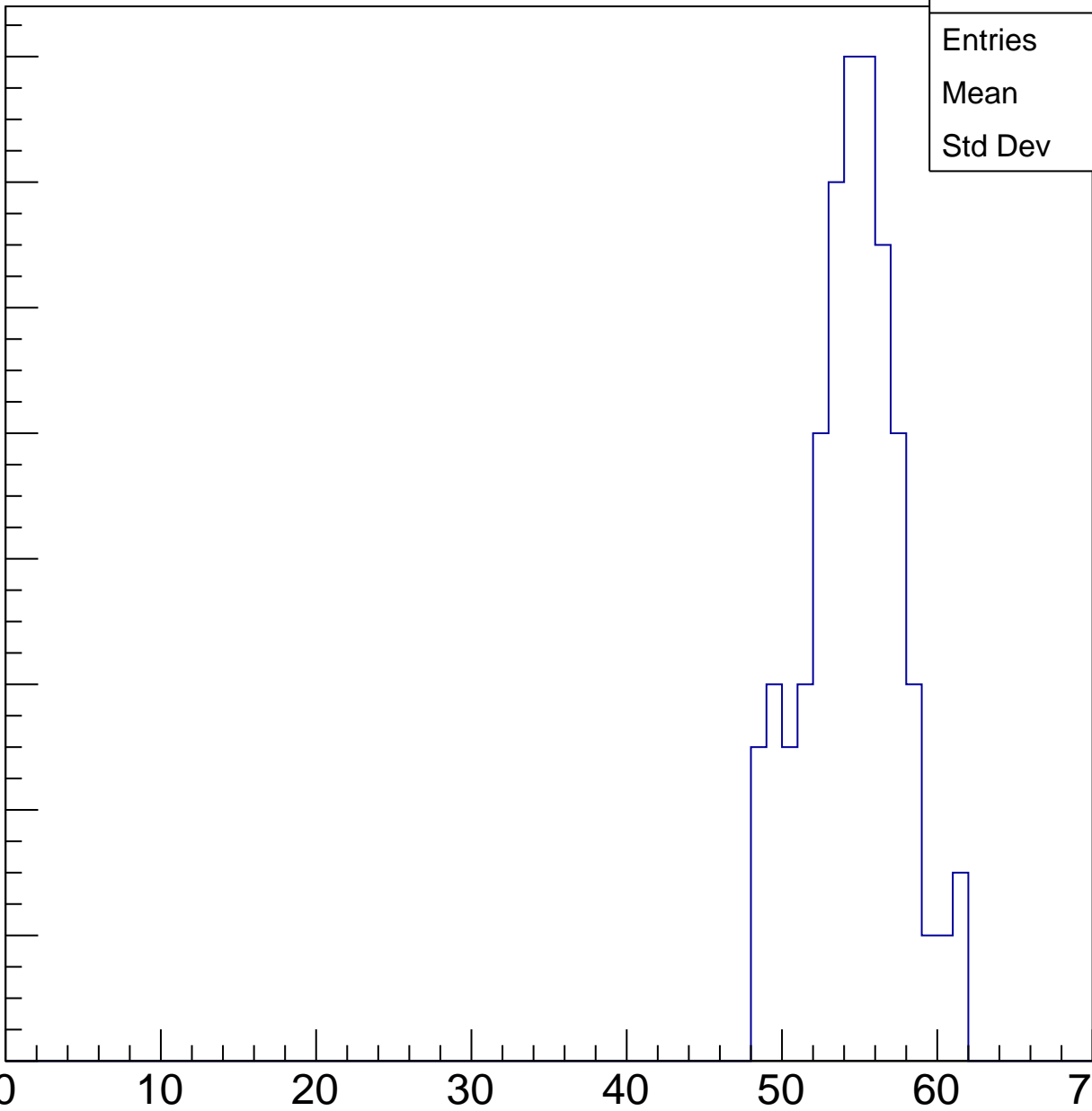
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	54.06
Std Dev	3.021

ampl



B1L001S, U21-ch40, adc5

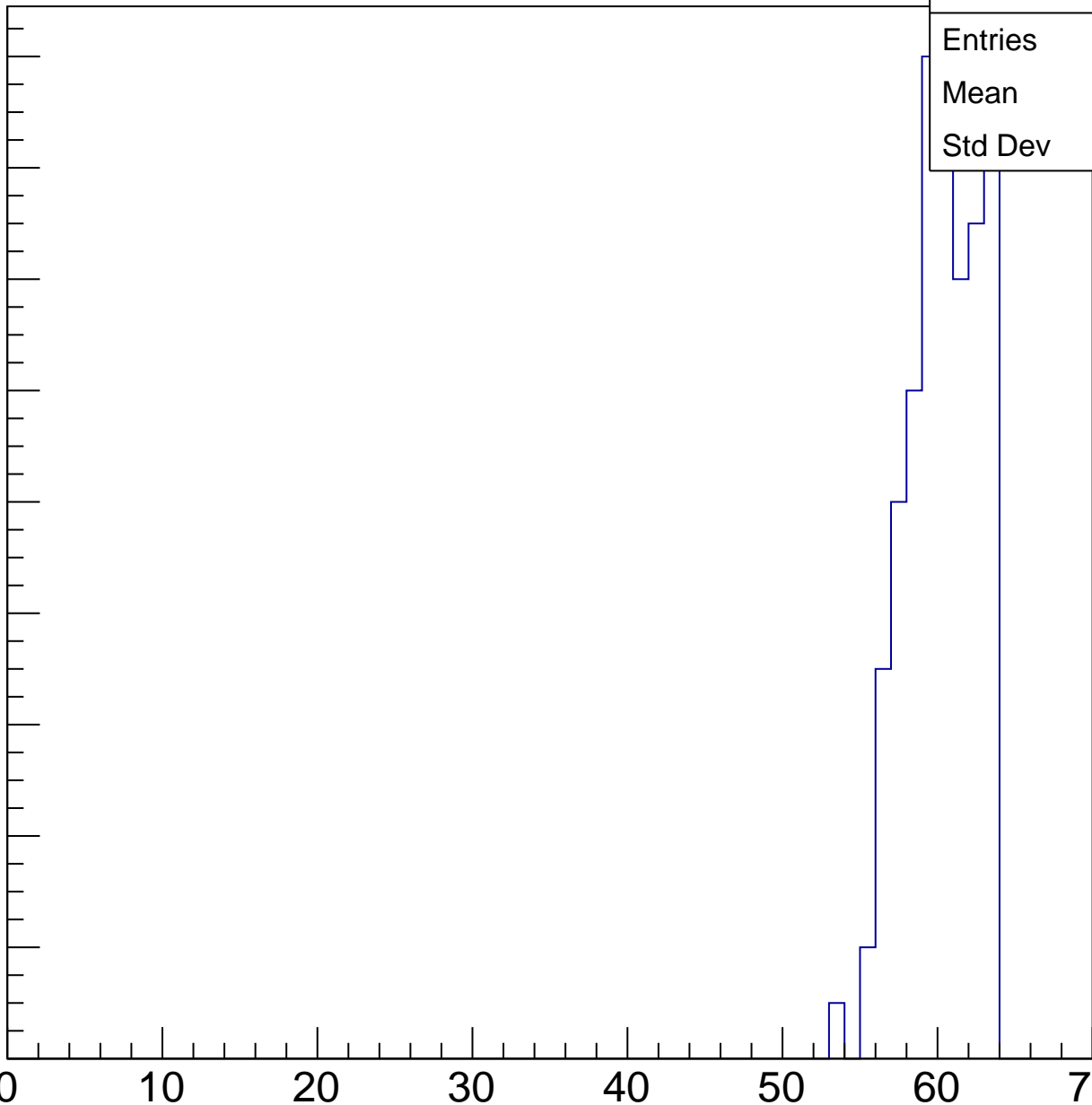
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	113
Mean	59.83
Std Dev	2.304

ampl

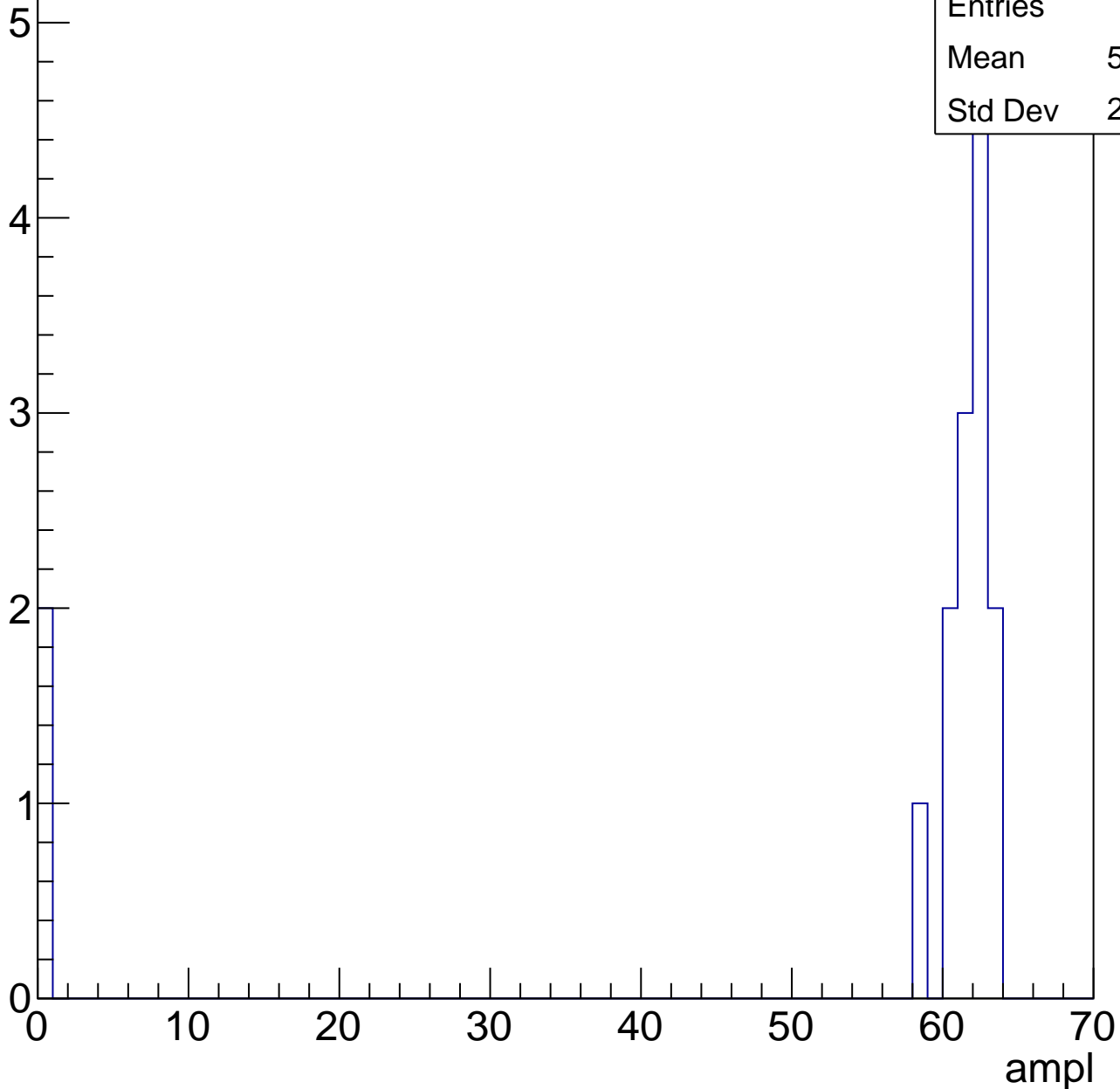


B1L001S, U21-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	53.13
Std Dev	20.88



B1L001S, U21-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U21-ch41, adc0

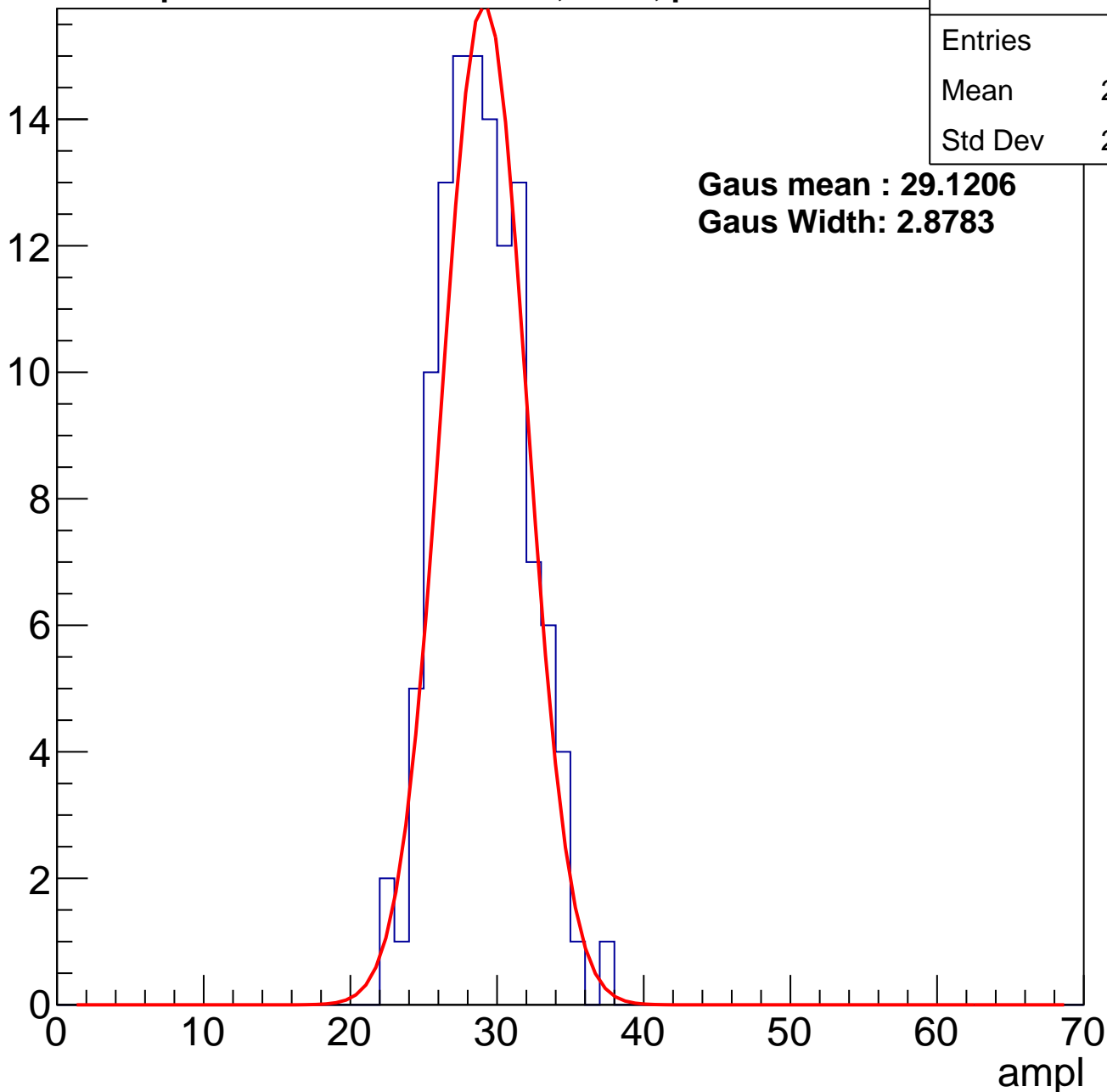
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	28.56
Std Dev	2.915

Gaus mean : 29.1206

Gaus Width: 2.8783

Entry



B1L001S, U21-ch41, adc1

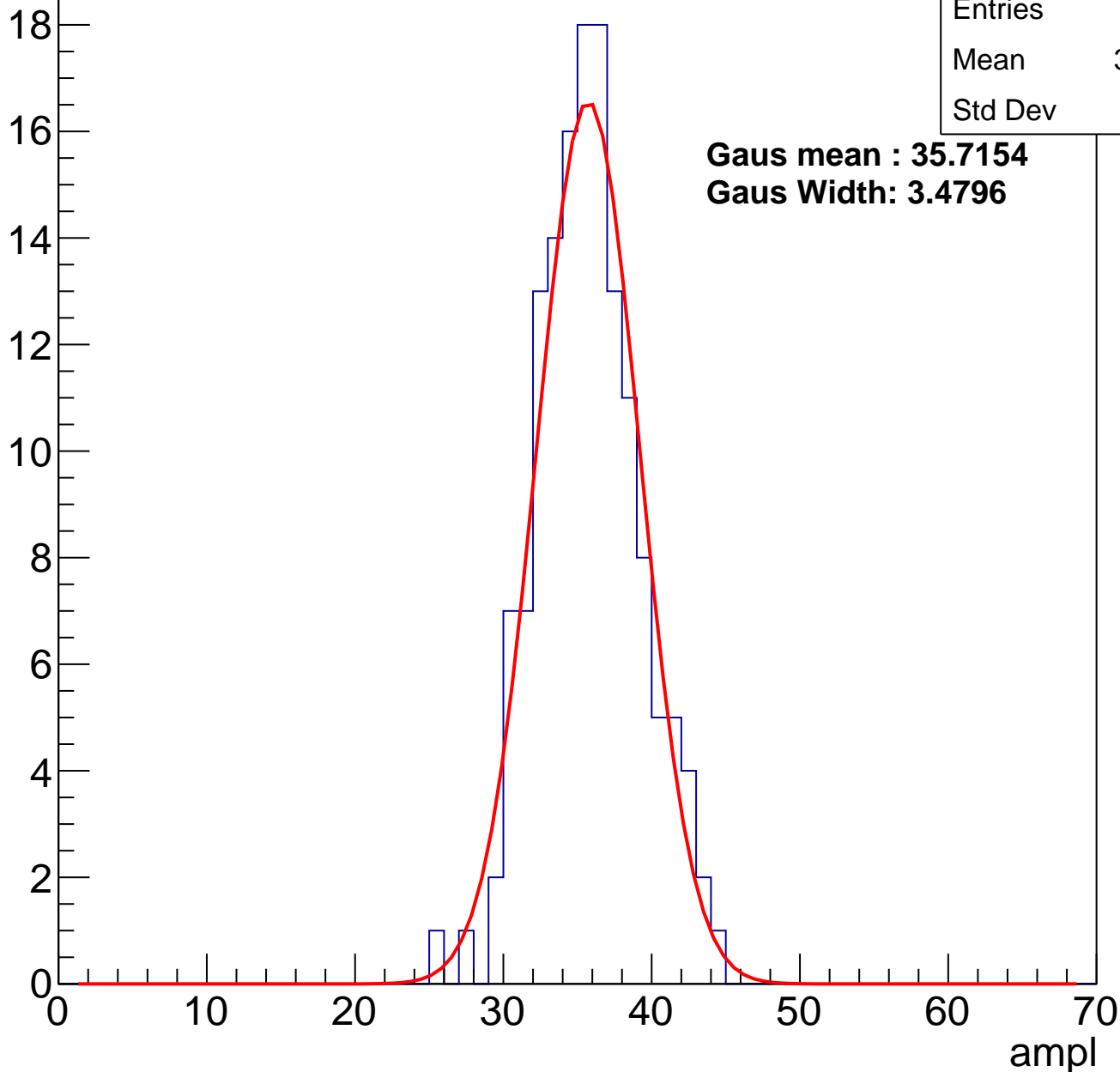
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	35.28
Std Dev	3.42

Gaus mean : 35.7154

Gaus Width: 3.4796

Entry



B1L001S, U21-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	42.36
Std Dev	3.165

Gaus mean : 42.8368

Gaus Width: 3.2960

Entry

14

12

10

8

6

4

2

0

0

10

20

30

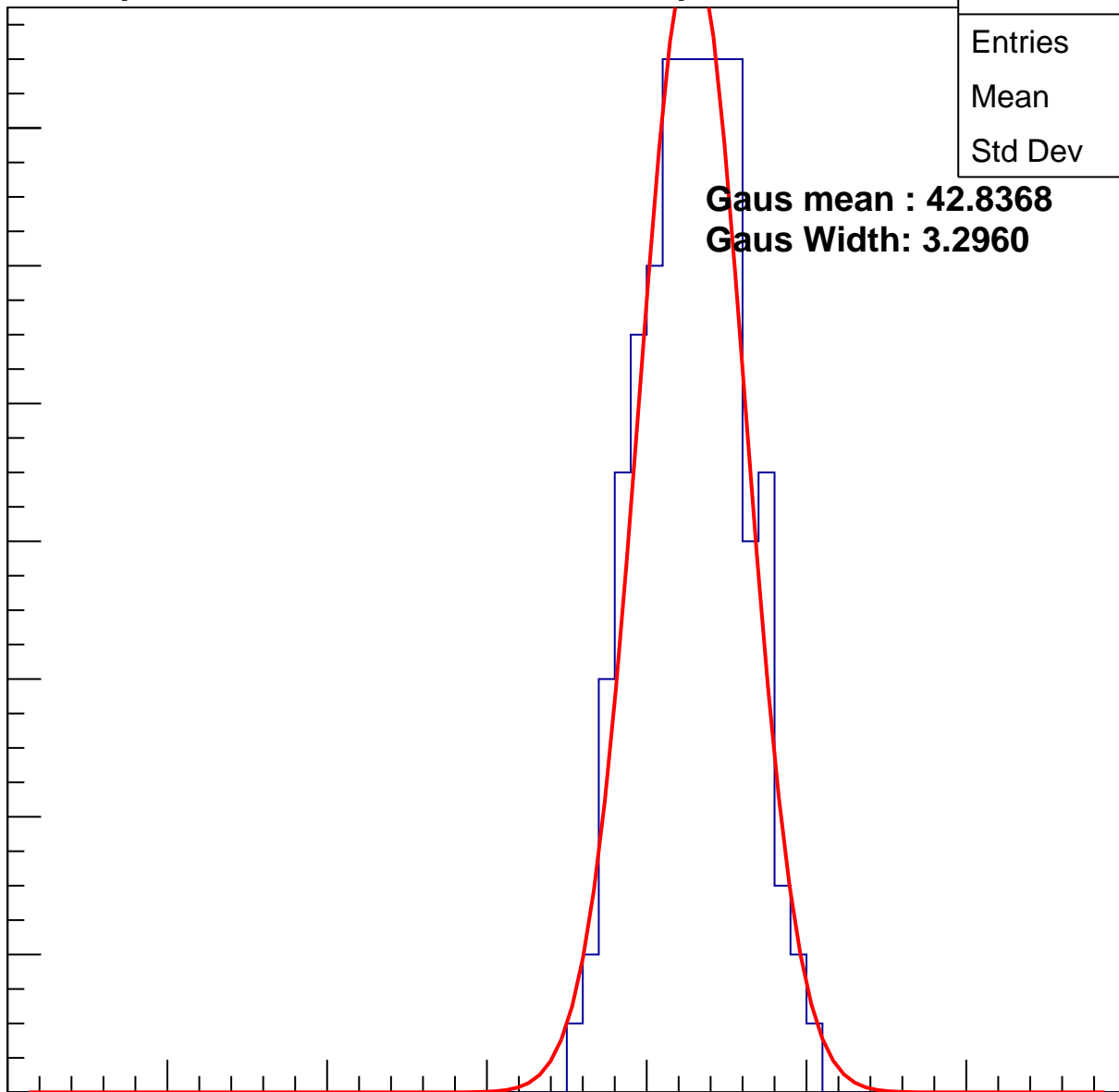
40

50

60

70

ampl



B1L001S, U21-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

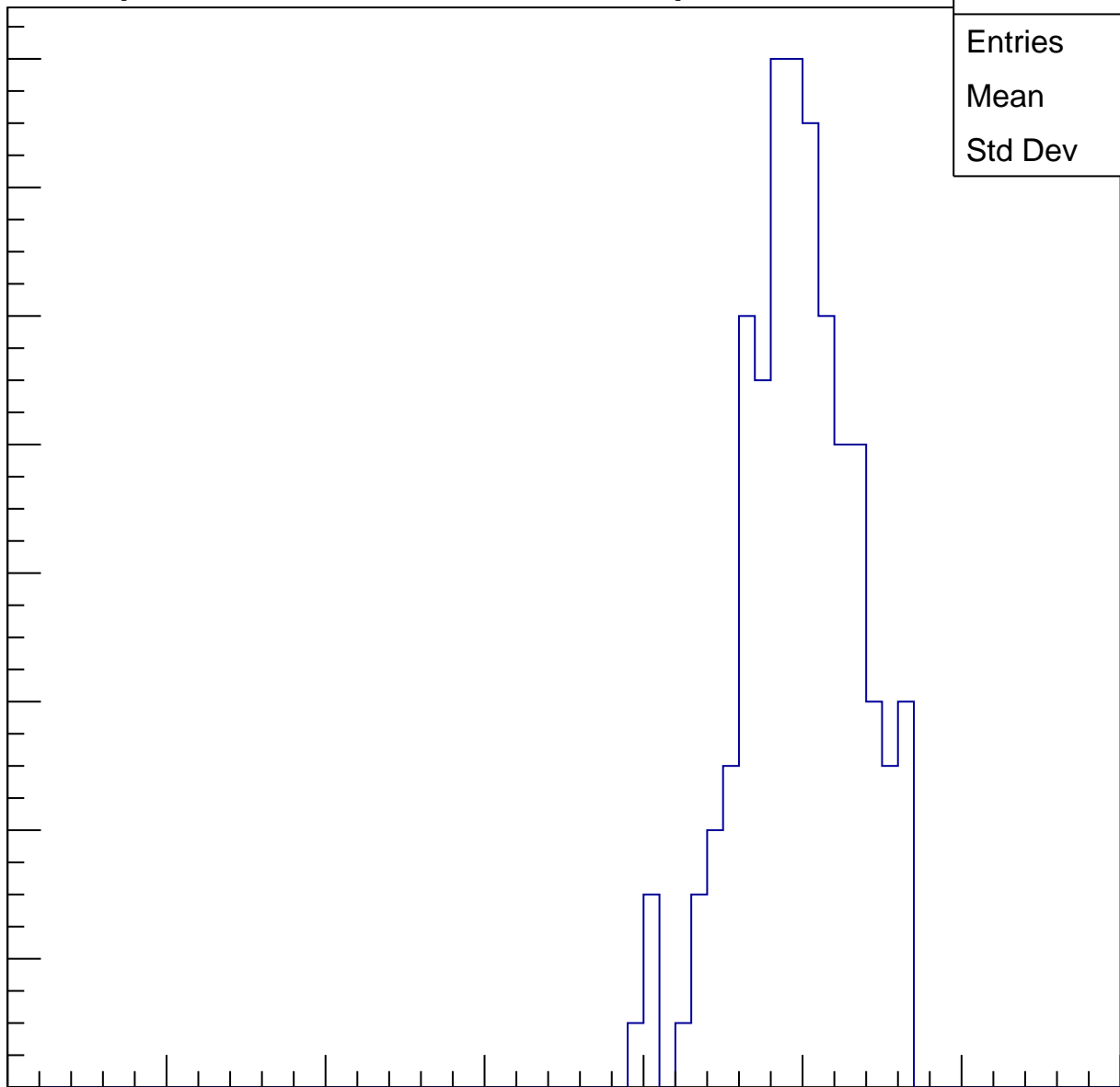
Entries	136
Mean	49.26
Std Dev	3.614

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

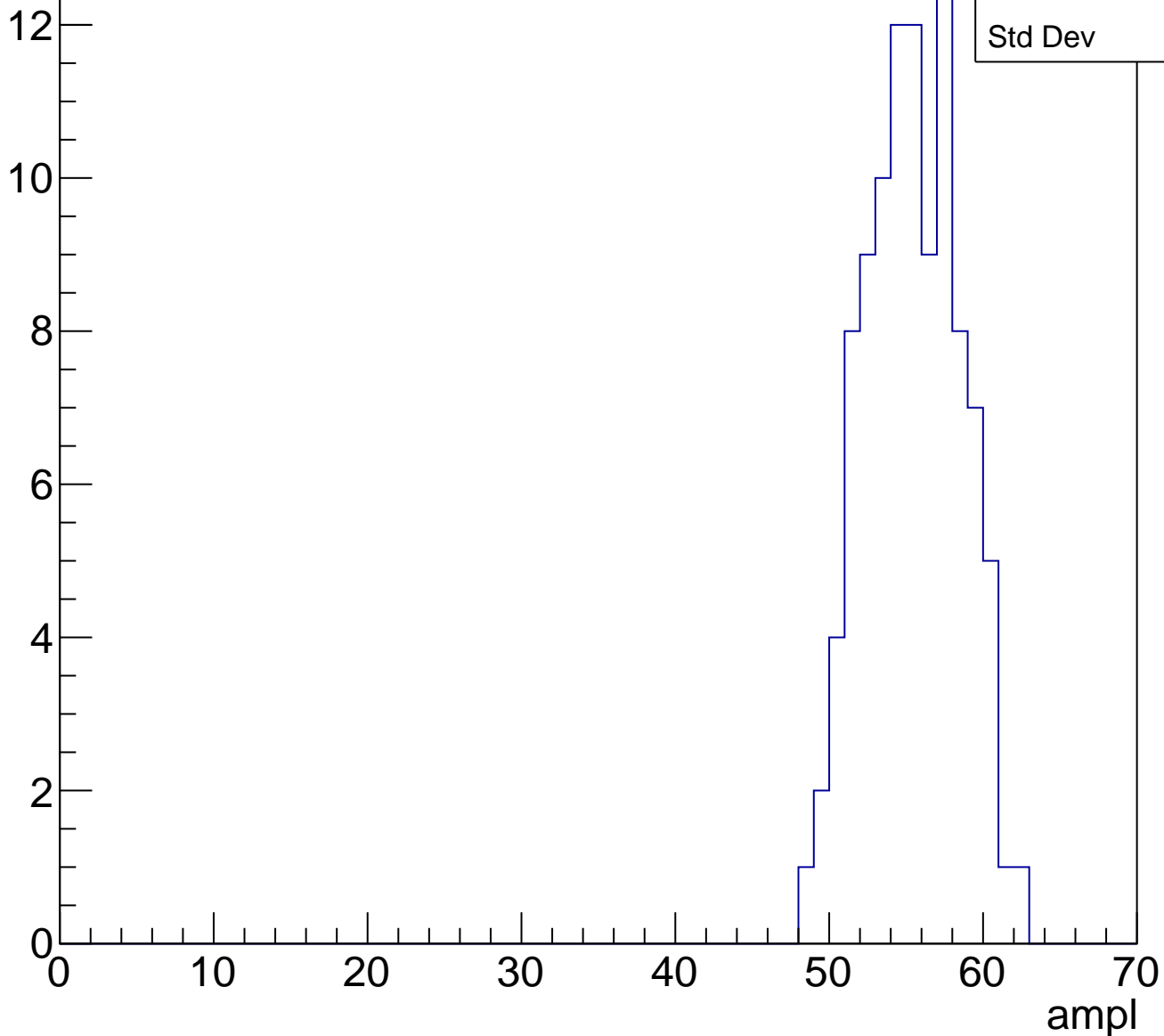


B1L001S, U21-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	54.95
Std Dev	3.03

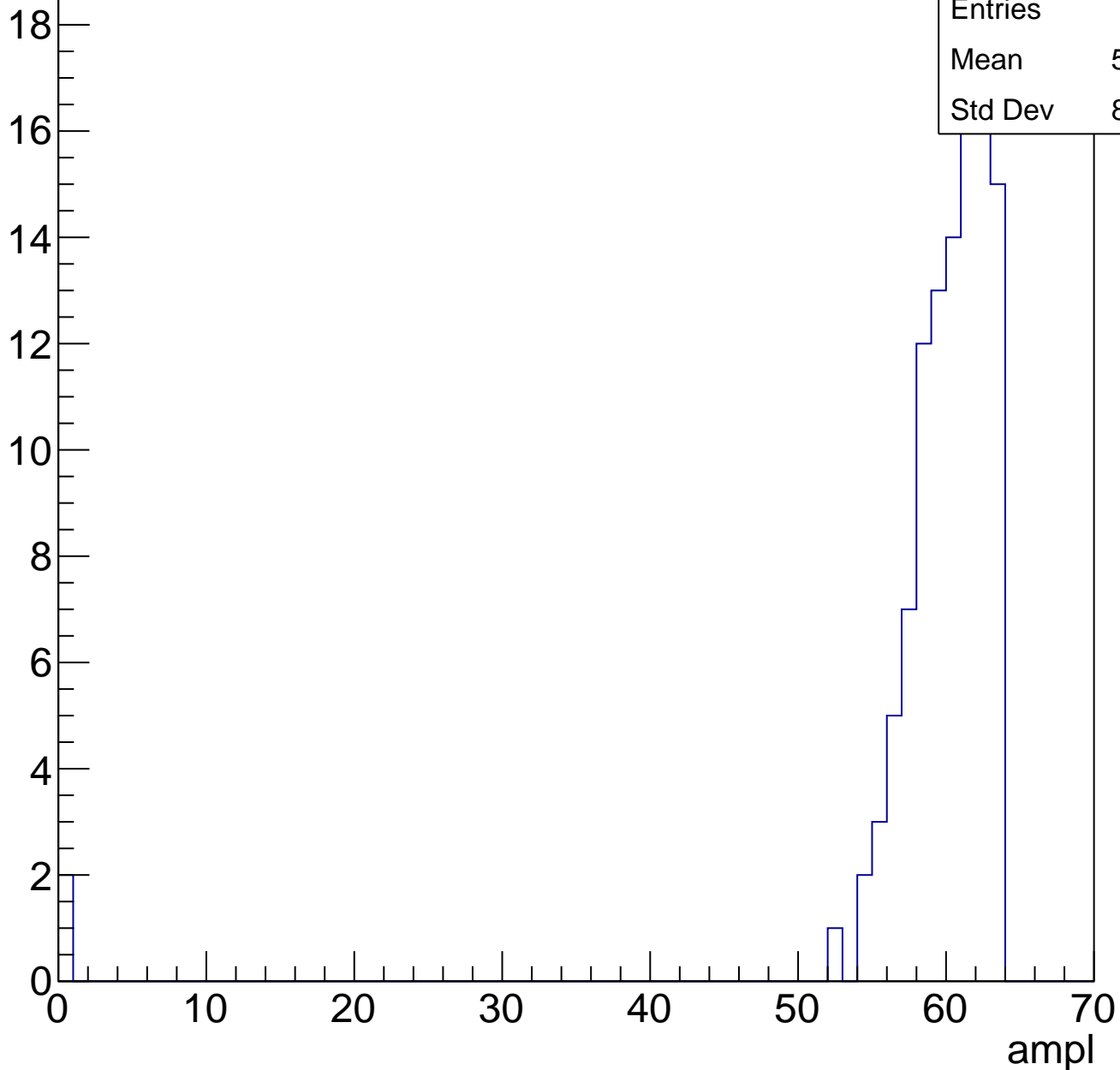
Entry



B1L001S, U21-ch41, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

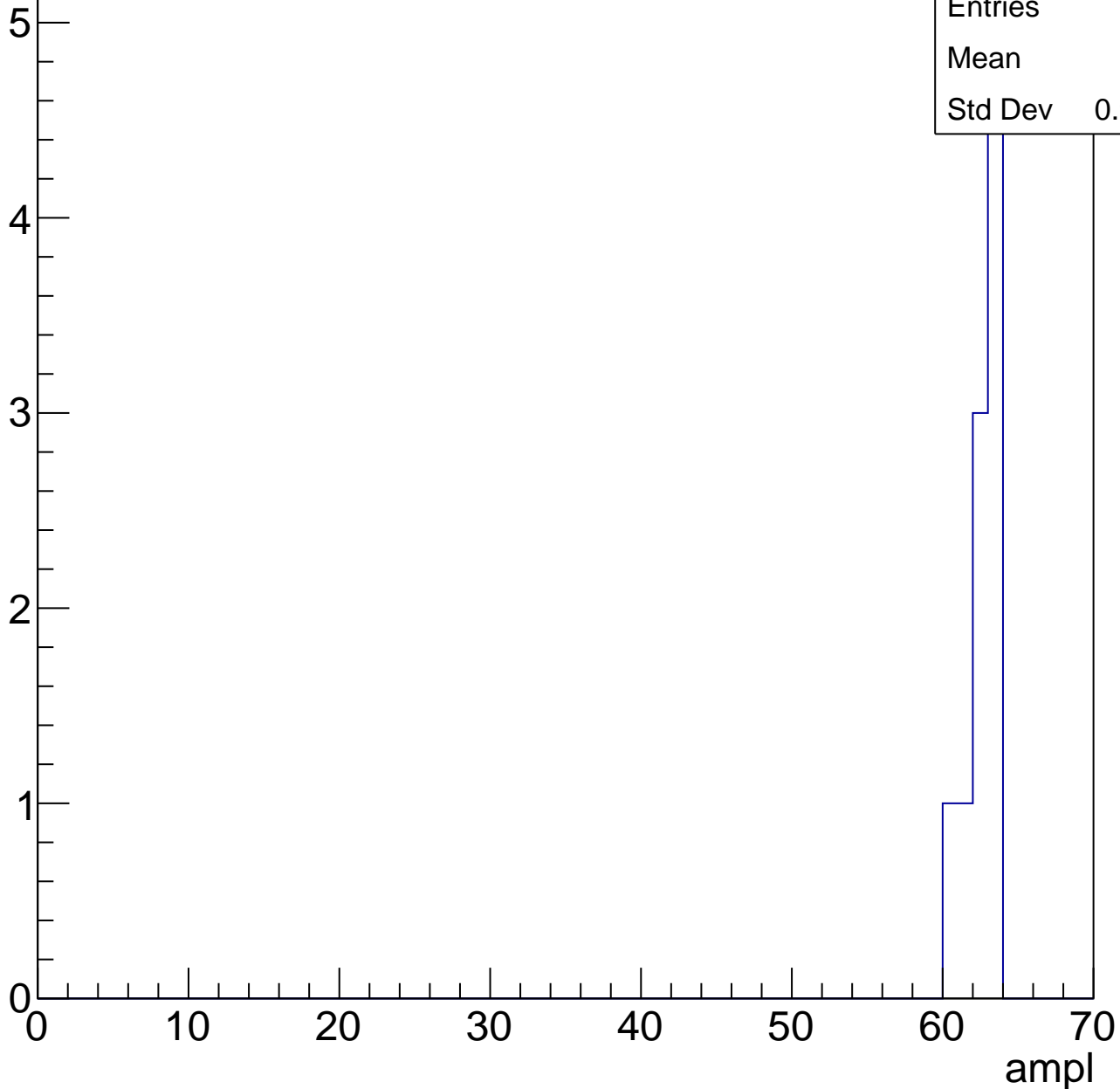


B1L001S, U21-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62.2
Std Dev	0.9798



B1L001S, U21-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch42, adc0

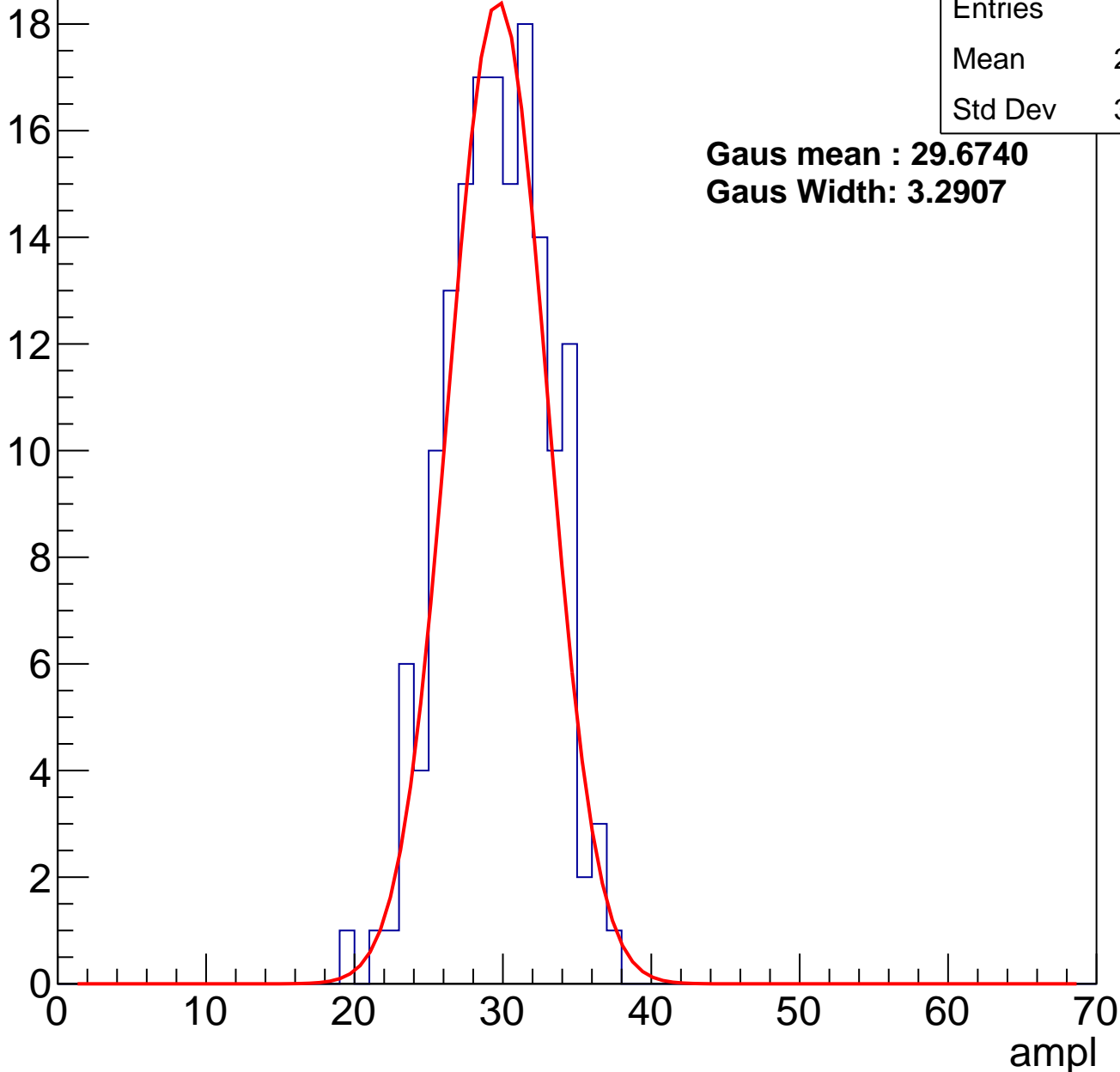
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	29.17
Std Dev	3.386

Gaus mean : 29.6740

Gaus Width: 3.2907

Entry



B1L001S, U21-ch42, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	35.88
Std Dev	2.791

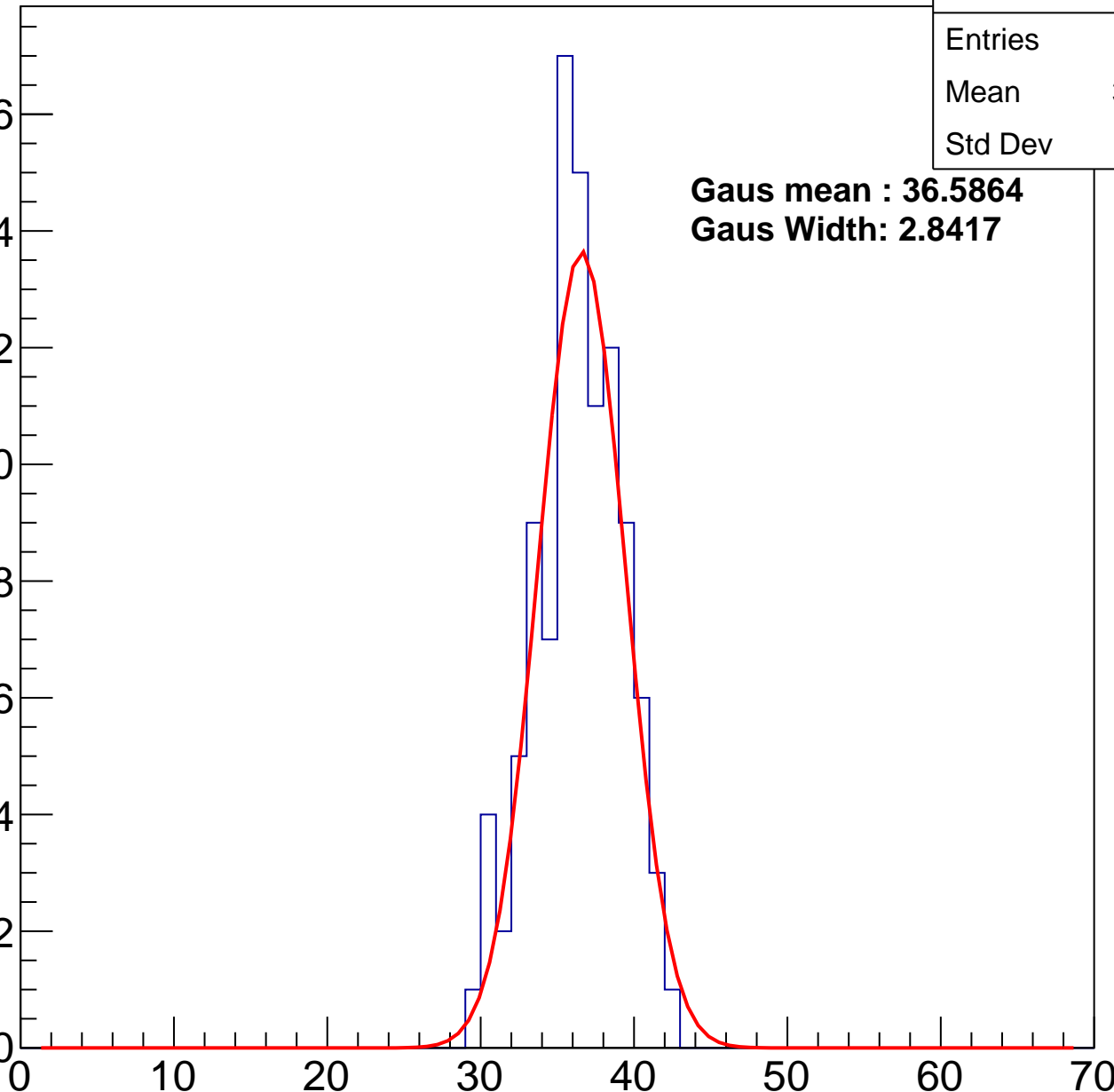
Gaus mean : 36.5864

Gaus Width: 2.8417

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	42.4
Std Dev	3.479

Gaus mean : 43.1468

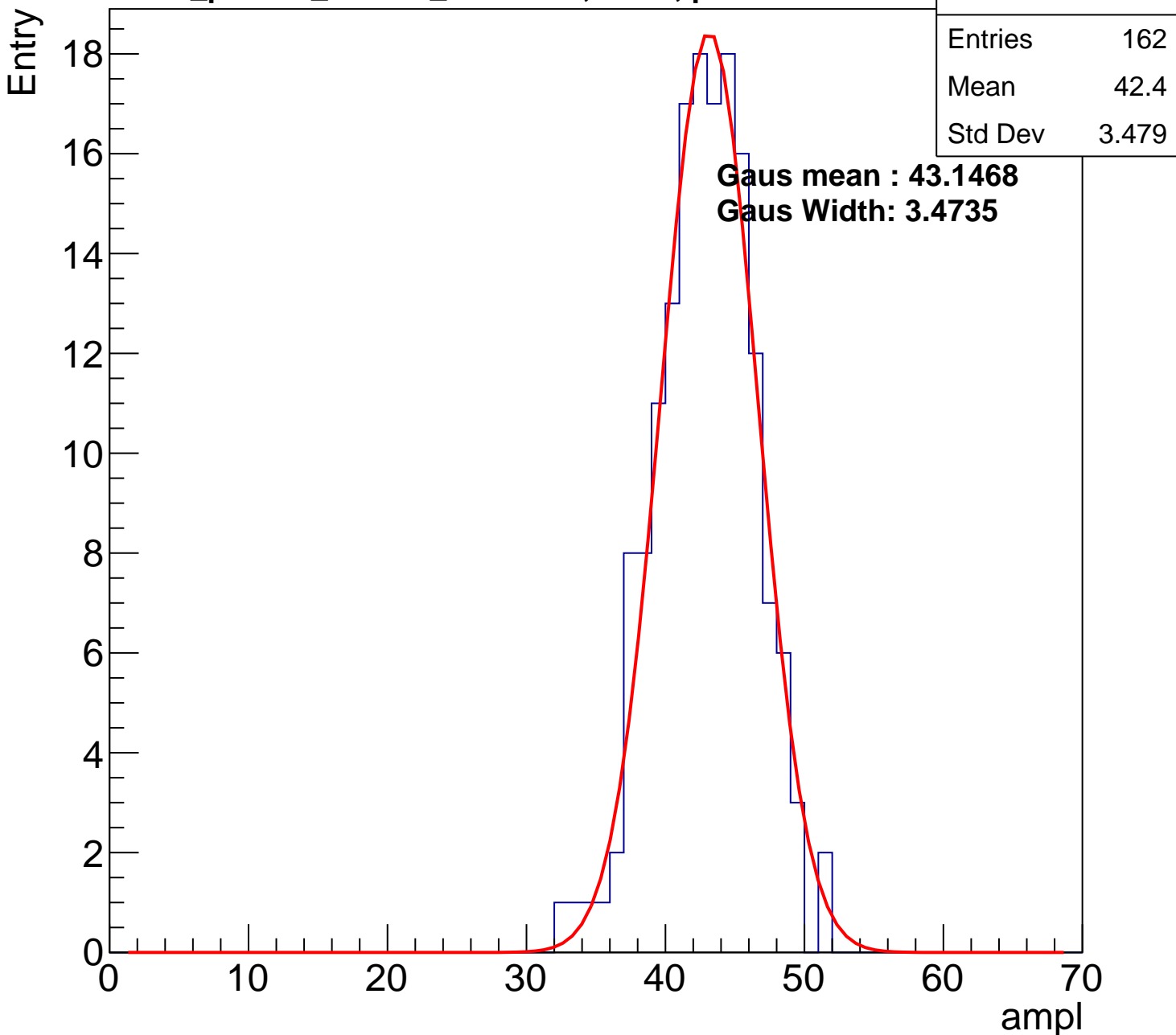
Gaus Width: 3.4735

Entry

18
16
14
12
10
8
6
4
2
0

ampl

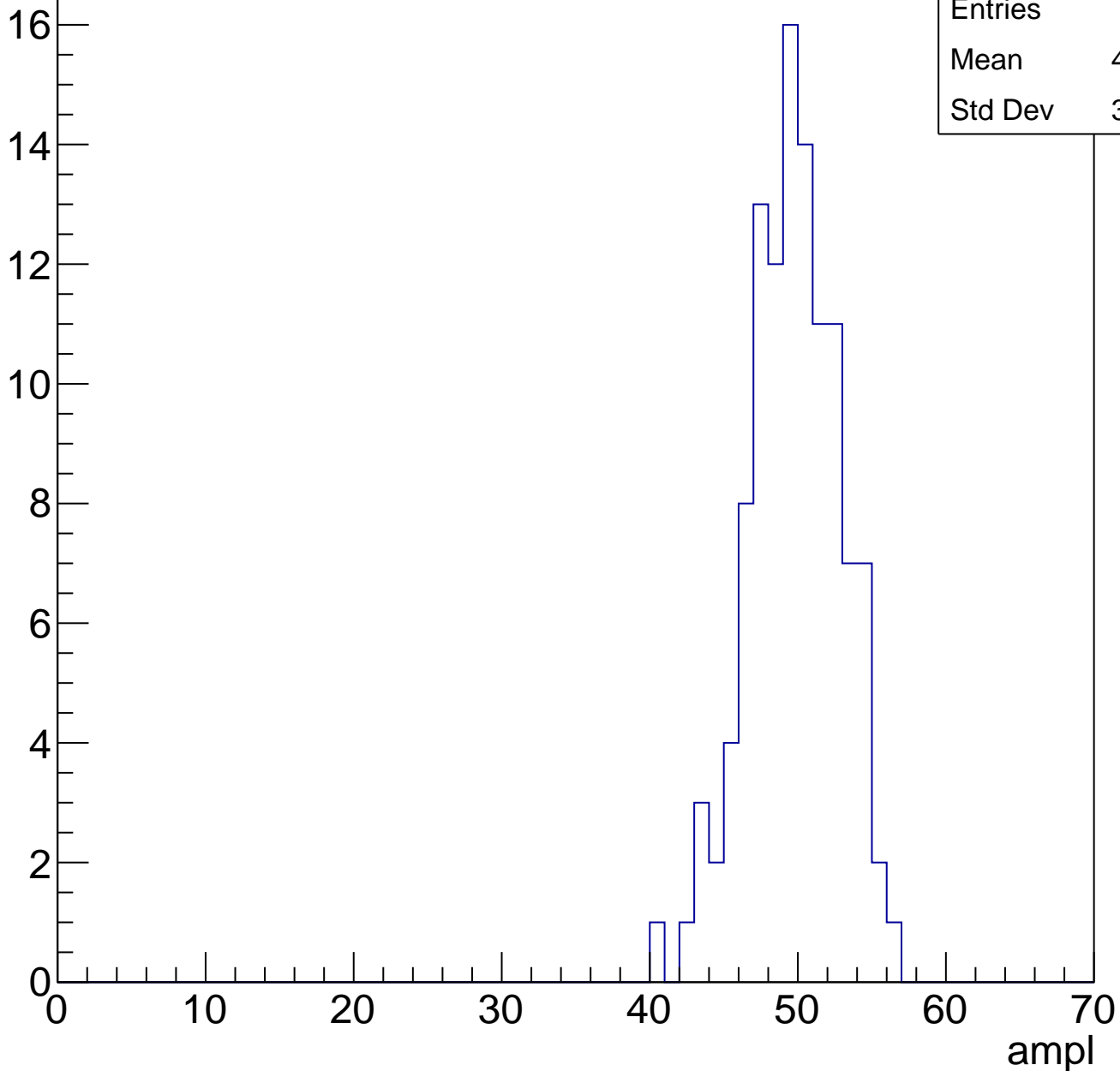
0 10 20 30 40 50 60 70



B1L001S, U21-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

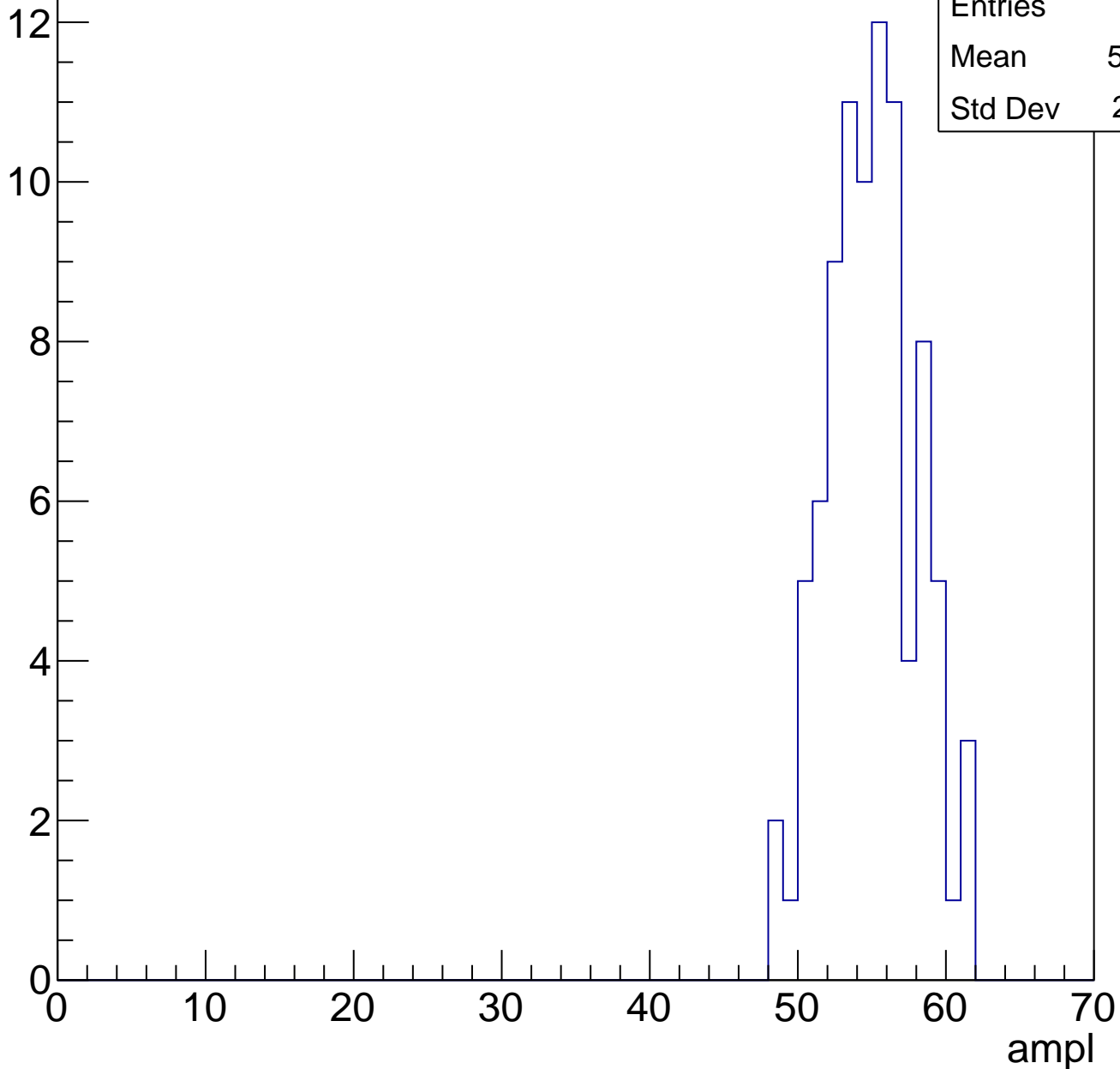
Entry



B1L001S, U21-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

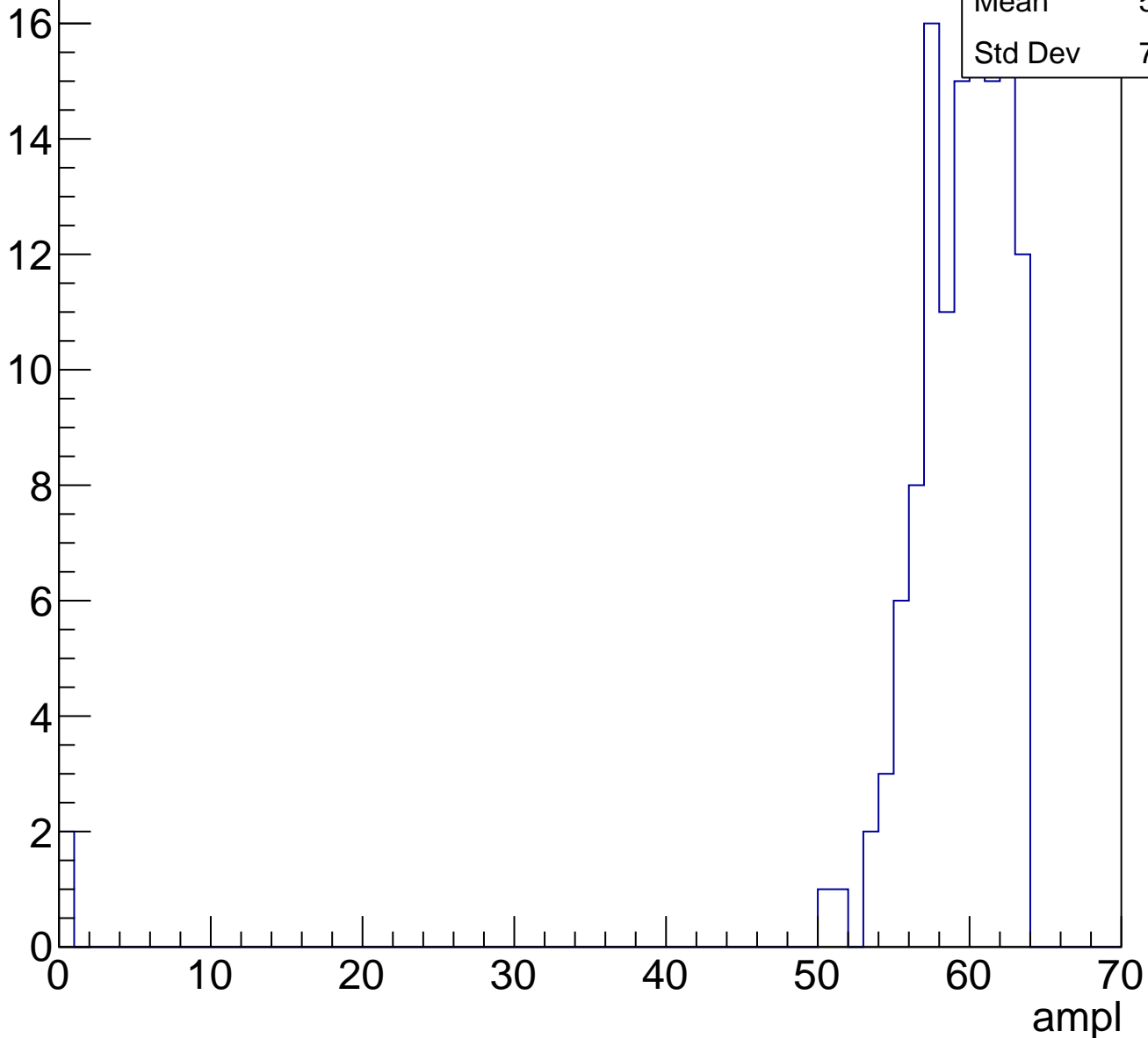
Entry



B1L001S, U21-ch42, adc5

calib_packv5_042523_0143.root, FC#2, port C2

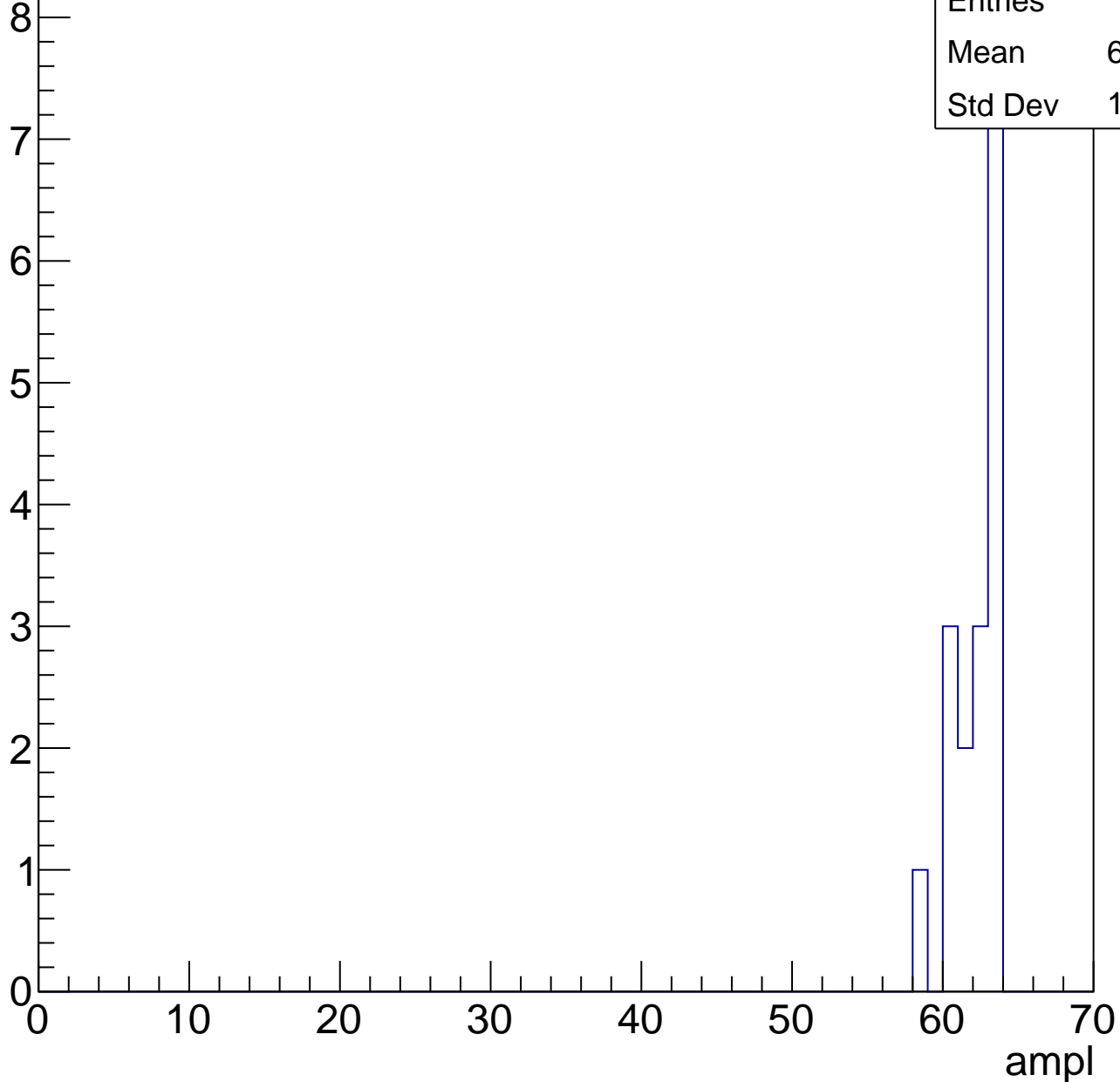
Entry



B1L001S, U21-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch43, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.19
Std Dev	3.177

Gaus mean : 28.4423

Gaus Width: 3.3763

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

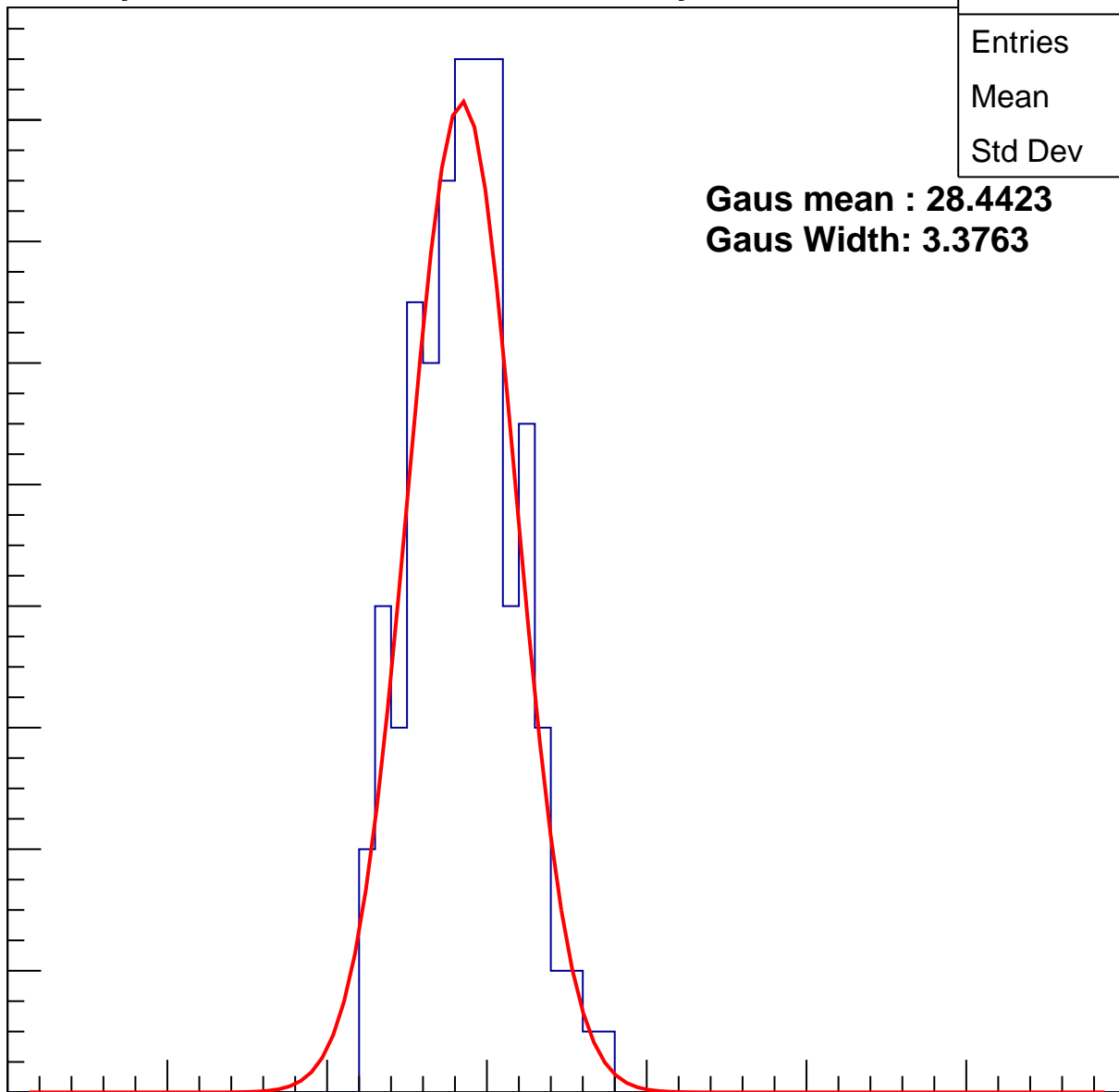
40

50

60

70

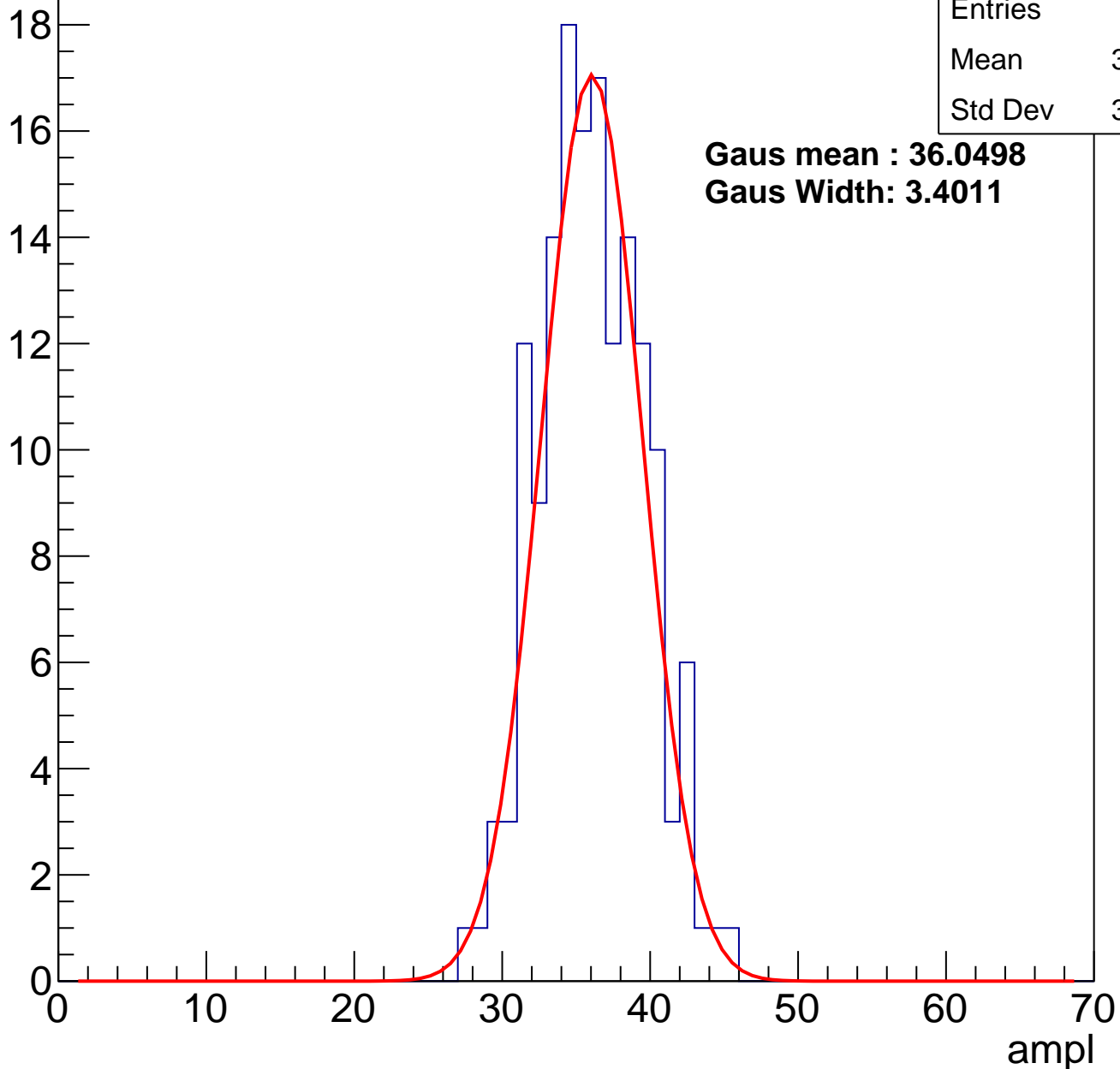
ampl



B1L001S, U21-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch43, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	41.89
Std Dev	2.983

Gaus mean : 42.4040

Gaus Width: 3.0465

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

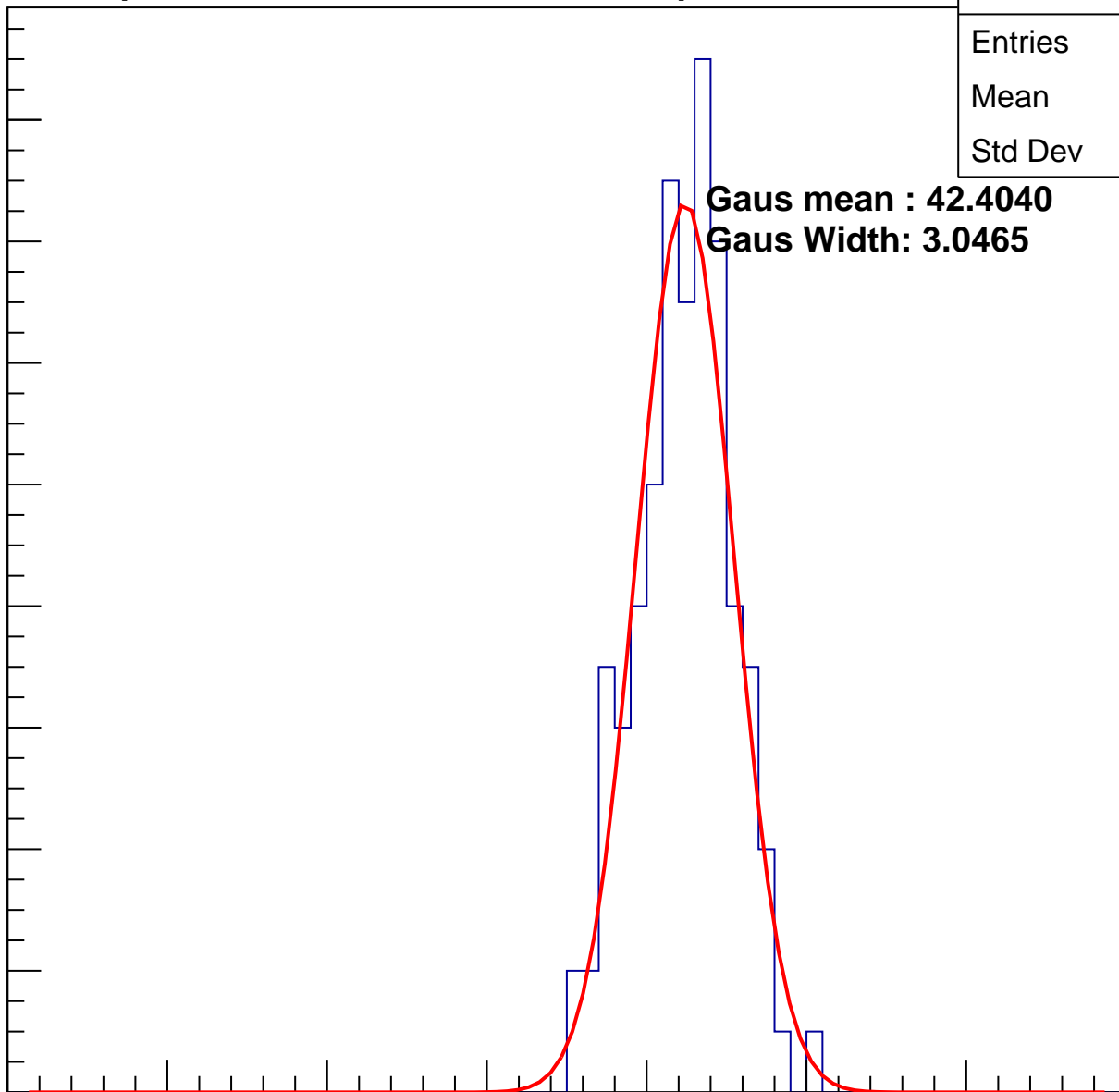
40

50

60

70

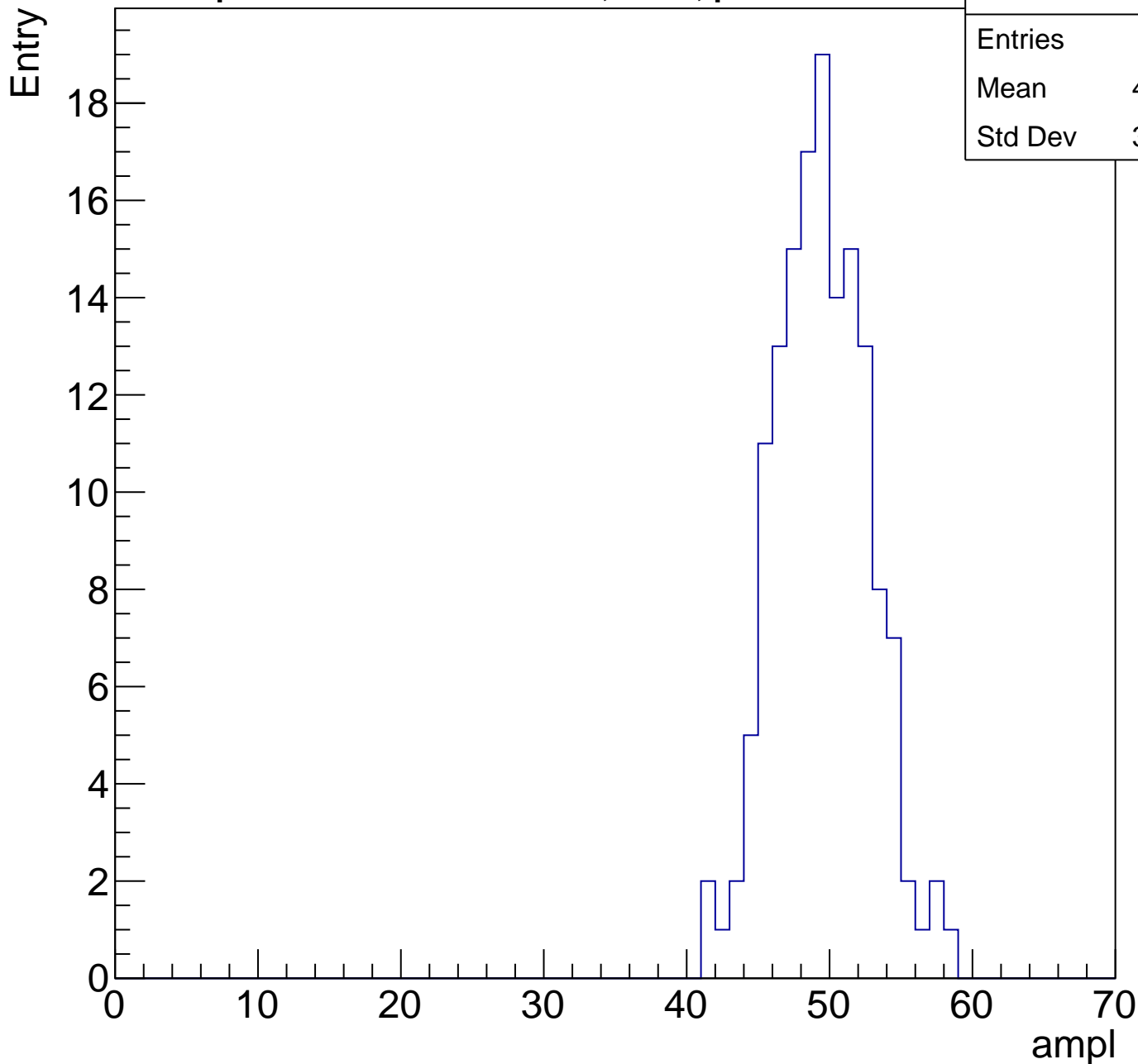
ampl



B1L001S, U21-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

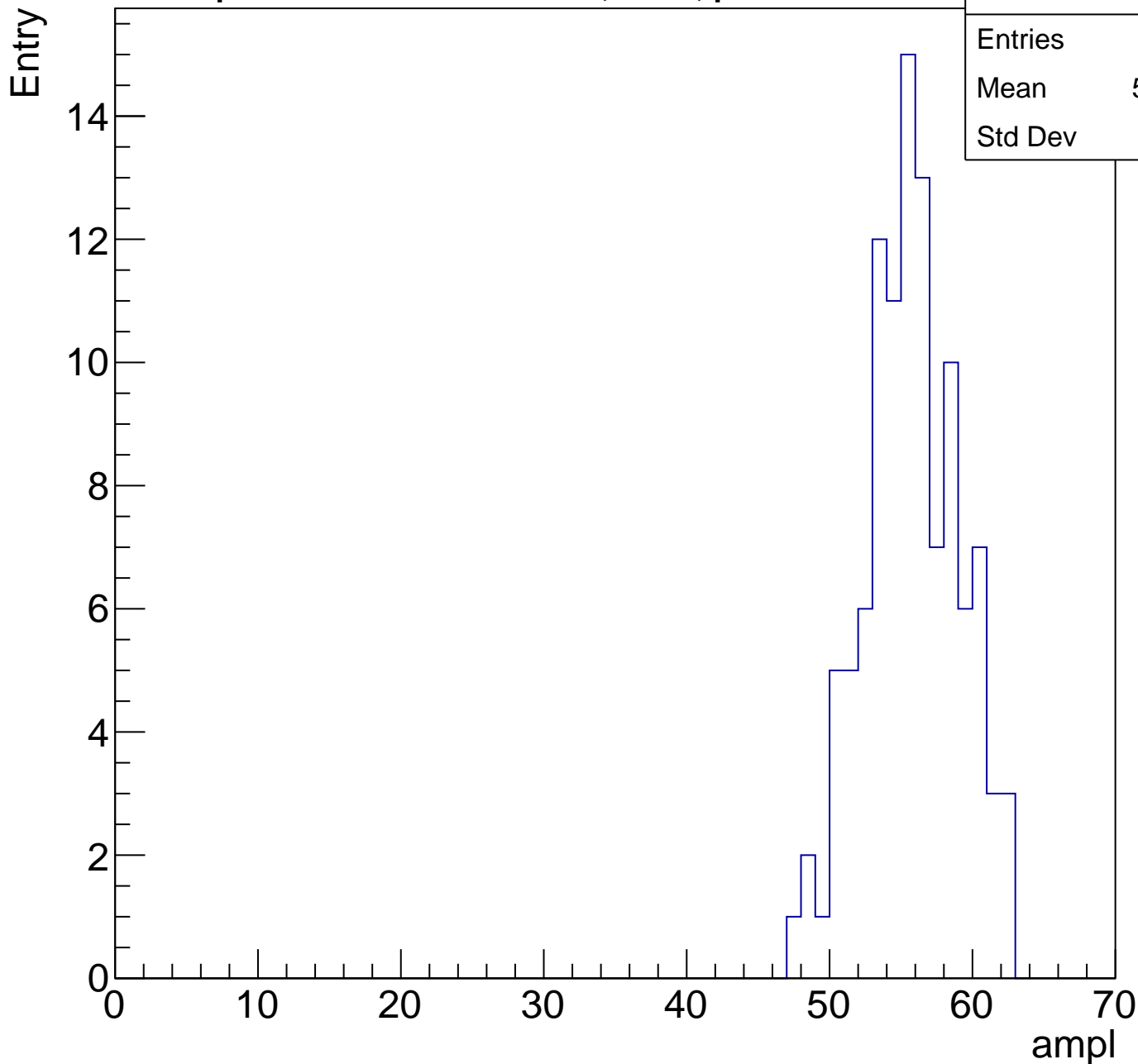
Entries	148
Mean	49.03
Std Dev	3.255



B1L001S, U21-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	55.27
Std Dev	3.3



B1L001S, U21-ch43, adc5

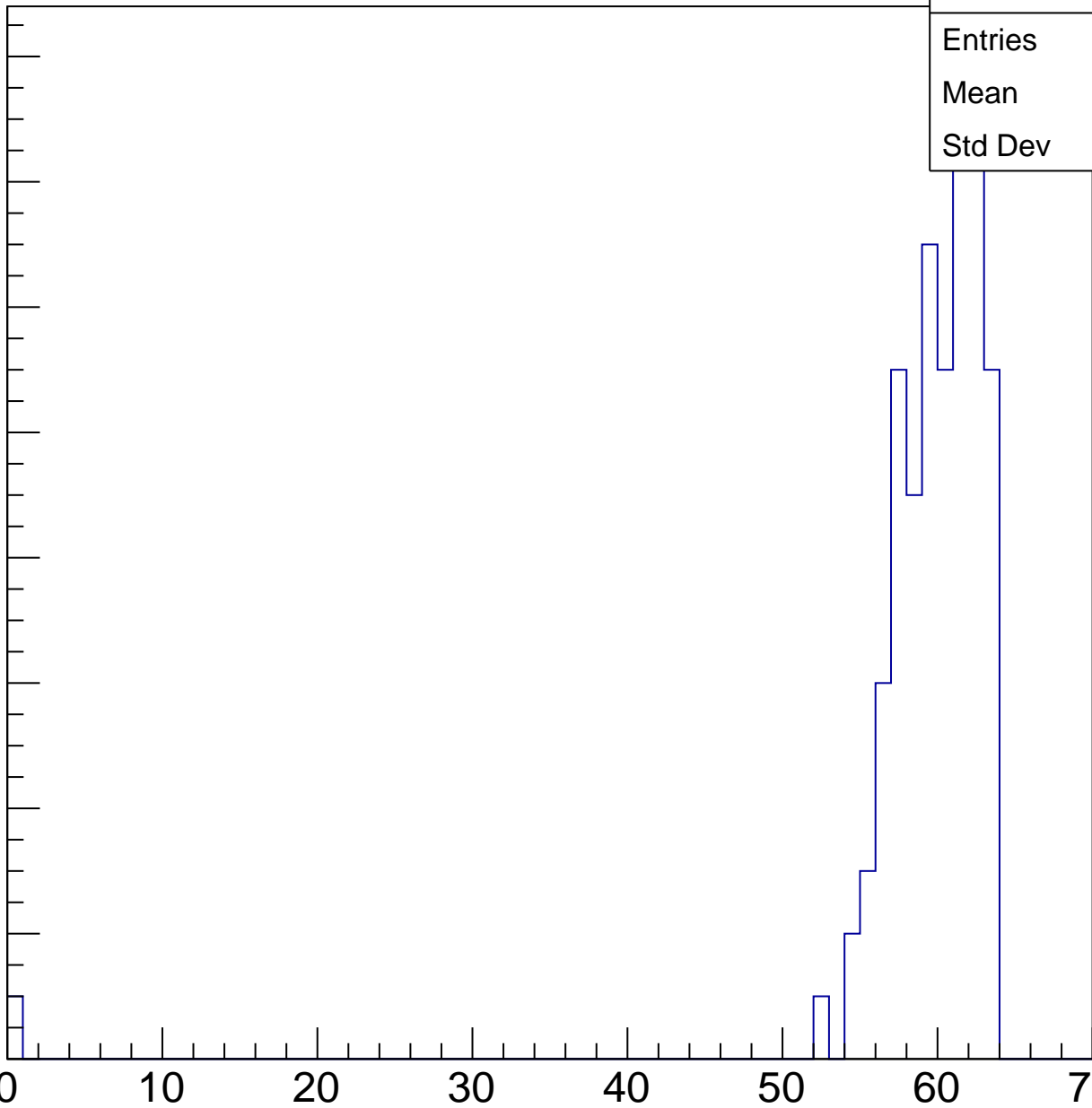
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	58.98
Std Dev	6.426

ampl

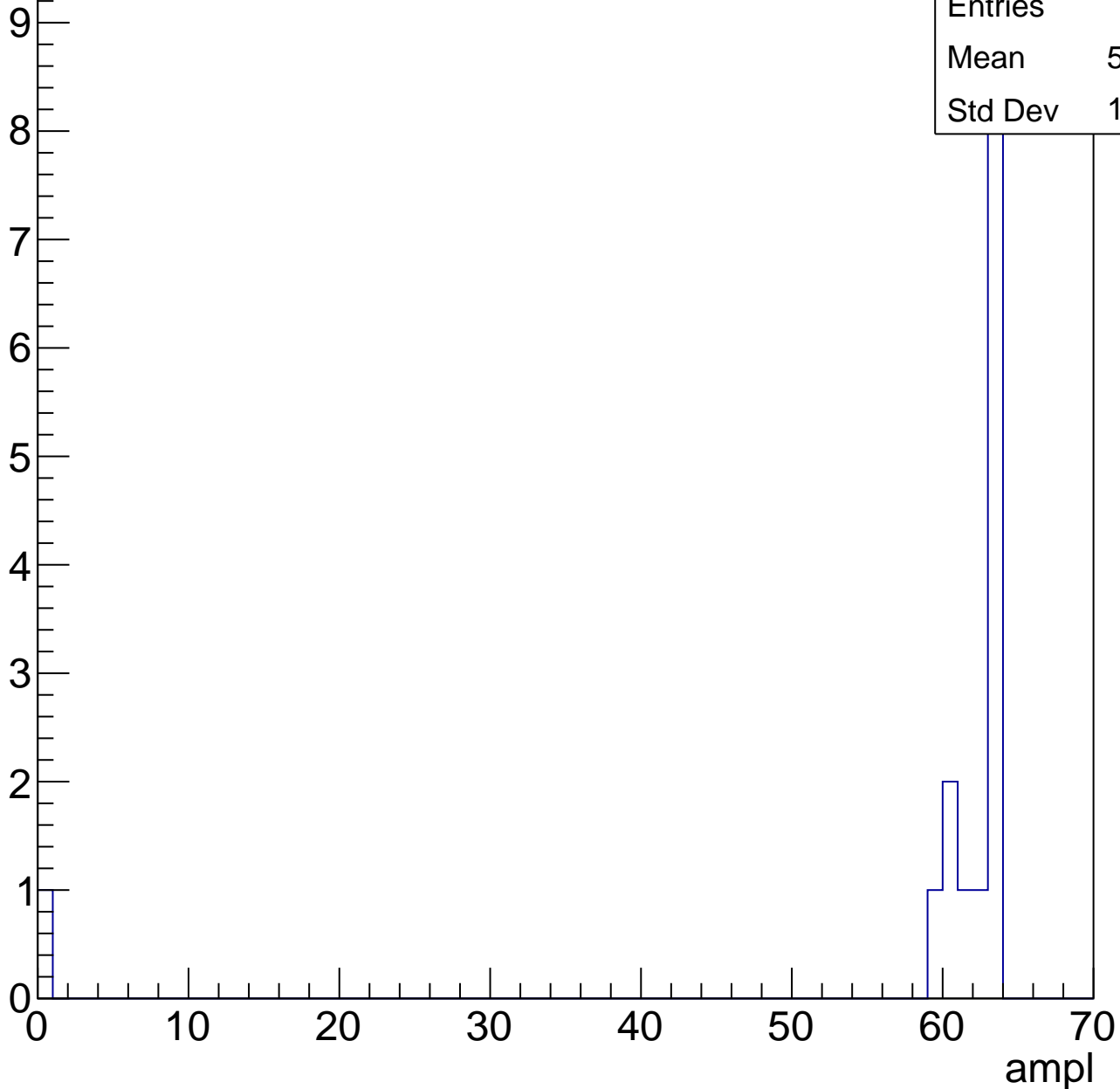


B1L001S, U21-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	57.93
Std Dev	15.54



B1L001S, U21-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U21-ch44, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	30.27
Std Dev	2.991

Gaus mean : 31.0978

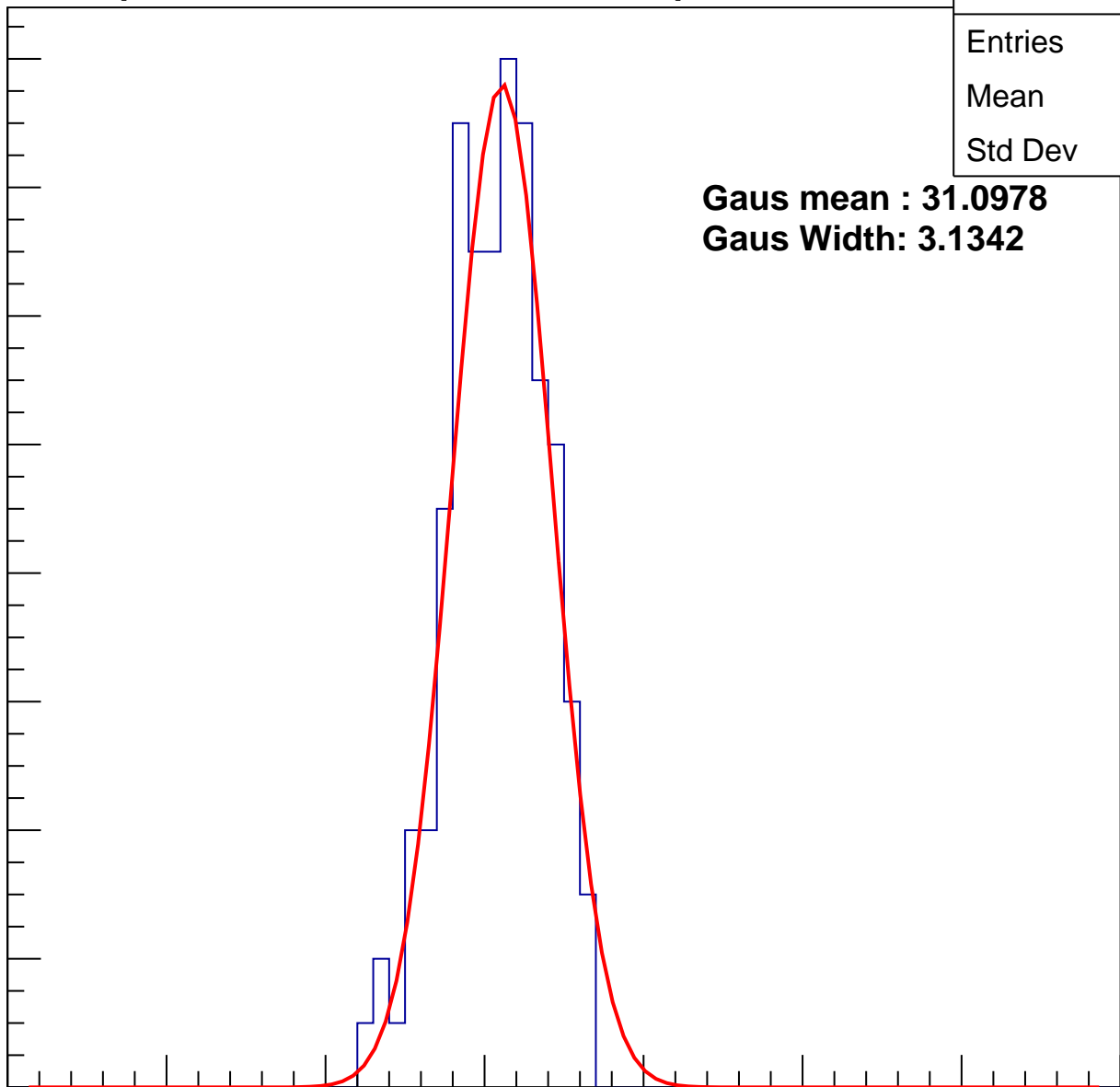
Gaus Width: 3.1342

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch44, adc1

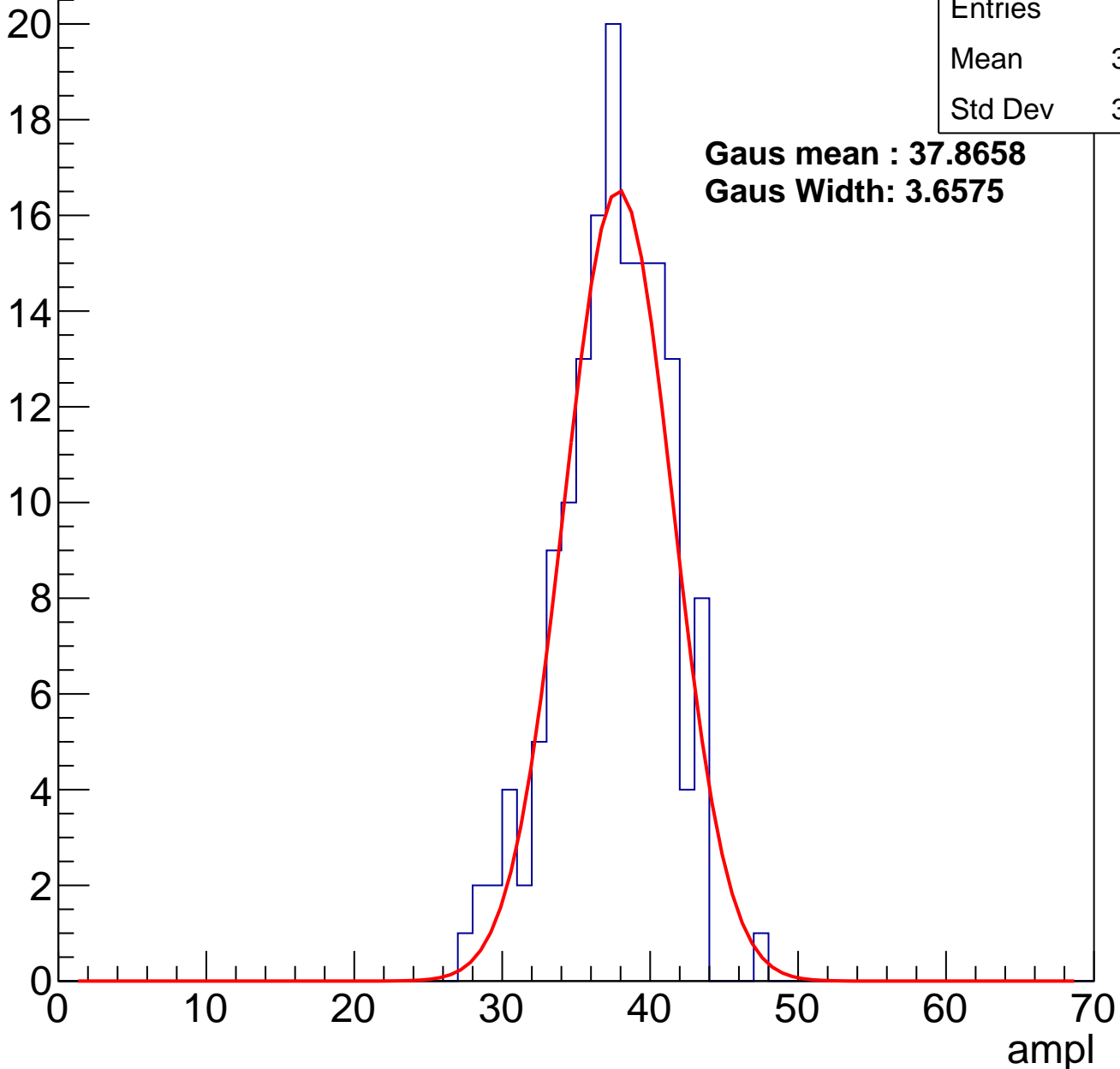
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	37.02
Std Dev	3.584

Gaus mean : 37.8658

Gaus Width: 3.6575

Entry



B1L001S, U21-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

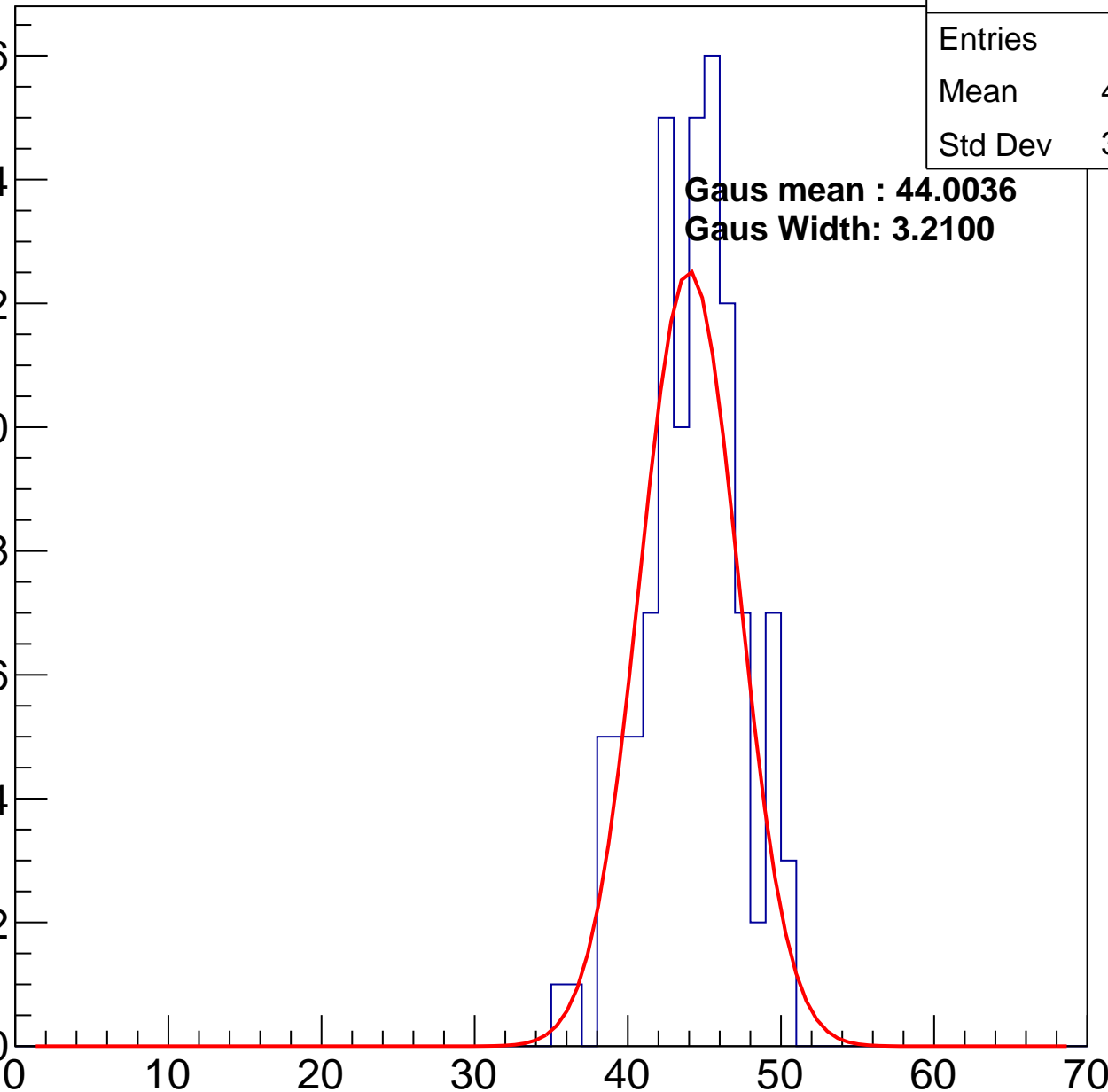
16
14
12
10
8
6
4
2
0

Entries	111
Mean	43.72
Std Dev	3.143

Gaus mean : 44.0036

Gaus Width: 3.2100

ampl

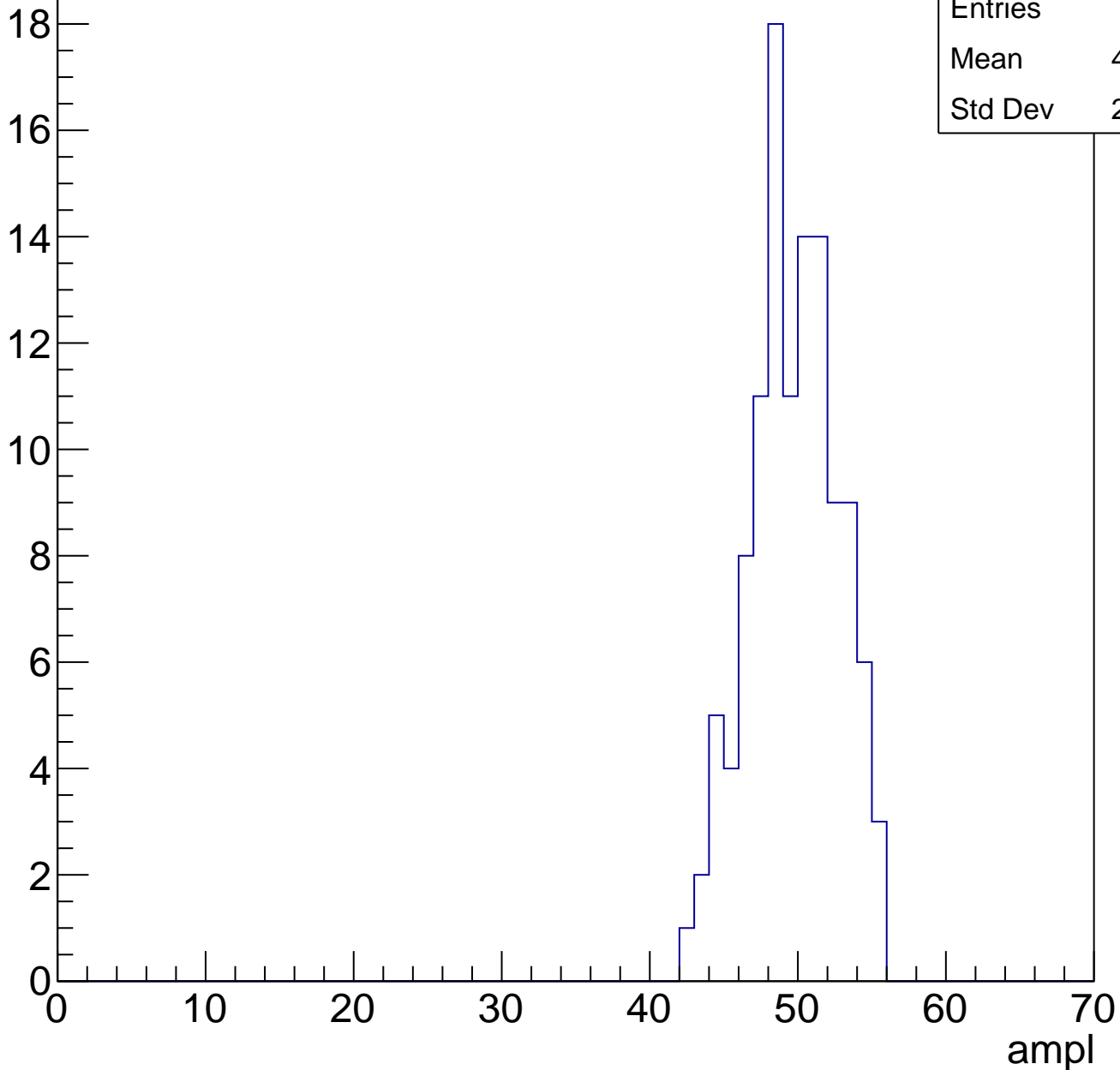


B1L001S, U21-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	49.25
Std Dev	2.934

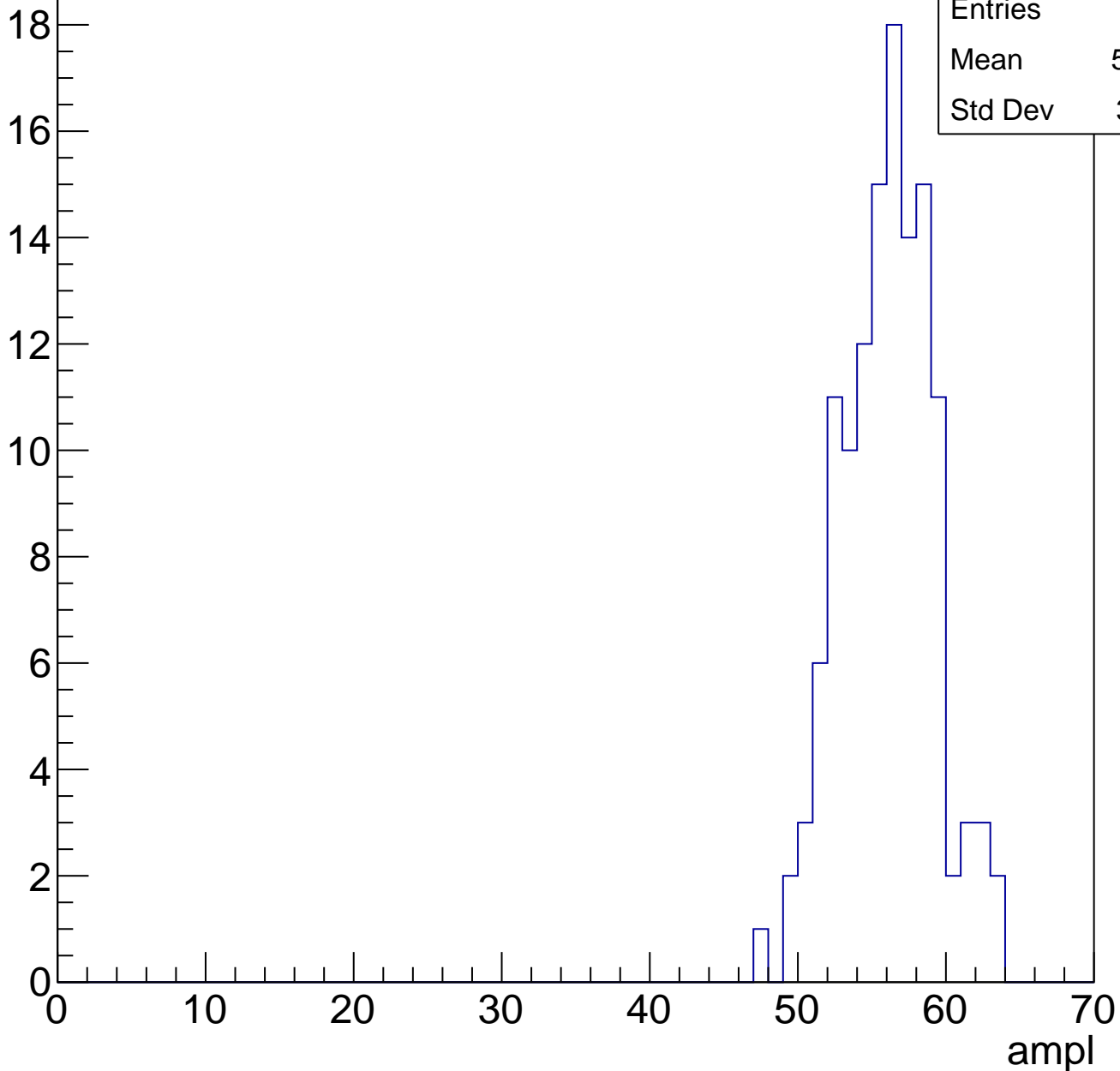
Entry



B1L001S, U21-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

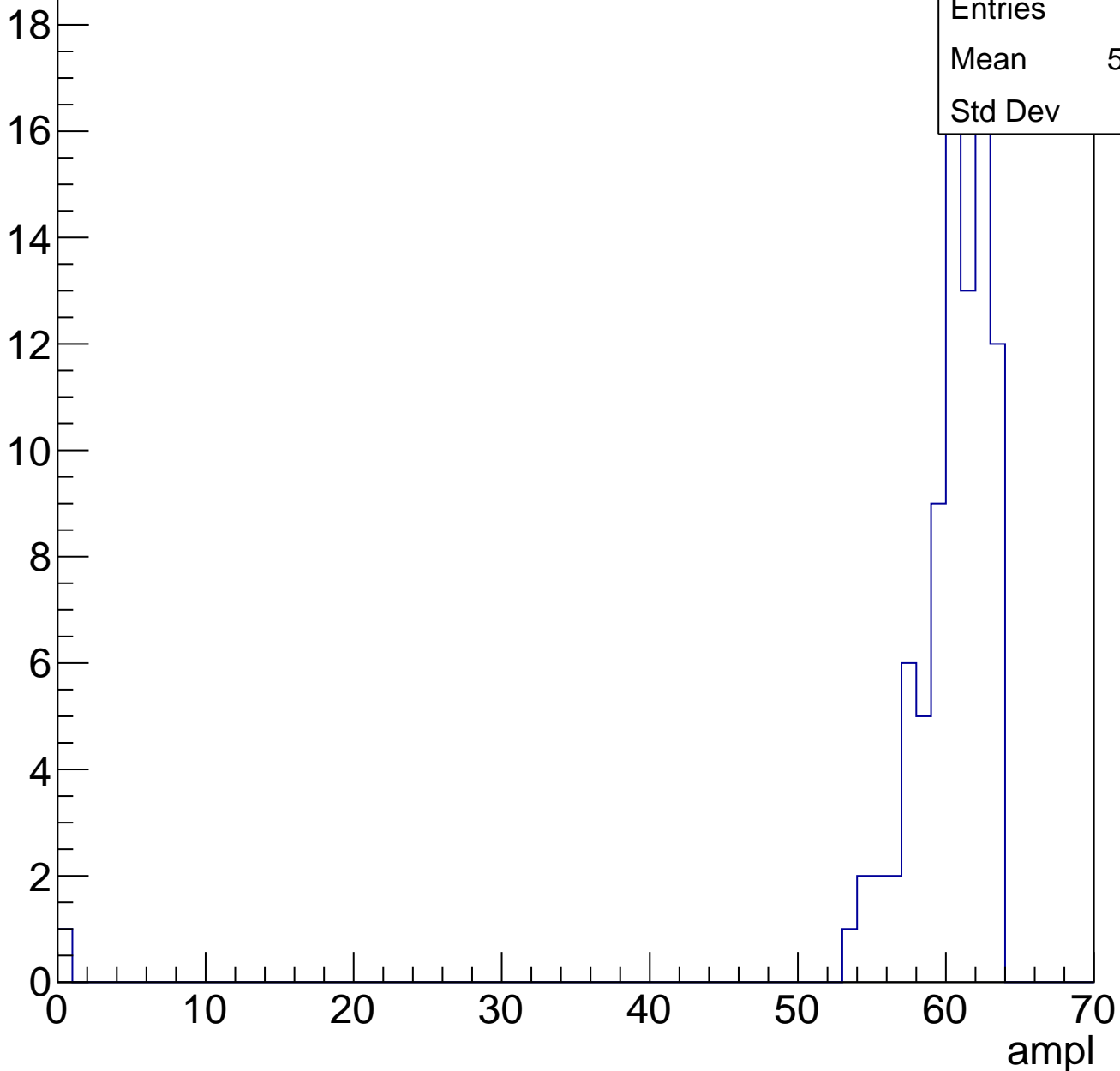
Entry



B1L001S, U21-ch44, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

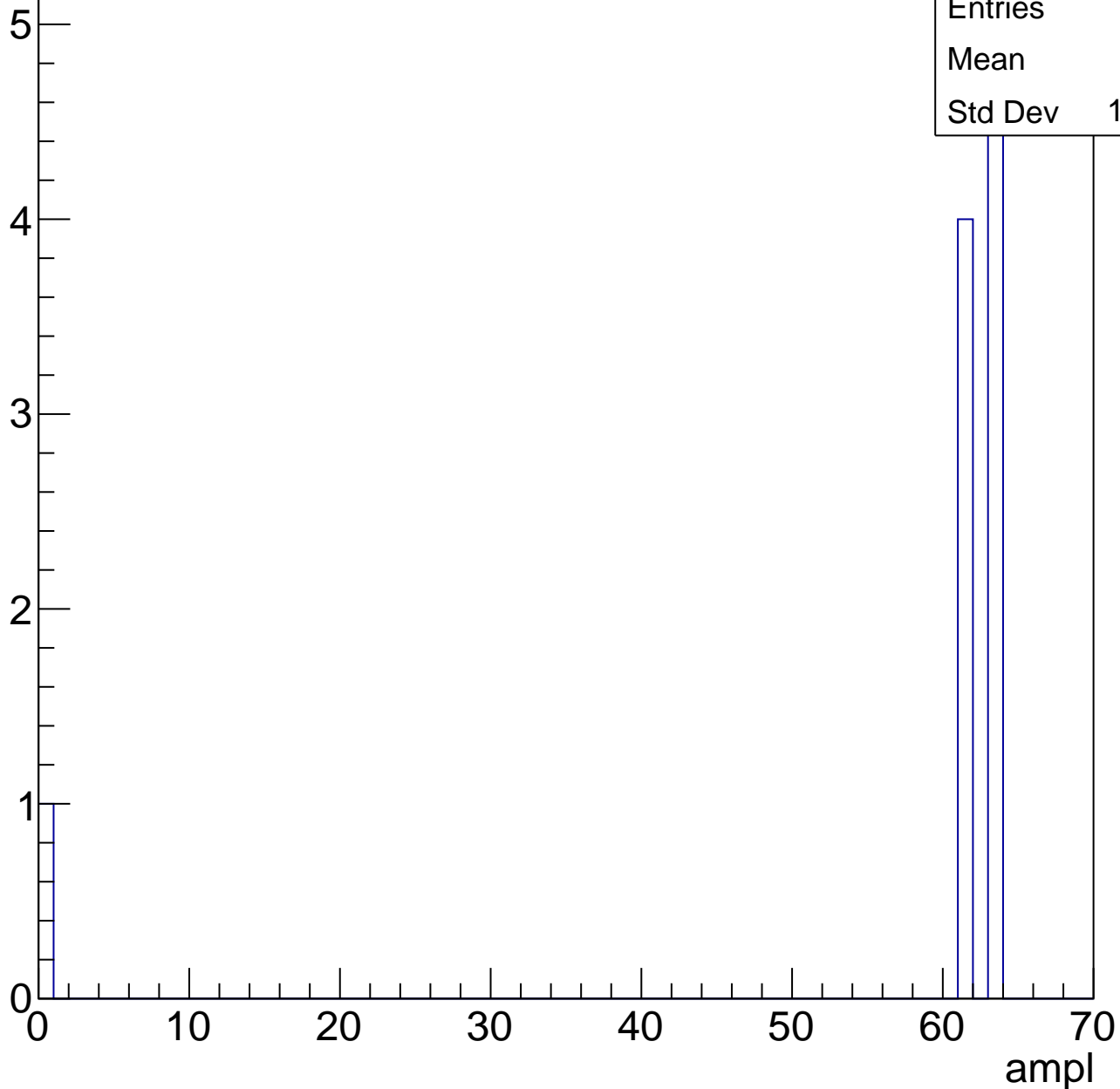


B1L001S, U21-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

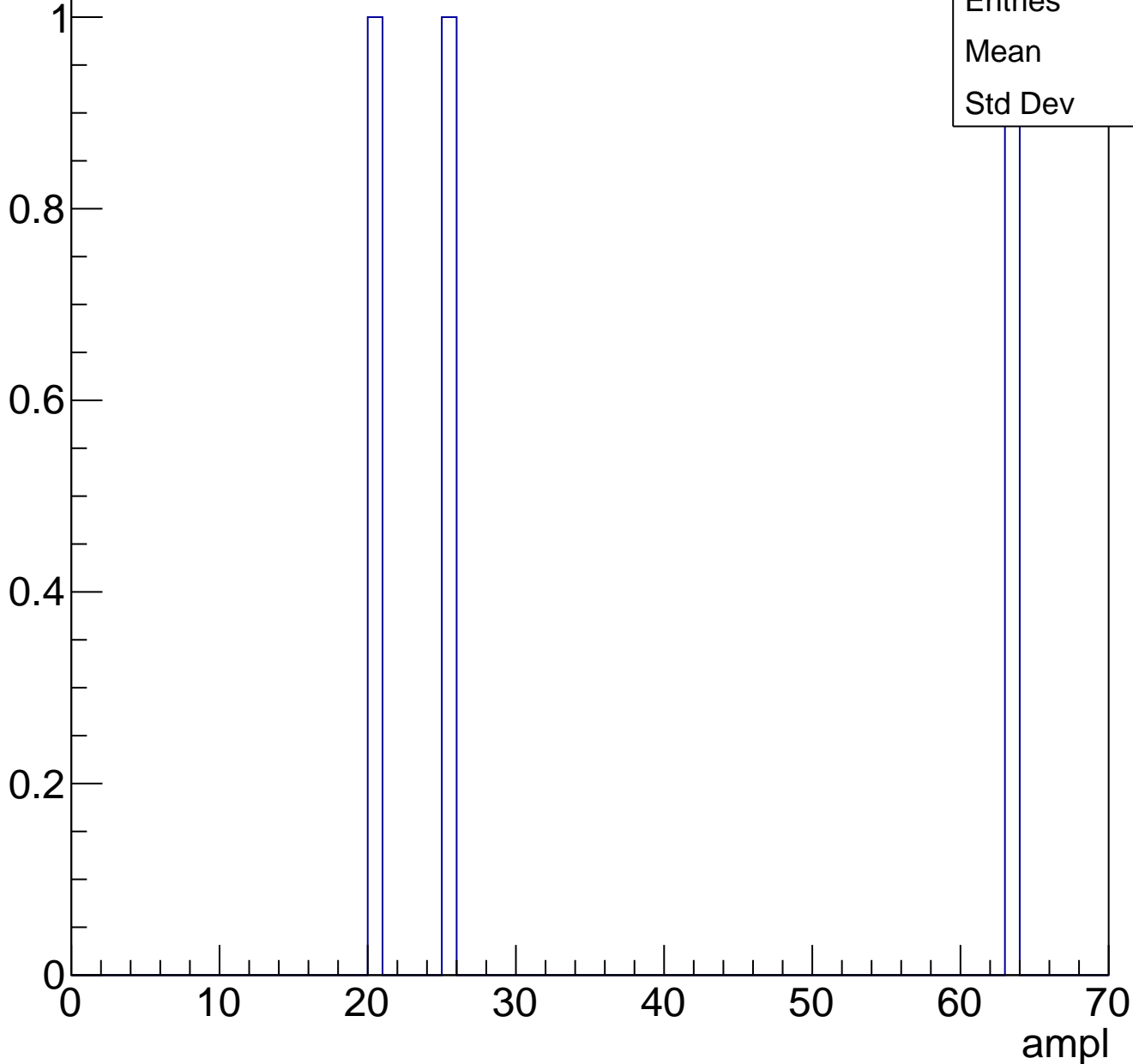
Entries	10
Mean	55.9
Std Dev	18.66



B1L001S, U21-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	29.4
Std Dev	3.243

Gaus mean : 29.9584

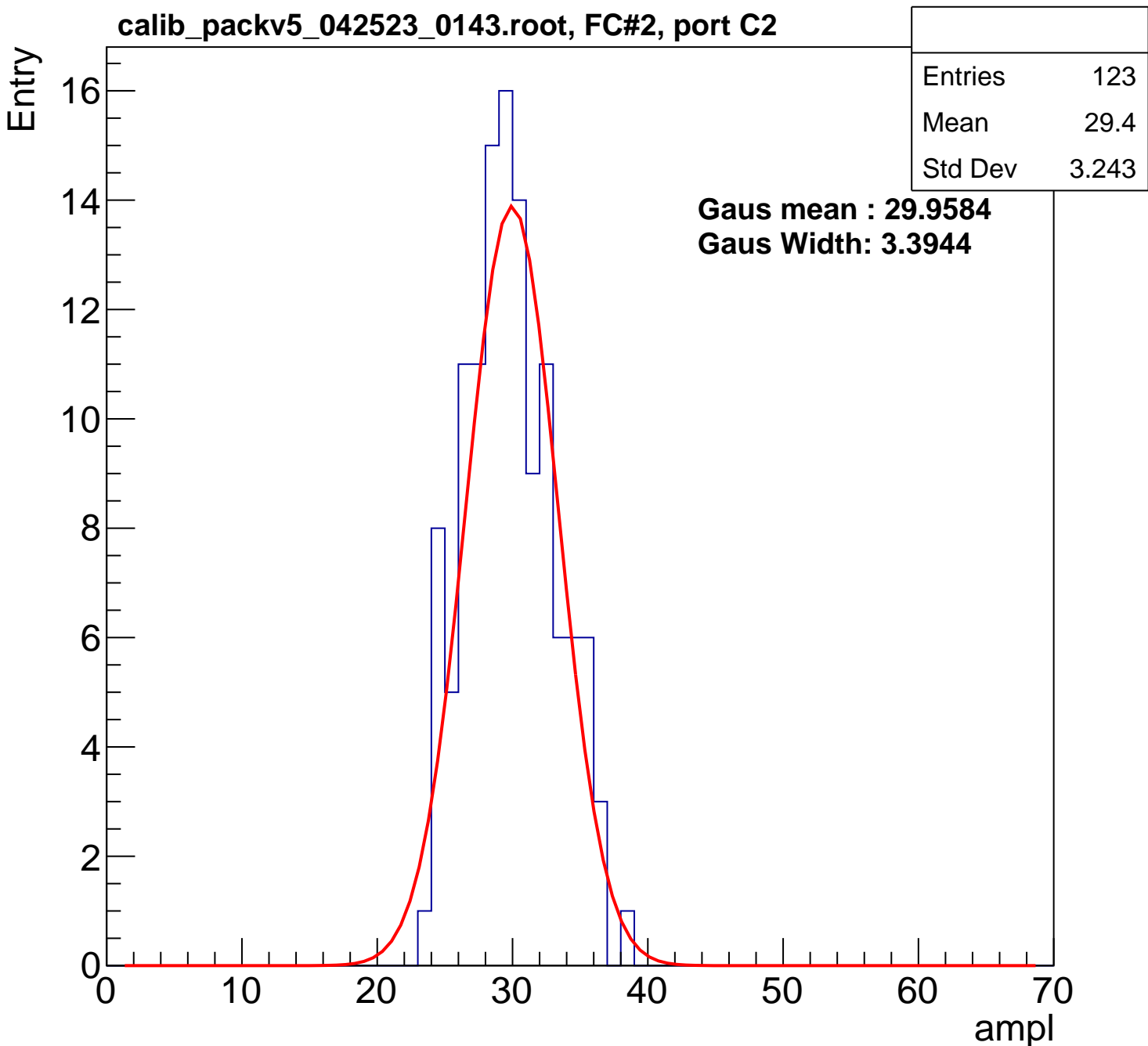
Gaus Width: 3.3944

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

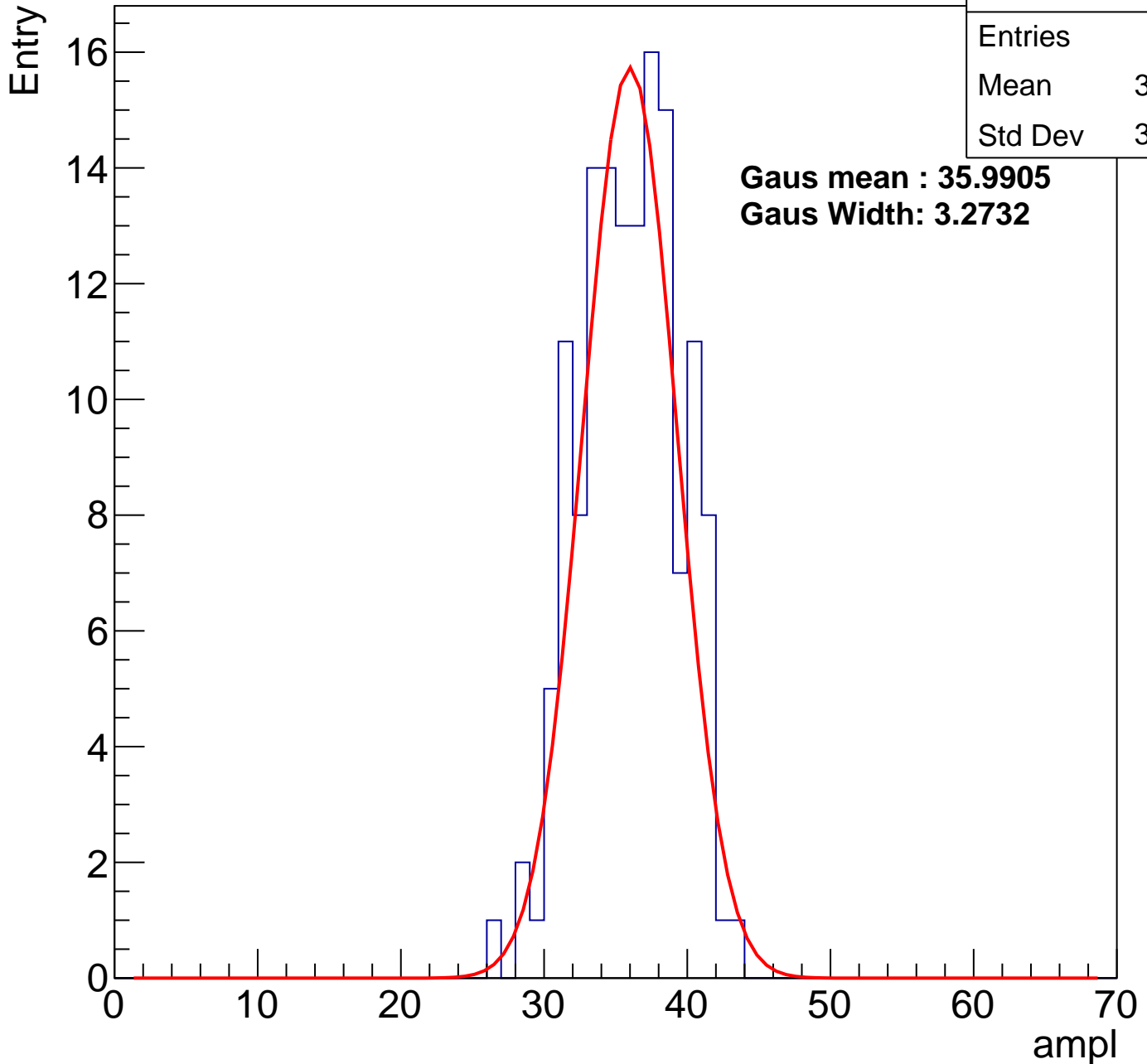


B1L001S, U21-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	35.51
Std Dev	3.391

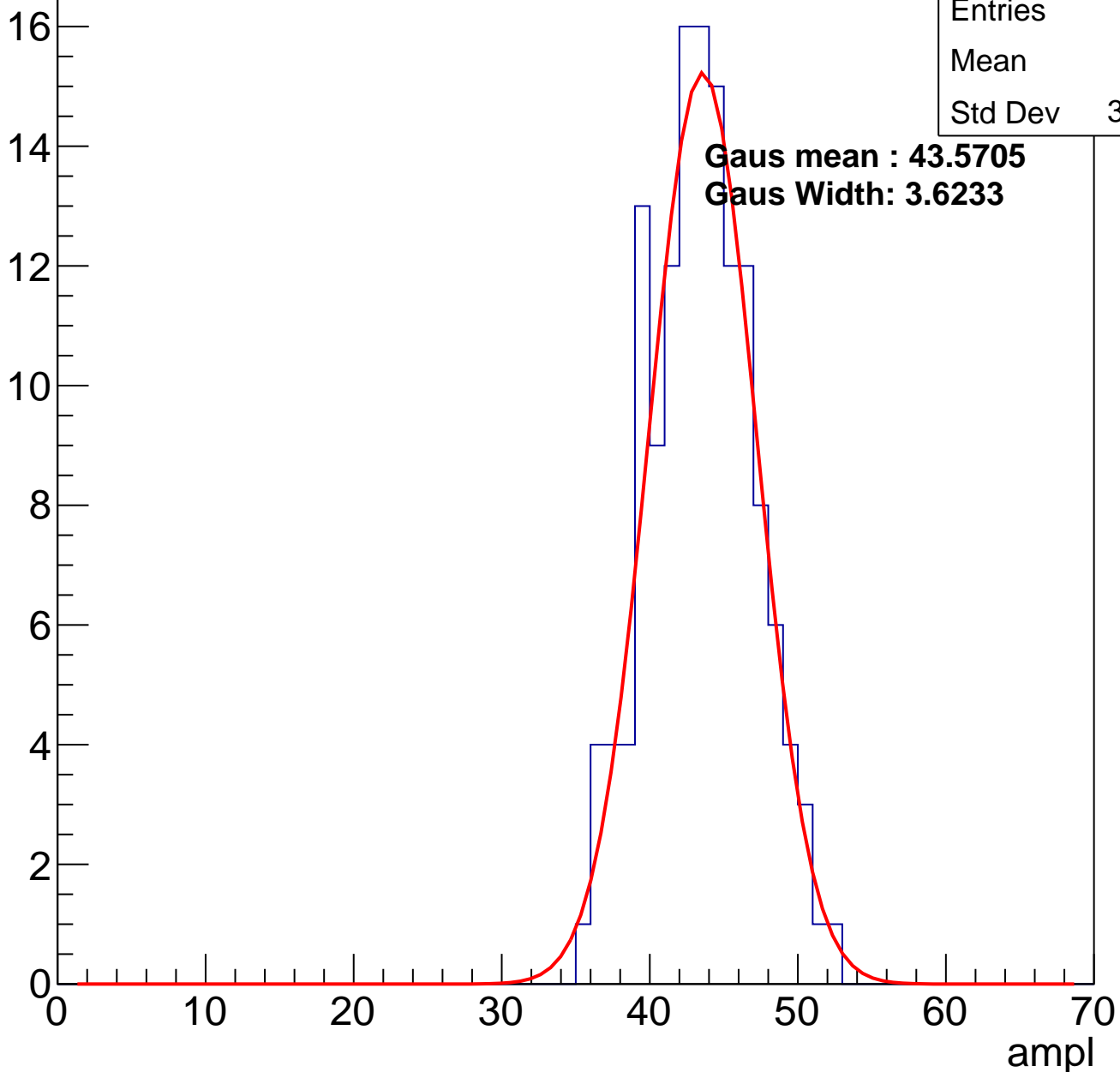
Gaus mean : 35.9905
Gaus Width: 3.2732



B1L001S, U21-ch45, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



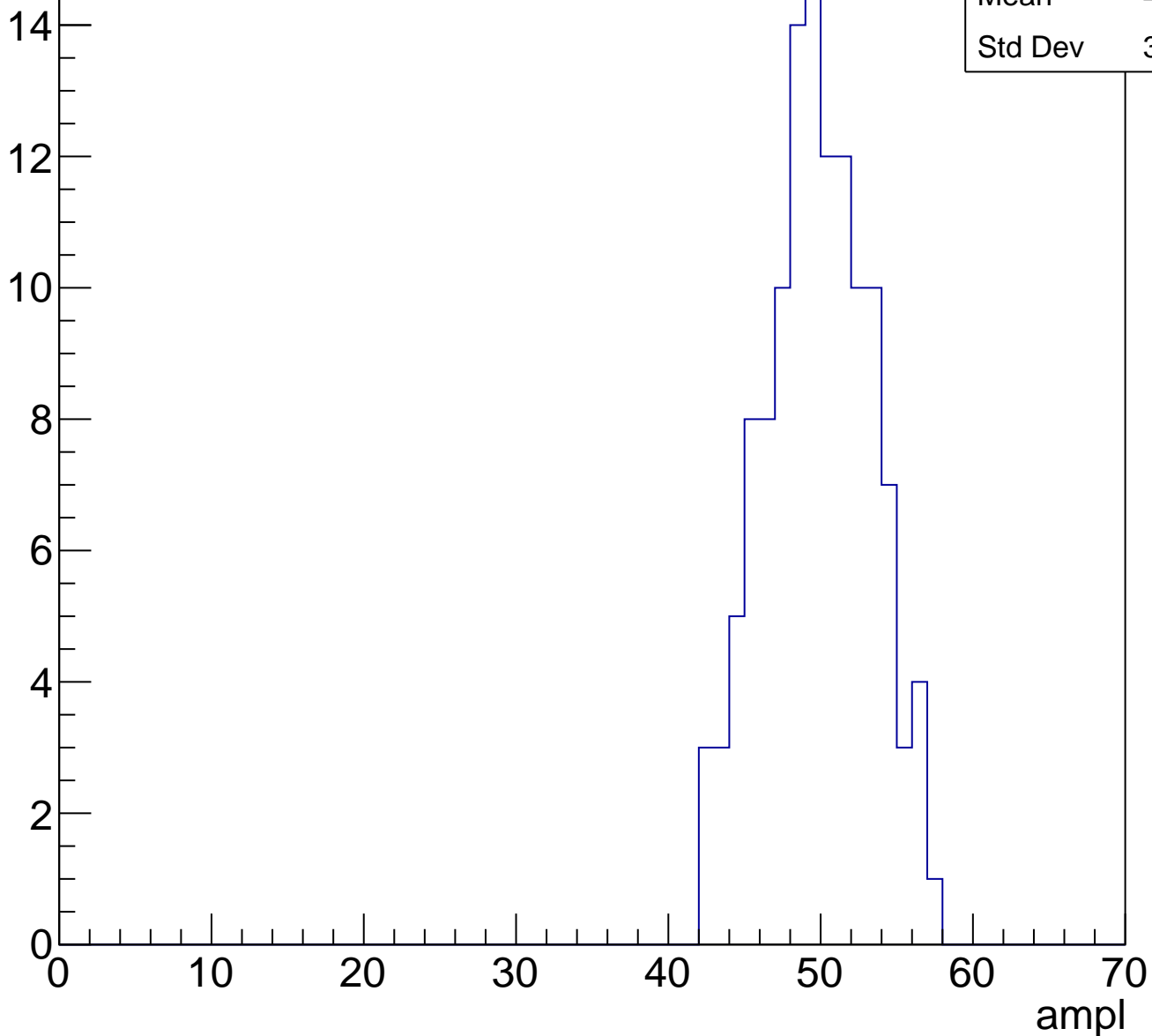
Entries	141
Mean	43
Std Dev	3.515

B1L001S, U21-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	49.33
Std Dev	3.433

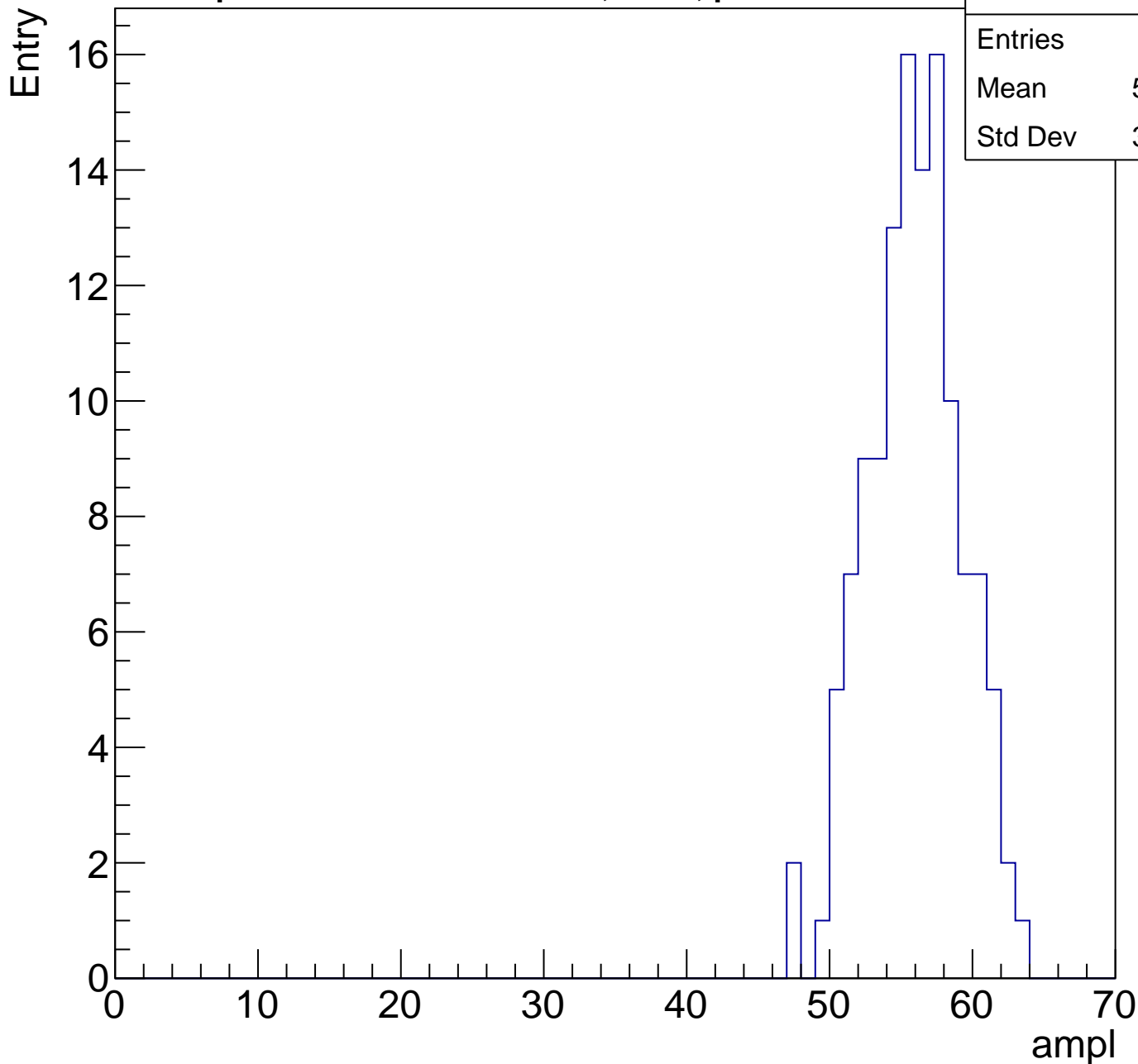
Entry



B1L001S, U21-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	55.47
Std Dev	3.239



B1L001S, U21-ch45, adc5

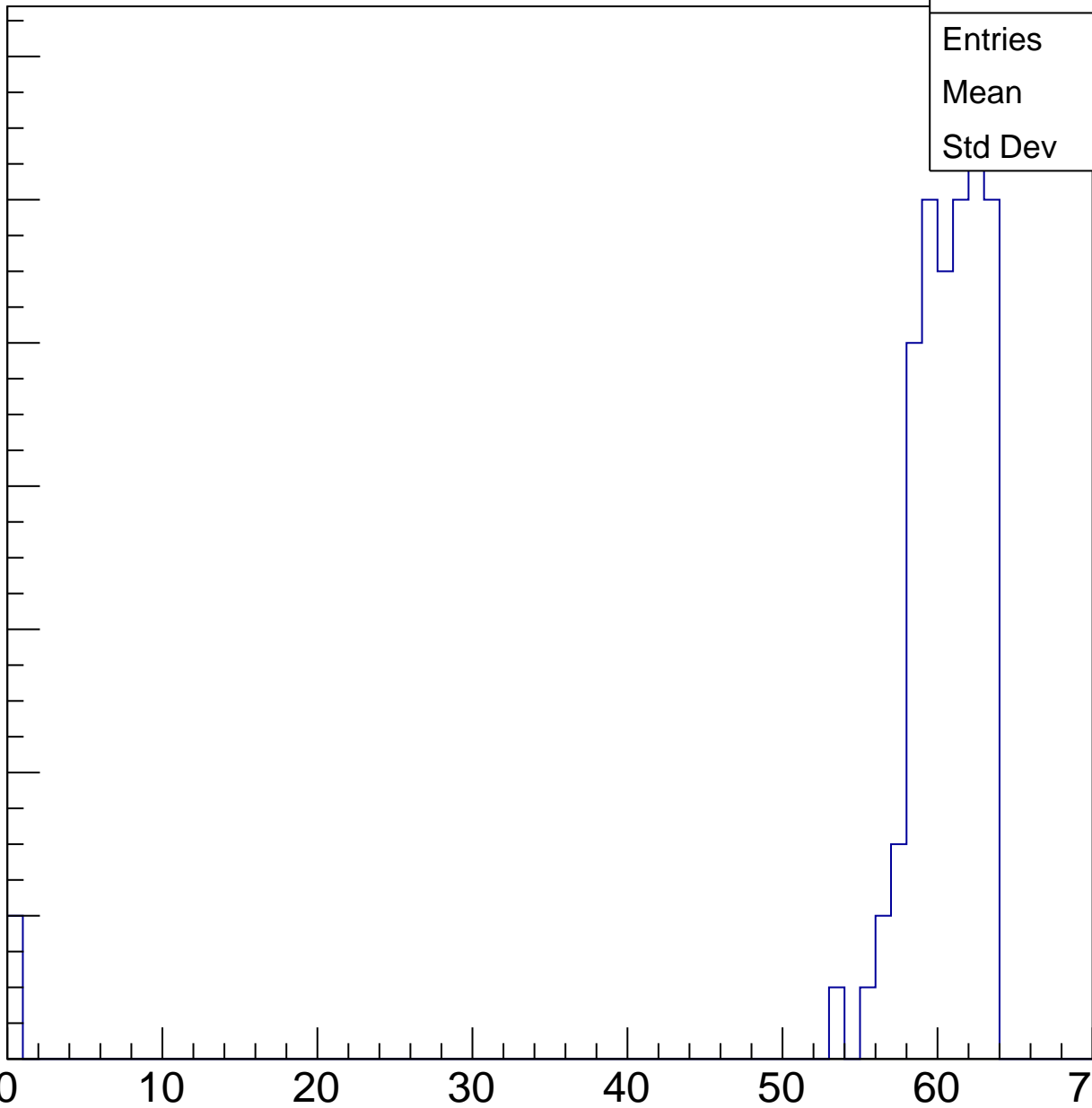
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	58.69
Std Dev	9.633

ampl

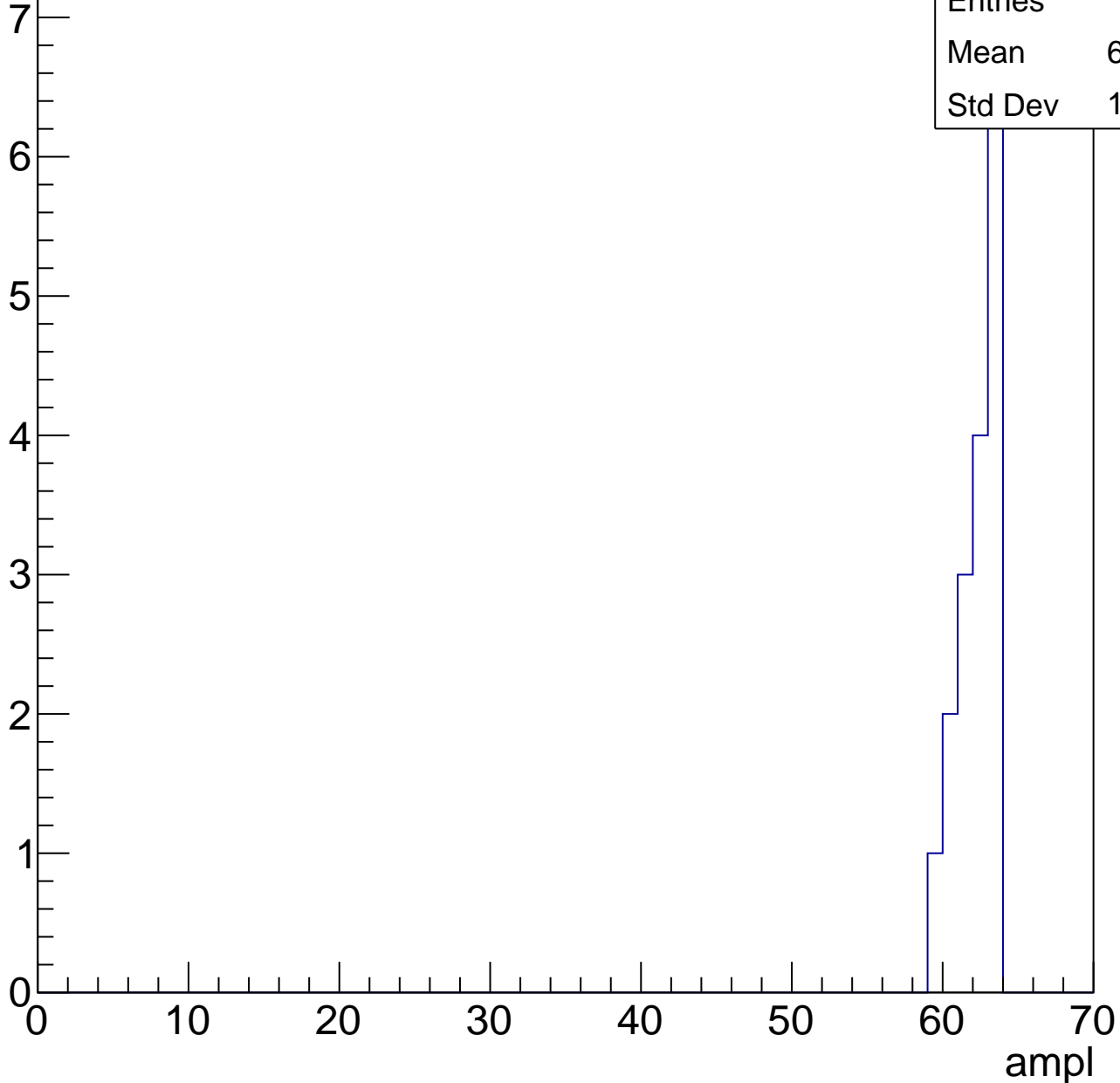


B1L001S, U21-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	61.82
Std Dev	1.248



B1L001S, U21-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch46, adc0

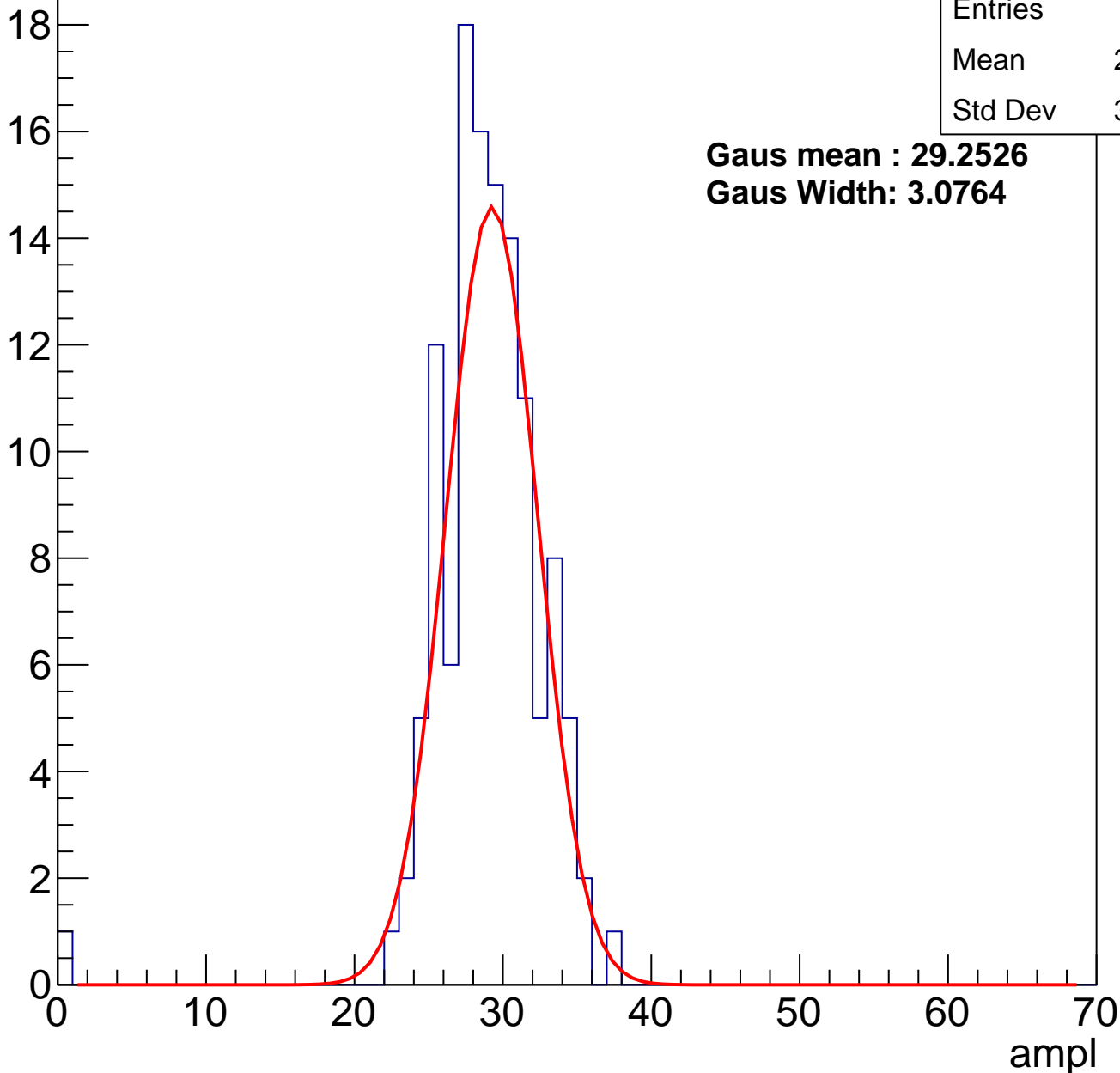
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.48
Std Dev	3.926

Gaus mean : 29.2526

Gaus Width: 3.0764

Entry

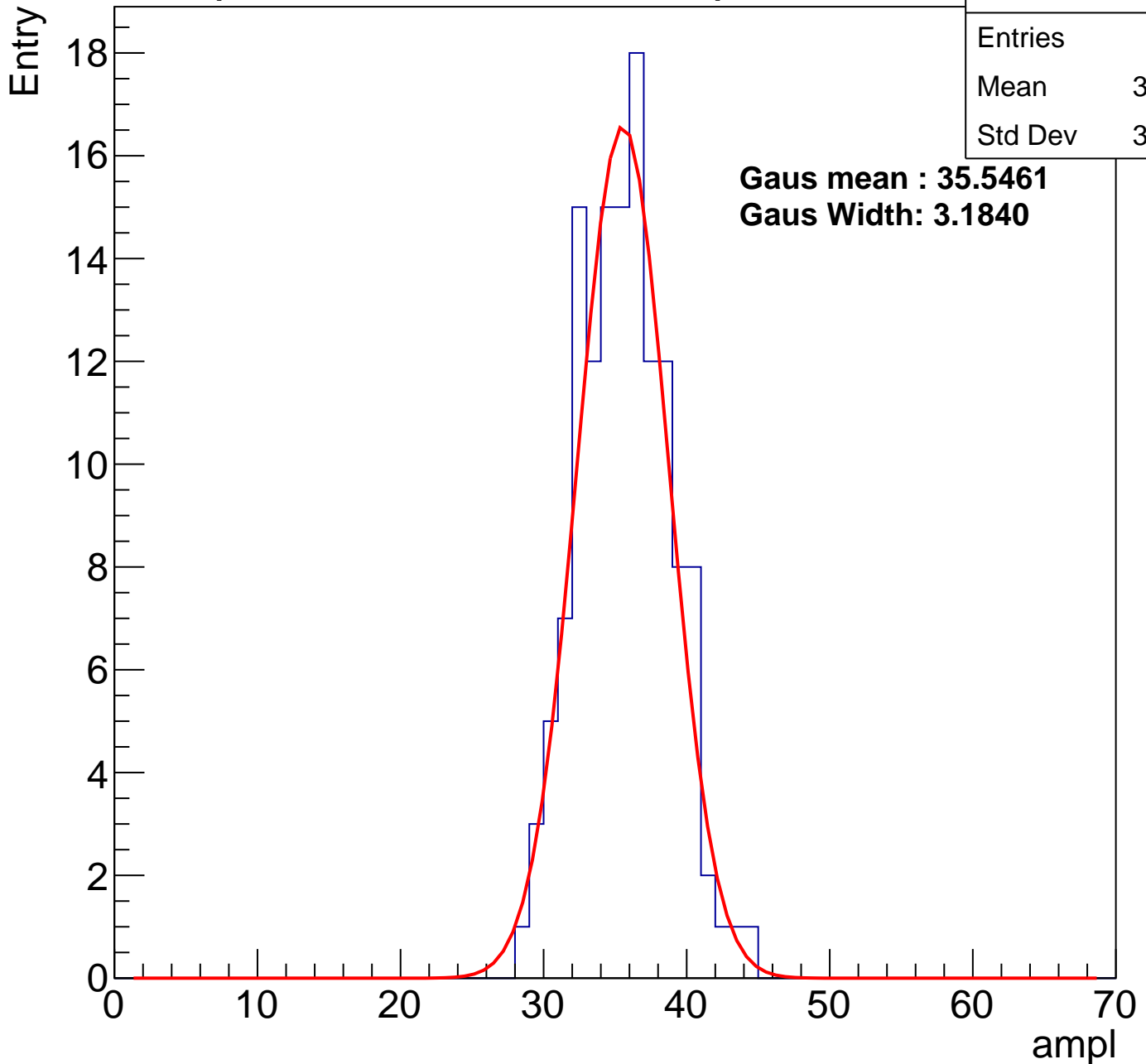


B1L001S, U21-ch46, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	35.18
Std Dev	3.157

Gaus mean : 35.5461
Gaus Width: 3.1840



B1L001S, U21-ch46, adc2

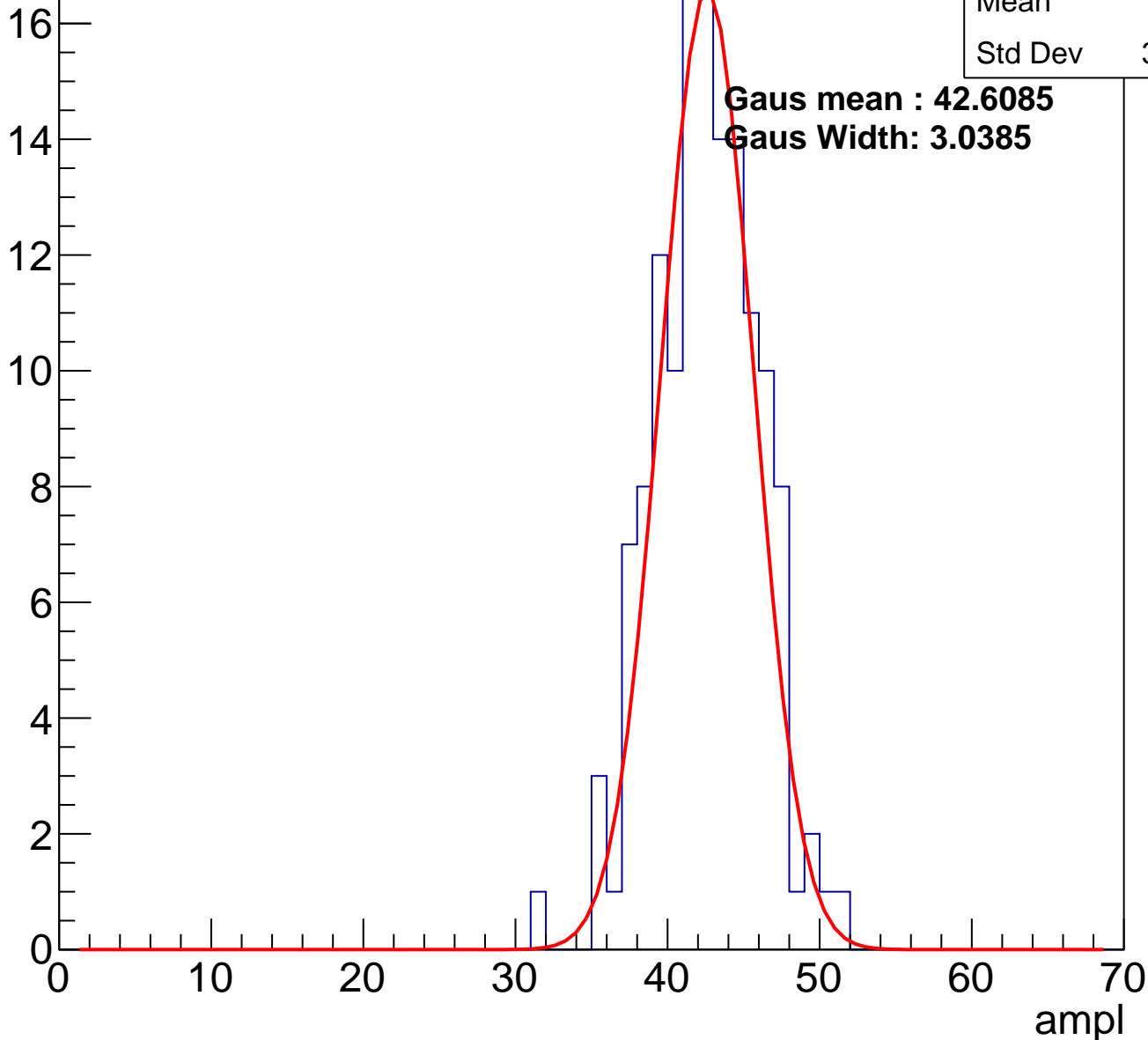
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	138
Mean	42.1
Std Dev	3.382

Gaus mean : 42.6085

Gaus Width: 3.0385



B1L001S, U21-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	48.75
Std Dev	3.399

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

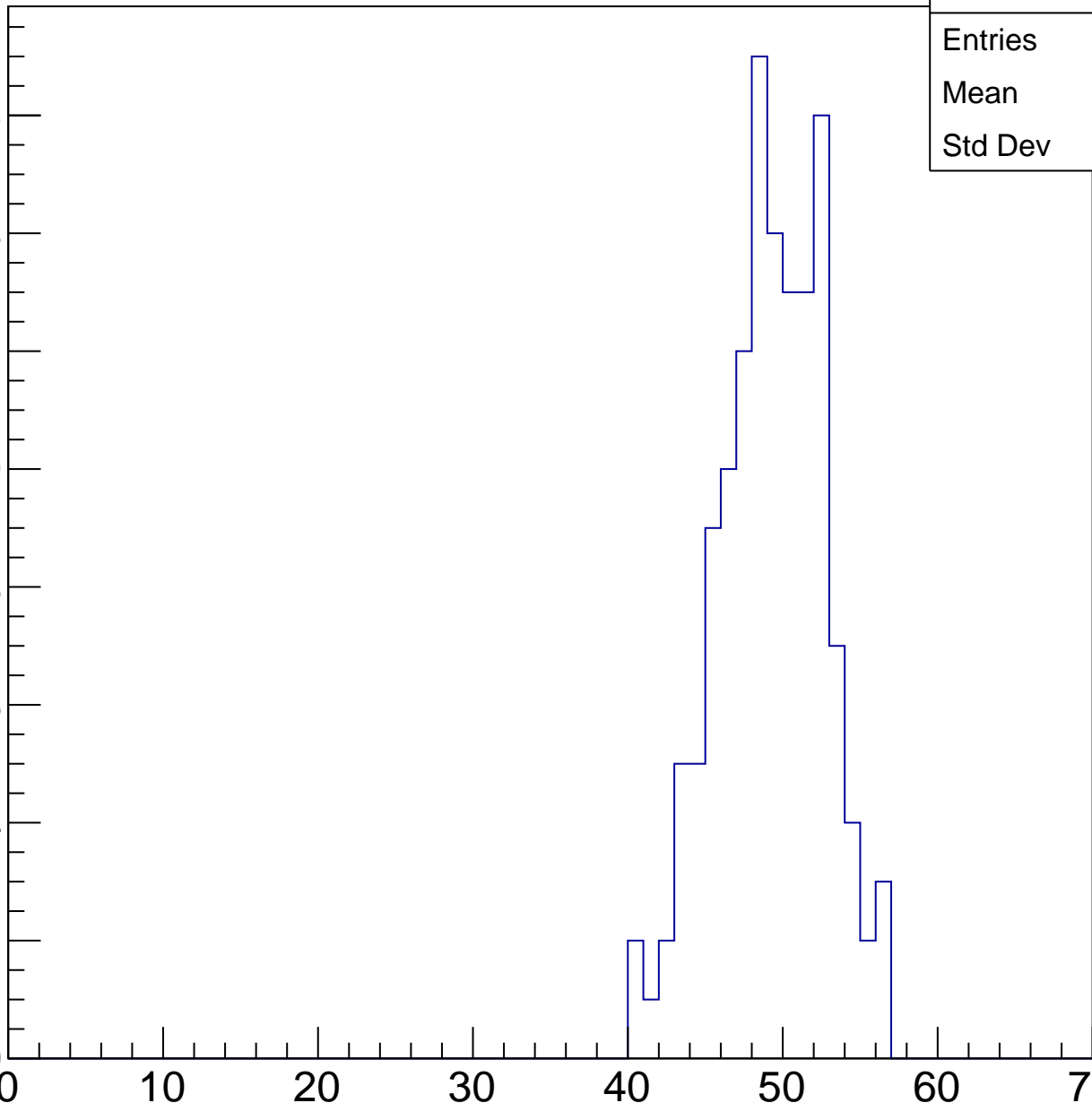
40

50

60

70

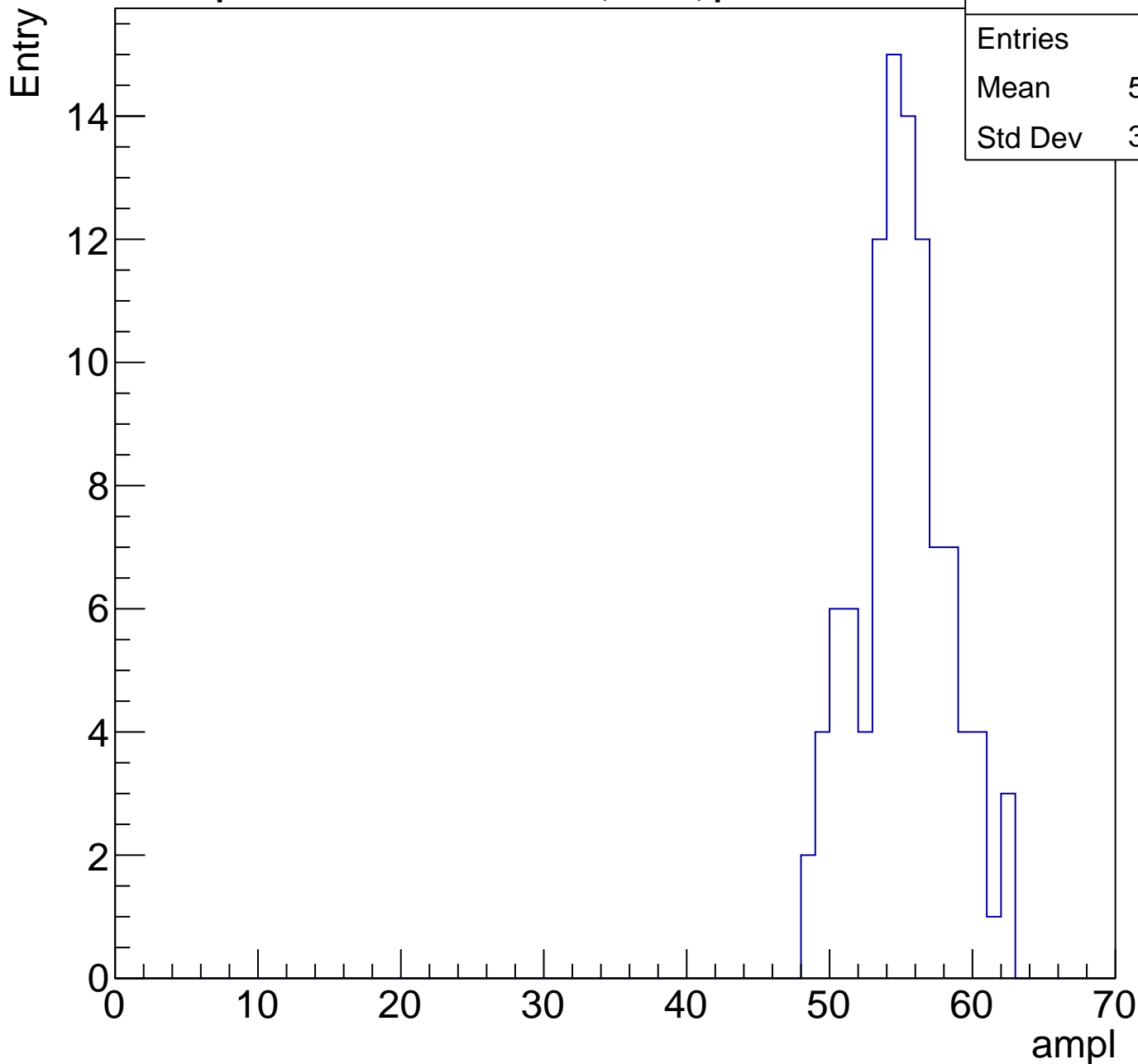
ampl



B1L001S, U21-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

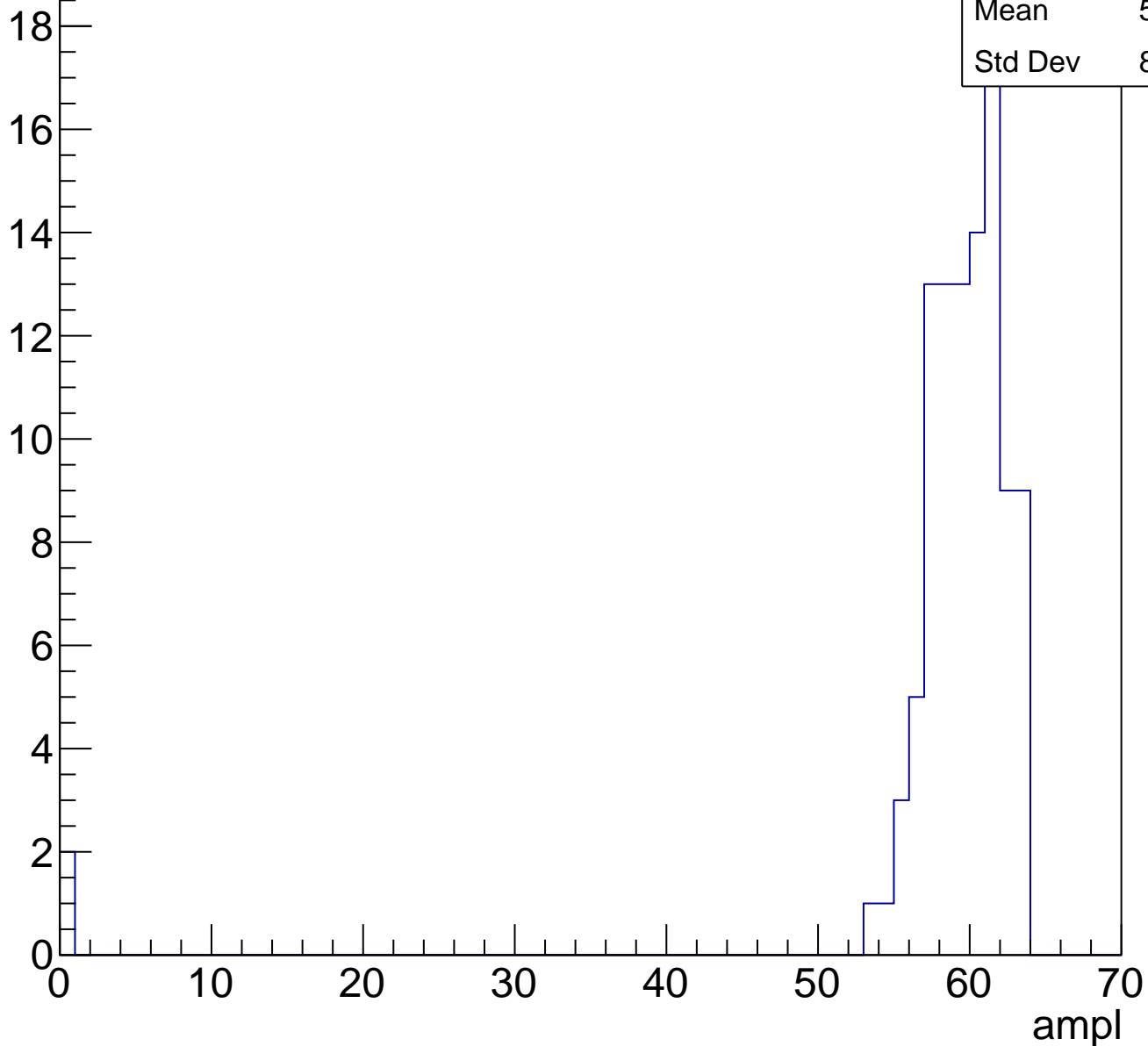
Entries	101
Mean	54.67
Std Dev	3.188



B1L001S, U21-ch46, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

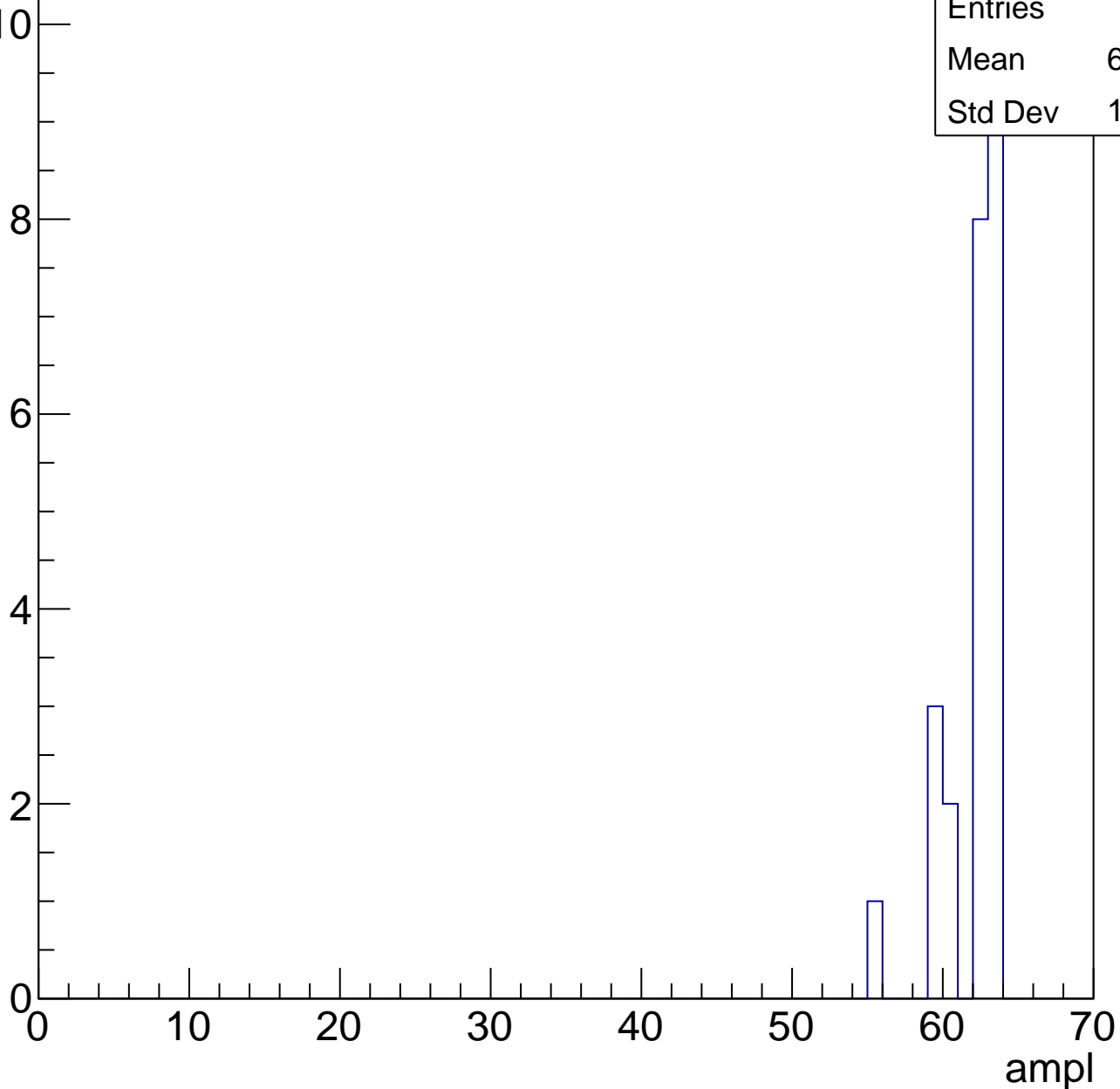


B1L001S, U21-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	24
Mean	61.58
Std Dev	1.935



B1L001S, U21-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

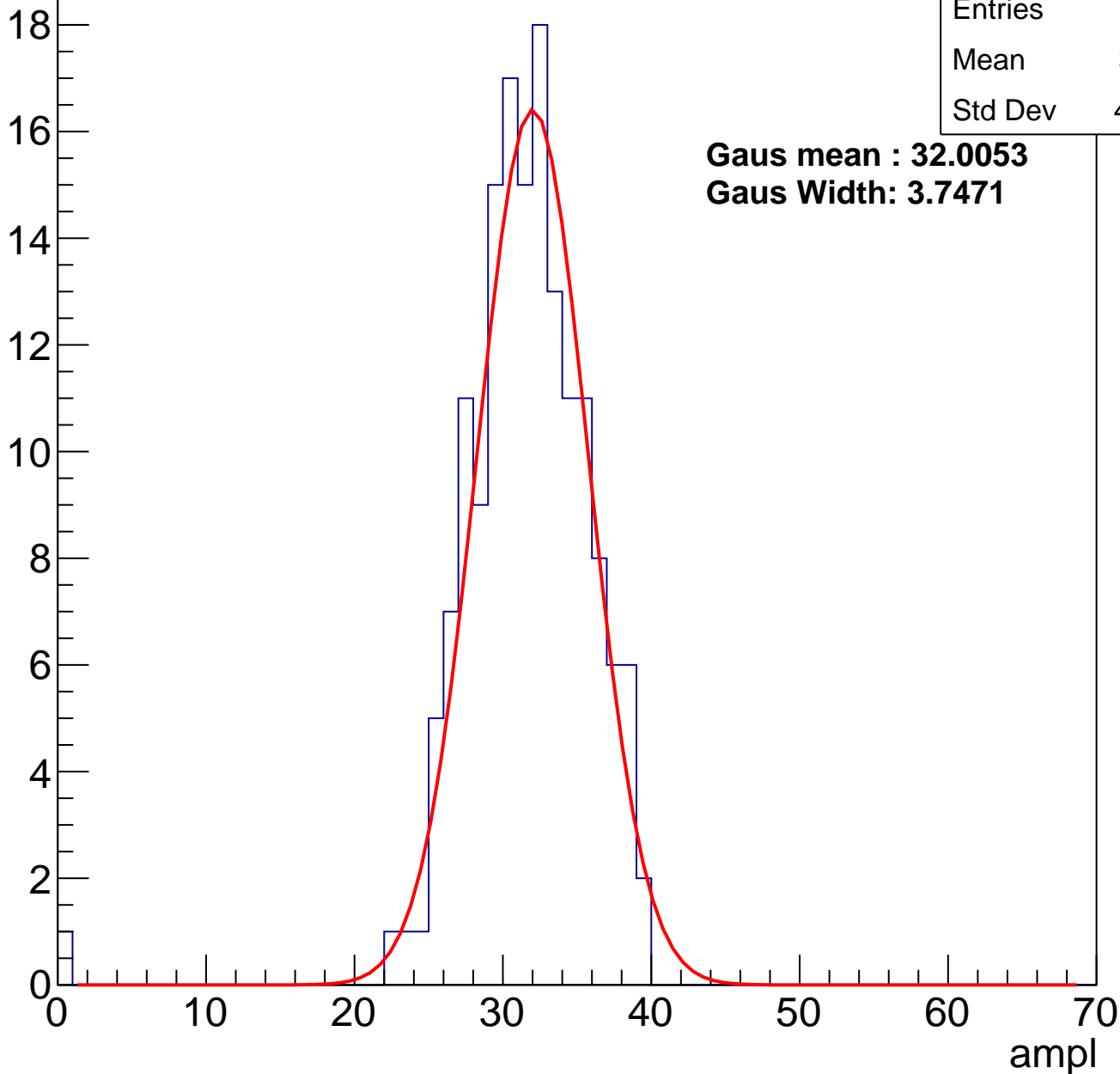
Entries	2
Mean	43
Std Dev	20

ampl

B1L001S, U21-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

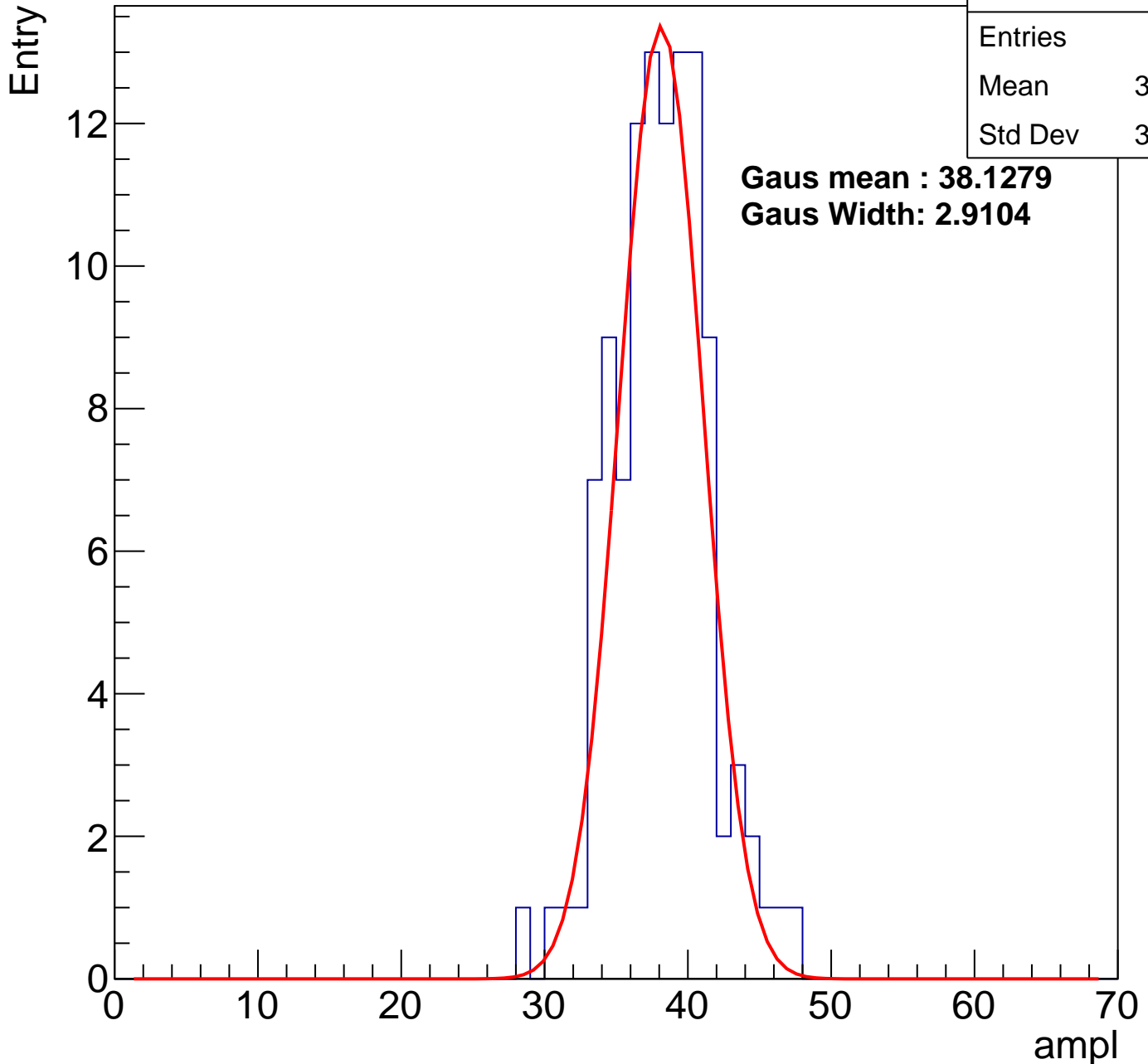


B1L001S, U21-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	37.68
Std Dev	3.318

Gaus mean : 38.1279
Gaus Width: 2.9104



B1L001S, U21-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

132

Mean

44.2

Std Dev

3.47

Gaus mean : 44.6123

Gaus Width: 3.2749

0

10

20

30

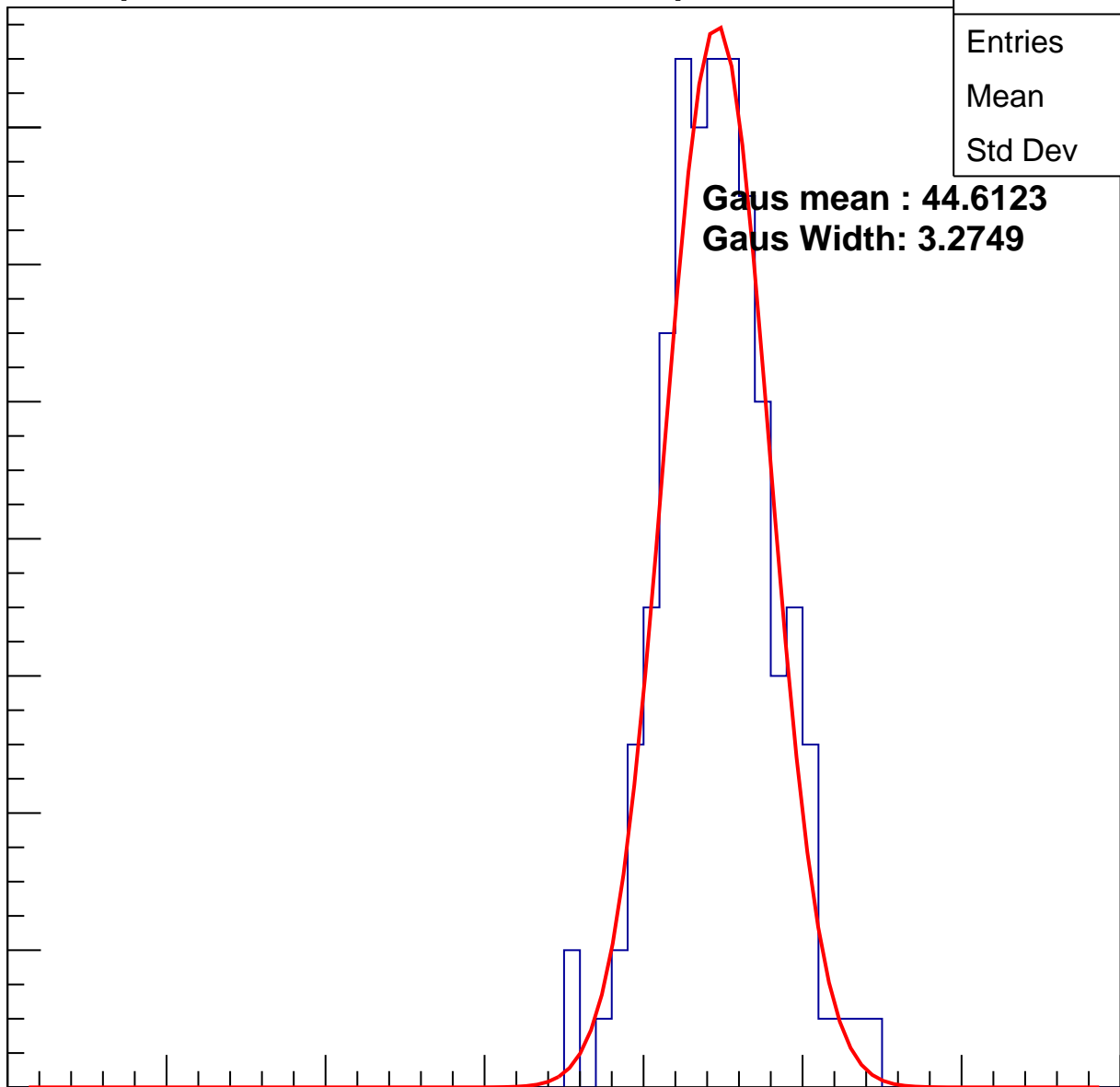
40

50

60

70

ampl



B1L001S, U21-ch47, adc3

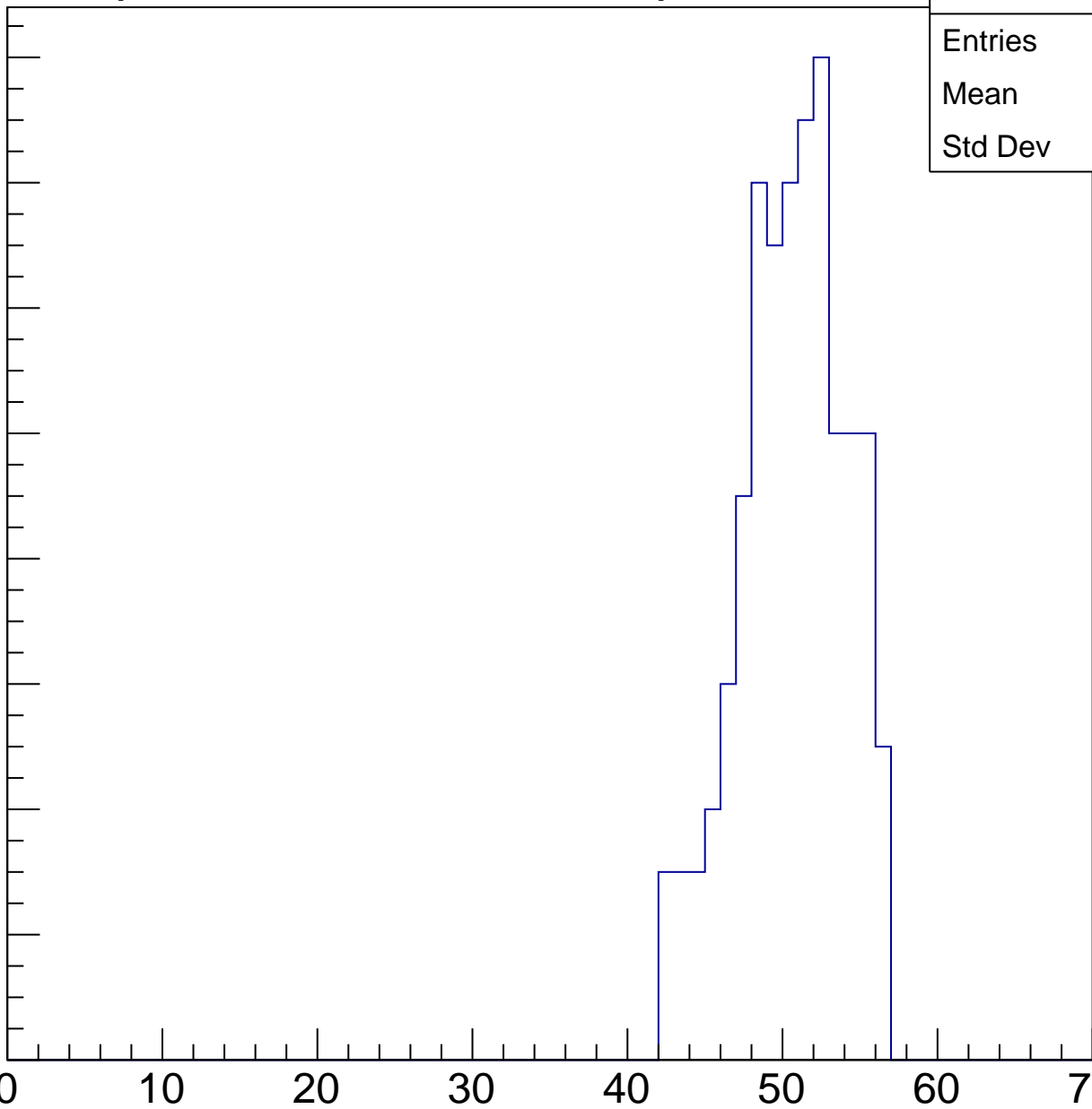
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	50.16
Std Dev	3.387

ampl



B1L001S, U21-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	55.99
Std Dev	6.197

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

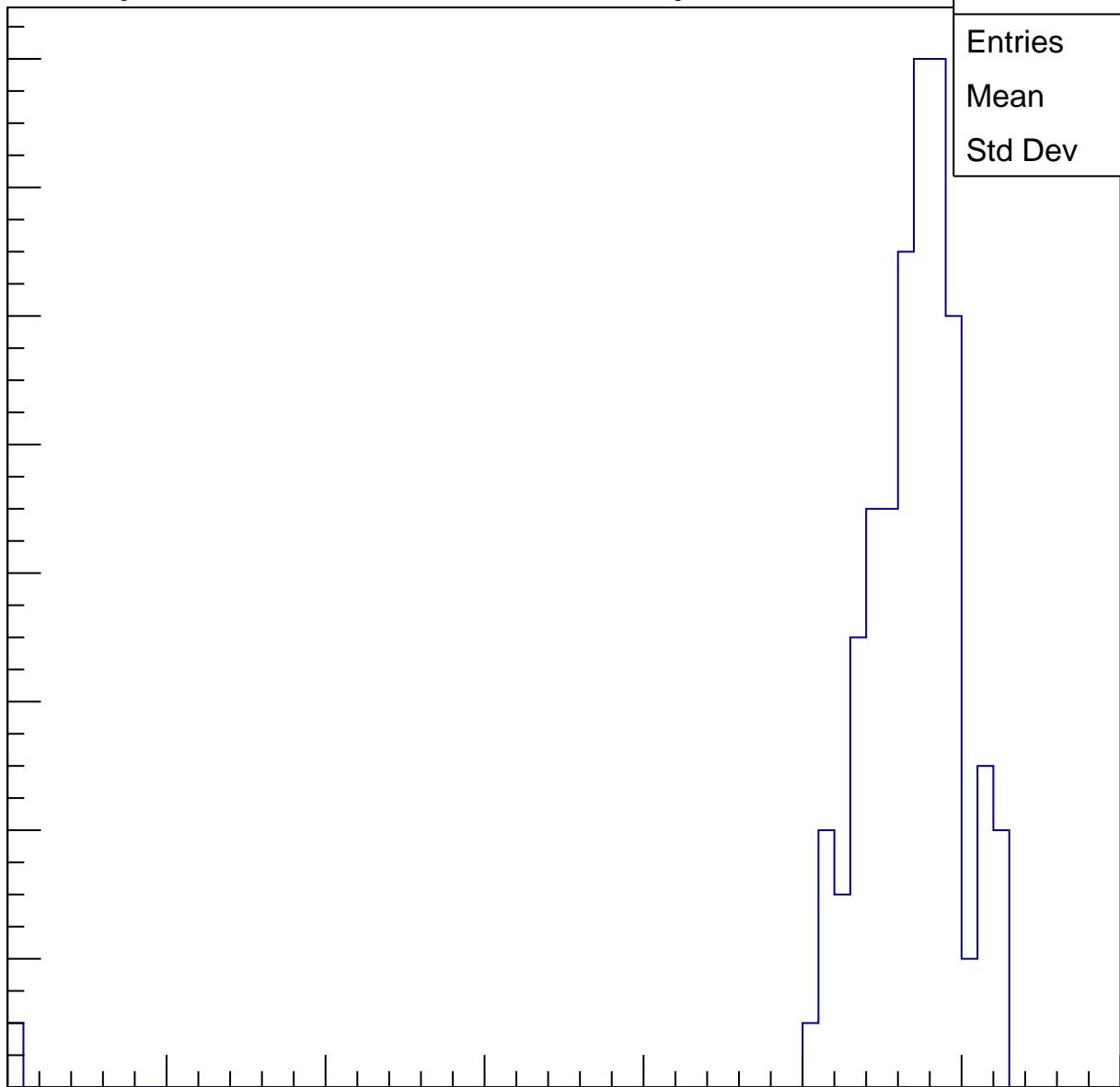
40

50

60

70

ampl



B1L001S, U21-ch47, adc5

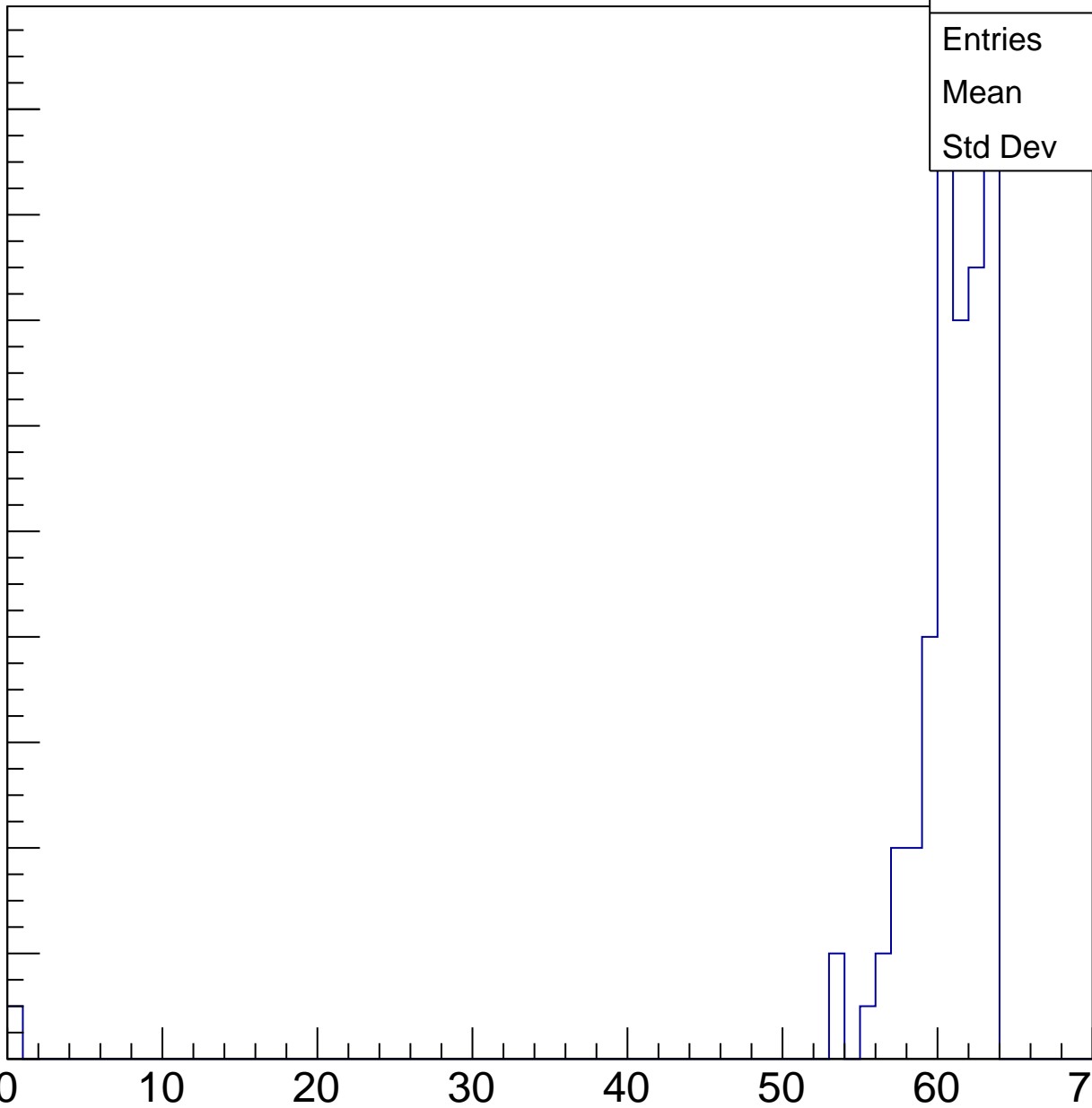
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.84
Std Dev	6.791

ampl



B1L001S, U21-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L001S, U21-ch48, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	28.56
Std Dev	3.323

Gaus mean : 28.9458

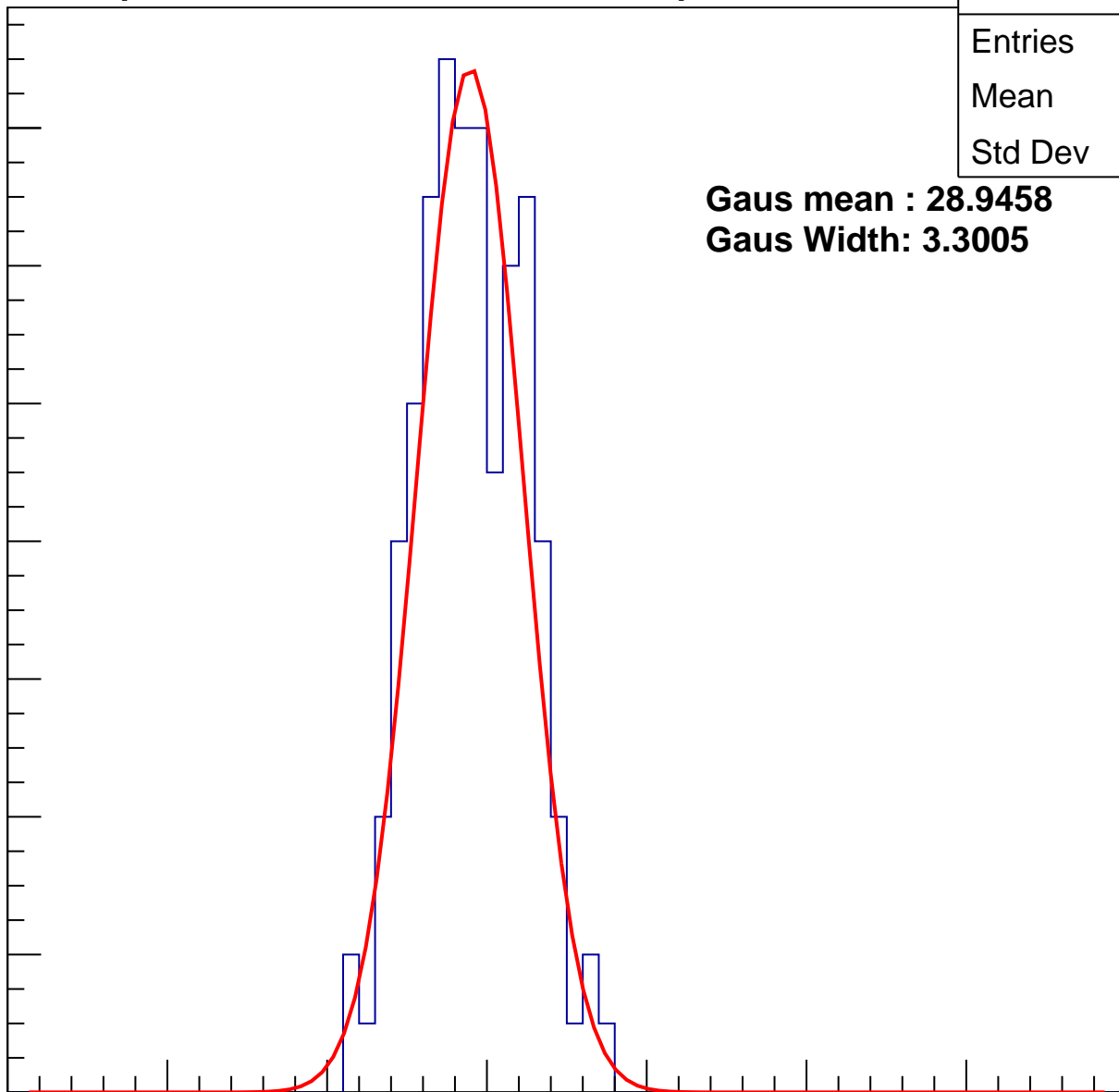
Gaus Width: 3.3005

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	35.77
Std Dev	3.619

Gaus mean : 36.0793

Gaus Width: 3.4356

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

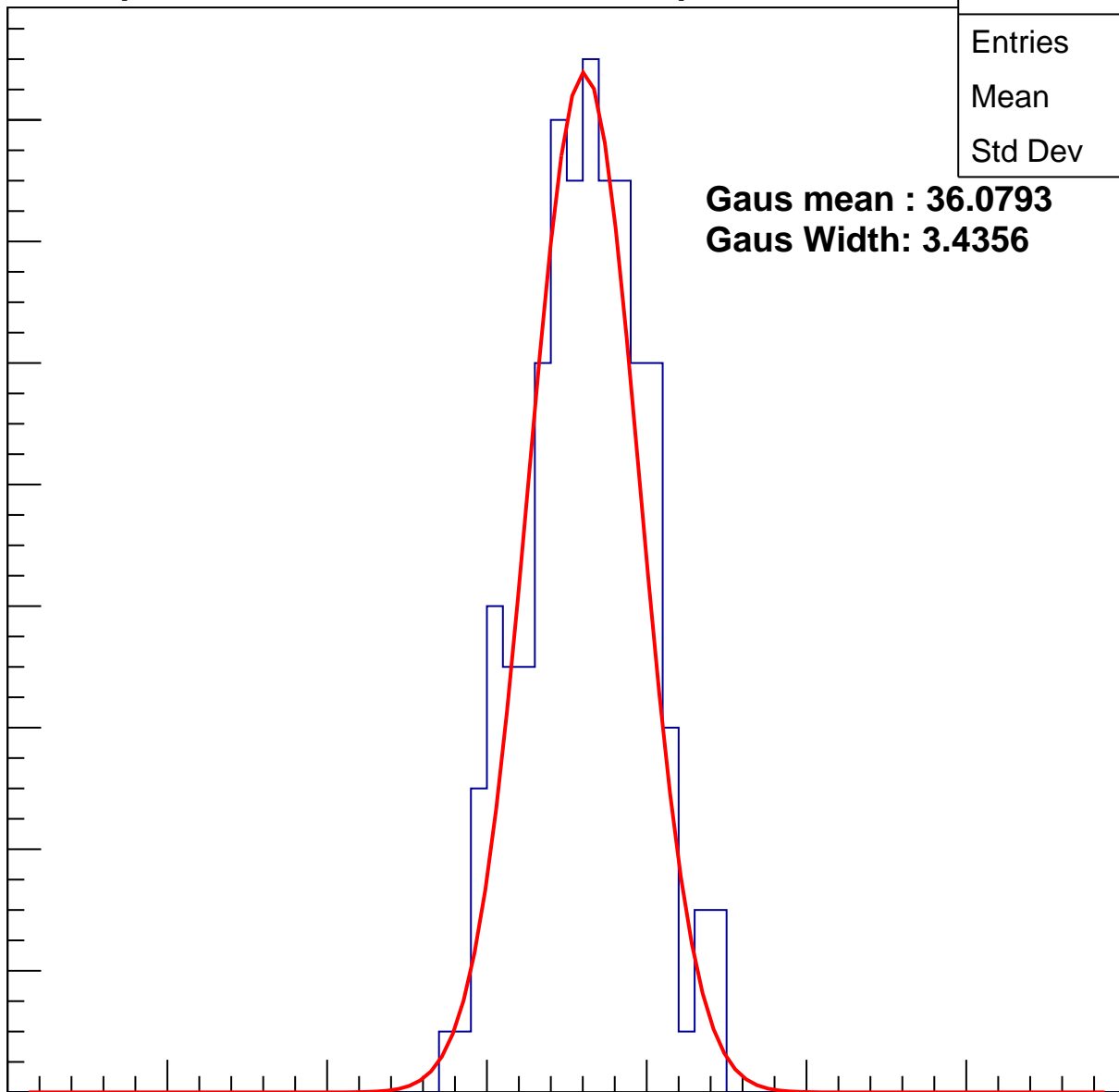
40

50

60

70

ampl



B1L001S, U21-ch48, adc2

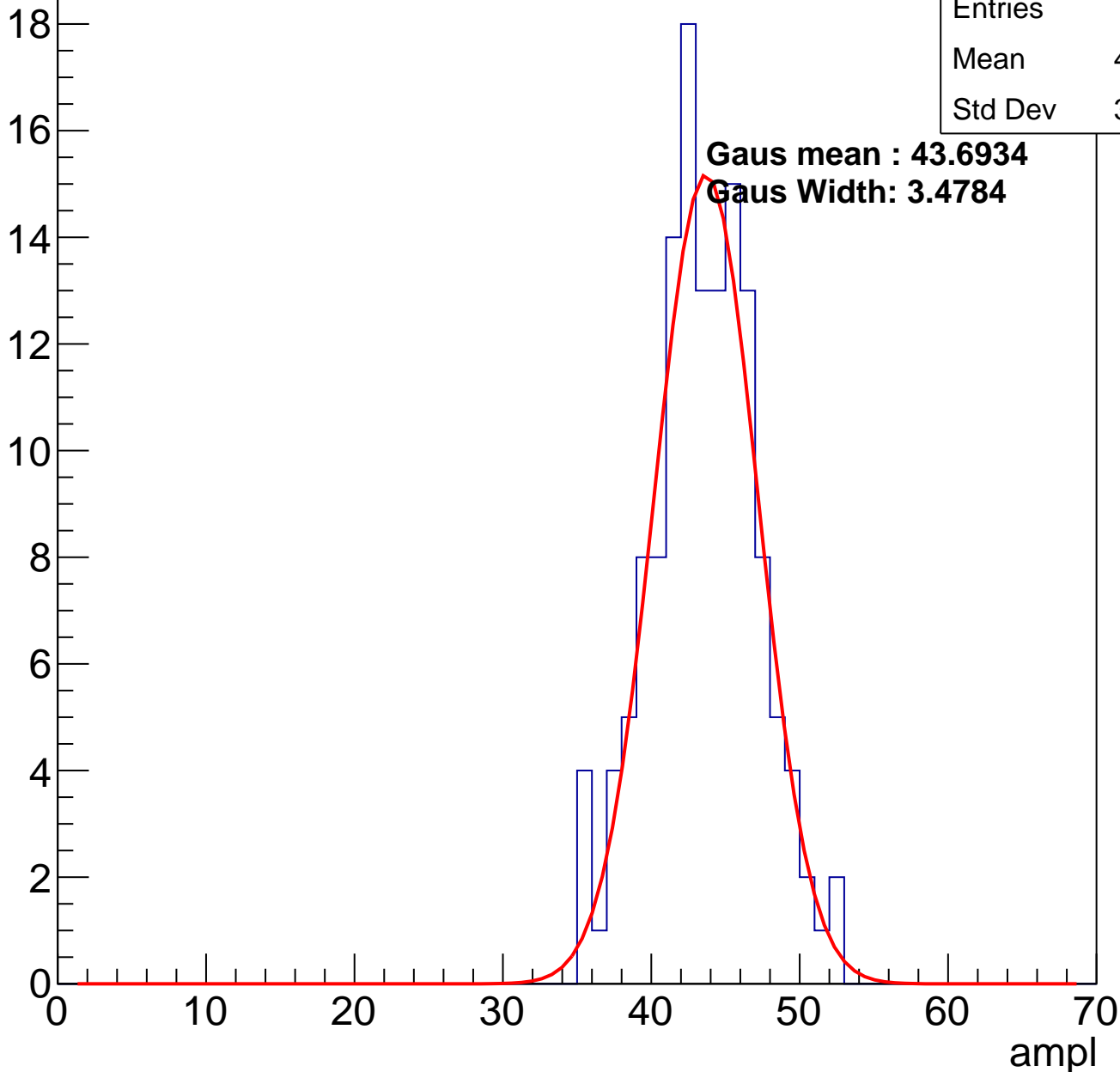
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	43.09
Std Dev	3.569

Gaus mean : 43.6934

Gaus Width: 3.4784

Entry

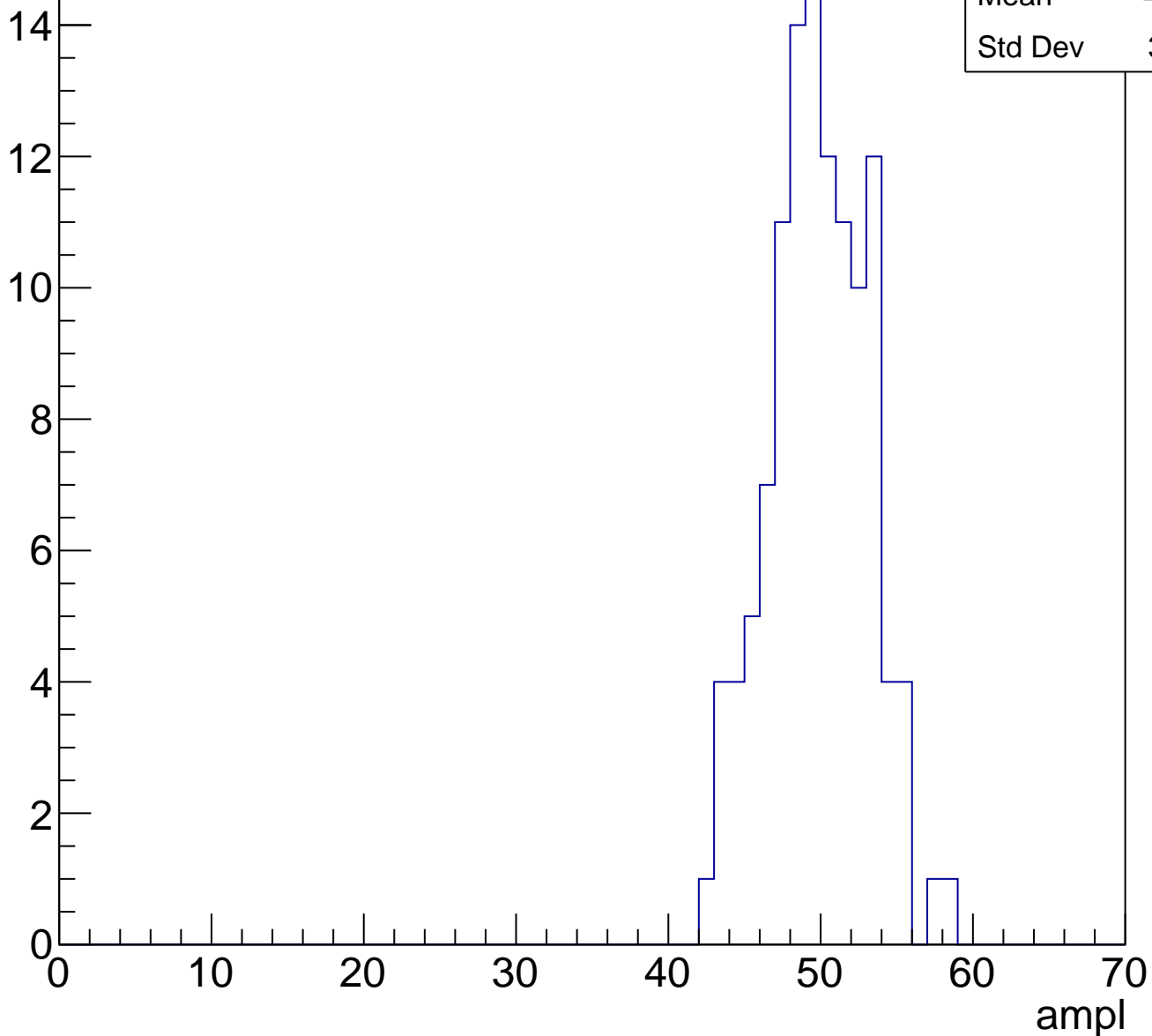


B1L001S, U21-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	49.39
Std Dev	3.221

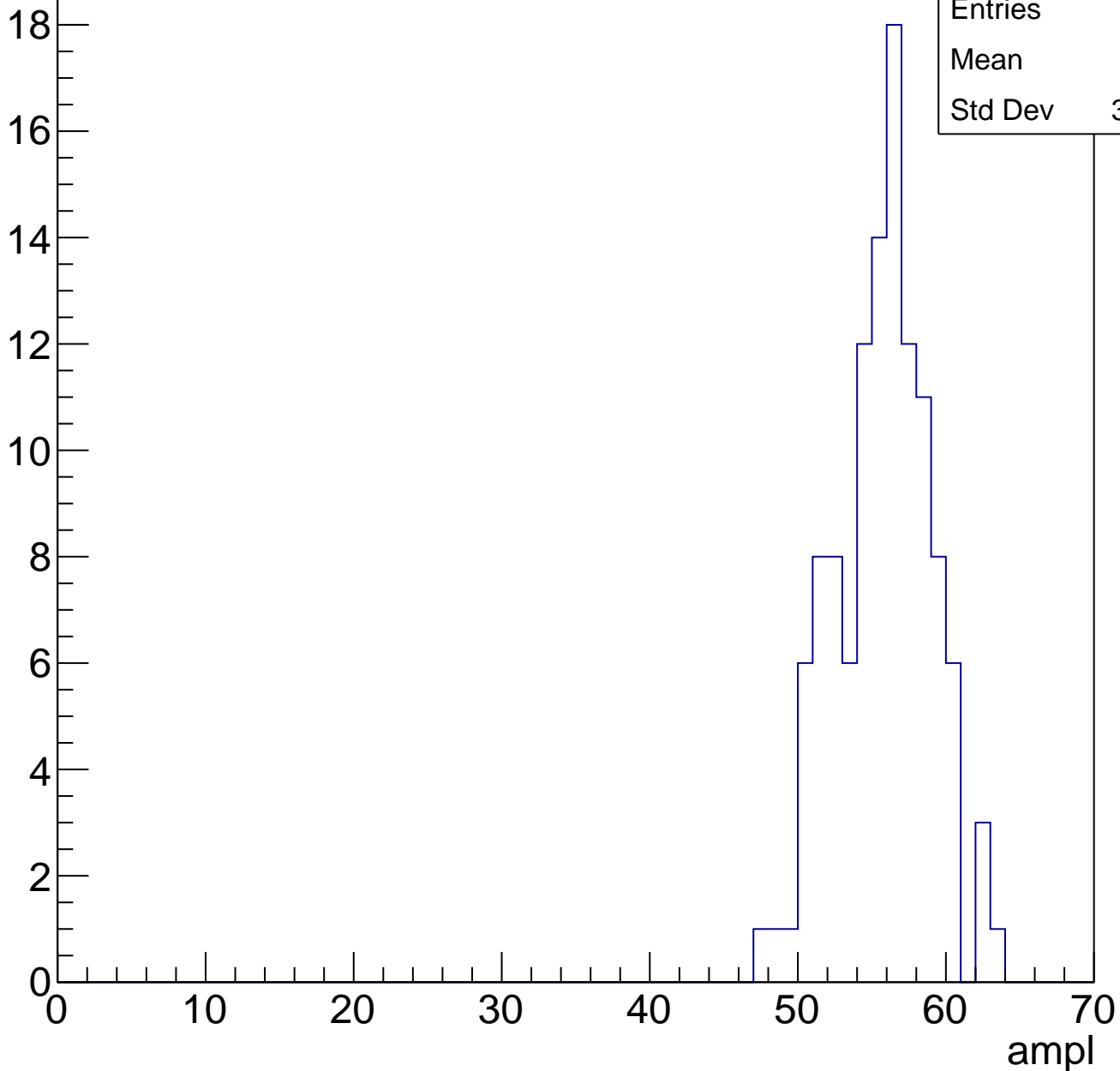
Entry



B1L001S, U21-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

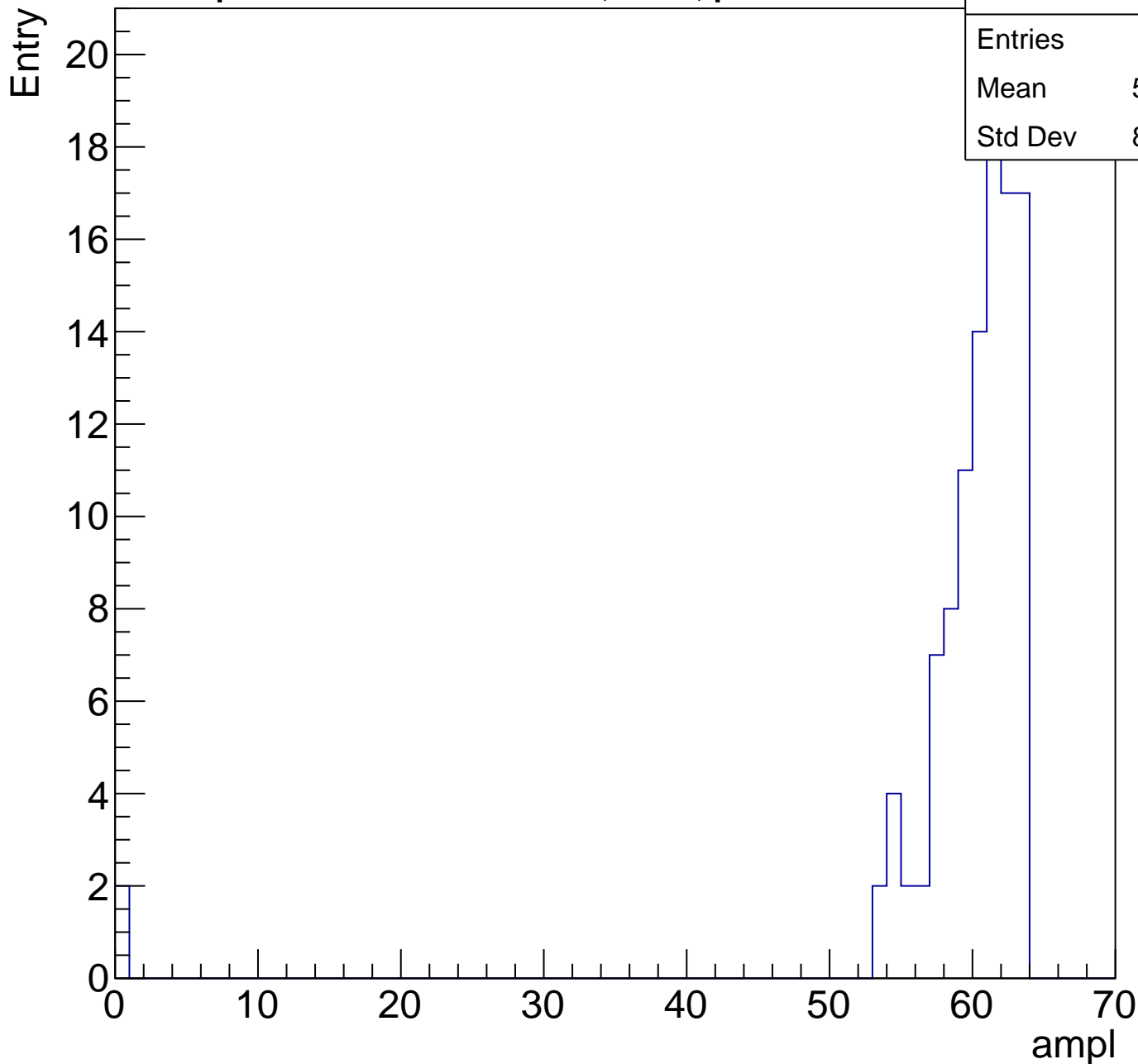
Entry



B1L001S, U21-ch48, adc5

calib_packv5_042523_0143.root, FC#2, port C2

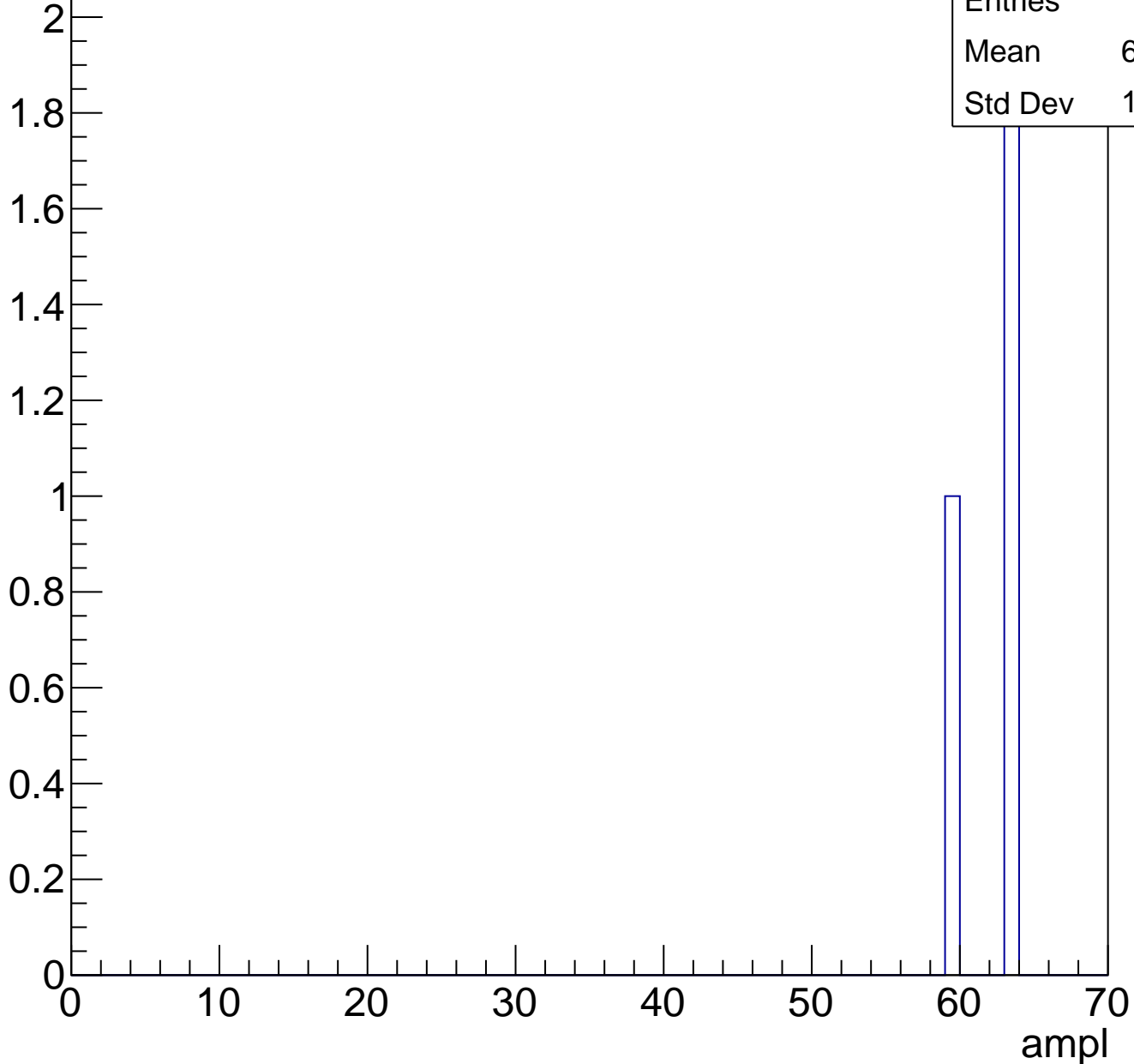
Entries	106
Mean	58.88
Std Dev	8.544



B1L001S, U21-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch49, adc0

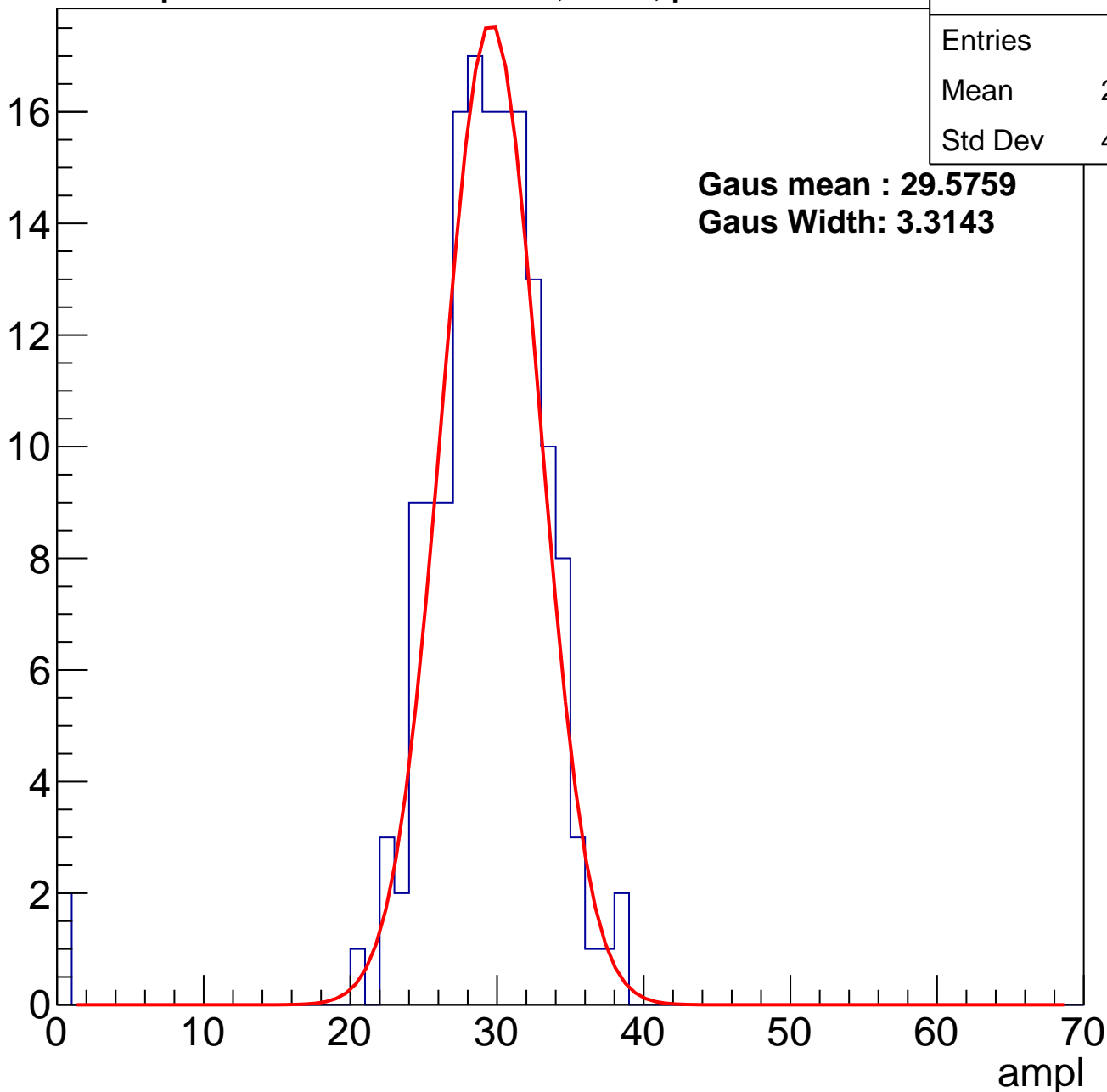
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	28.75
Std Dev	4.728

Gaus mean : 29.5759

Gaus Width: 3.3143

Entry



B1L001S, U21-ch49, adc1

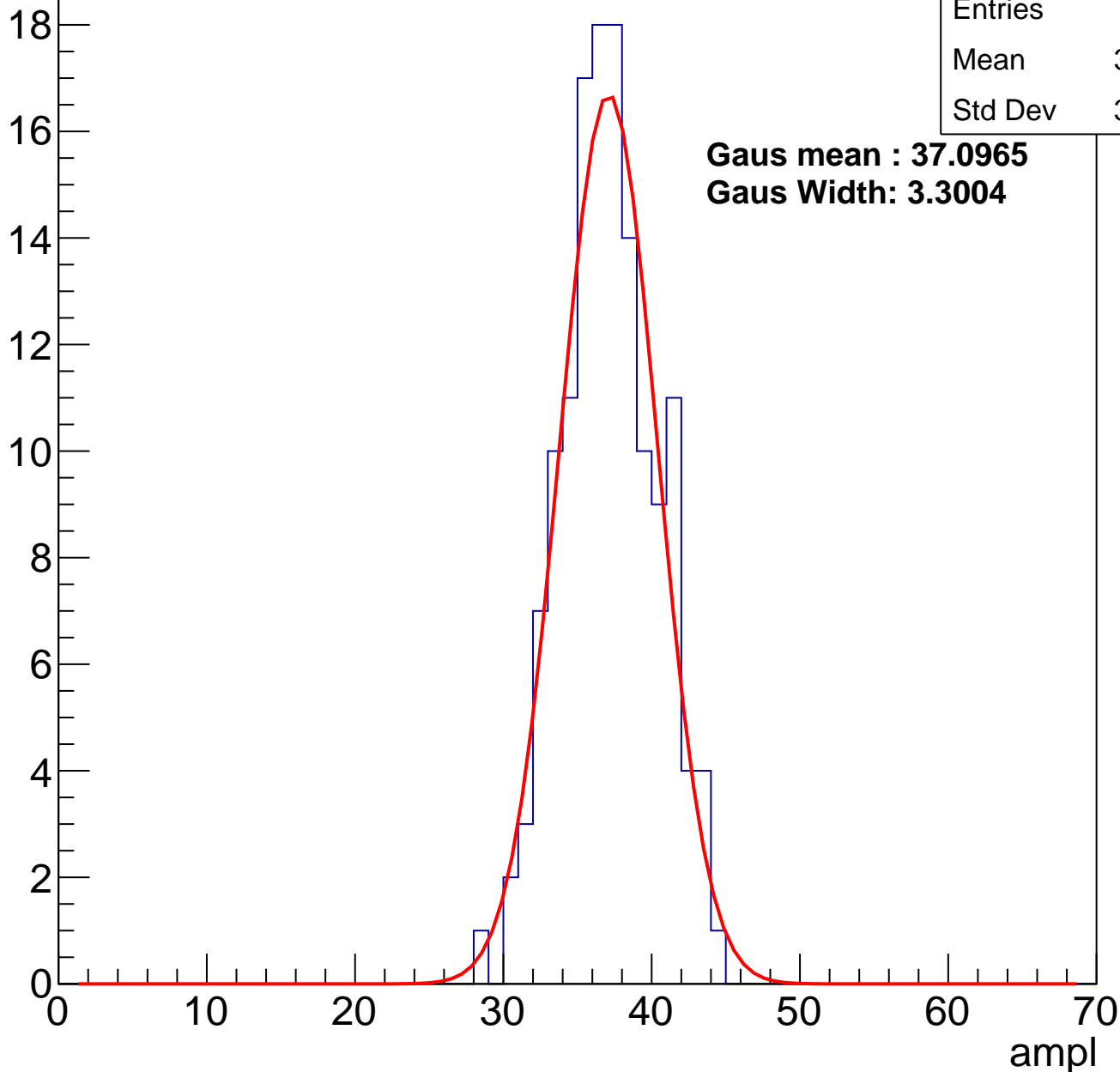
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.68
Std Dev	3.158

Gaus mean : 37.0965

Gaus Width: 3.3004

Entry



B1L001S, U21-ch49, adc2

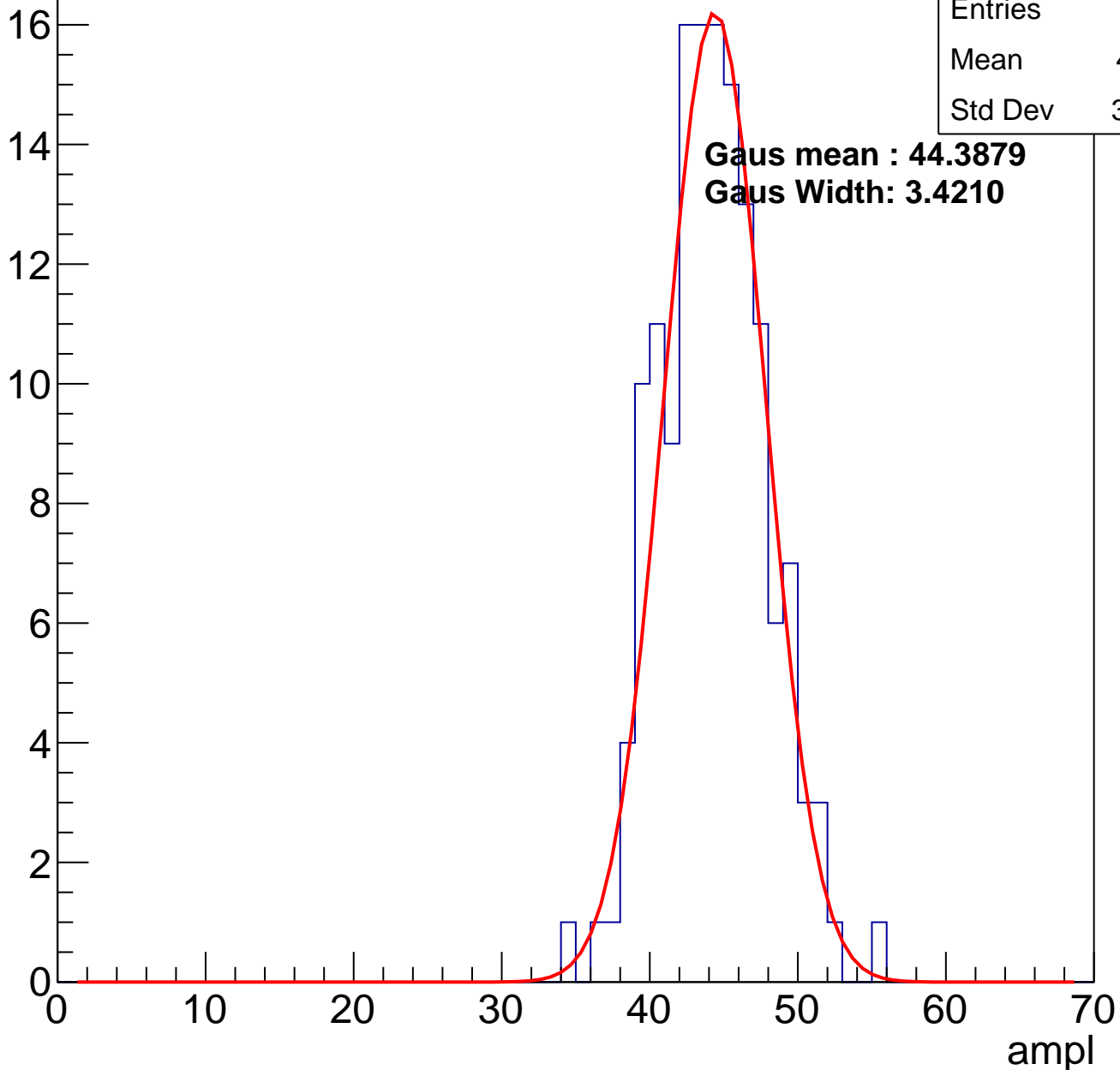
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	43.81
Std Dev	3.539

Gaus mean : 44.3879

Gaus Width: 3.4210

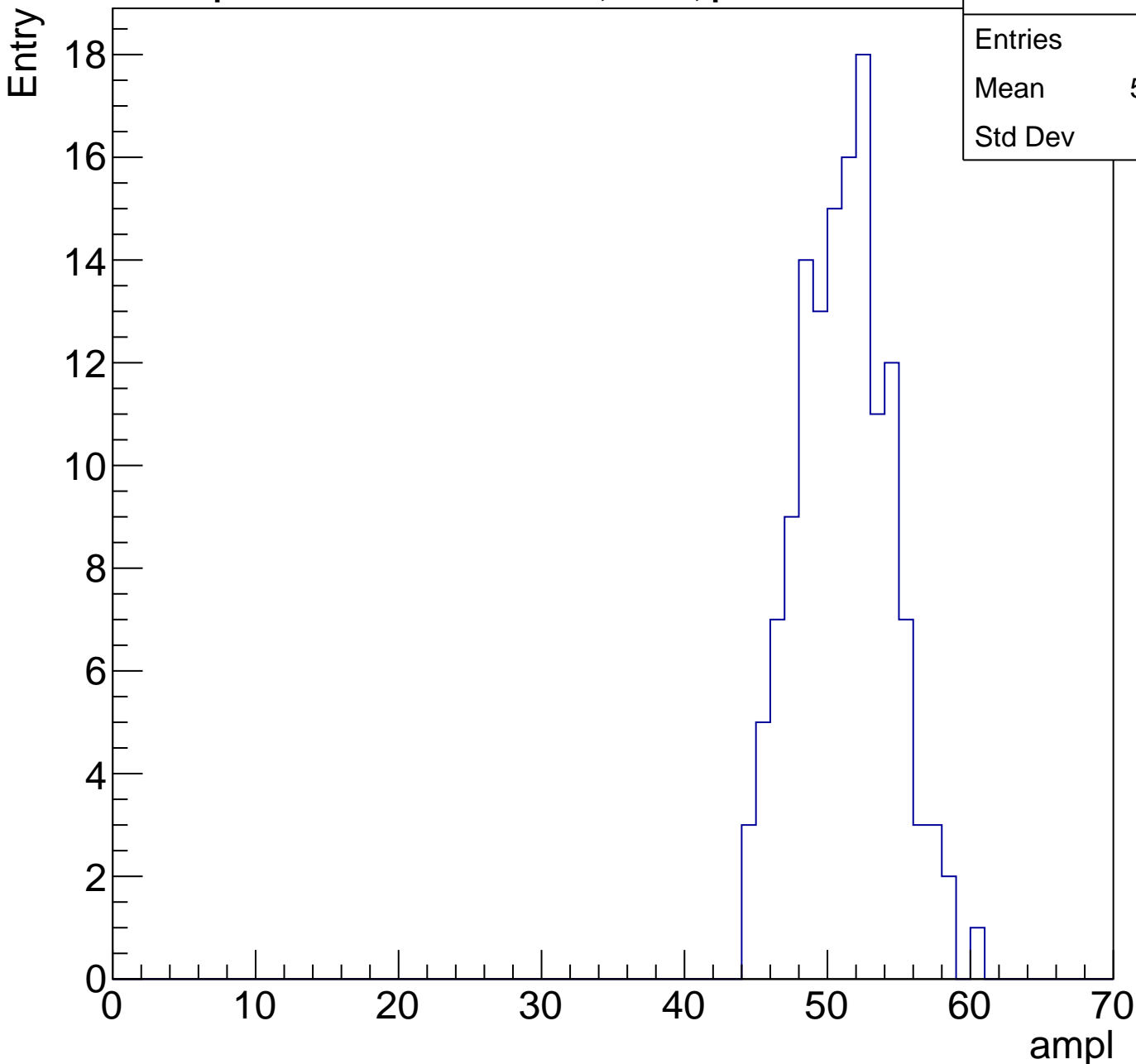
Entry



B1L001S, U21-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.68
Std Dev	3.25

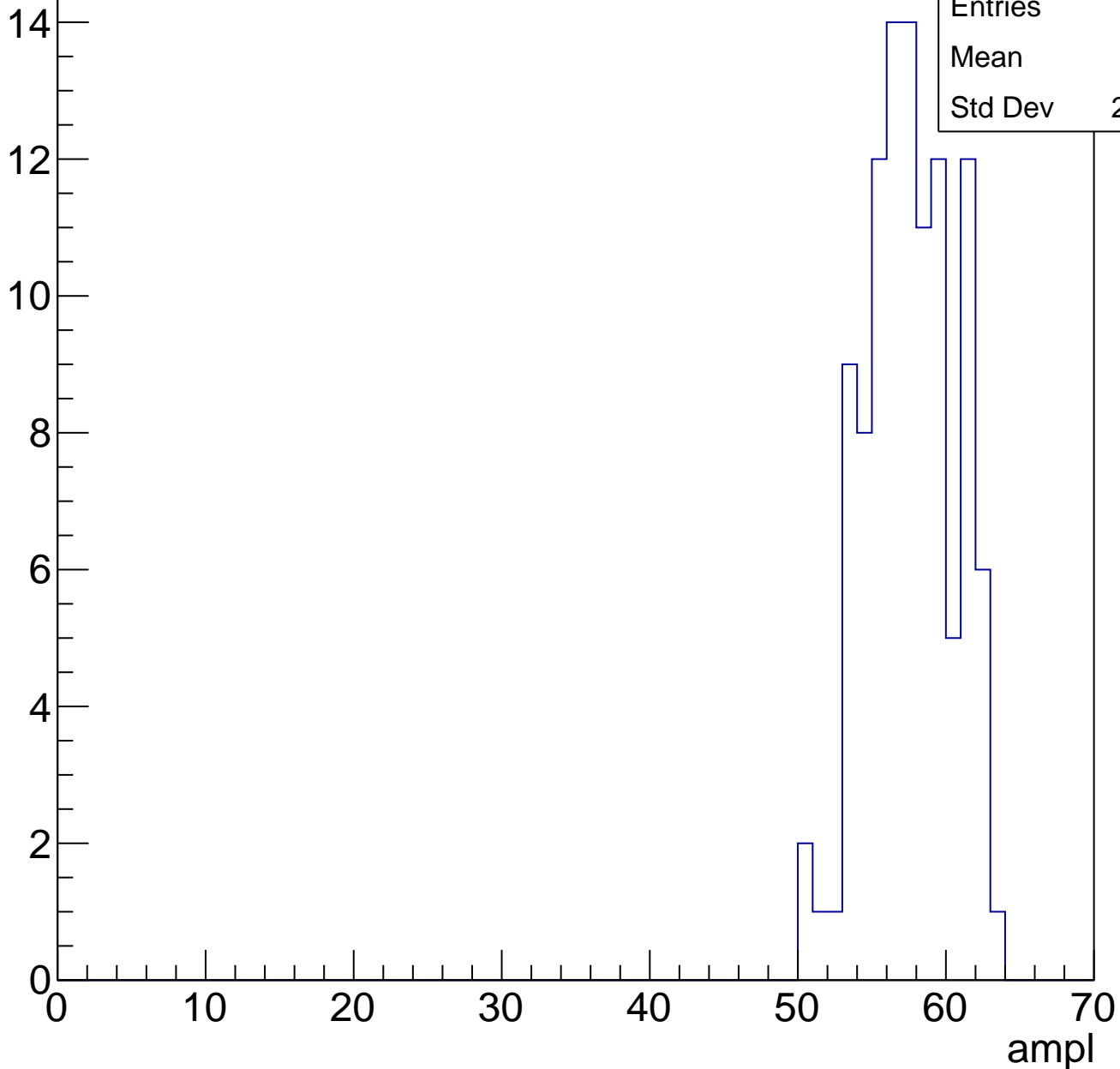


B1L001S, U21-ch49, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	57.1
Std Dev	2.915

Entry



B1L001S, U21-ch49, adc5

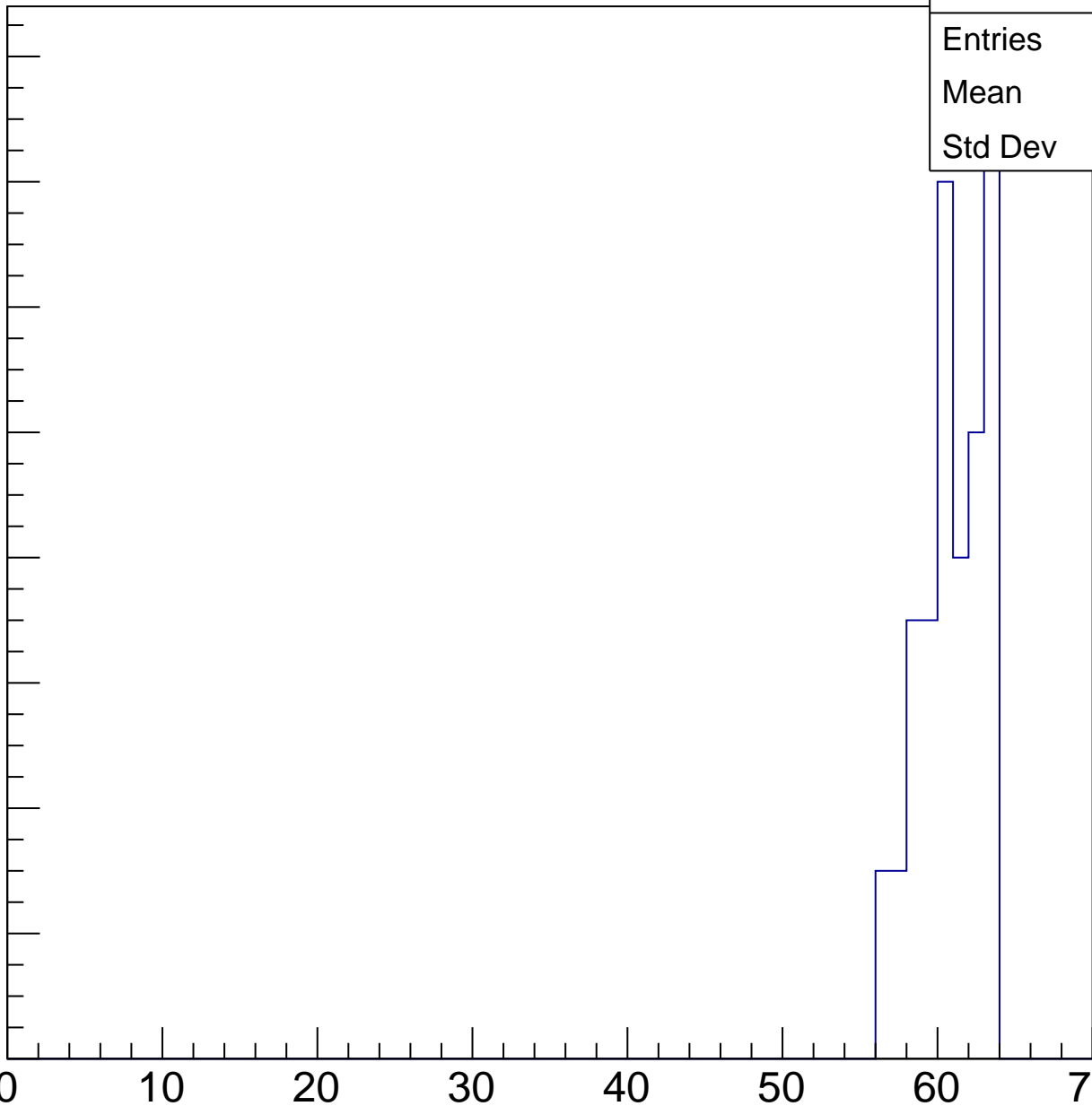
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	60.5
Std Dev	2.047

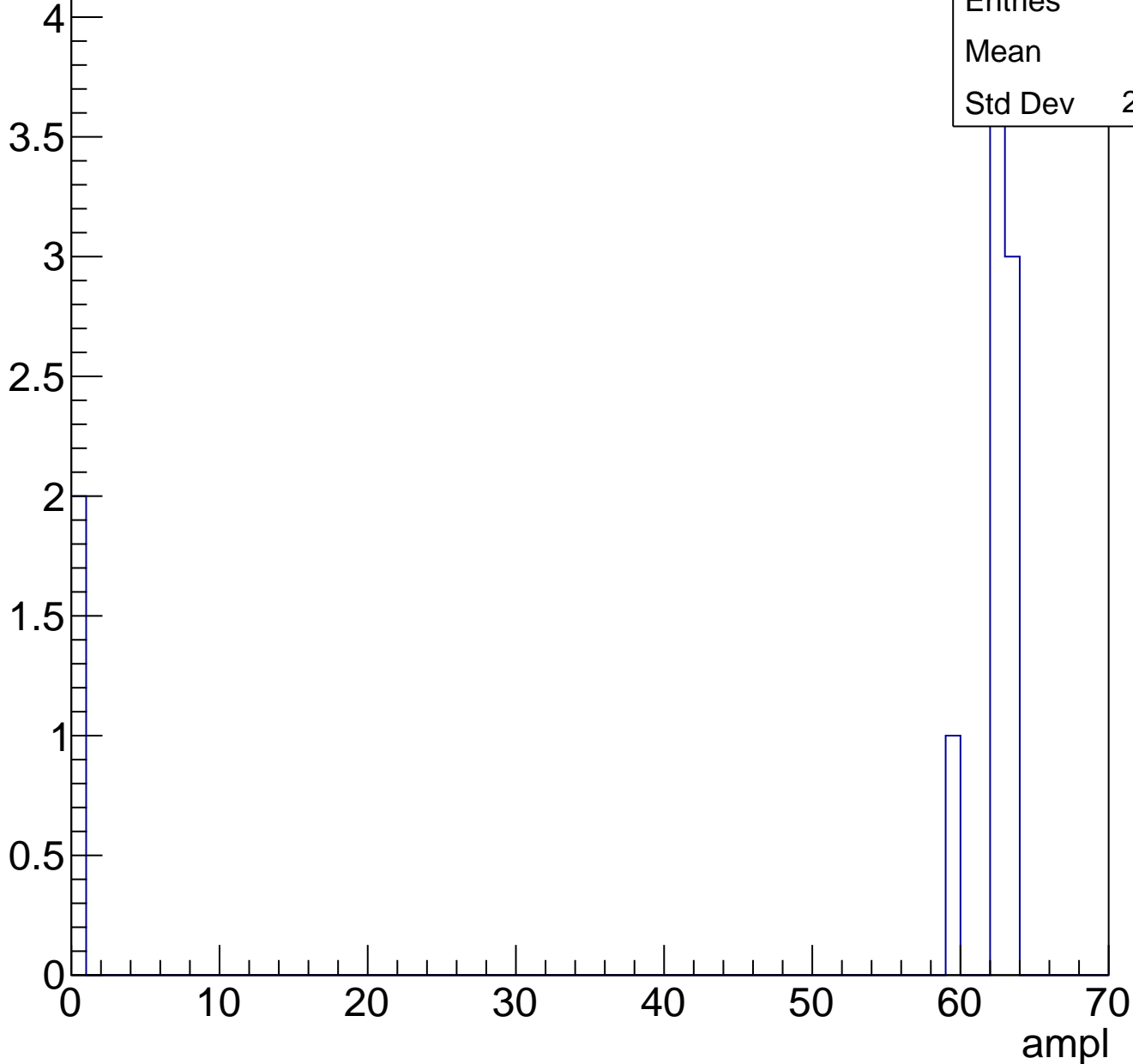
ampl



B1L001S, U21-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U21-ch50, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	29.69
Std Dev	2.549

Gaus mean : 29.7281

Gaus Width: 2.5031

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

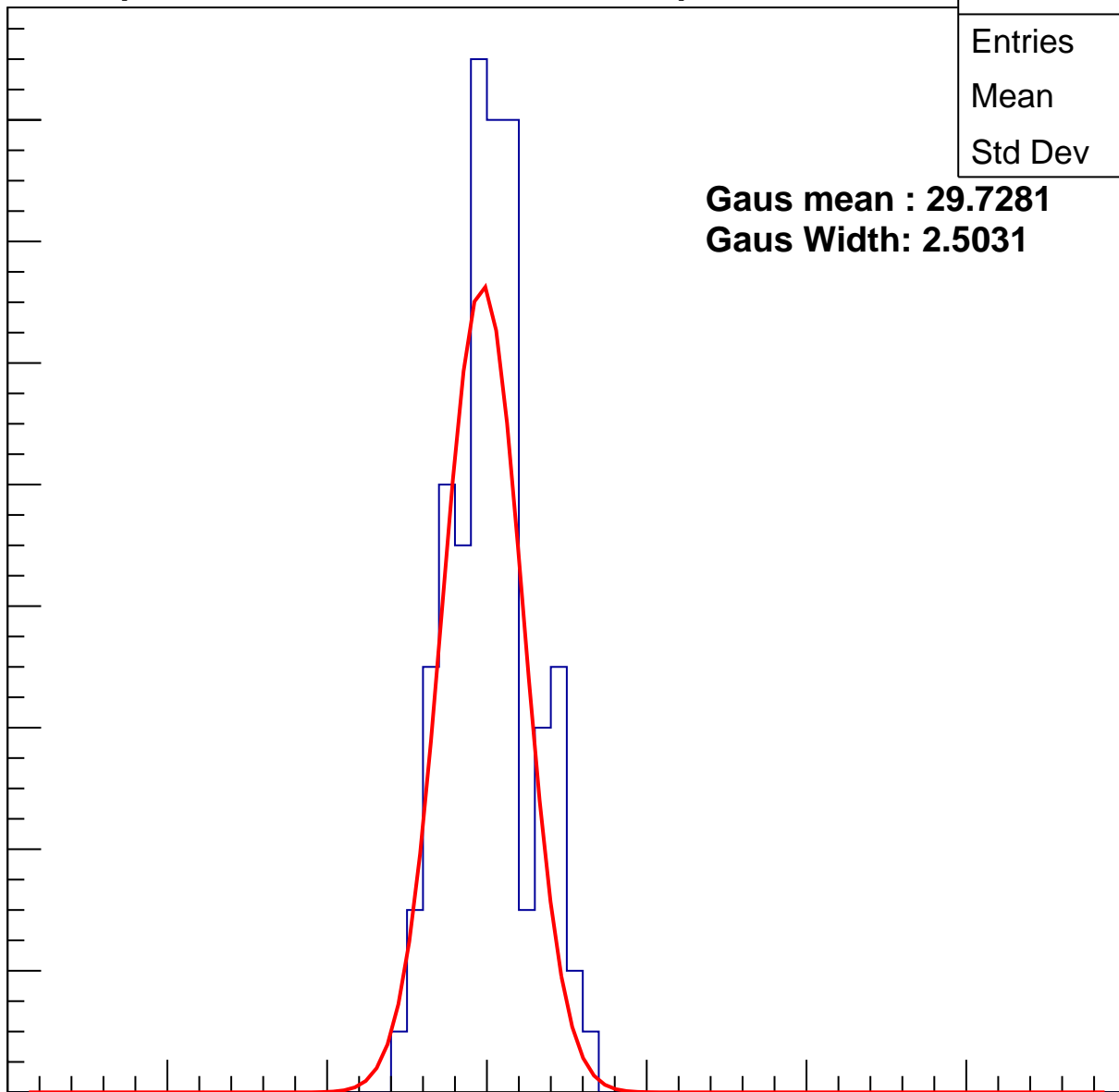
40

50

60

70

ampl



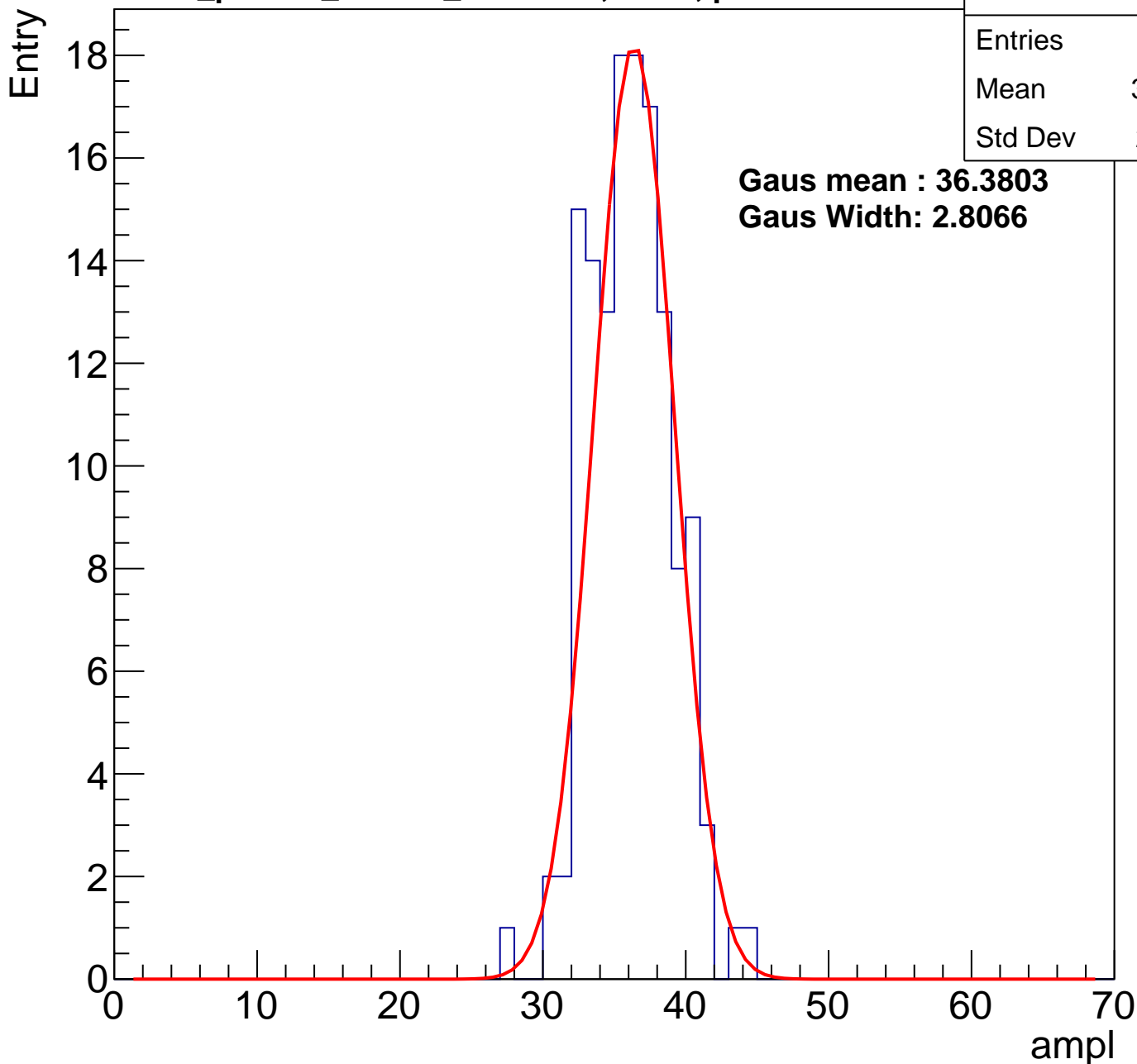
B1L001S, U21-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	35.67
Std Dev	2.851

Gaus mean : 36.3803

Gaus Width: 2.8066



B1L001S, U21-ch50, adc2

calib_packv5_042523_0143.root, FC#2, port C2

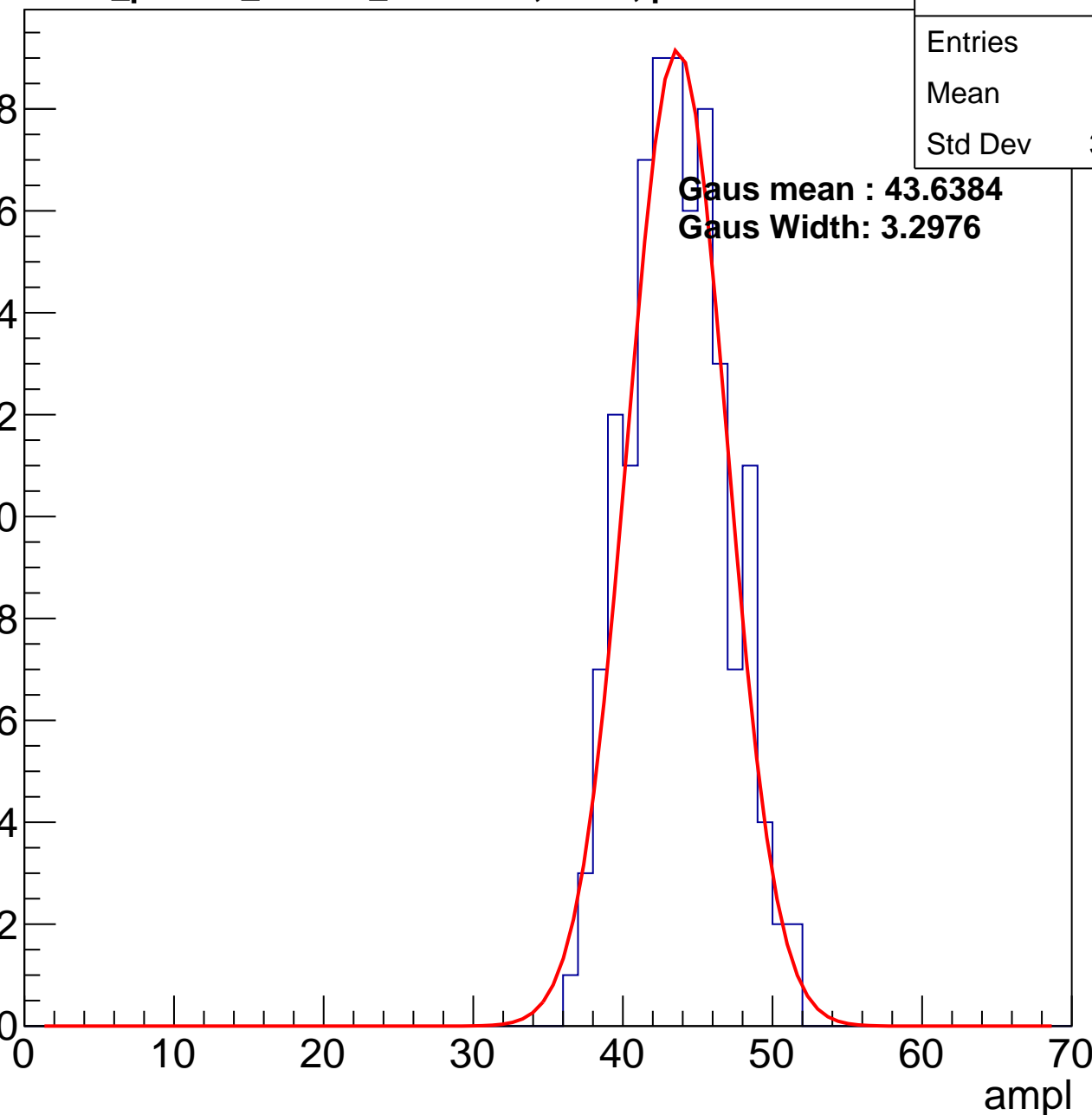
Entry

18
16
14
12
10
8
6
4
2
0

Entries	162
Mean	43.21
Std Dev	3.206

Gaus mean : 43.6384

Gaus Width: 3.2976

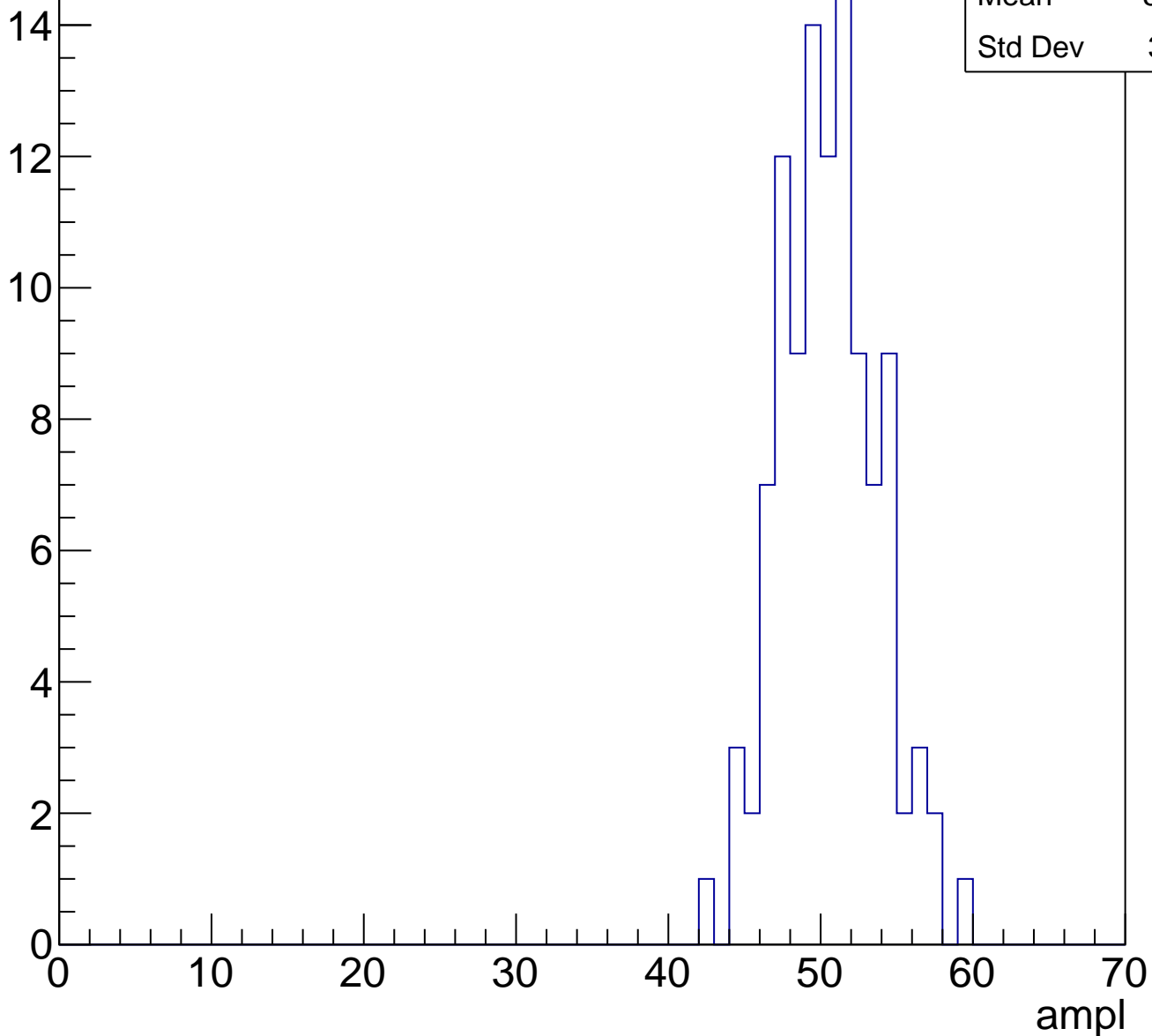


B1L001S, U21-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	50.08
Std Dev	3.171

Entry



B1L001S, U21-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	55.67
Std Dev	3.131

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

B1L001S, U21-ch50, adc5

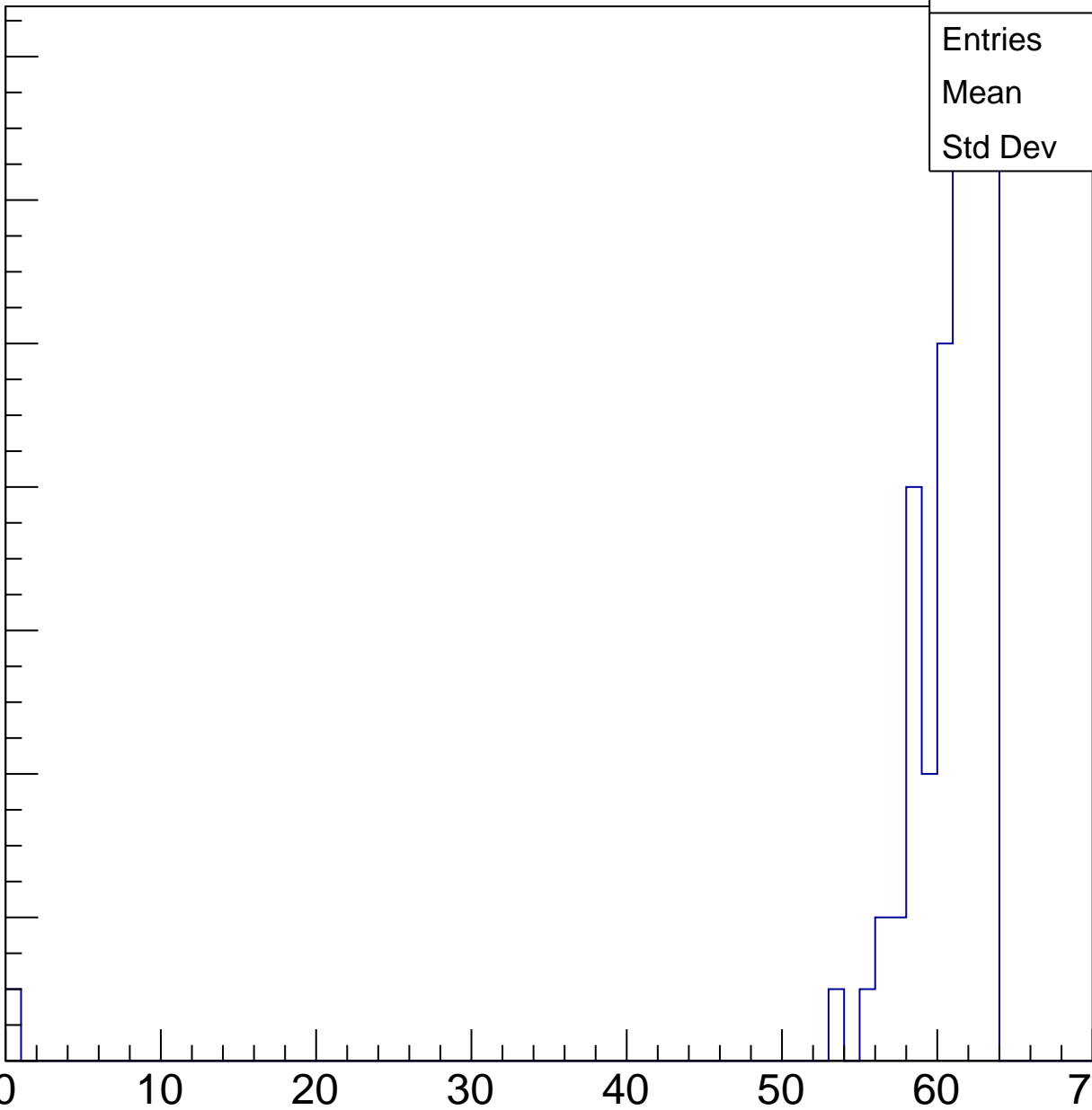
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.64
Std Dev	7.501

ampl

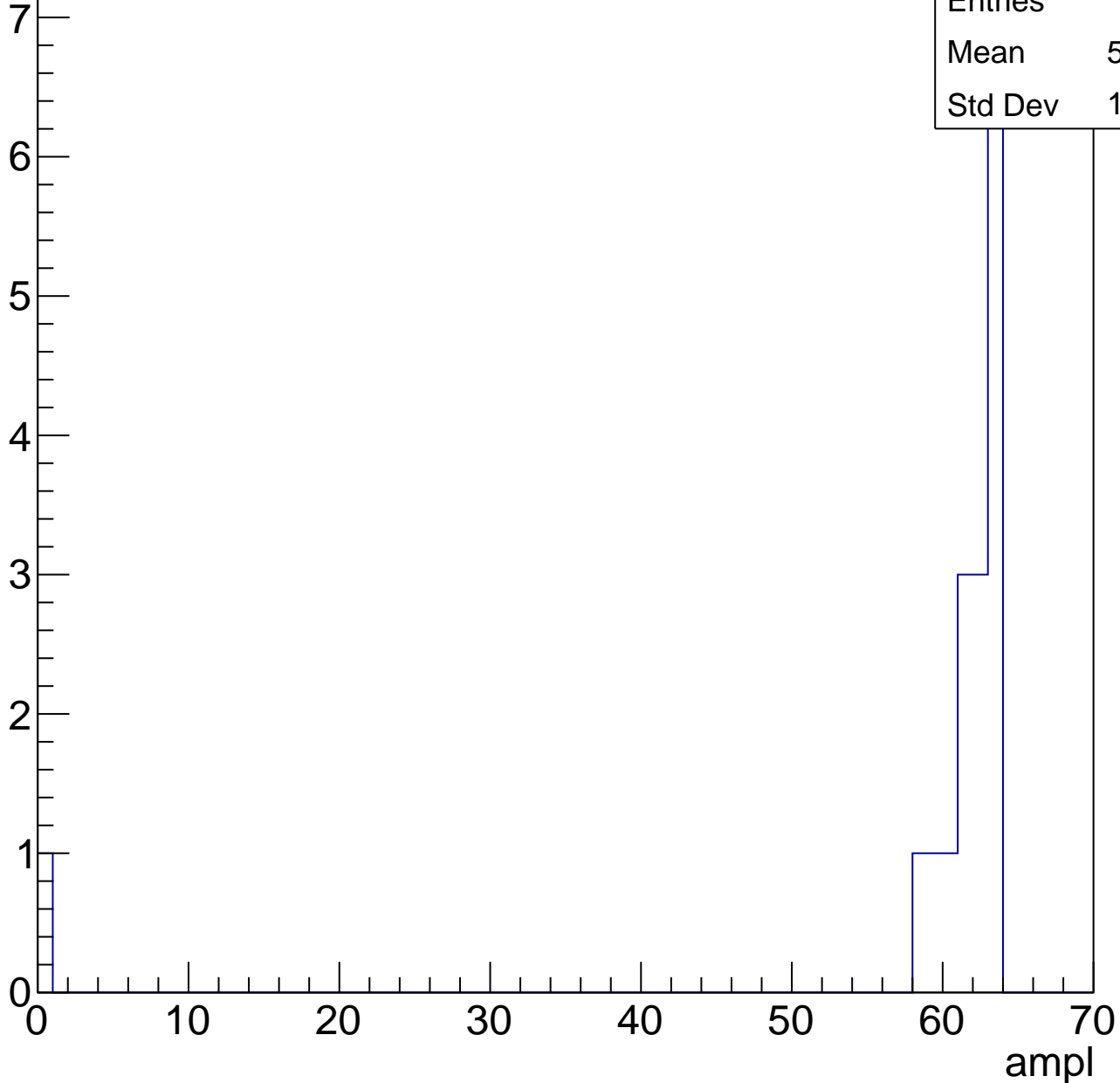


B1L001S, U21-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.06
Std Dev	14.59



B1L001S, U21-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U21-ch51, adc0

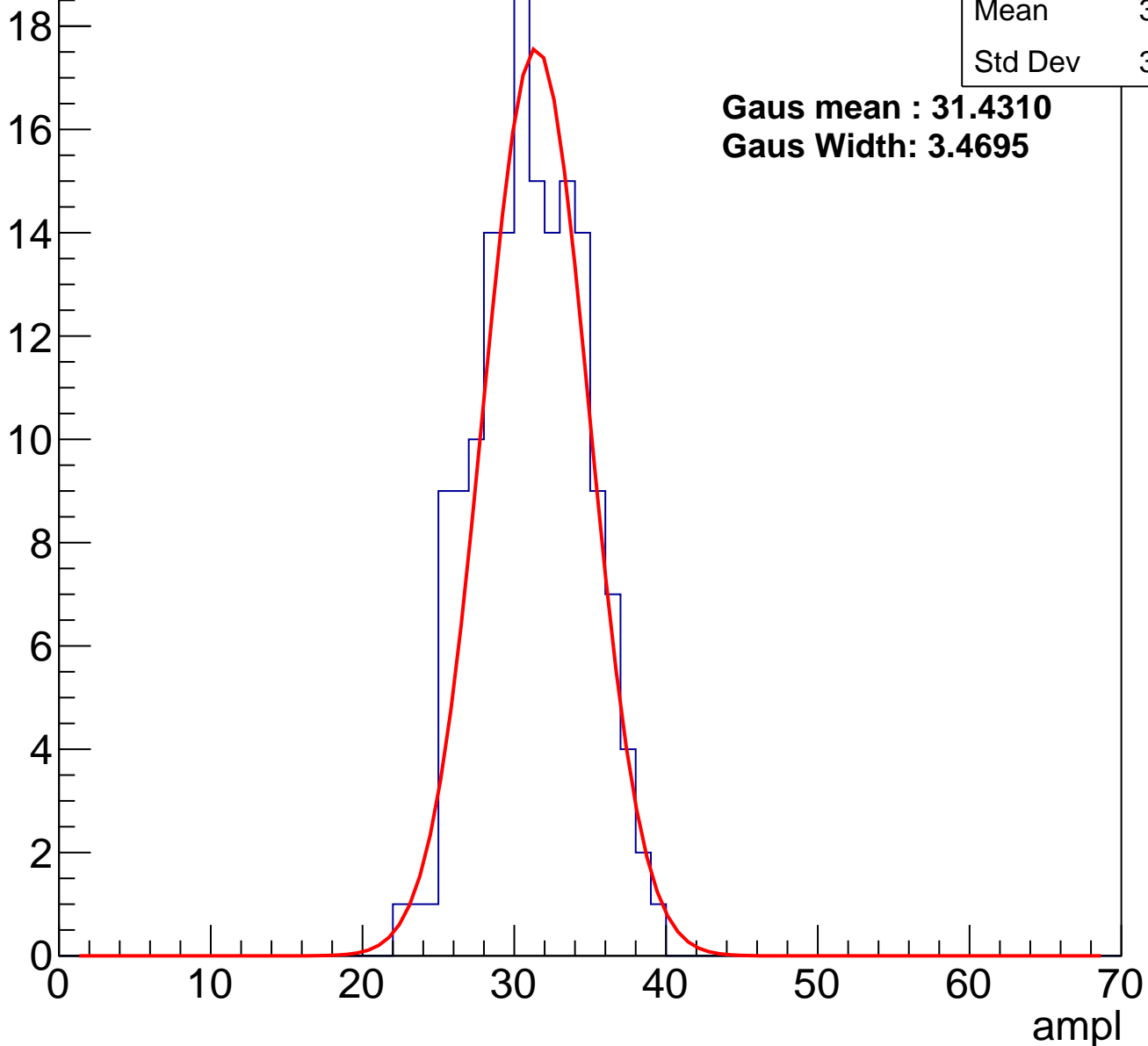
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	30.69
Std Dev	3.469

Gaus mean : 31.4310

Gaus Width: 3.4695

Entry



B1L001S, U21-ch51, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	37.35
Std Dev	3.465

Gaus mean : 37.8270

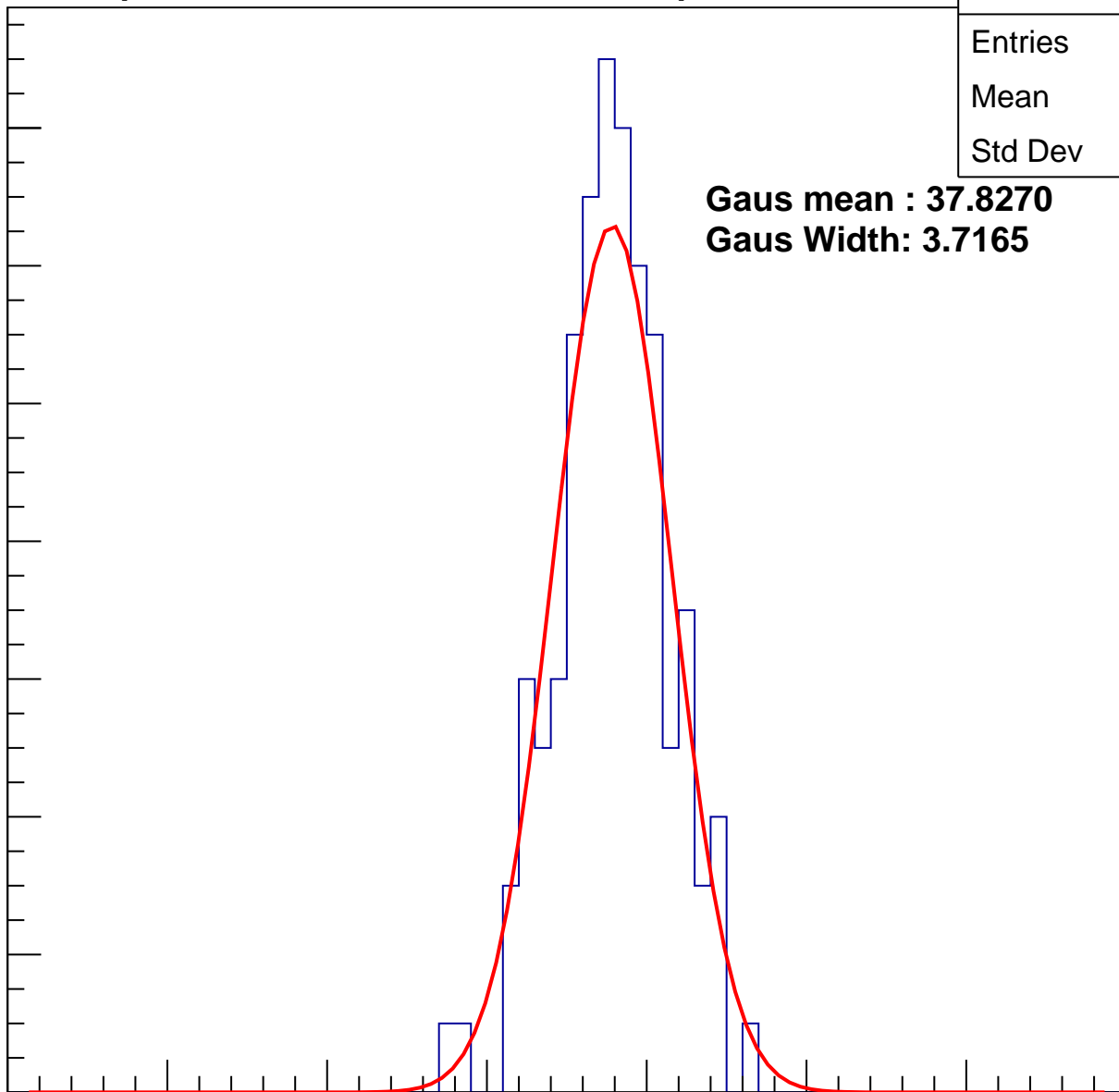
Gaus Width: 3.7165

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch51, adc2

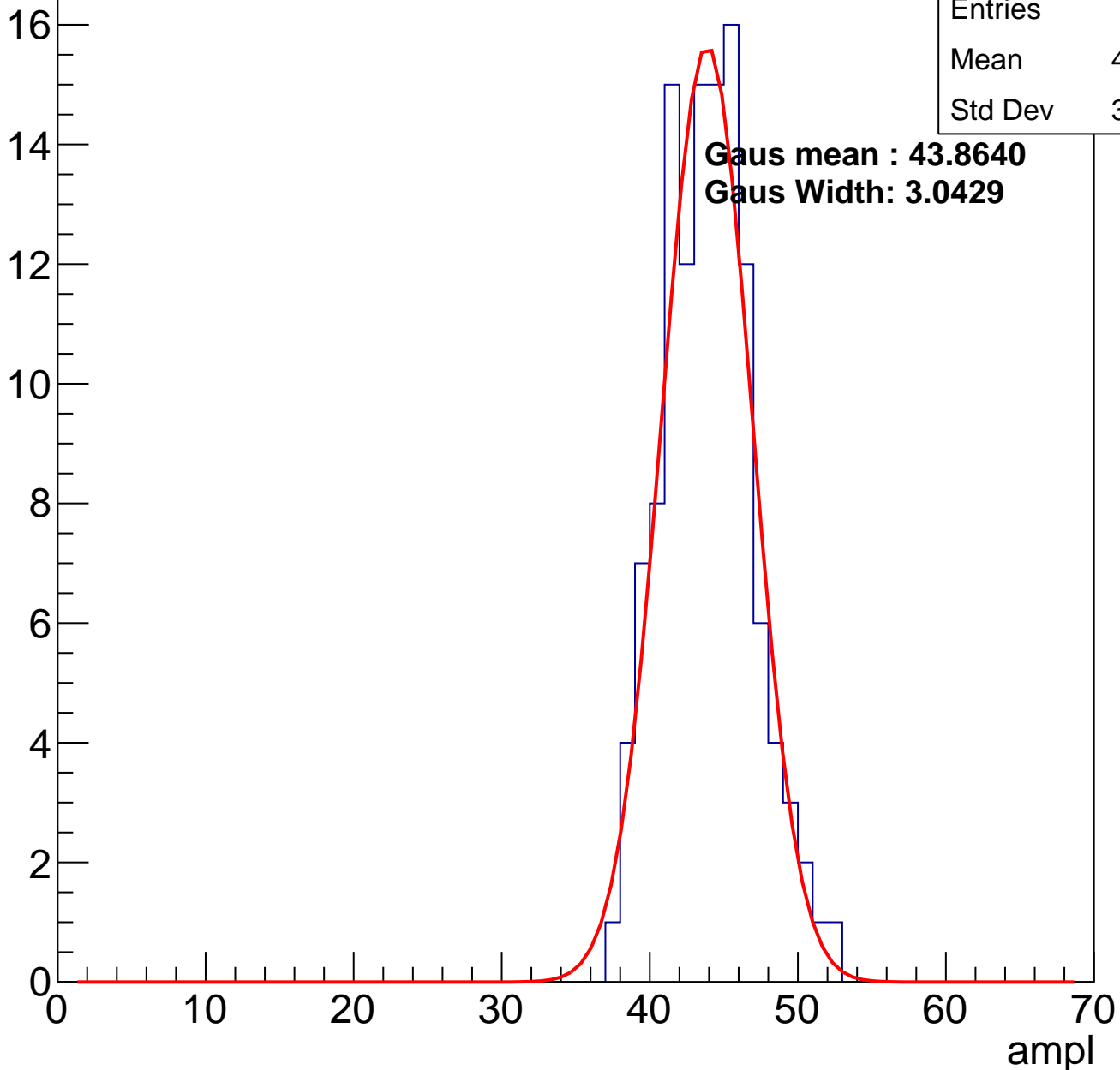
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	43.46
Std Dev	3.003

Gaus mean : 43.8640

Gaus Width: 3.0429

Entry



B1L001S, U21-ch51, adc3

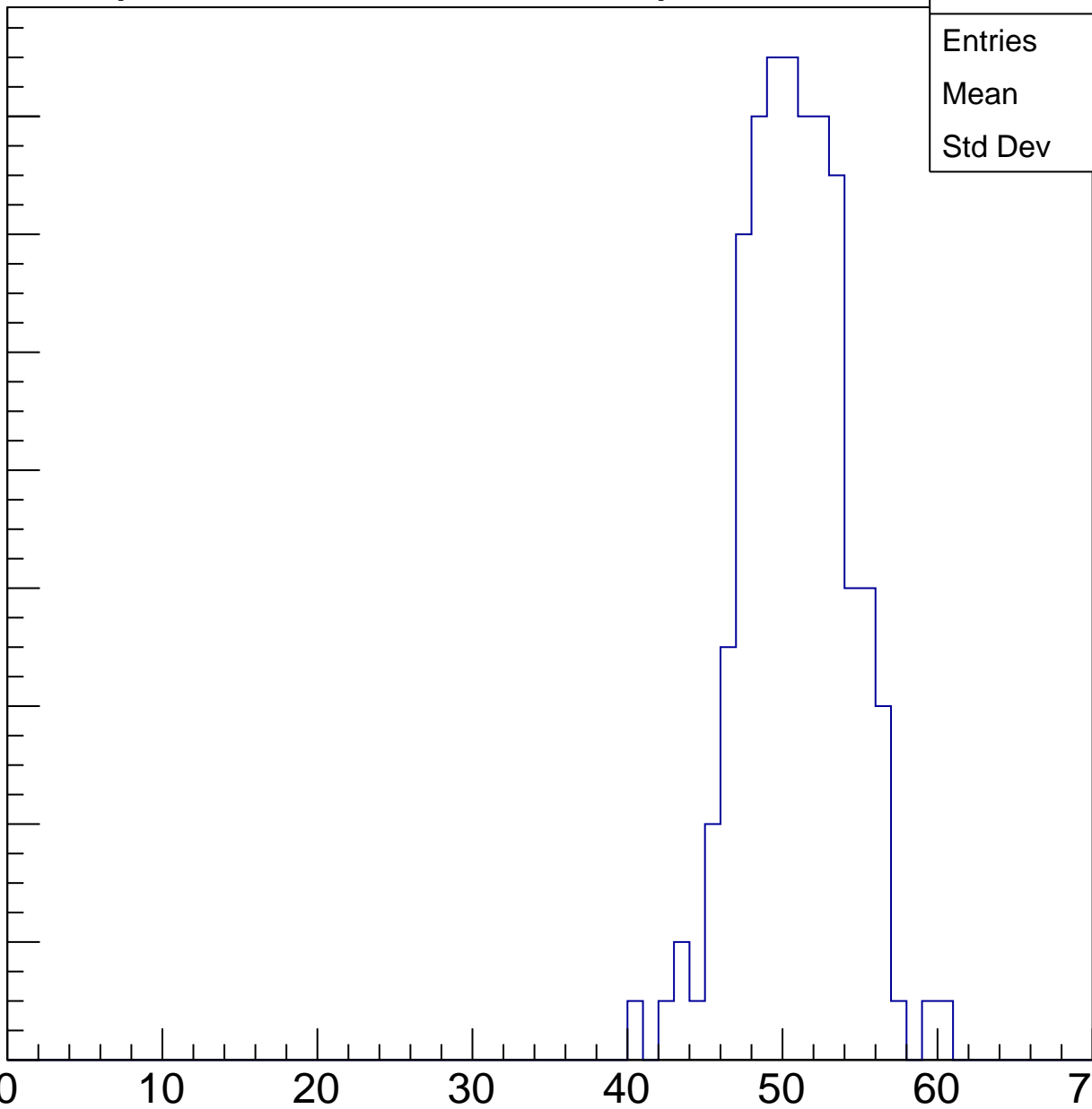
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	152
Mean	50.33
Std Dev	3.334

ampl



B1L001S, U21-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

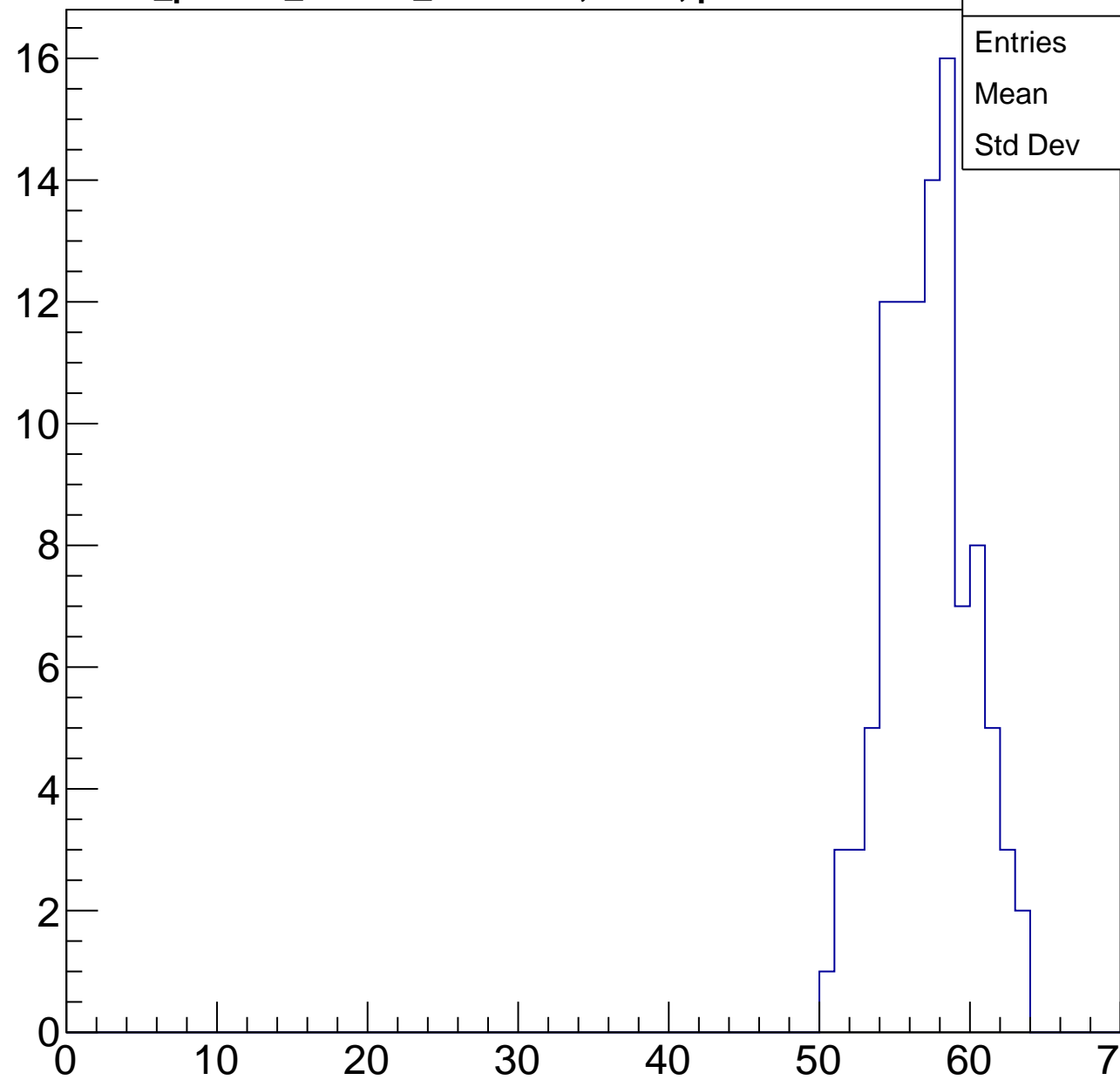
Entries	103
Mean	56.7
Std Dev	2.811

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch51, adc5

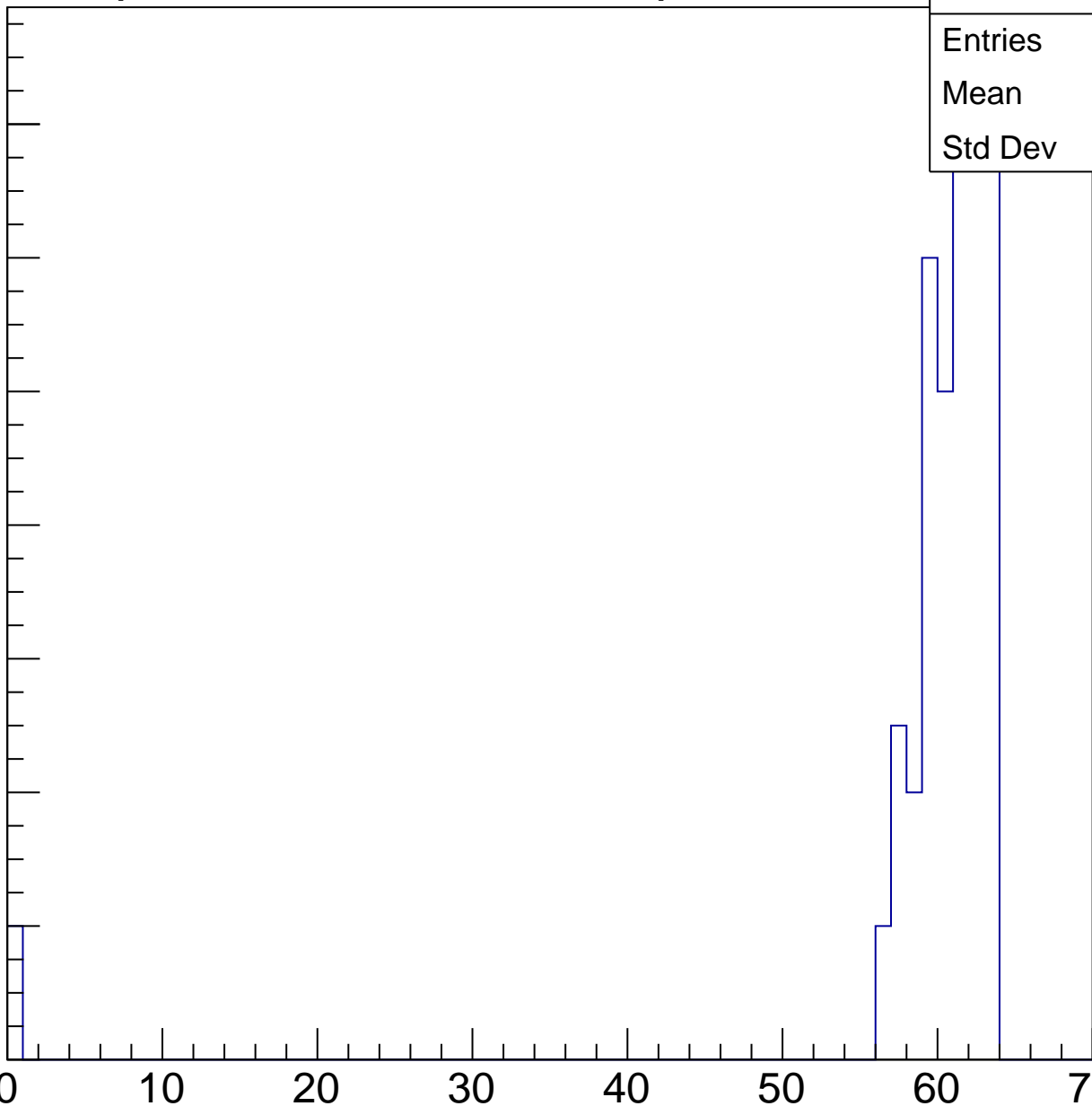
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.03
Std Dev	9.764

ampl



B1L001S, U21-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

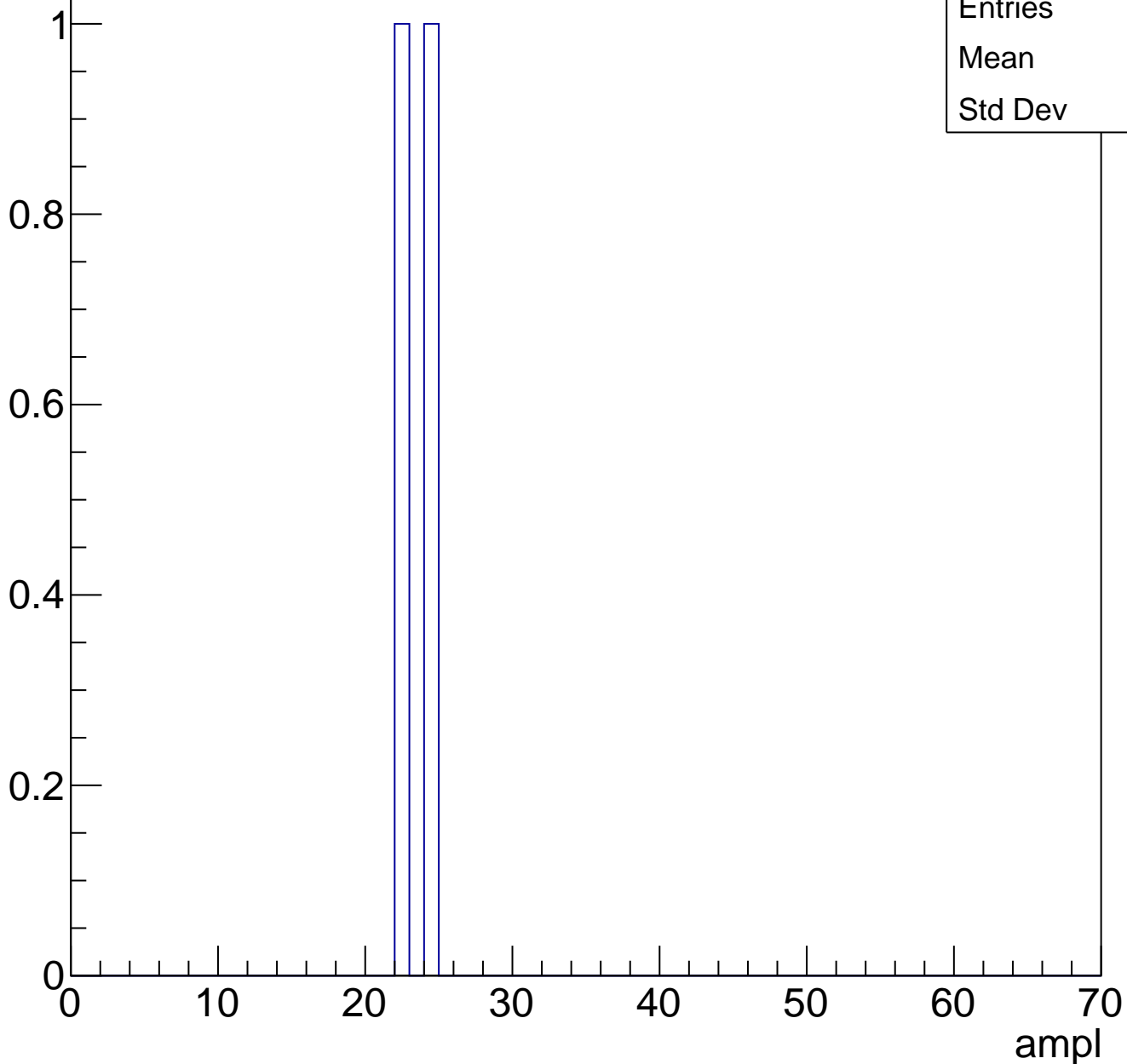
Entry



B1L001S, U21-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	30.35
Std Dev	3.263

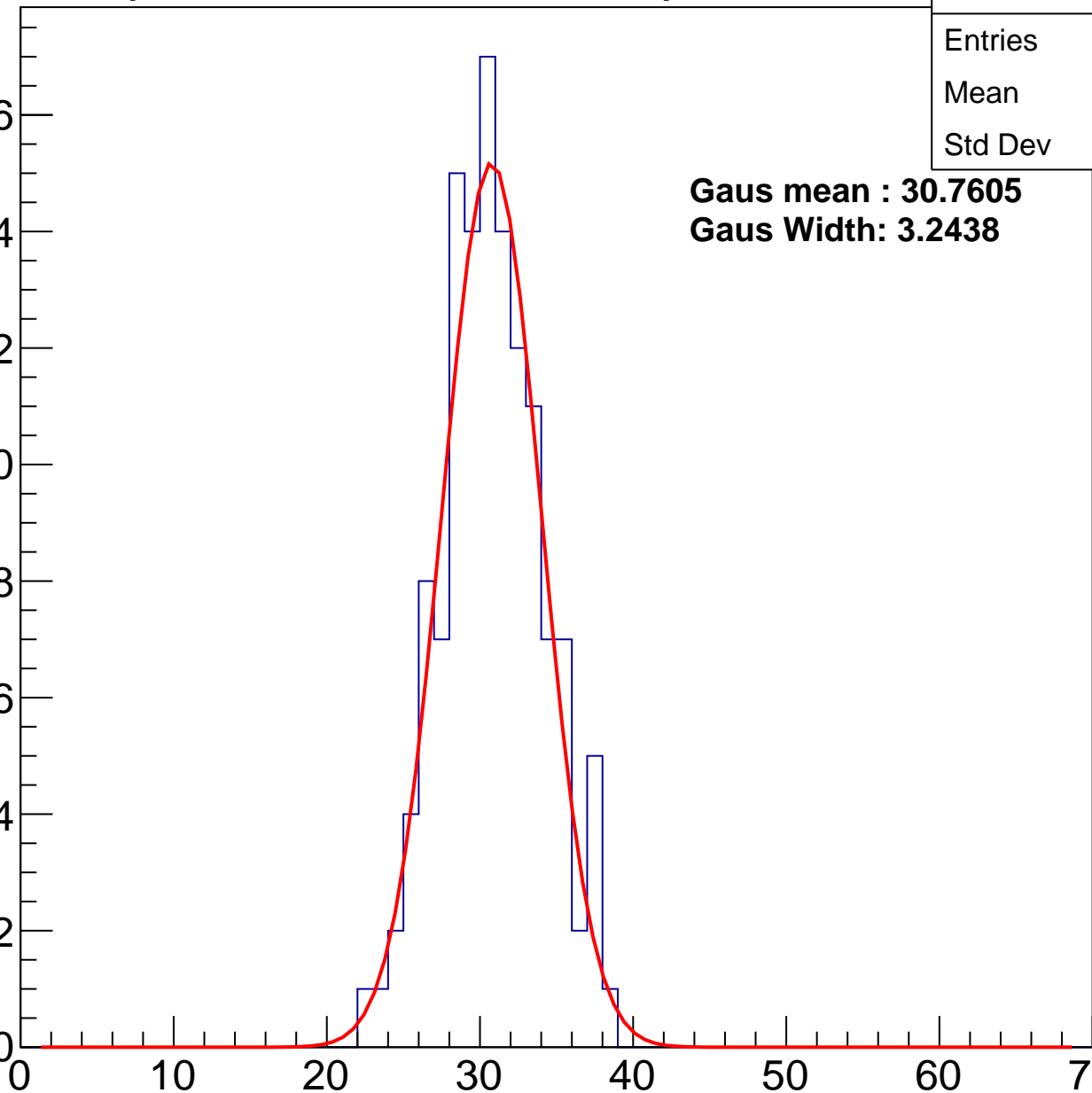
Gaus mean : 30.7605

Gaus Width: 3.2438

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch52, adc1

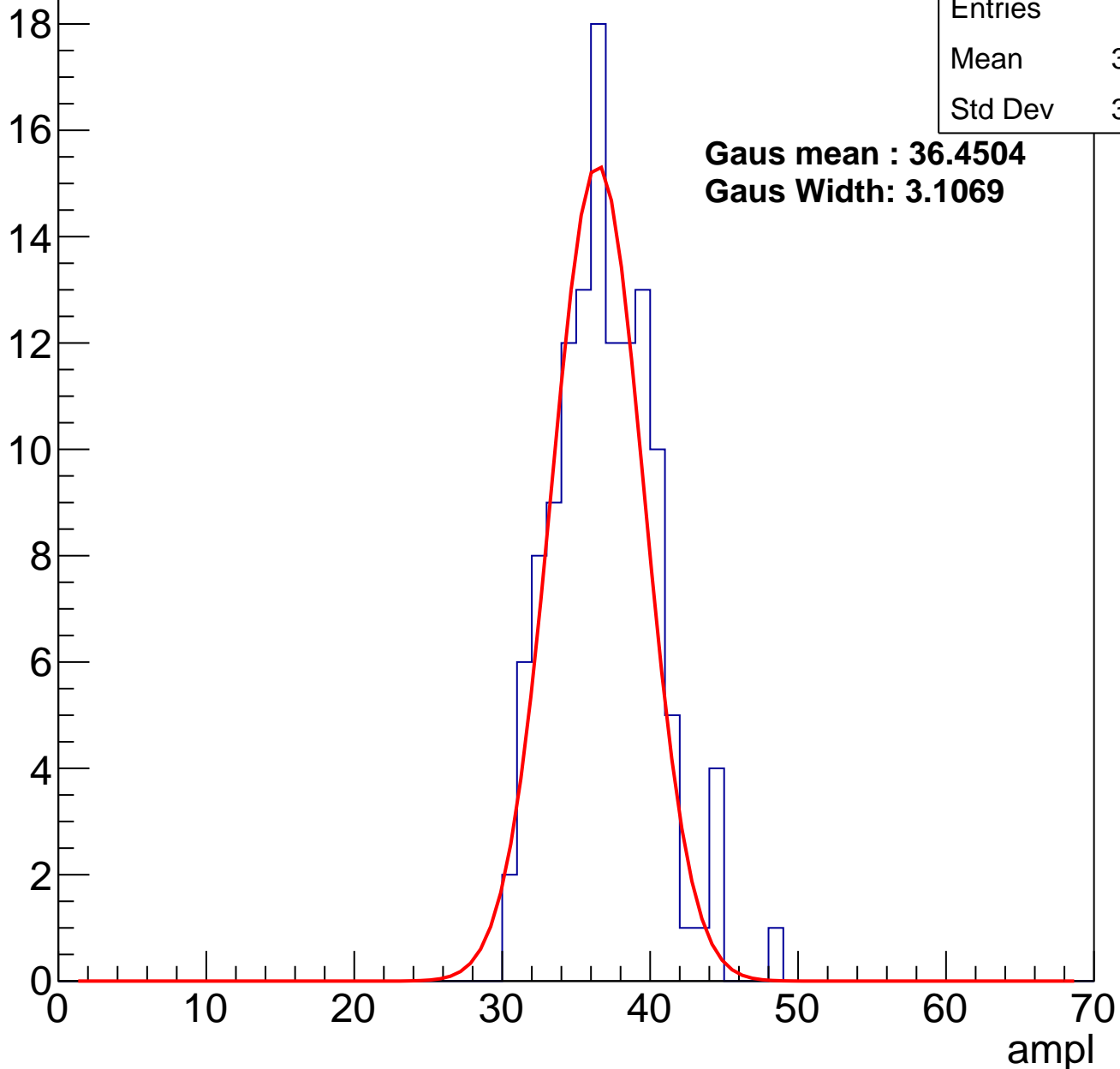
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.46
Std Dev	3.334

Gaus mean : 36.4504

Gaus Width: 3.1069

Entry



B1L001S, U21-ch52, adc2

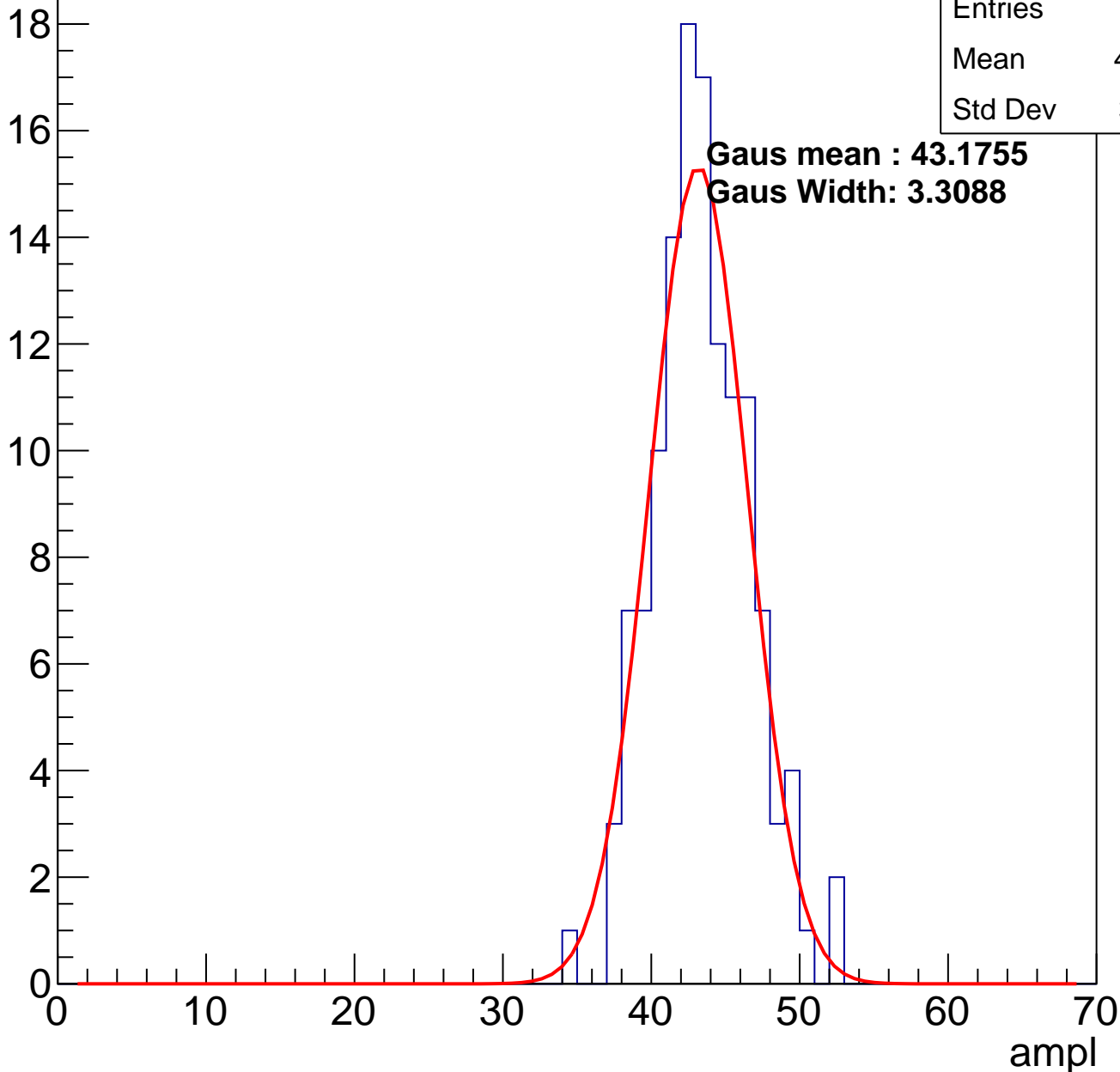
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	42.95
Std Dev	3.231

Gaus mean : 43.1755

Gaus Width: 3.3088

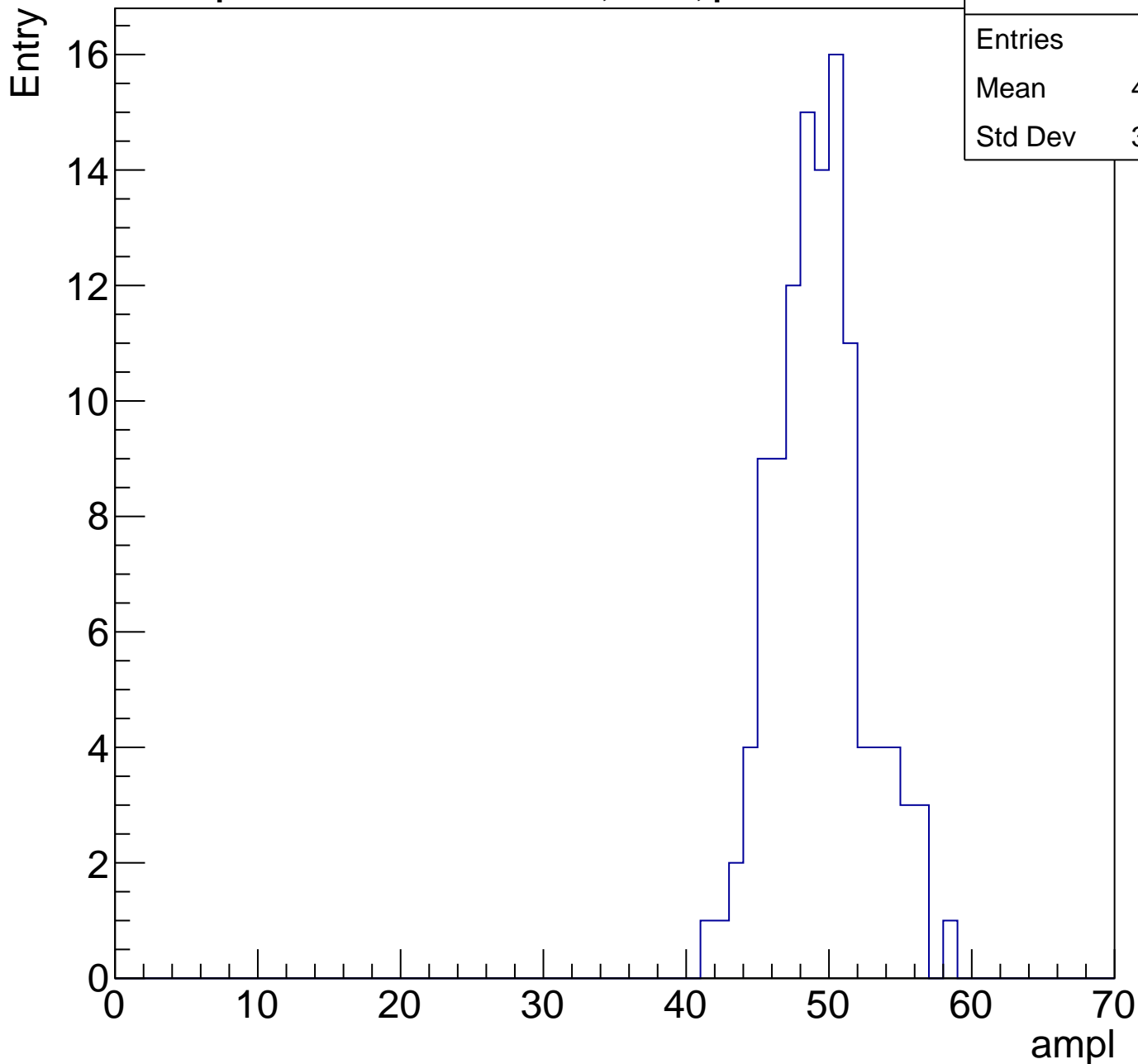
Entry



B1L001S, U21-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	48.87
Std Dev	3.214



B1L001S, U21-ch52, adc4

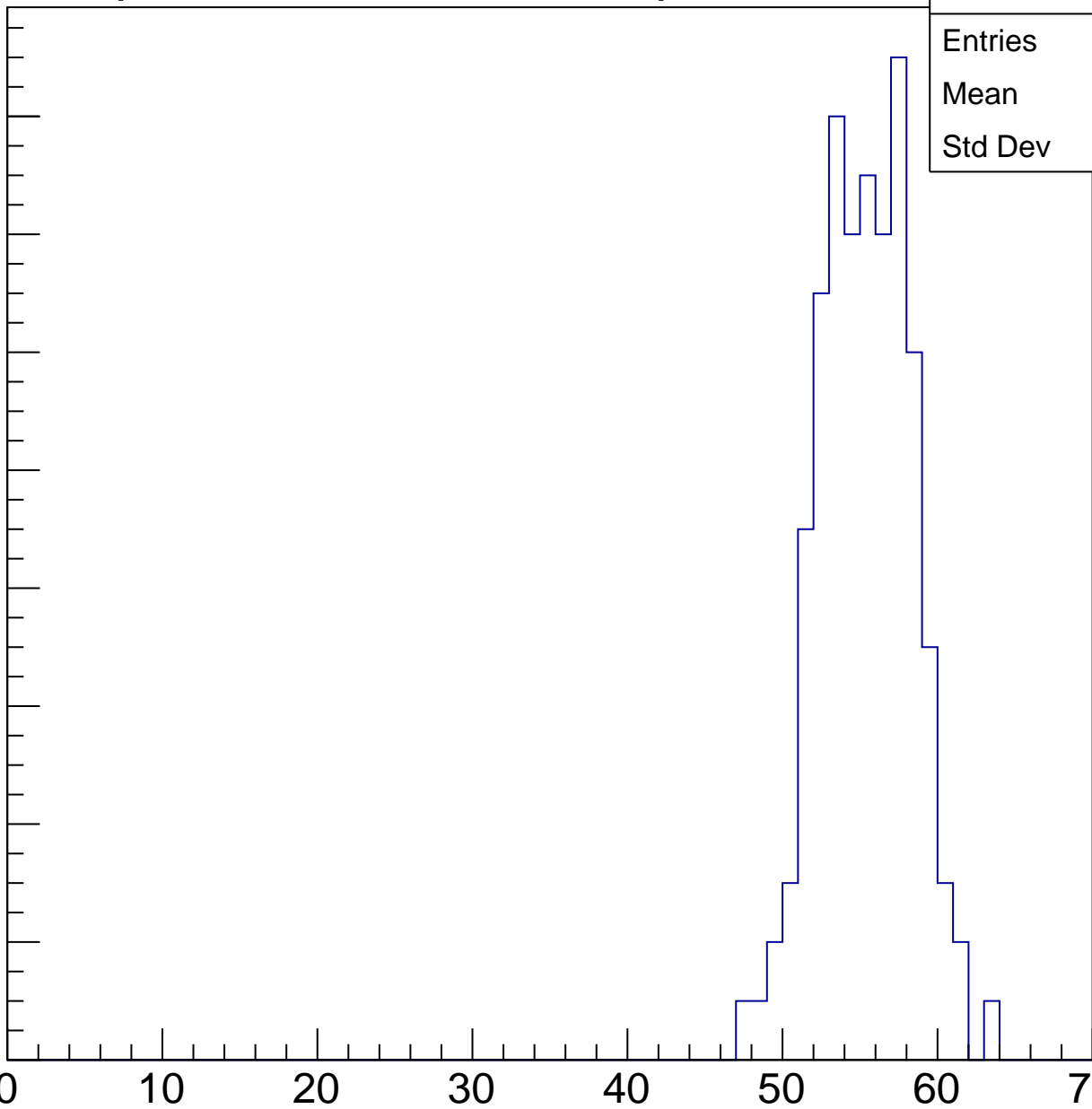
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	130
Mean	54.88
Std Dev	2.922

ampl



B1L001S, U21-ch52, adc5

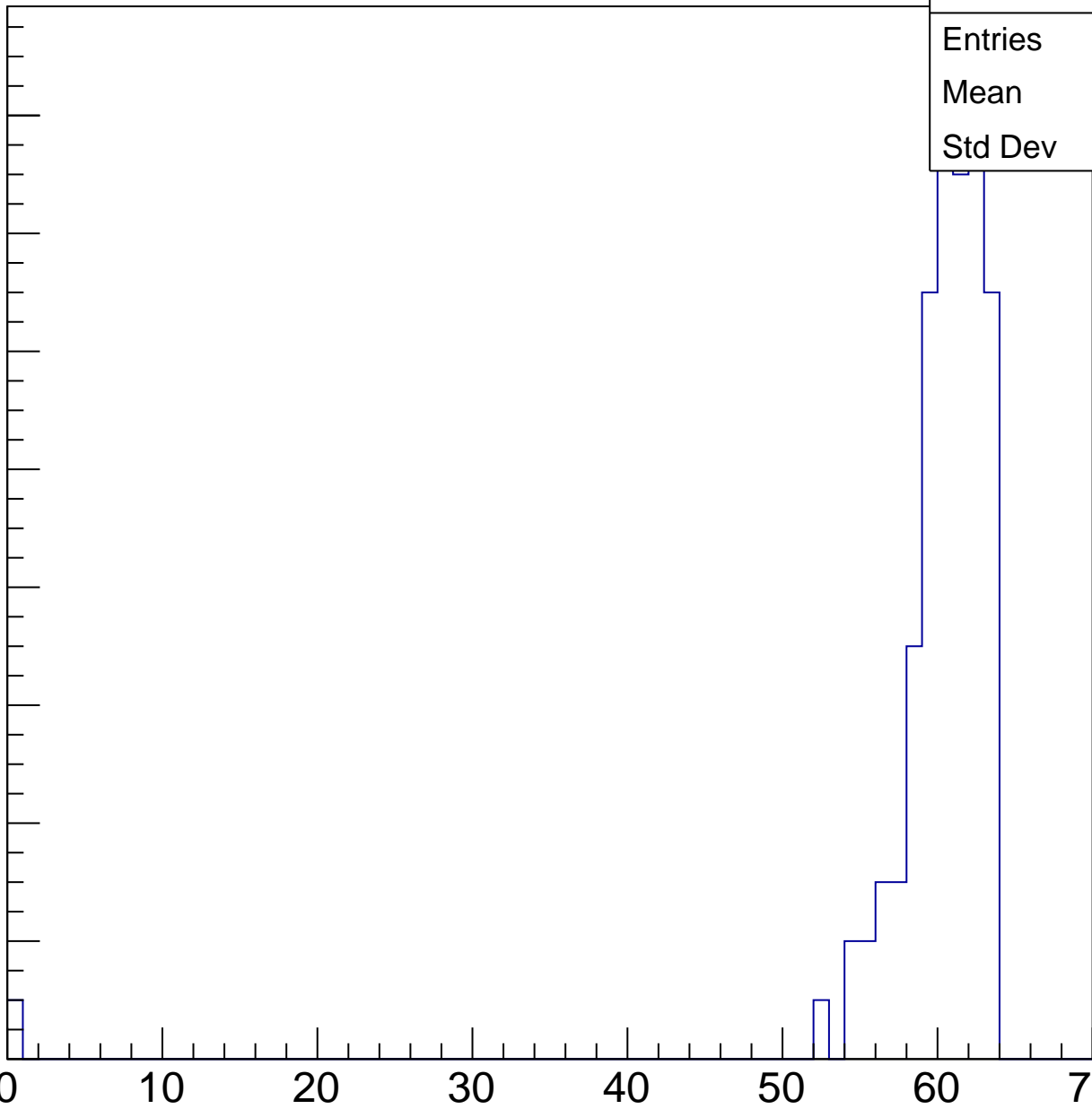
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.46
Std Dev	6.62

ampl

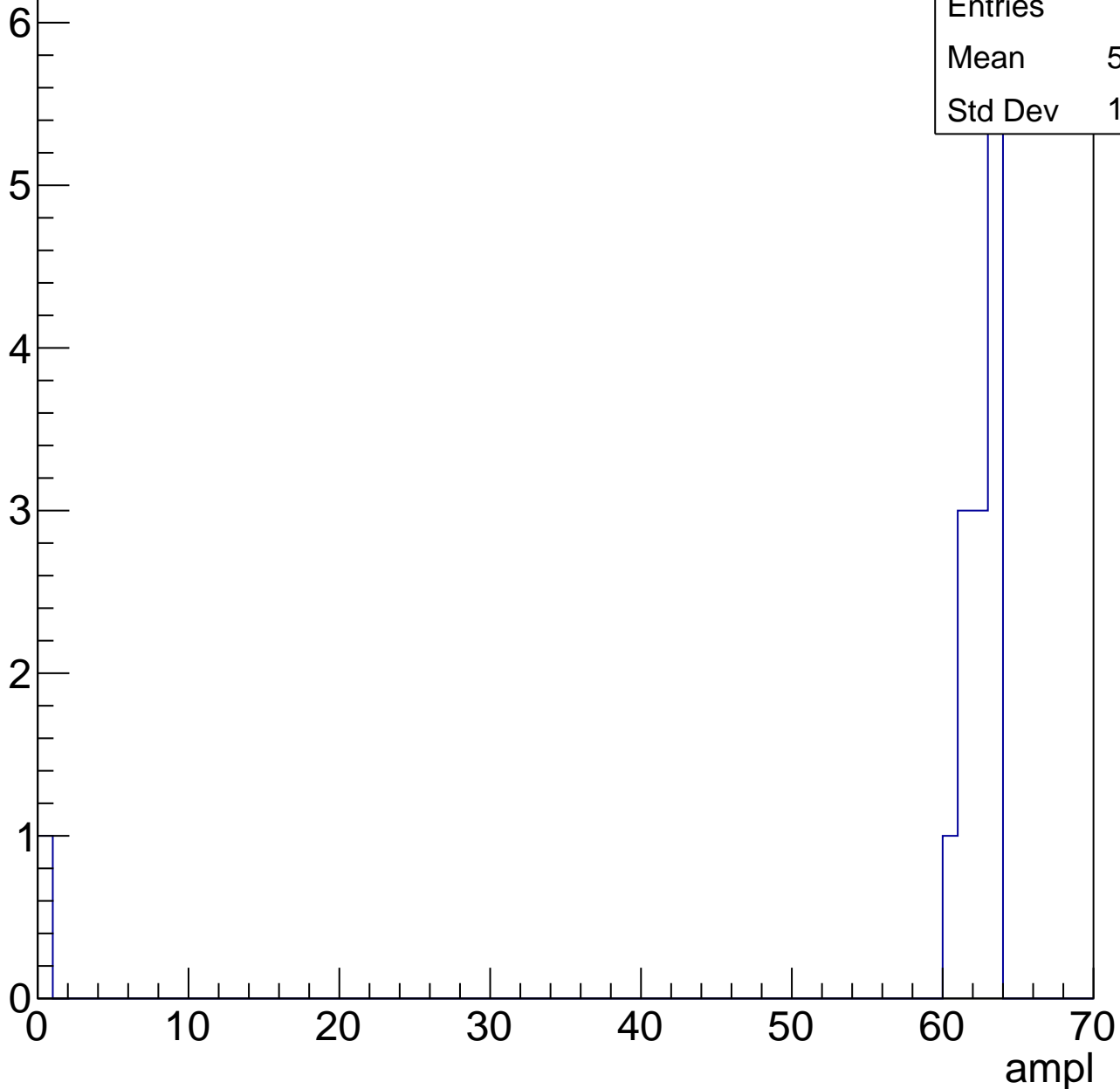


B1L001S, U21-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.02



B1L001S, U21-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



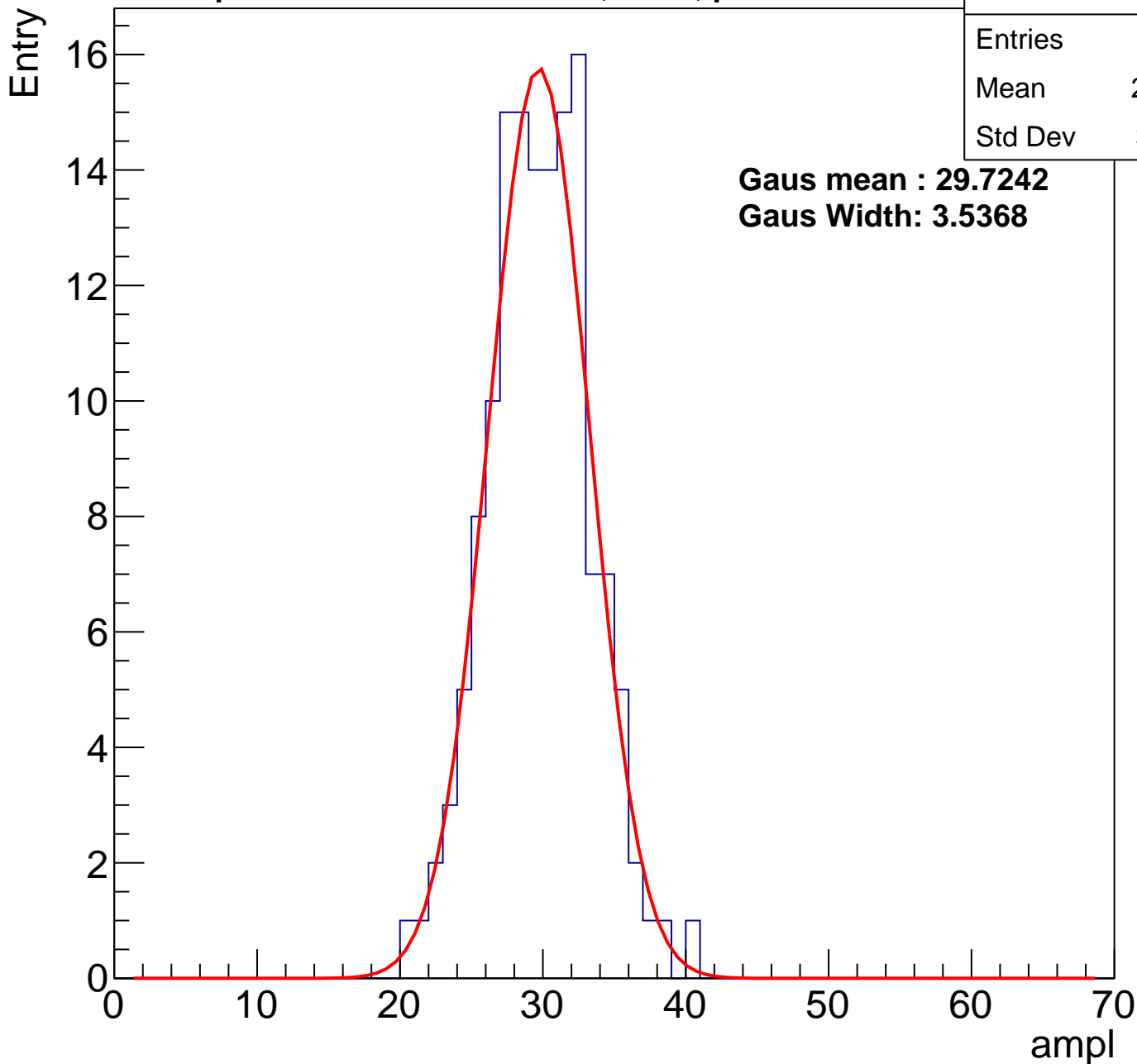
B1L001S, U21-ch53, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.32
Std Dev	3.551

Gaus mean : 29.7242

Gaus Width: 3.5368

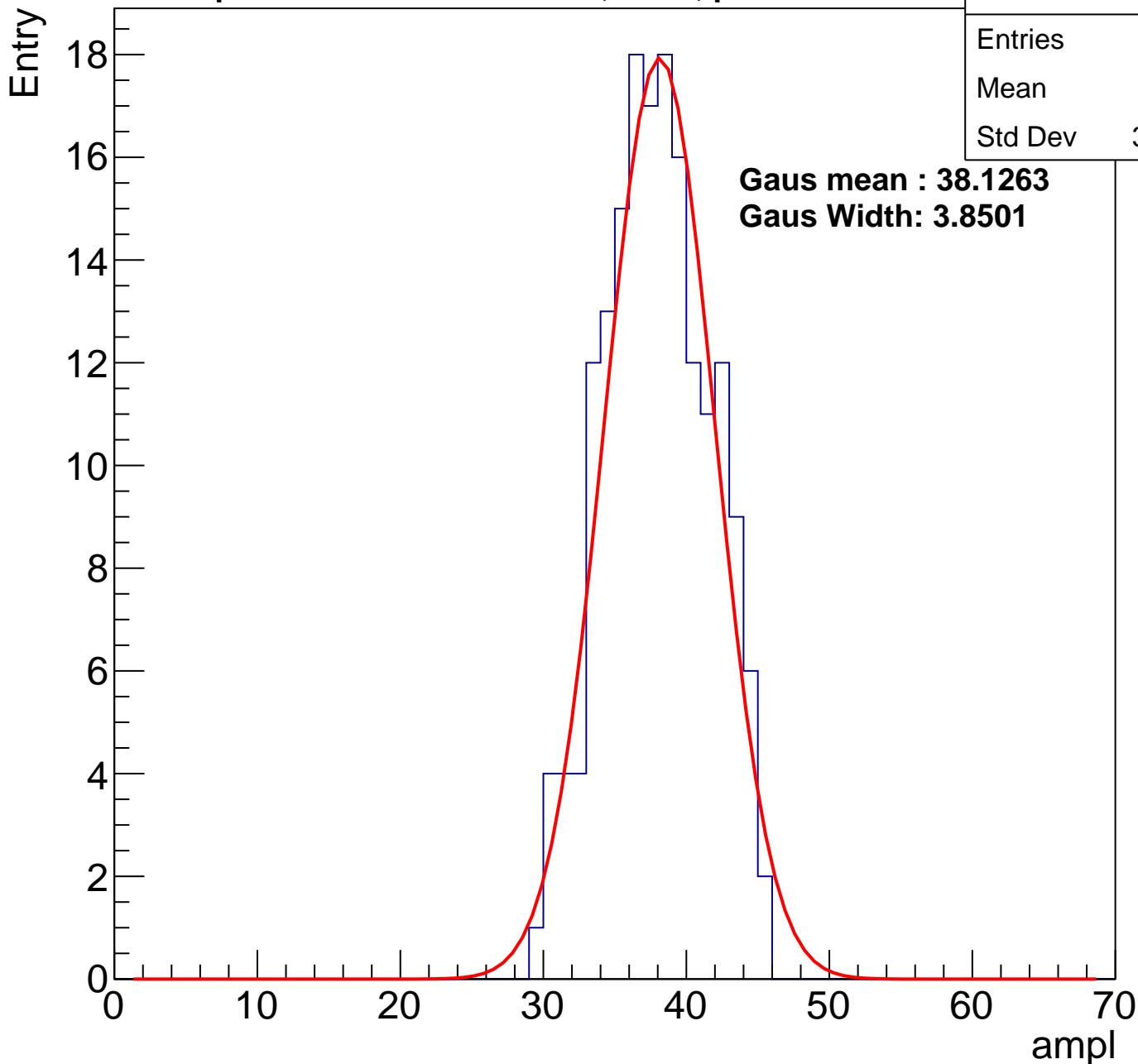


B1L001S, U21-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	37.5
Std Dev	3.604

Gaus mean : 38.1263
Gaus Width: 3.8501



B1L001S, U21-ch53, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

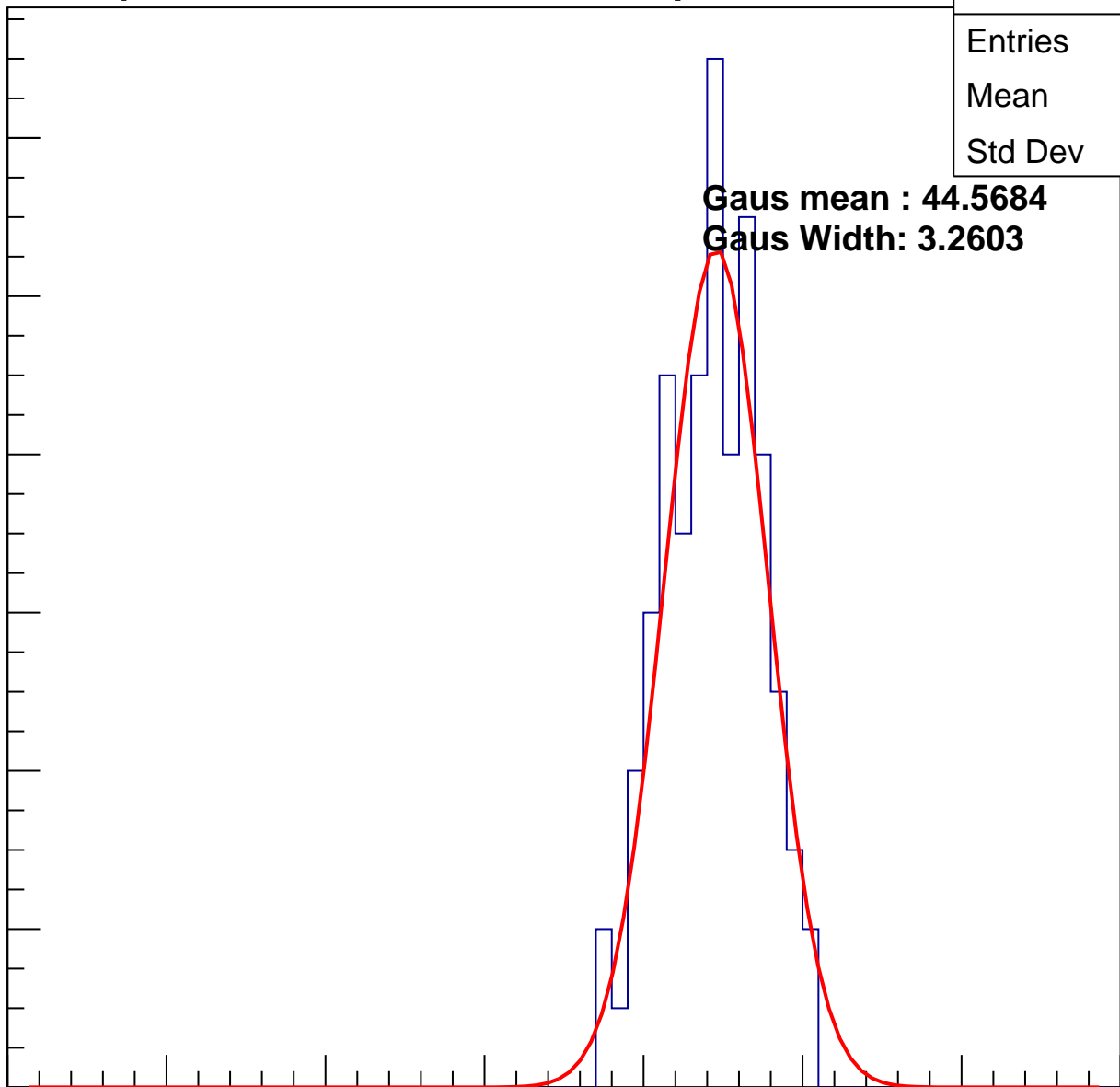
Entries	88
Mean	43.85
Std Dev	3.01

Gaus mean : 44.5684

Gaus Width: 3.2603

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	49.2
Std Dev	3.106

Entry

12

10

8

6

4

2

0

0

10

20

30

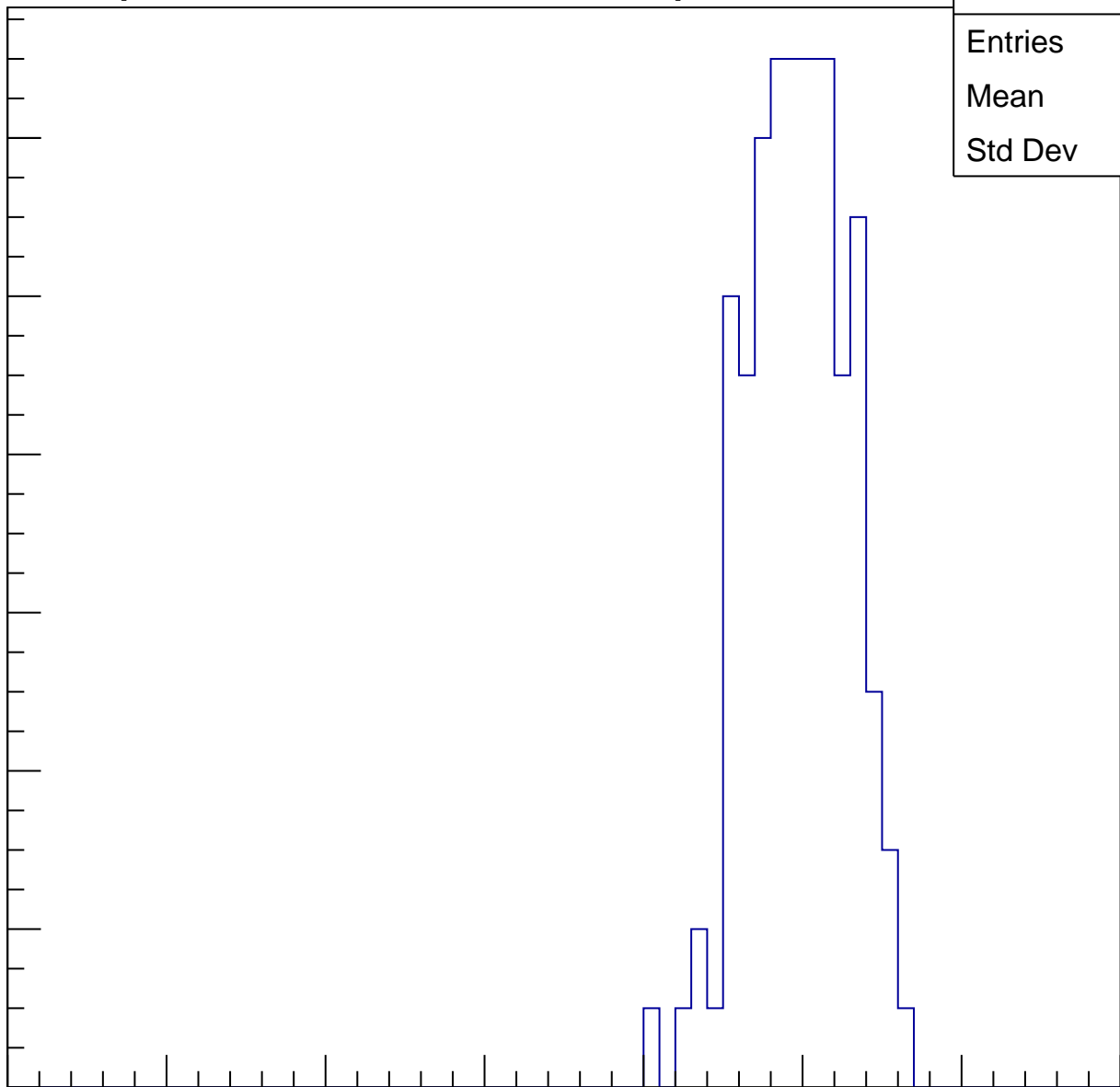
40

50

60

70

ampl



B1L001S, U21-ch53, adc4

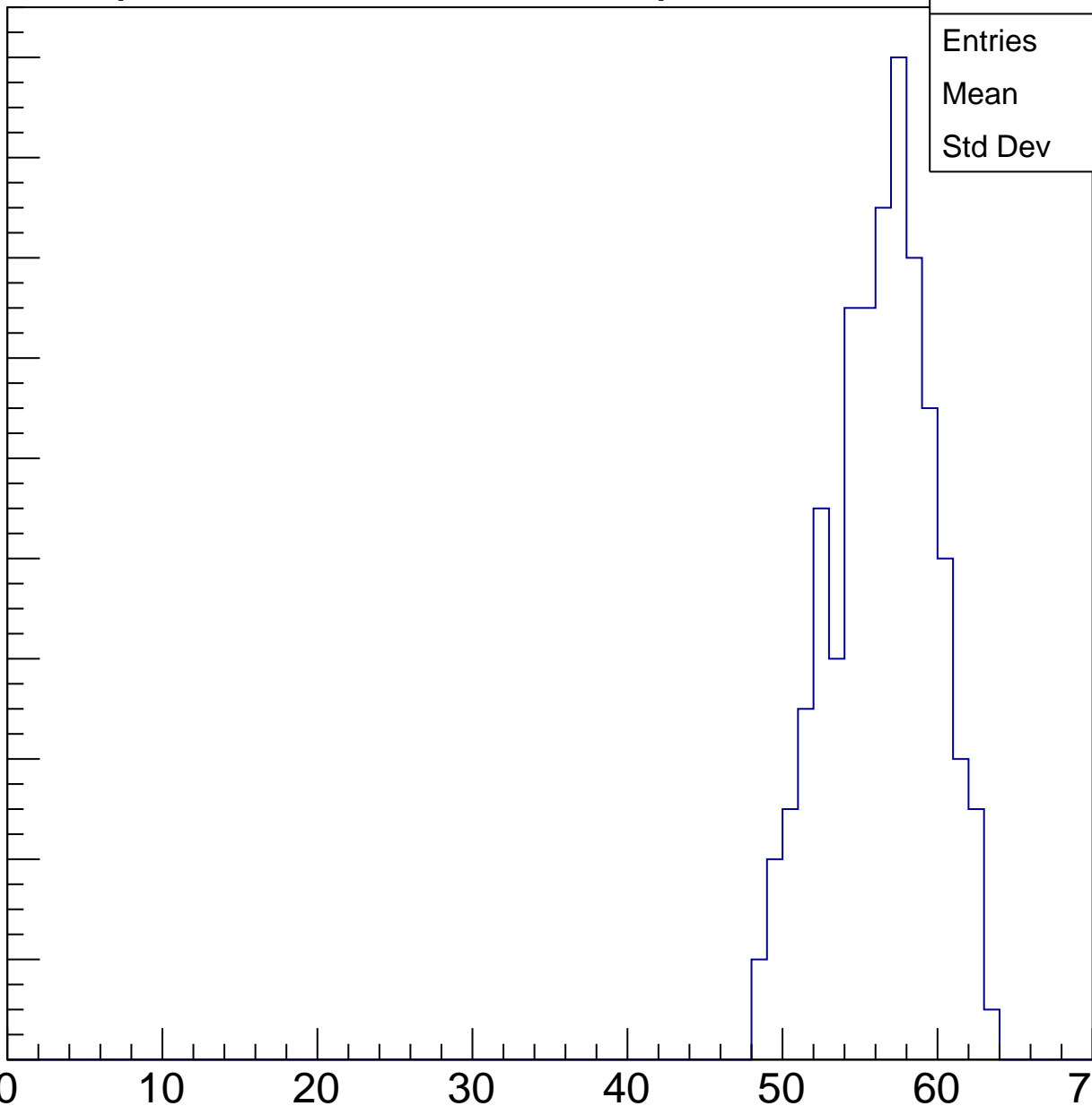
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	155
Mean	55.85
Std Dev	3.349

ampl



B1L001S, U21-ch53, adc5

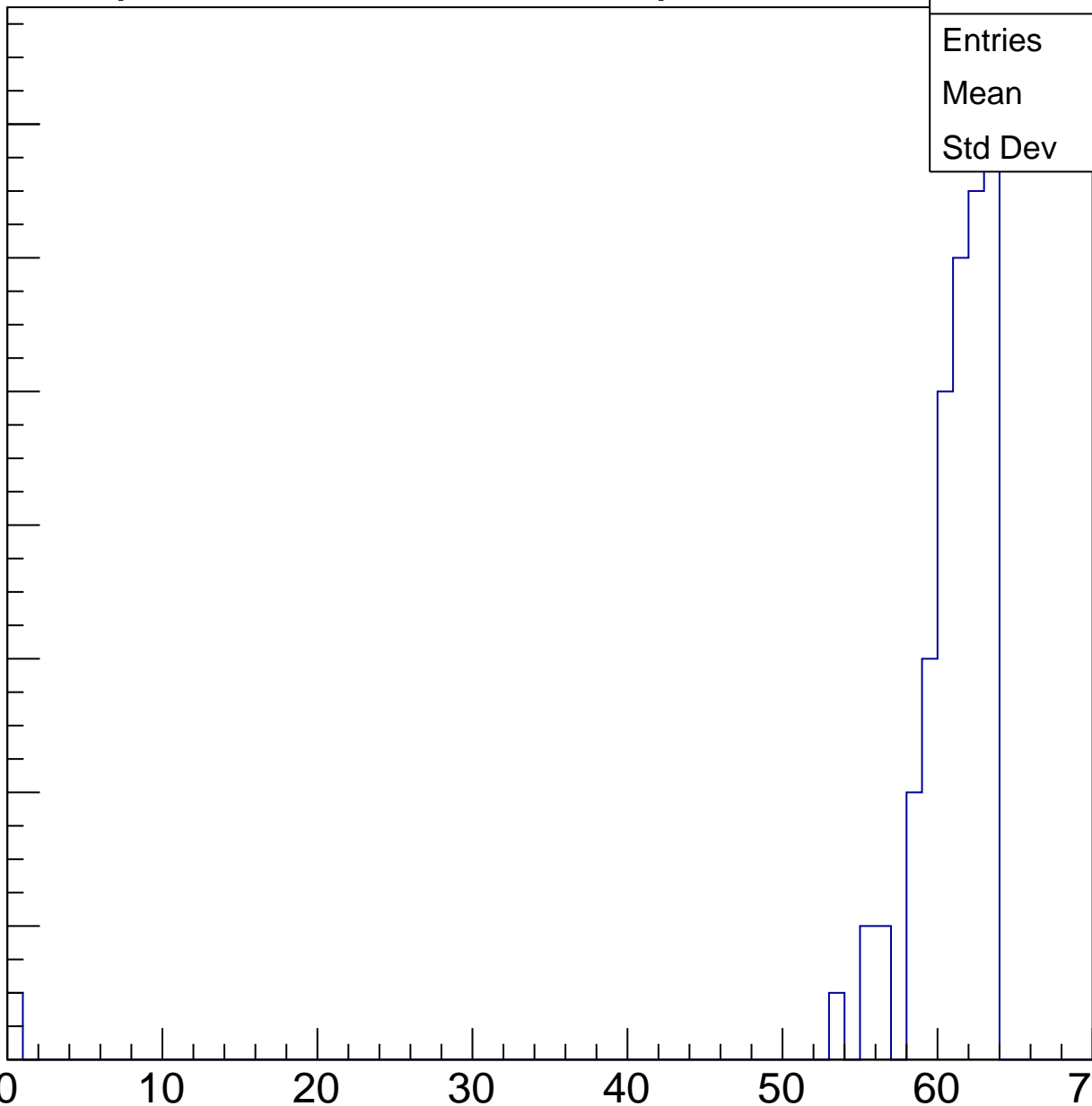
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.76
Std Dev	7.734

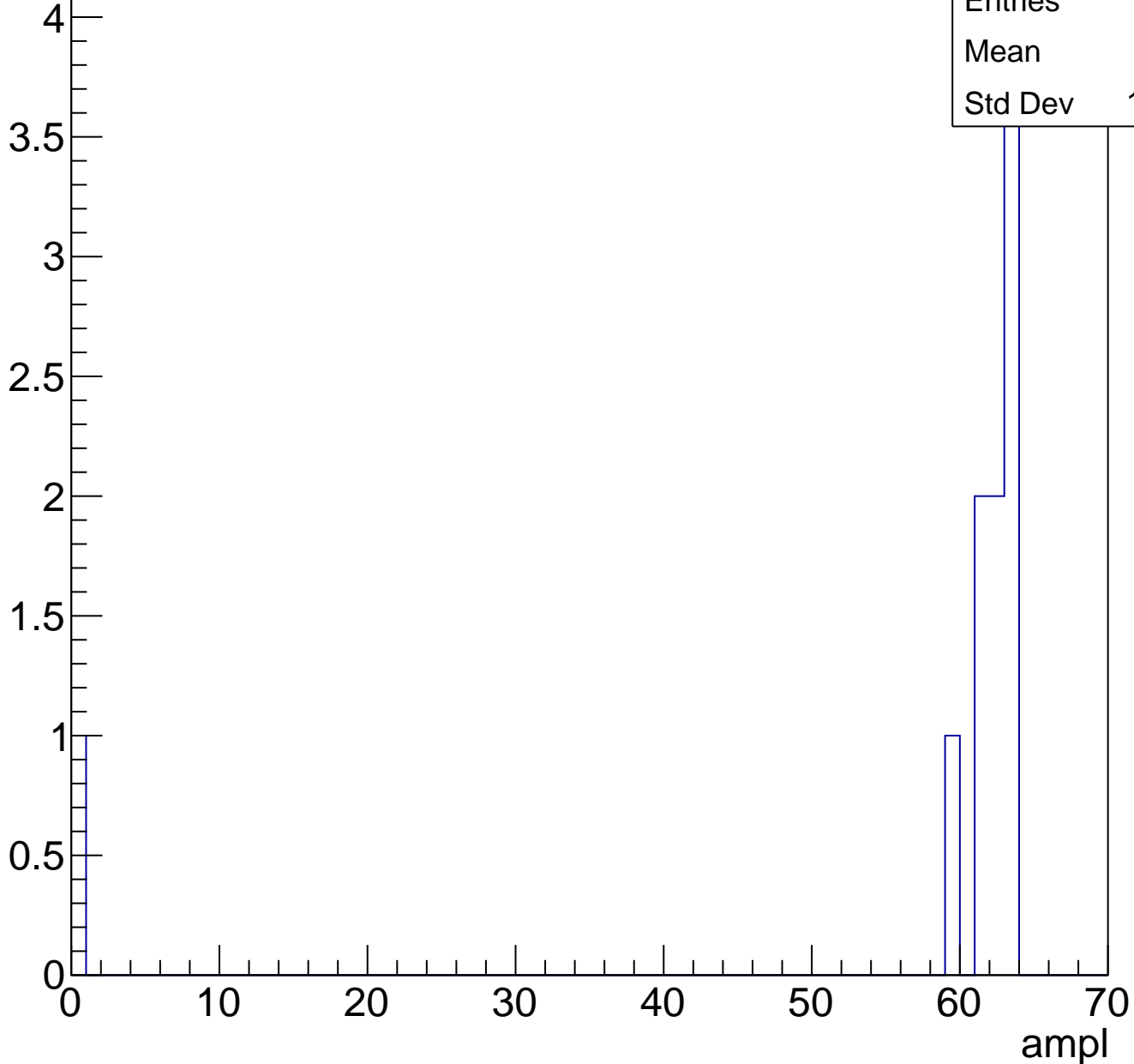
ampl



B1L001S, U21-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch54, adc0

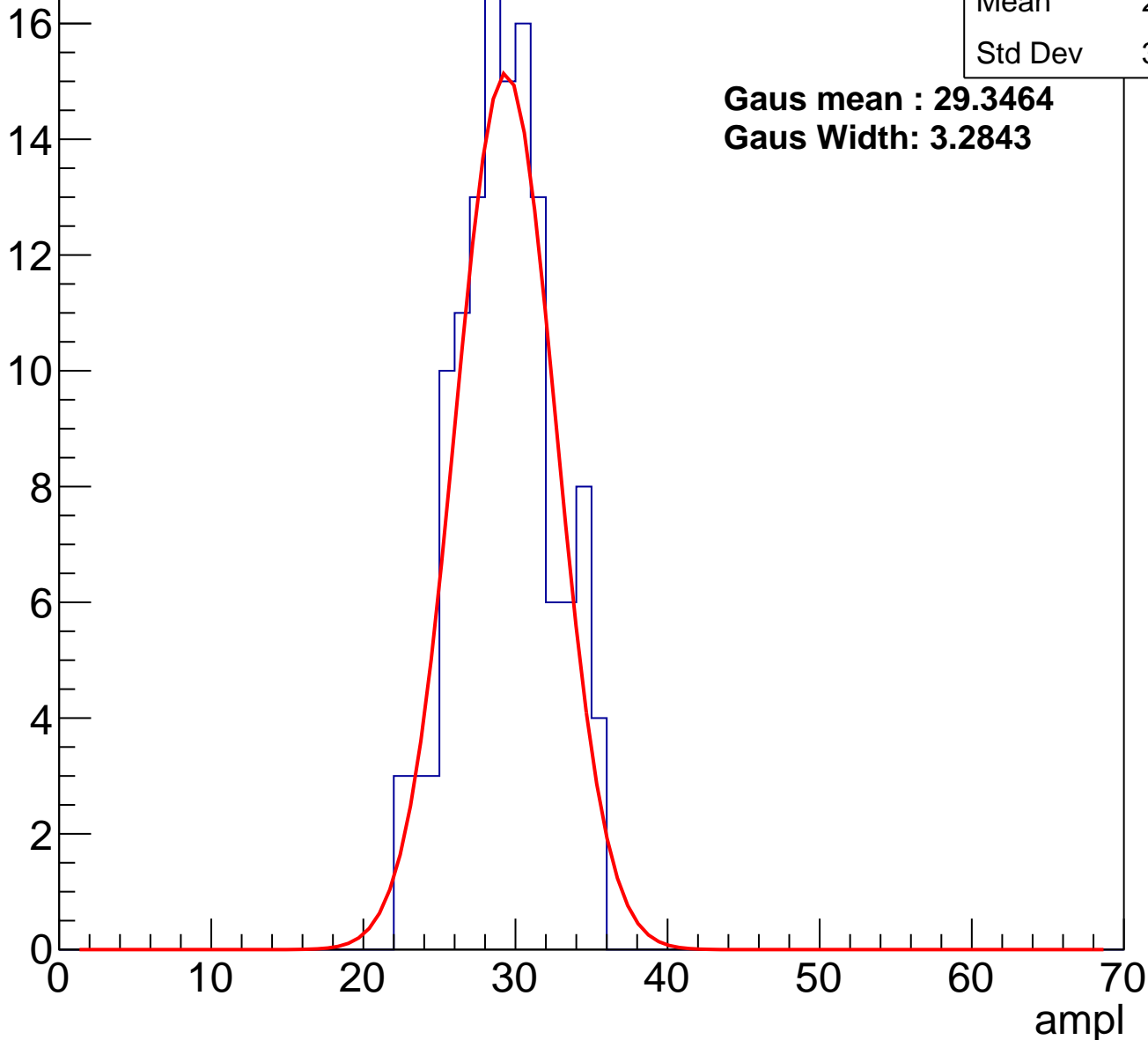
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	128
Mean	28.83
Std Dev	3.082

Gaus mean : 29.3464

Gaus Width: 3.2843



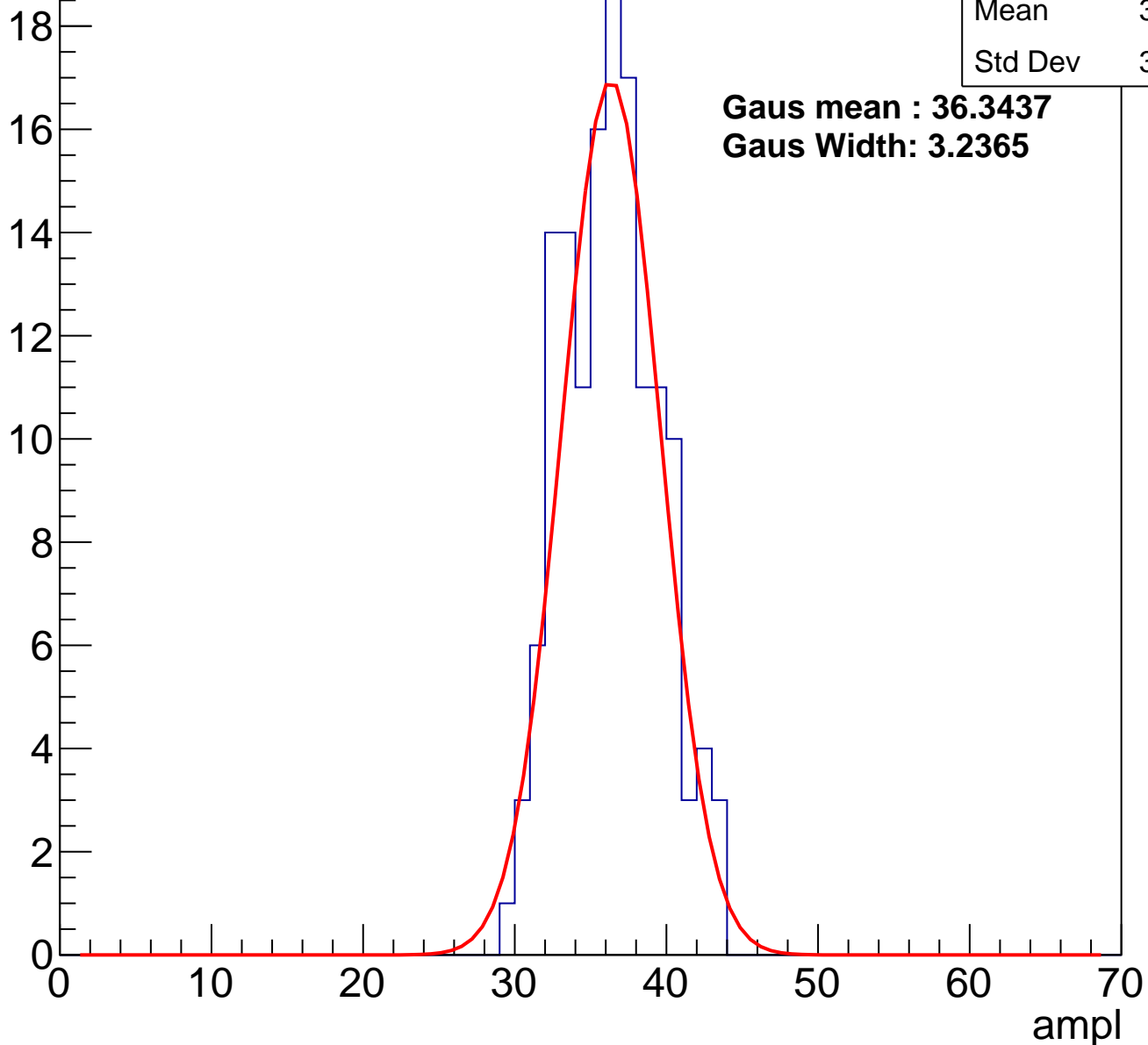
B1L001S, U21-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	35.87
Std Dev	3.129

Gaus mean : 36.3437
Gaus Width: 3.2365

Entry



B1L001S, U21-ch54, adc2

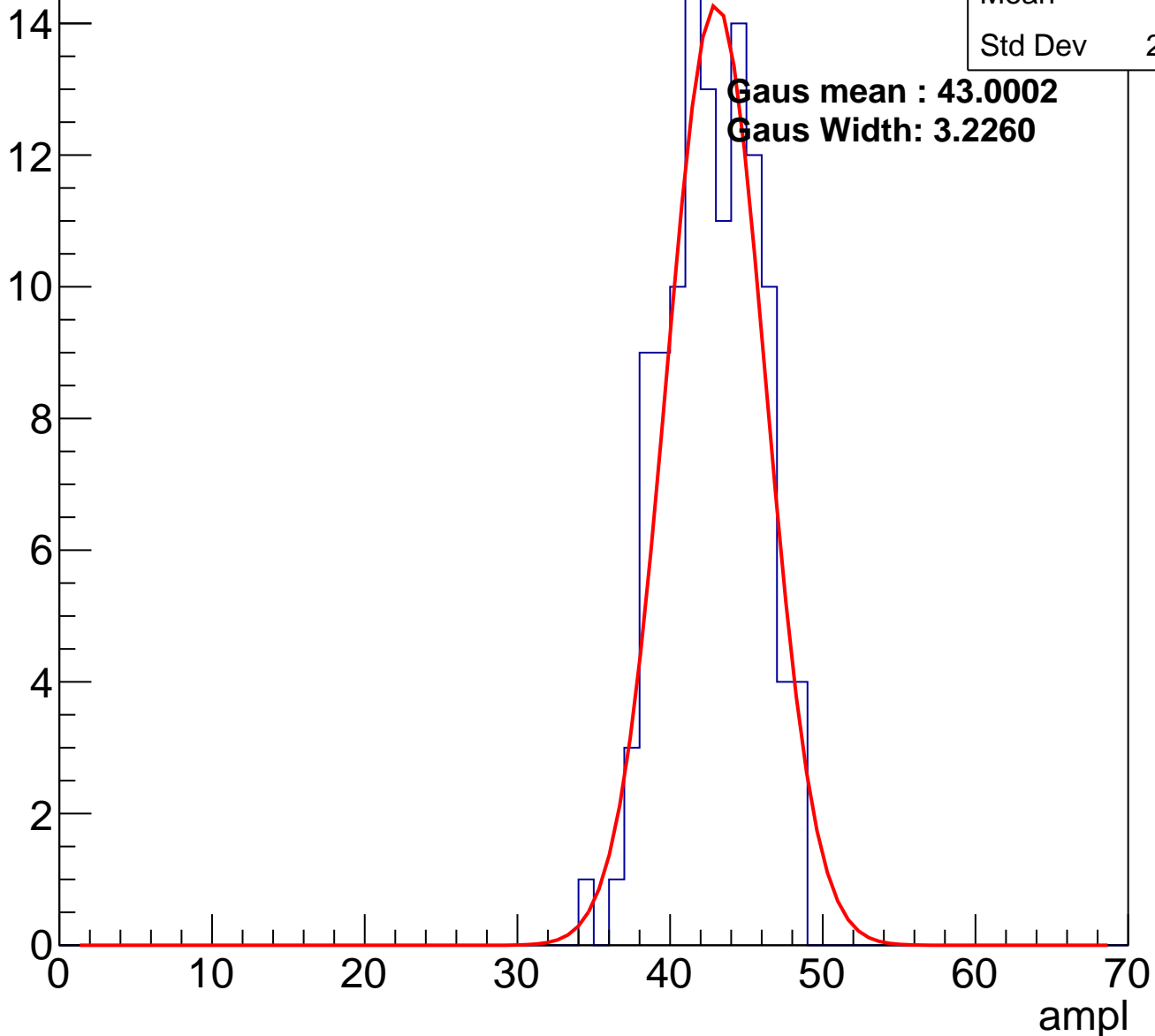
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	42.28
Std Dev	2.973

Gaus mean : 43.0002

Gaus Width: 3.2260

Entry



B1L001S, U21-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

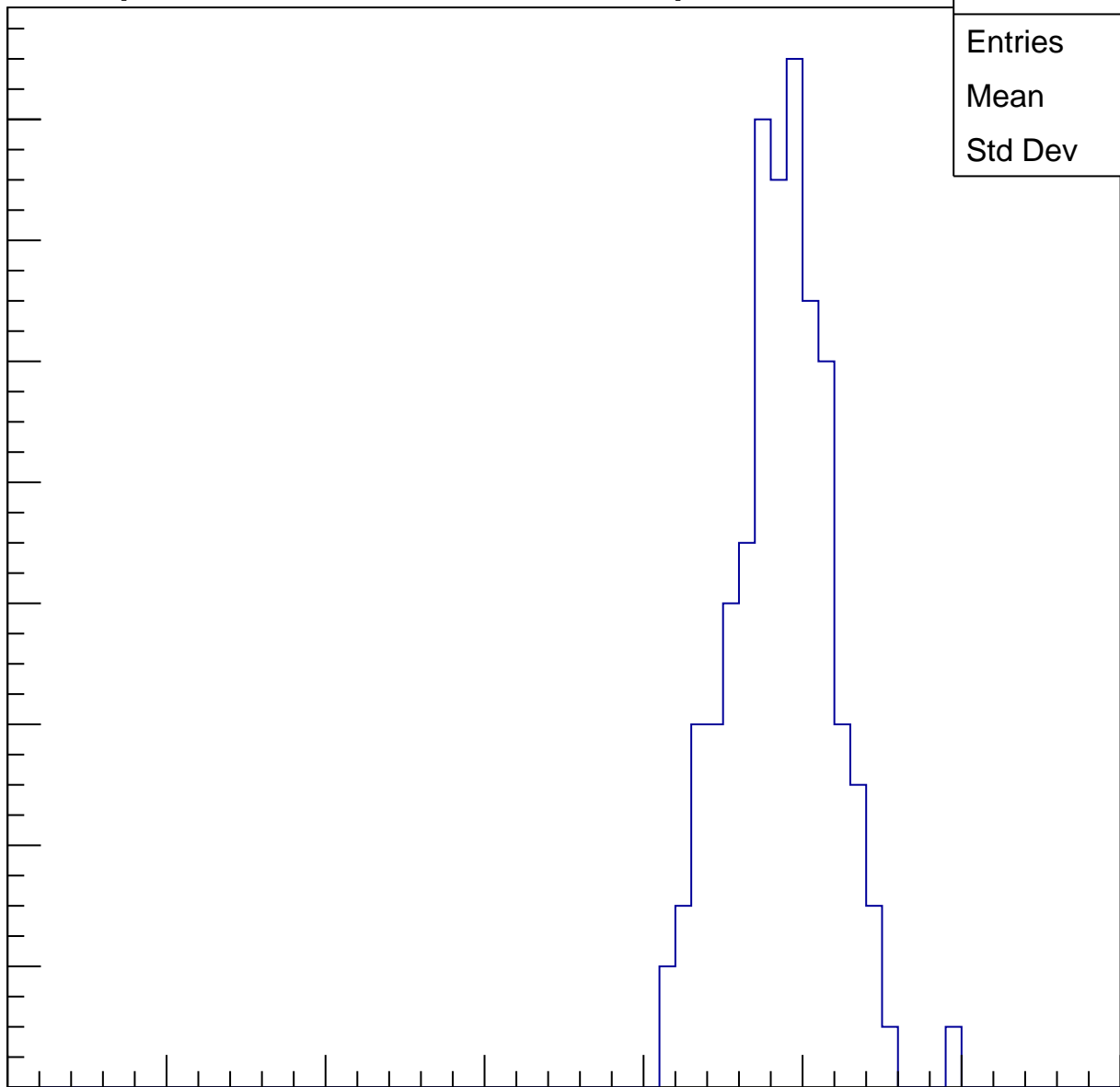
Entries	123
Mean	48.16
Std Dev	3.186

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch54, adc4

calib_packv5_042523_0143.root, FC#2, port C2

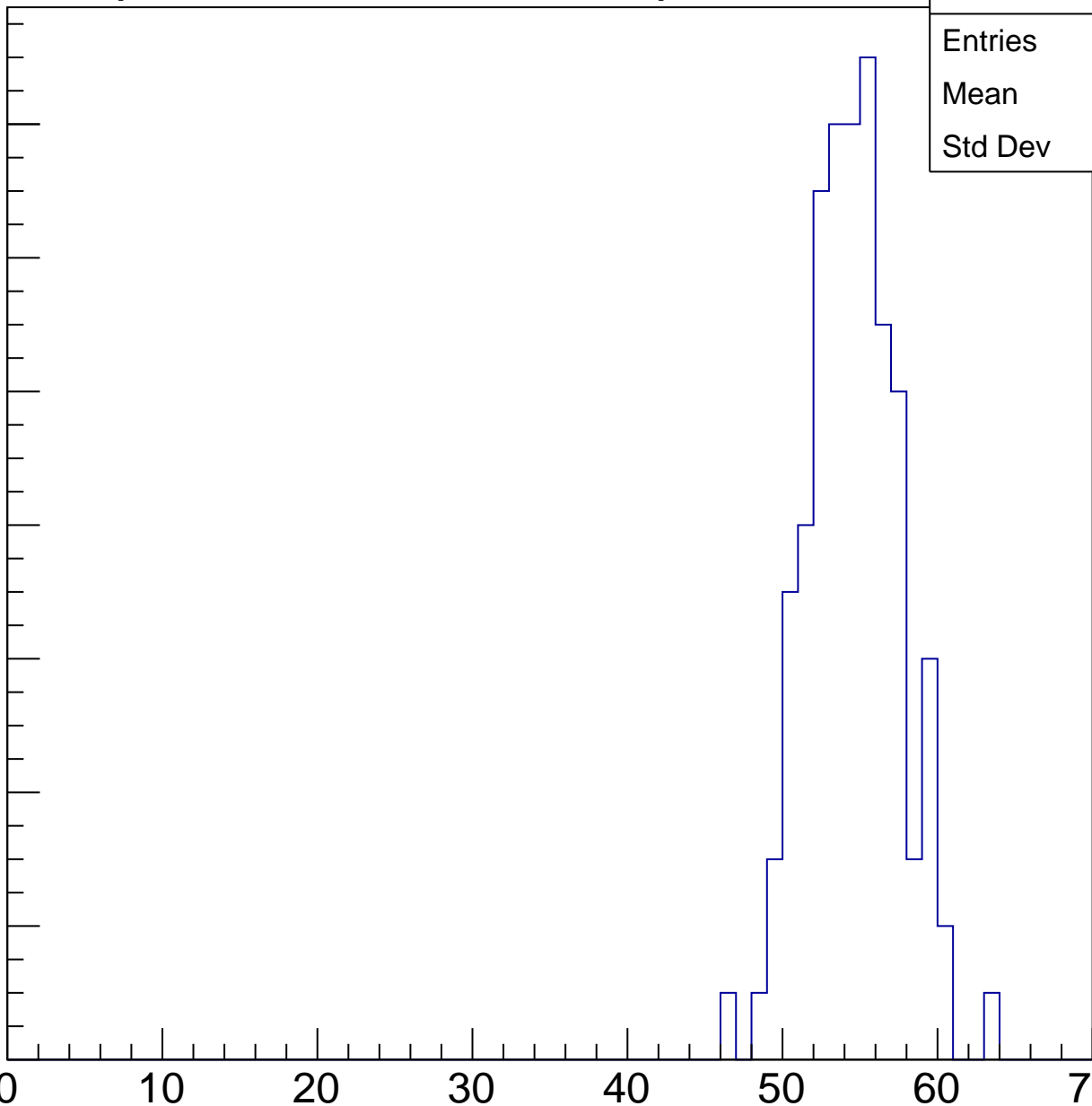
Entries	109
Mean	54.08
Std Dev	2.912

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch54, adc5

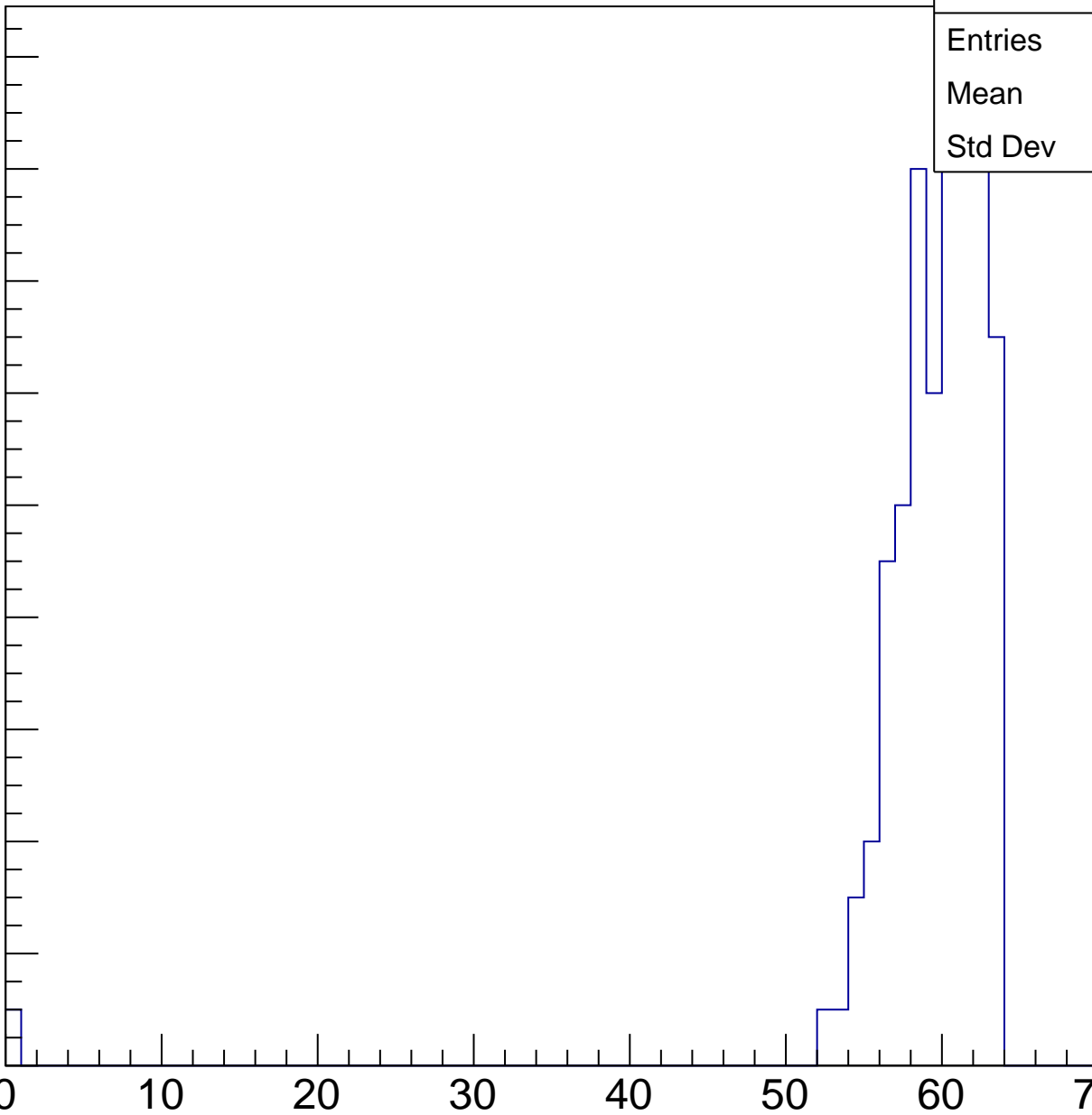
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	120
Mean	58.87
Std Dev	5.968

ampl

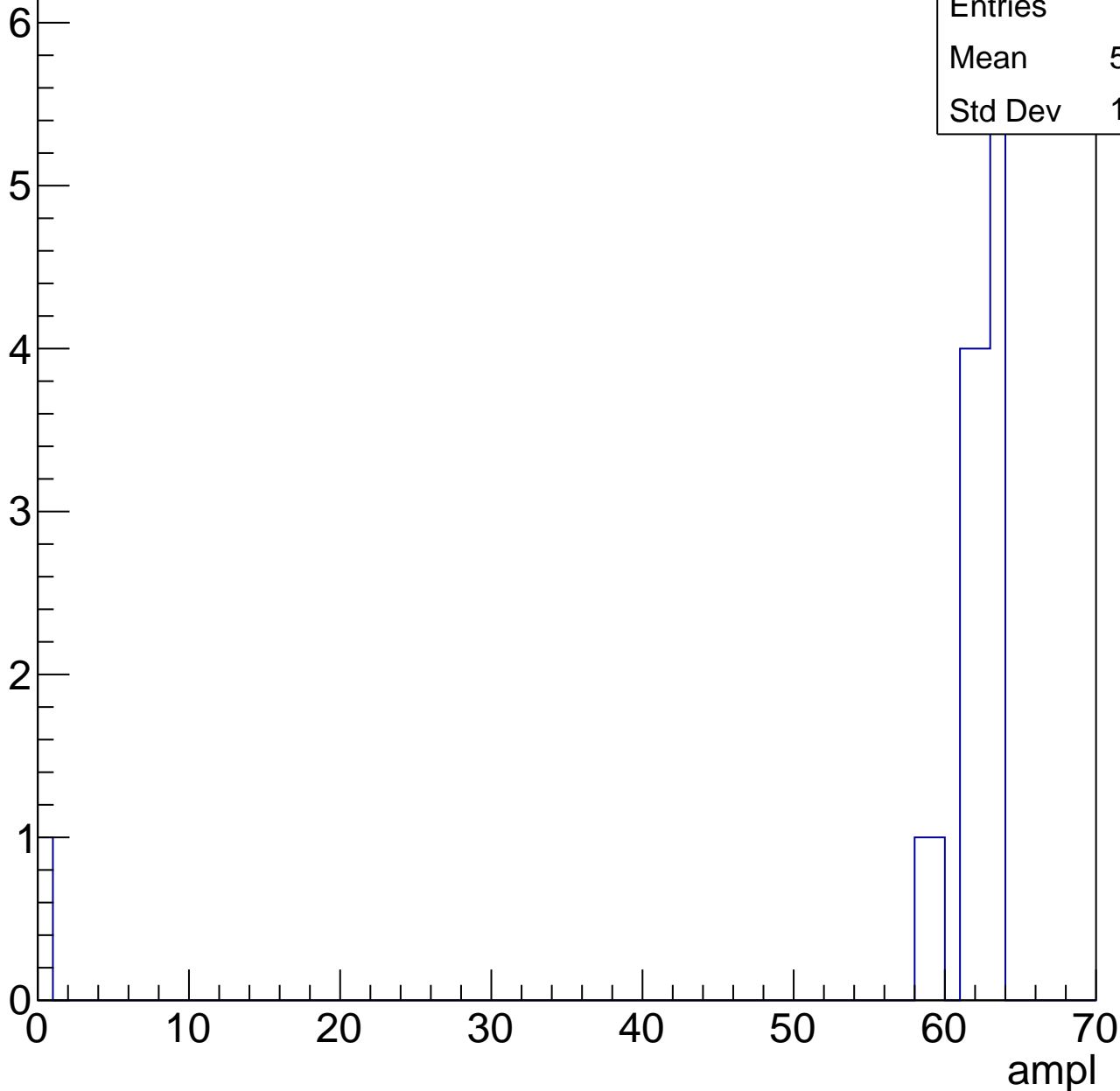


B1L001S, U21-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.06
Std Dev	14.58



B1L001S, U21-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

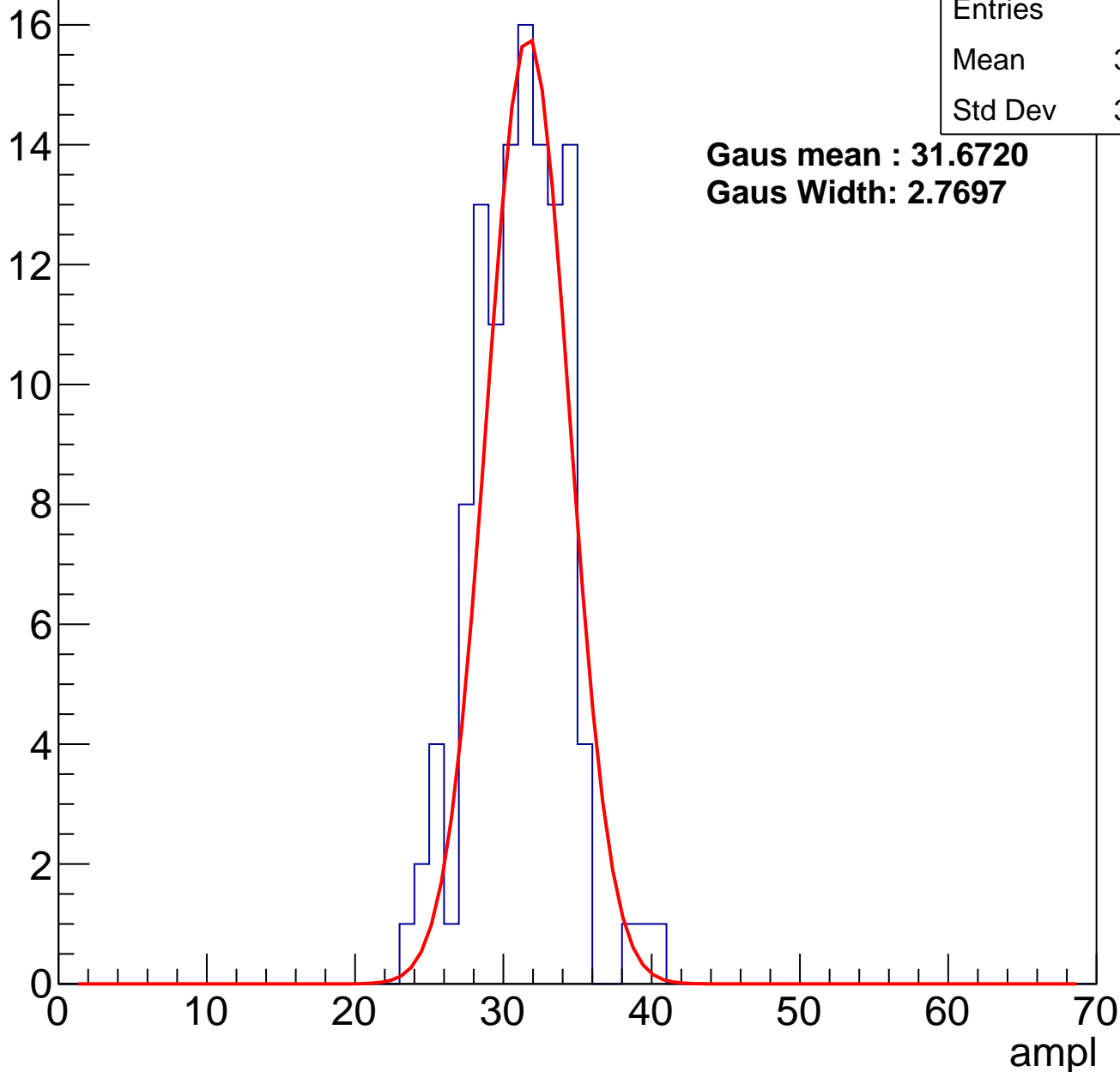
Entries	1
Mean	23
Std Dev	0

ampl

B1L001S, U21-ch55, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



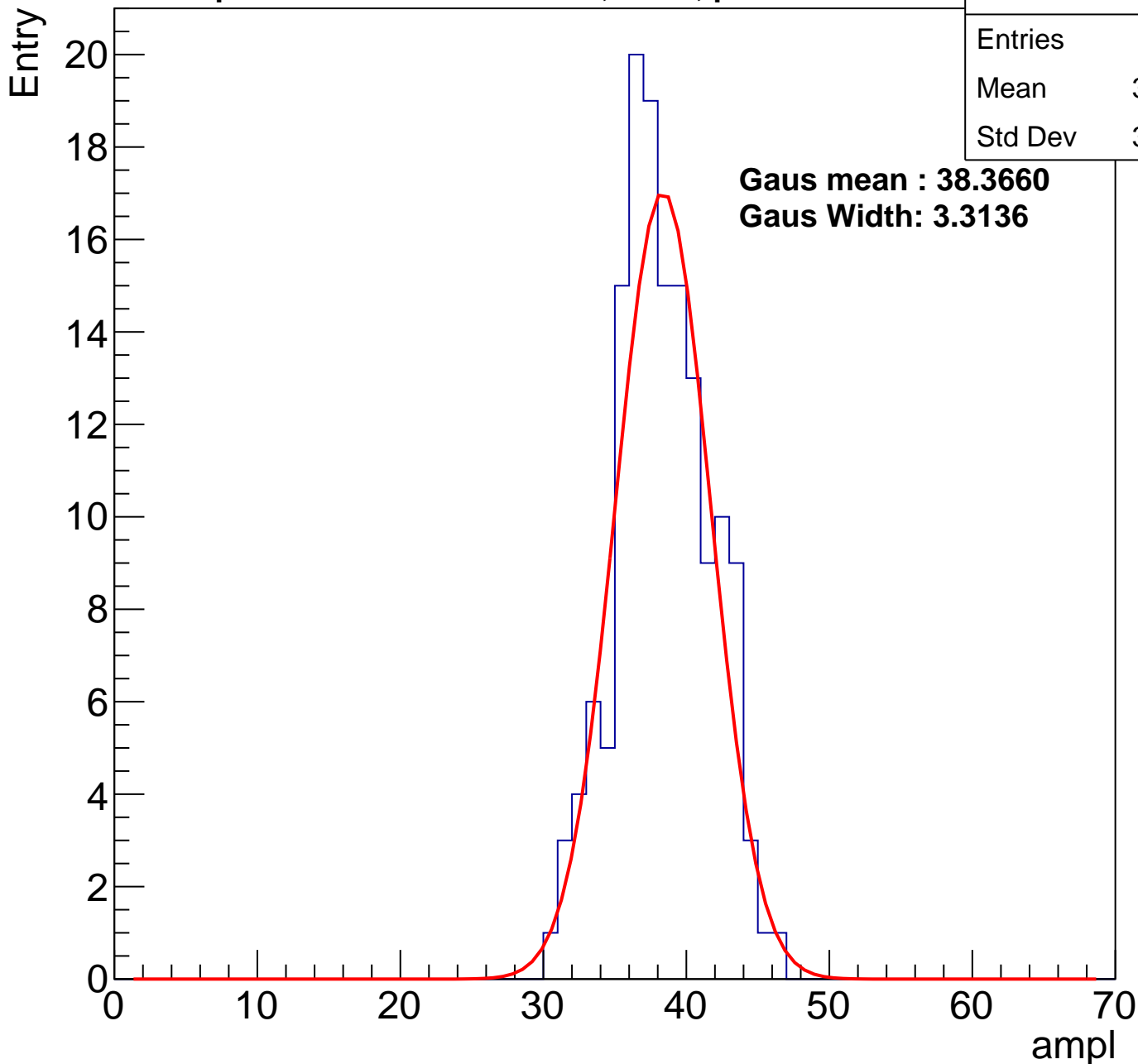
B1L001S, U21-ch55, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.86
Std Dev	3.246

Gaus mean : 38.3660

Gaus Width: 3.3136



B1L001S, U21-ch55, adc2

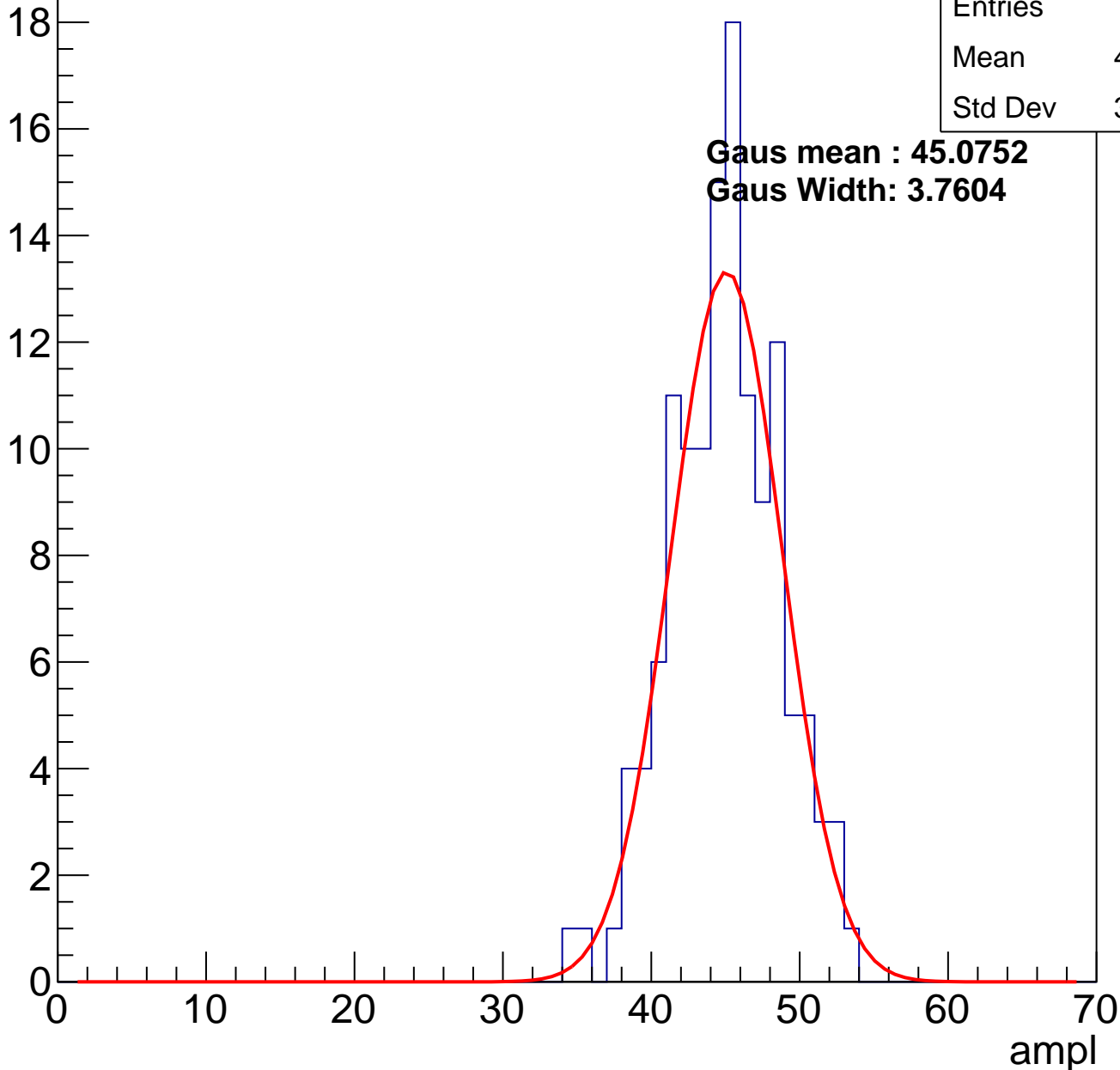
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.52
Std Dev	3.663

Gaus mean : 45.0752

Gaus Width: 3.7604

Entry



B1L001S, U21-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

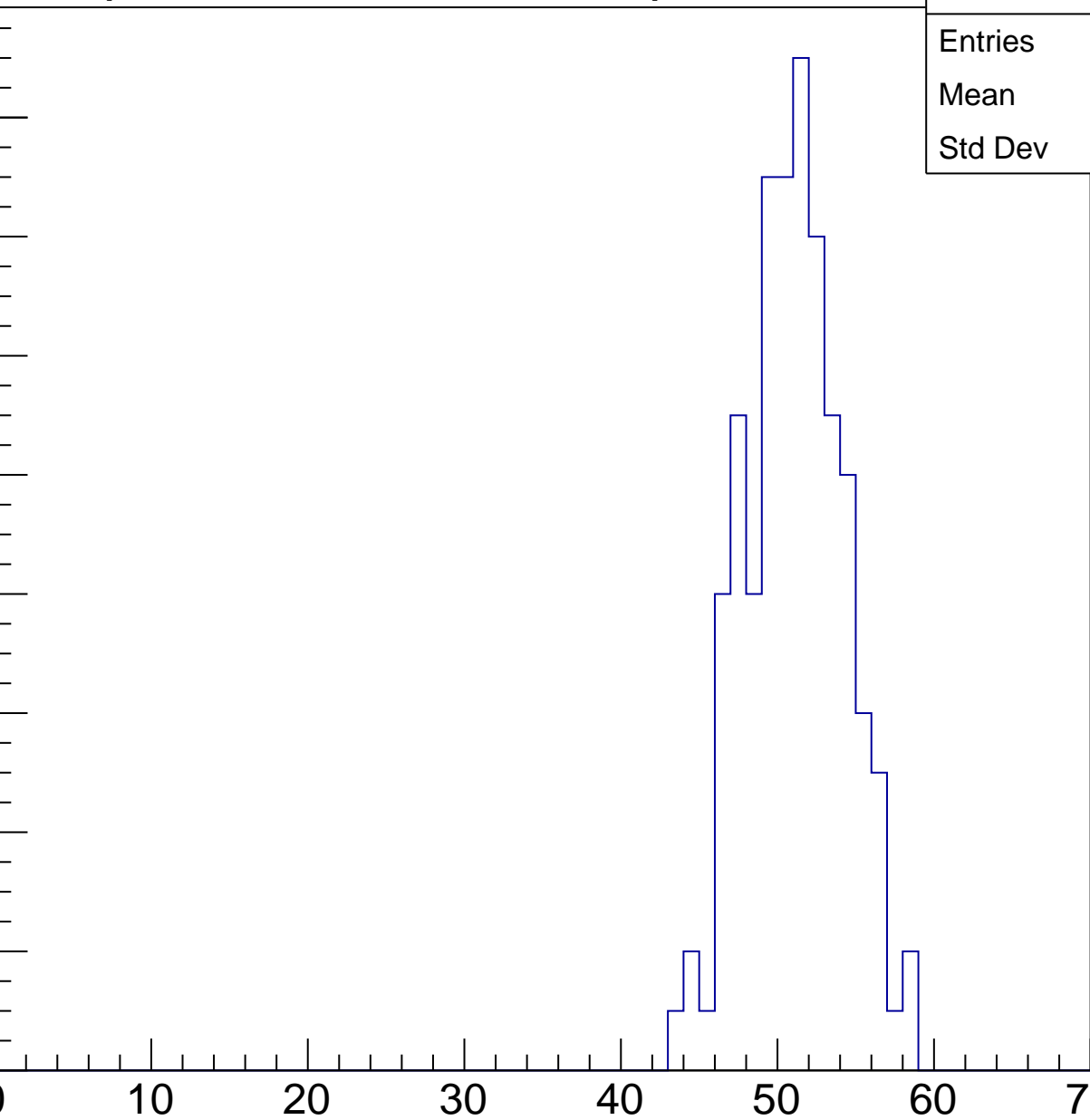
Entries	127
Mean	50.64
Std Dev	3.083

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch55, adc4

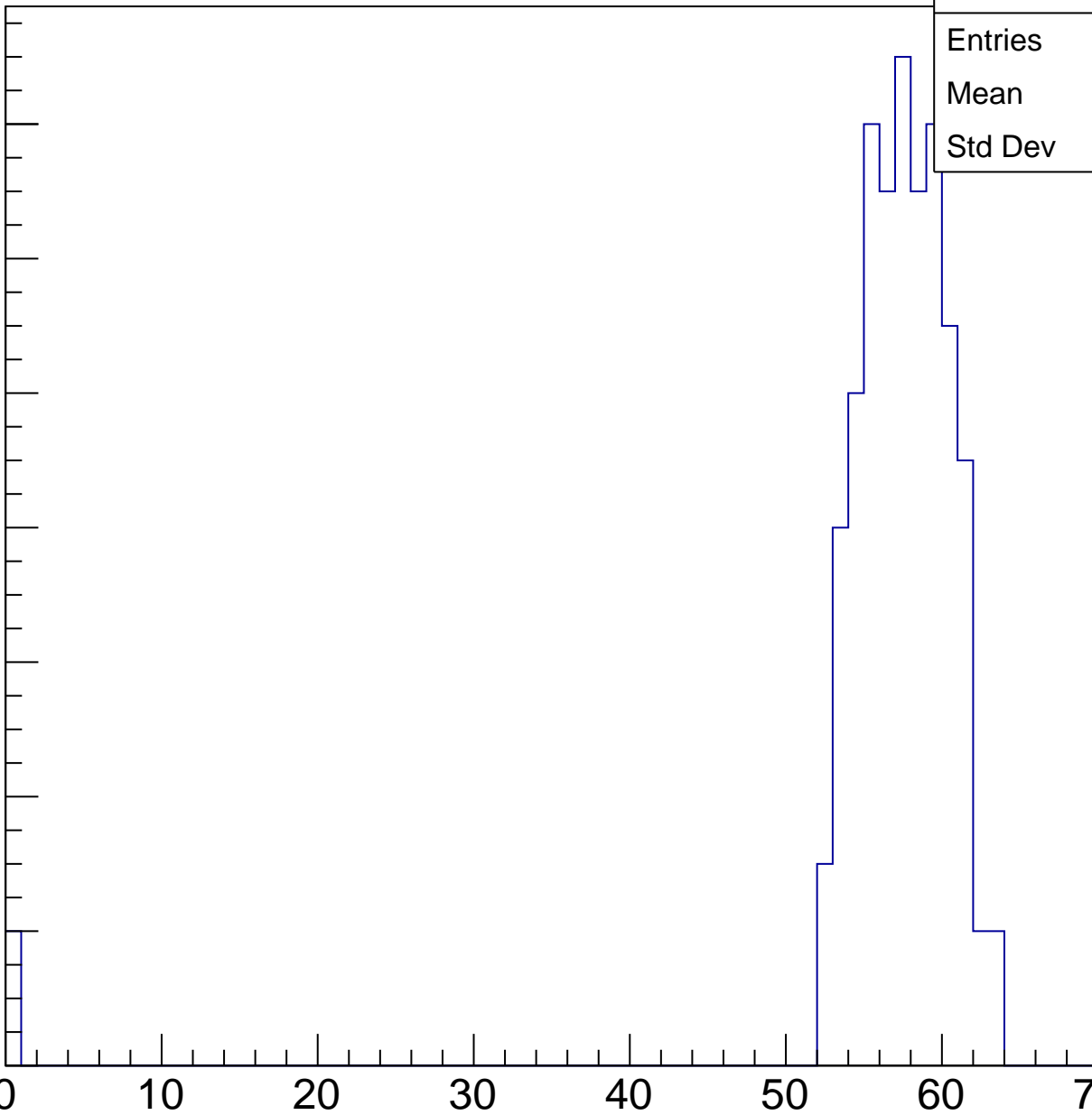
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	56.14
Std Dev	7.883

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch55, adc5

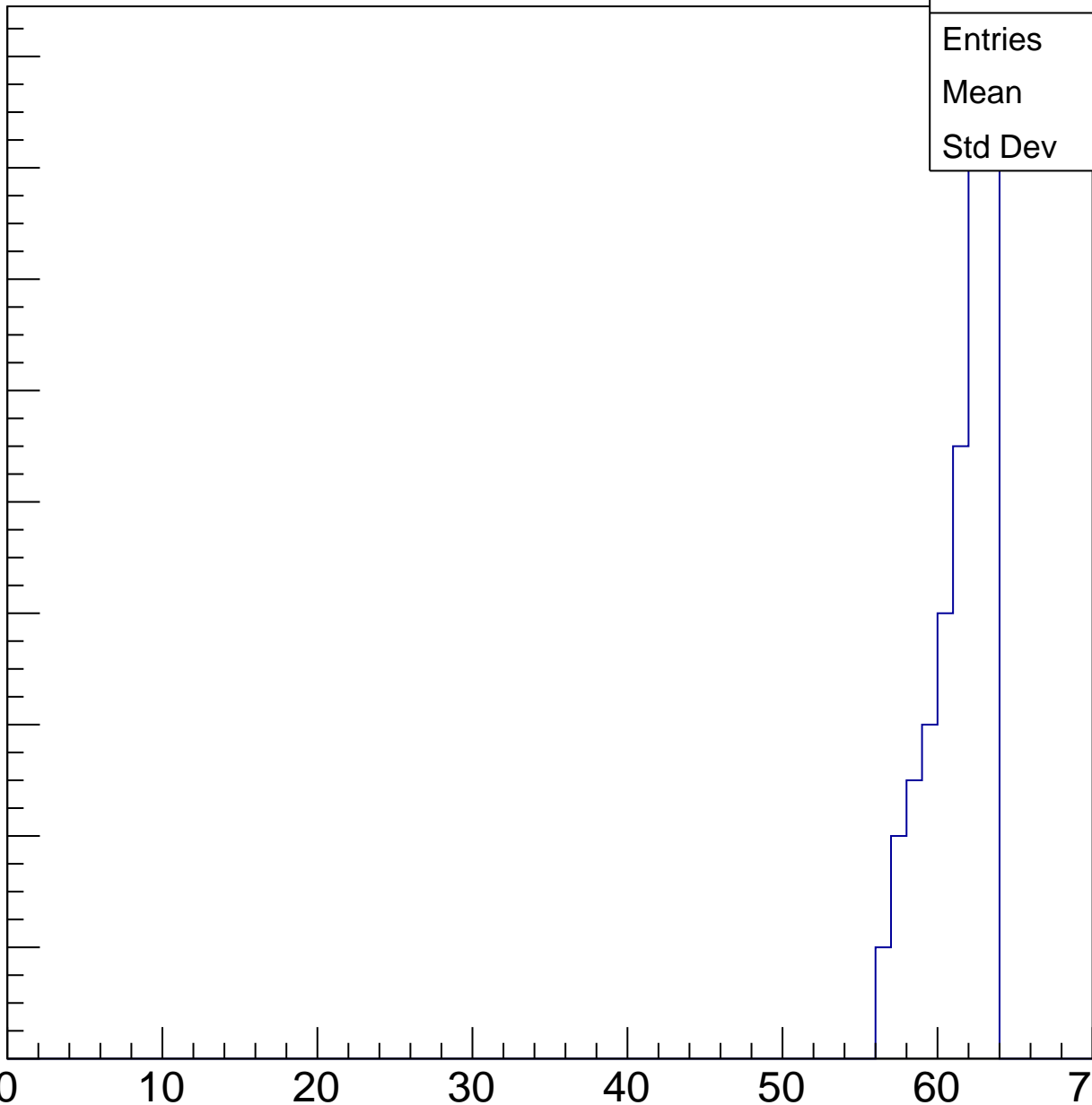
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	72
Mean	60.9
Std Dev	1.973

ampl



B1L001S, U21-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

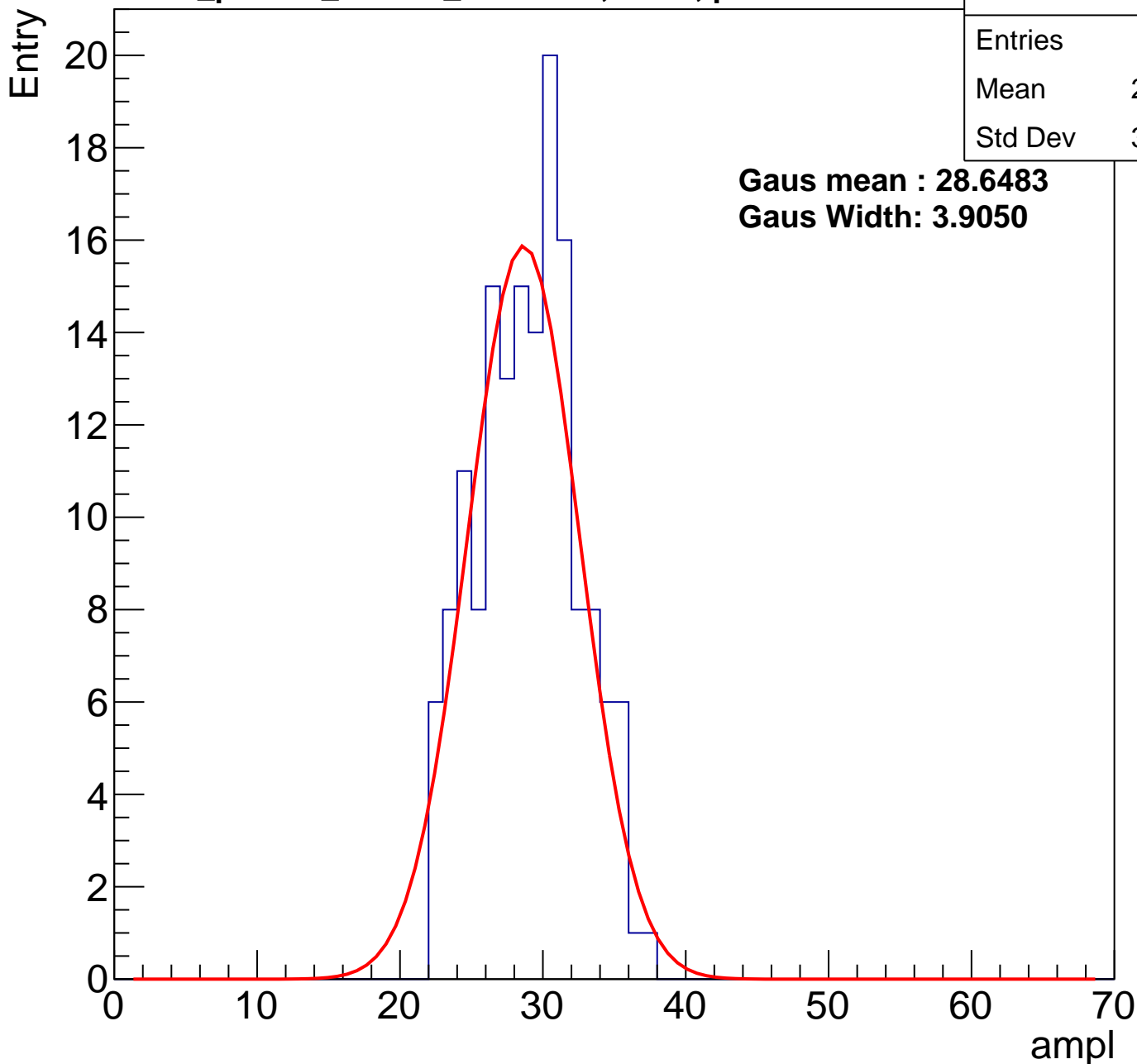
B1L001S, U21-ch56, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	28.53
Std Dev	3.504

Gaus mean : 28.6483

Gaus Width: 3.9050



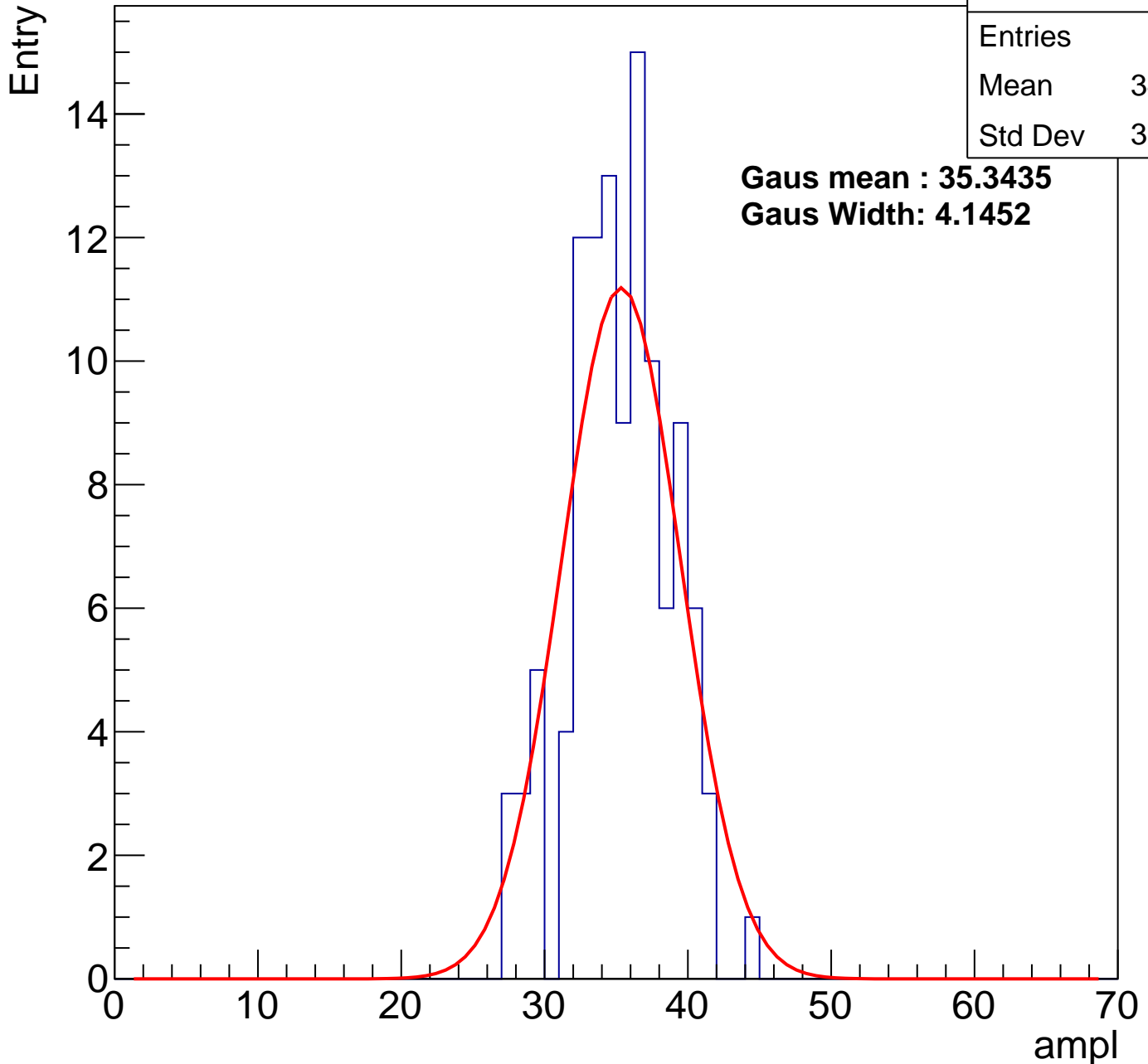
B1L001S, U21-ch56, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	34.84
Std Dev	3.476

Gaus mean : 35.3435

Gaus Width: 4.1452



B1L001S, U21-ch56, adc2

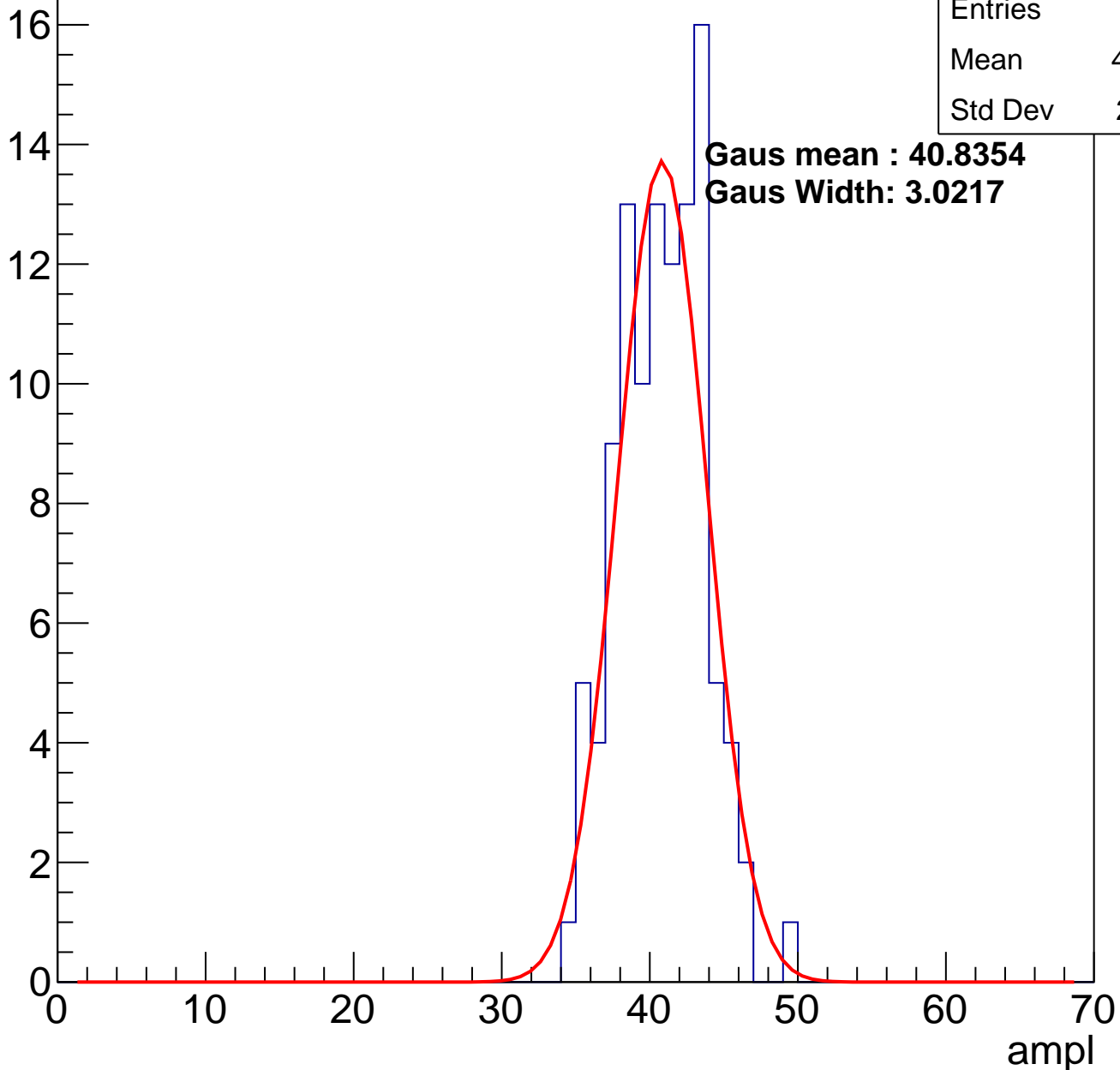
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	40.34
Std Dev	2.881

Gaus mean : 40.8354

Gaus Width: 3.0217

Entry

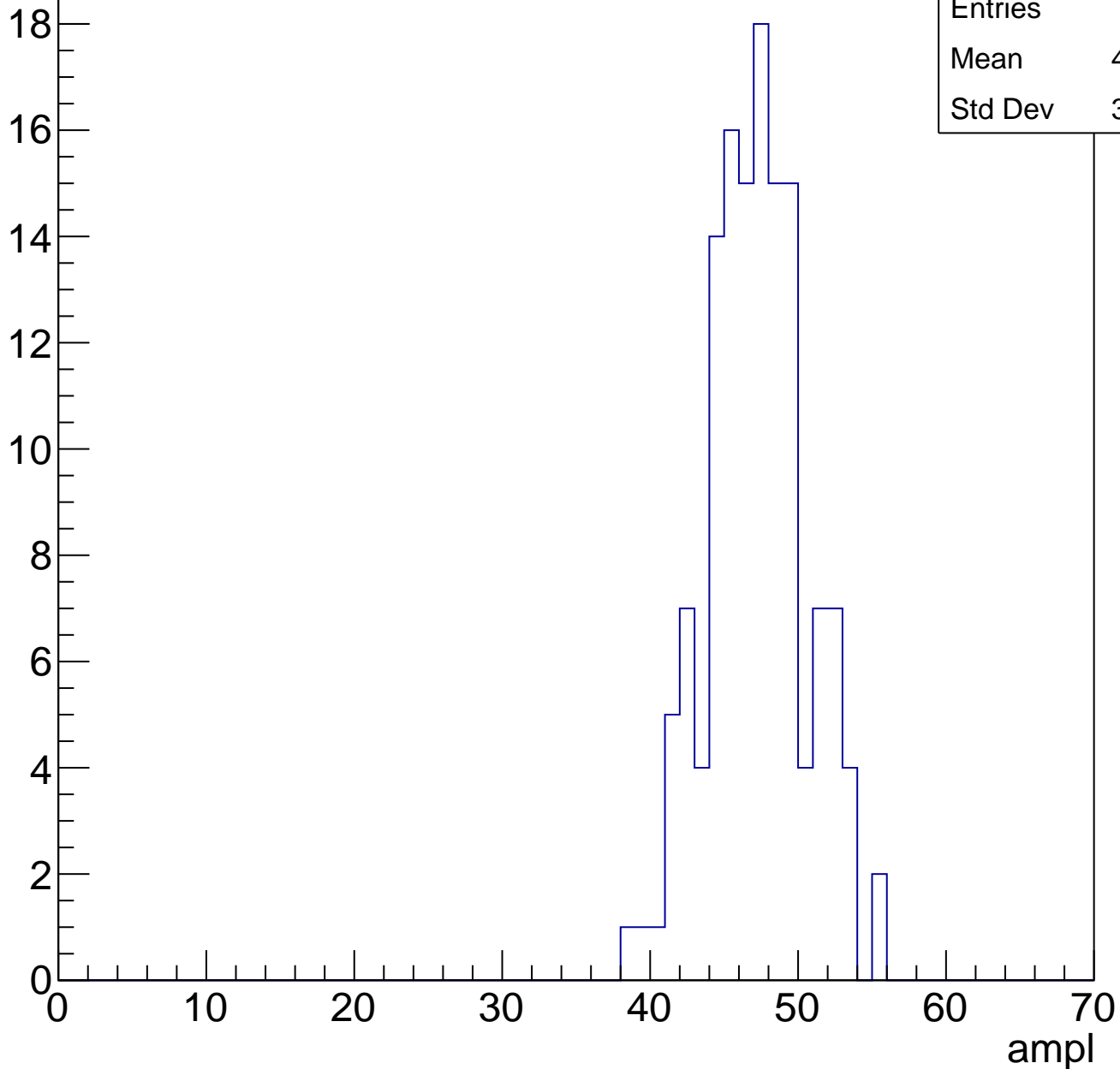


B1L001S, U21-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	46.75
Std Dev	3.298

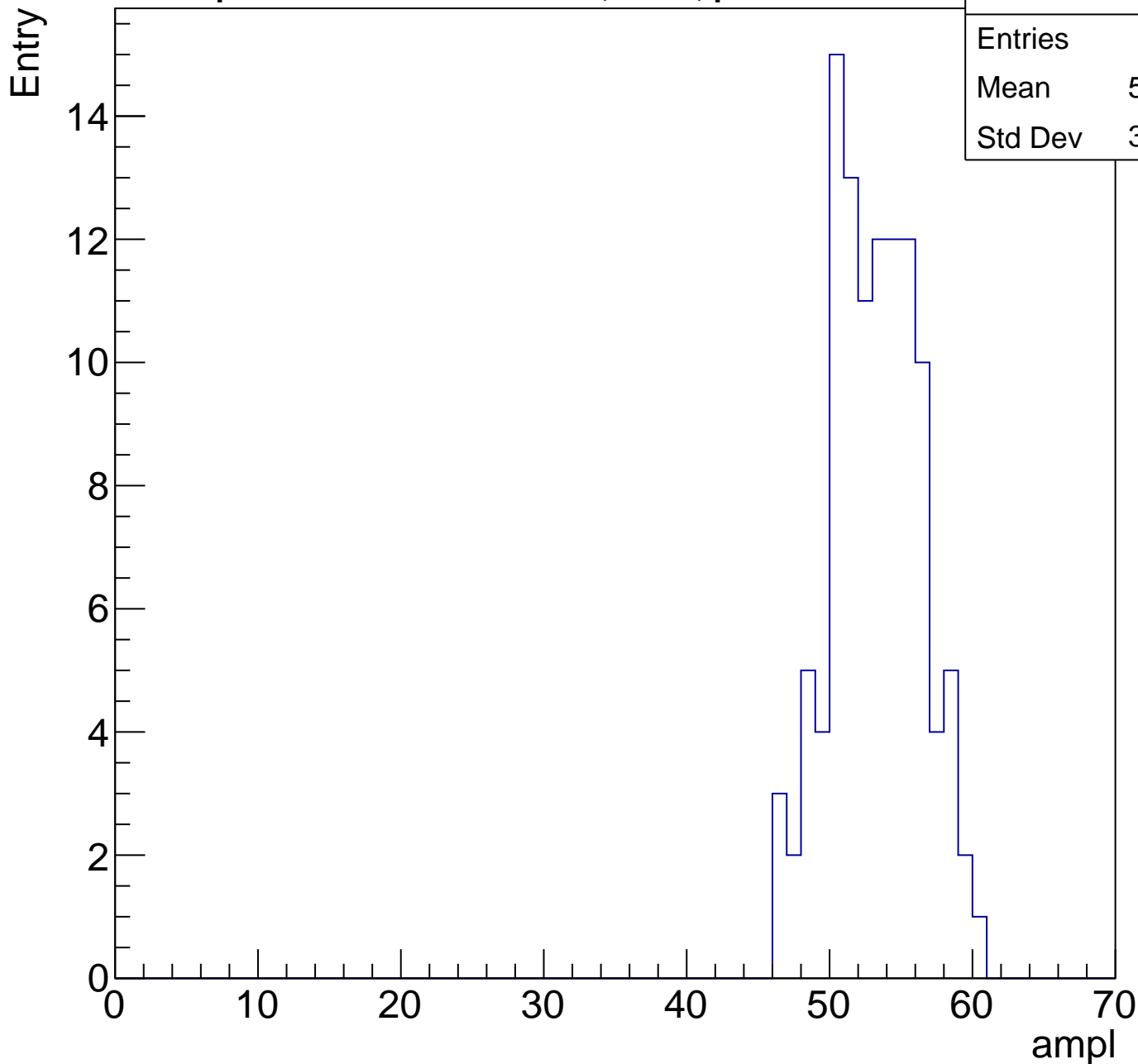
Entry



B1L001S, U21-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

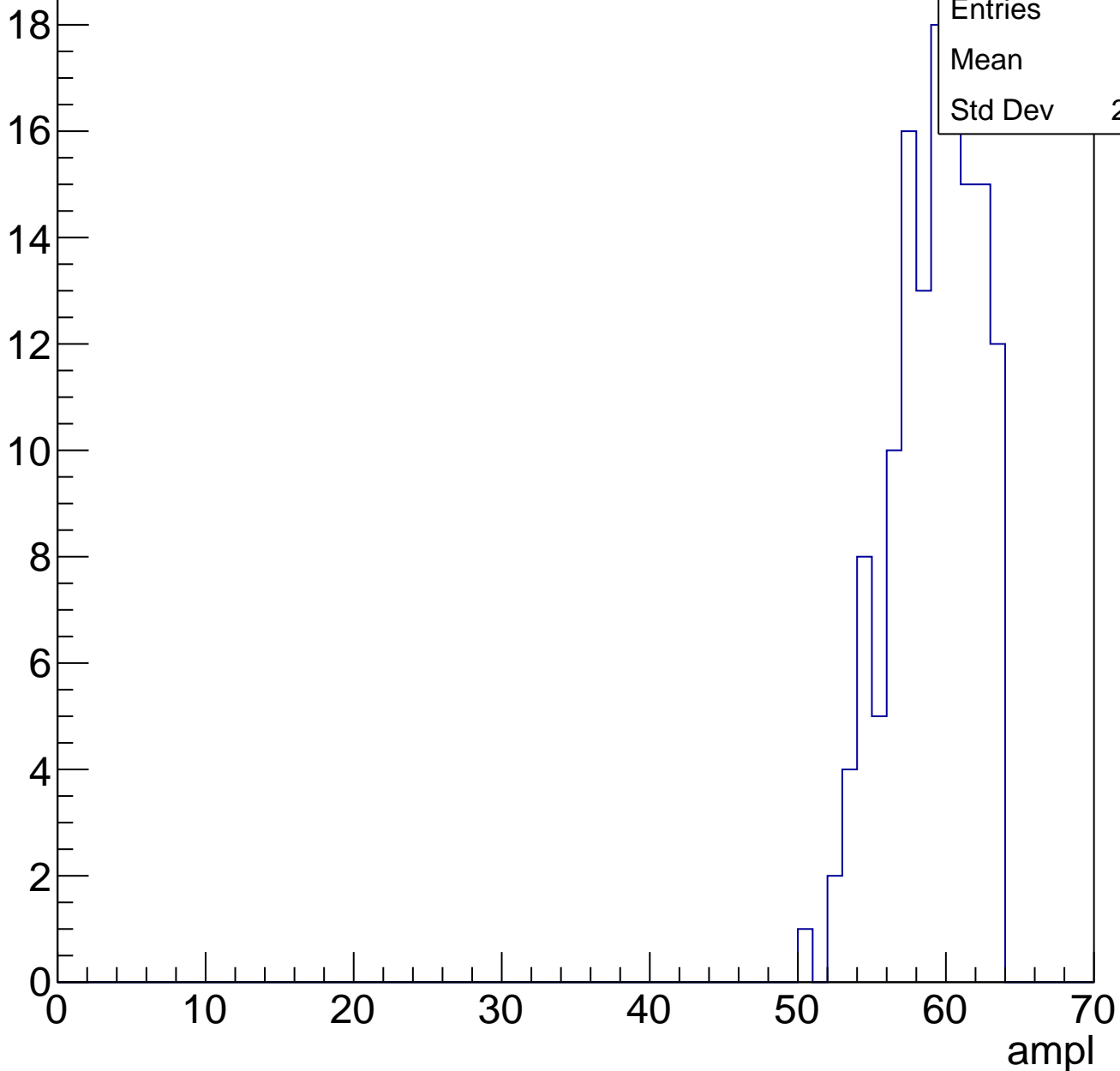
Entries	111
Mean	52.73
Std Dev	3.087



B1L001S, U21-ch56, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

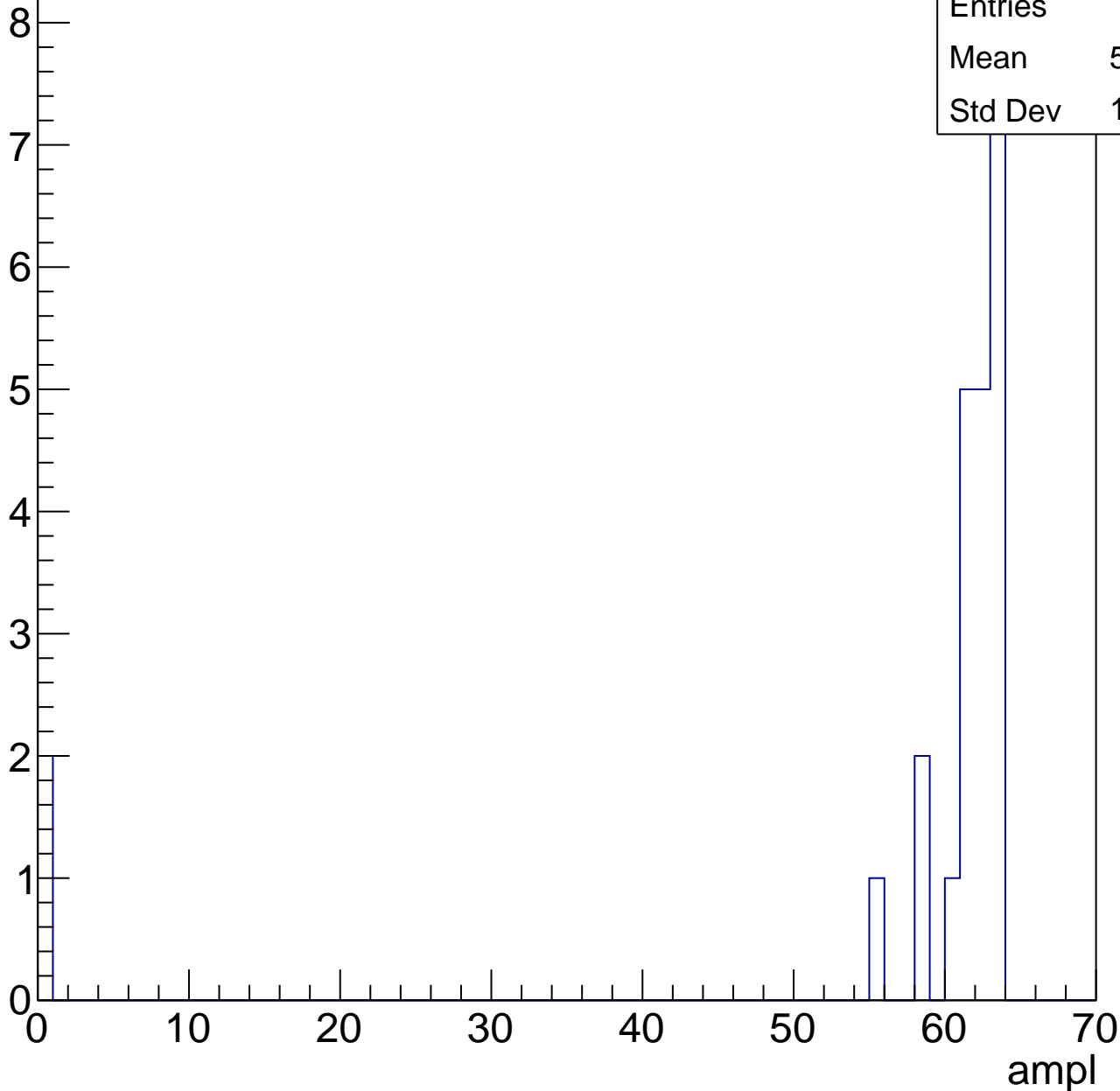


B1L001S, U21-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	24
Mean	56.25
Std Dev	17.07



B1L001S, U21-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U21-ch57, adc0

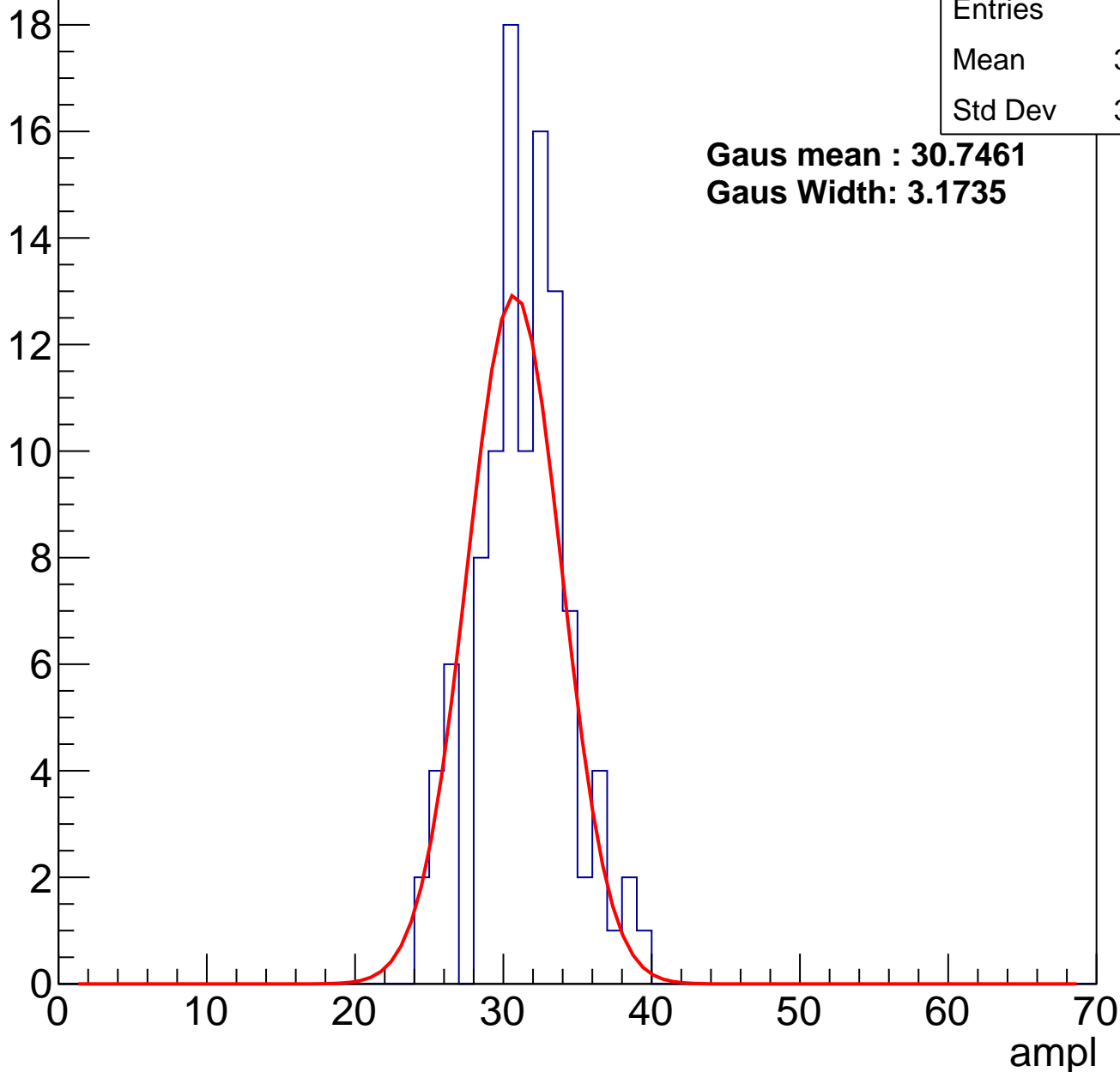
calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	30.89
Std Dev	3.073

Gaus mean : 30.7461

Gaus Width: 3.1735

Entry



B1L001S, U21-ch57, adc1

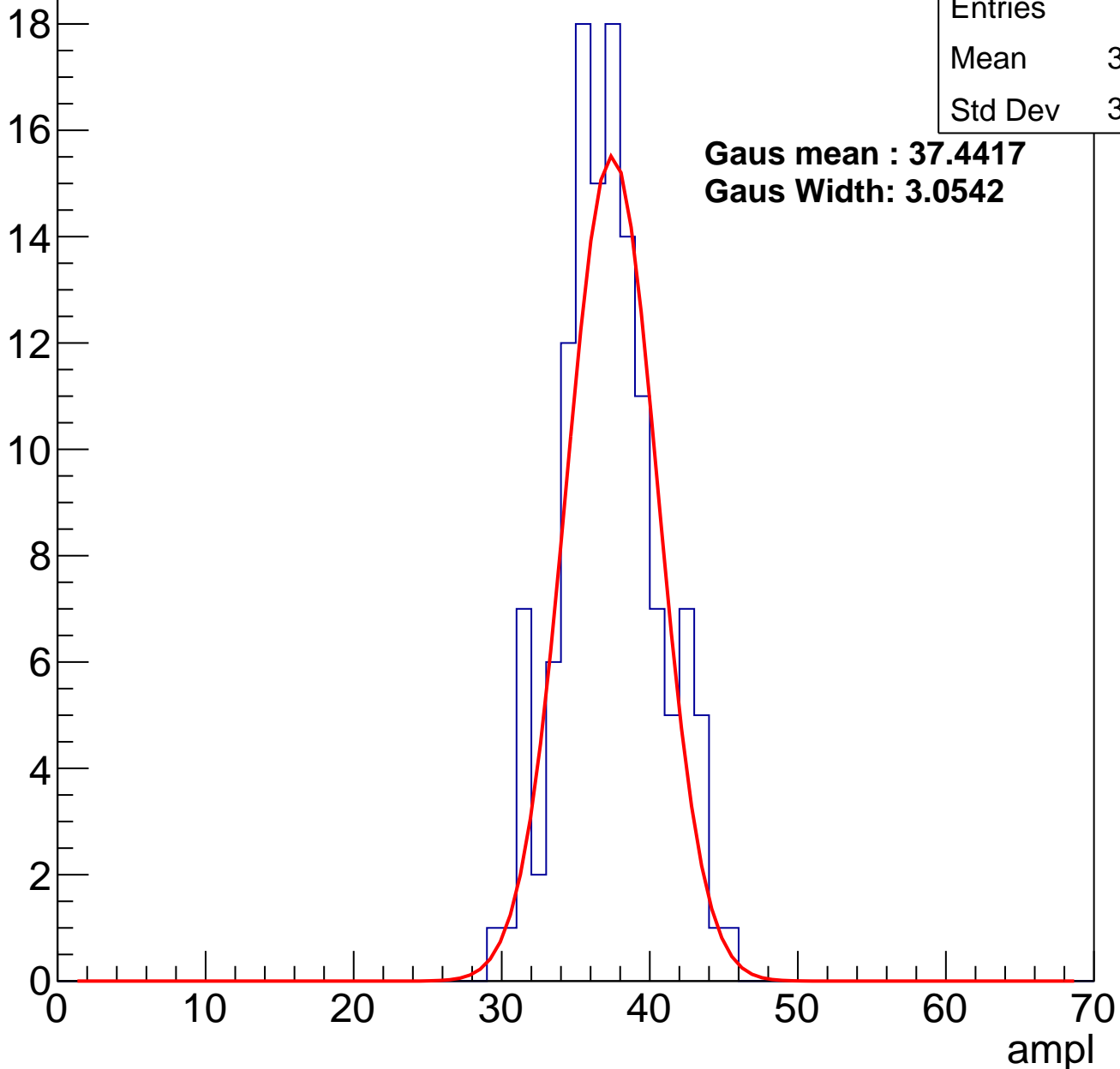
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.84
Std Dev	3.236

Gaus mean : 37.4417

Gaus Width: 3.0542

Entry



B1L001S, U21-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

117

Mean

42.94

Std Dev

2.956

Gaus mean : 43.5596

Gaus Width: 3.0876

0

10

20

30

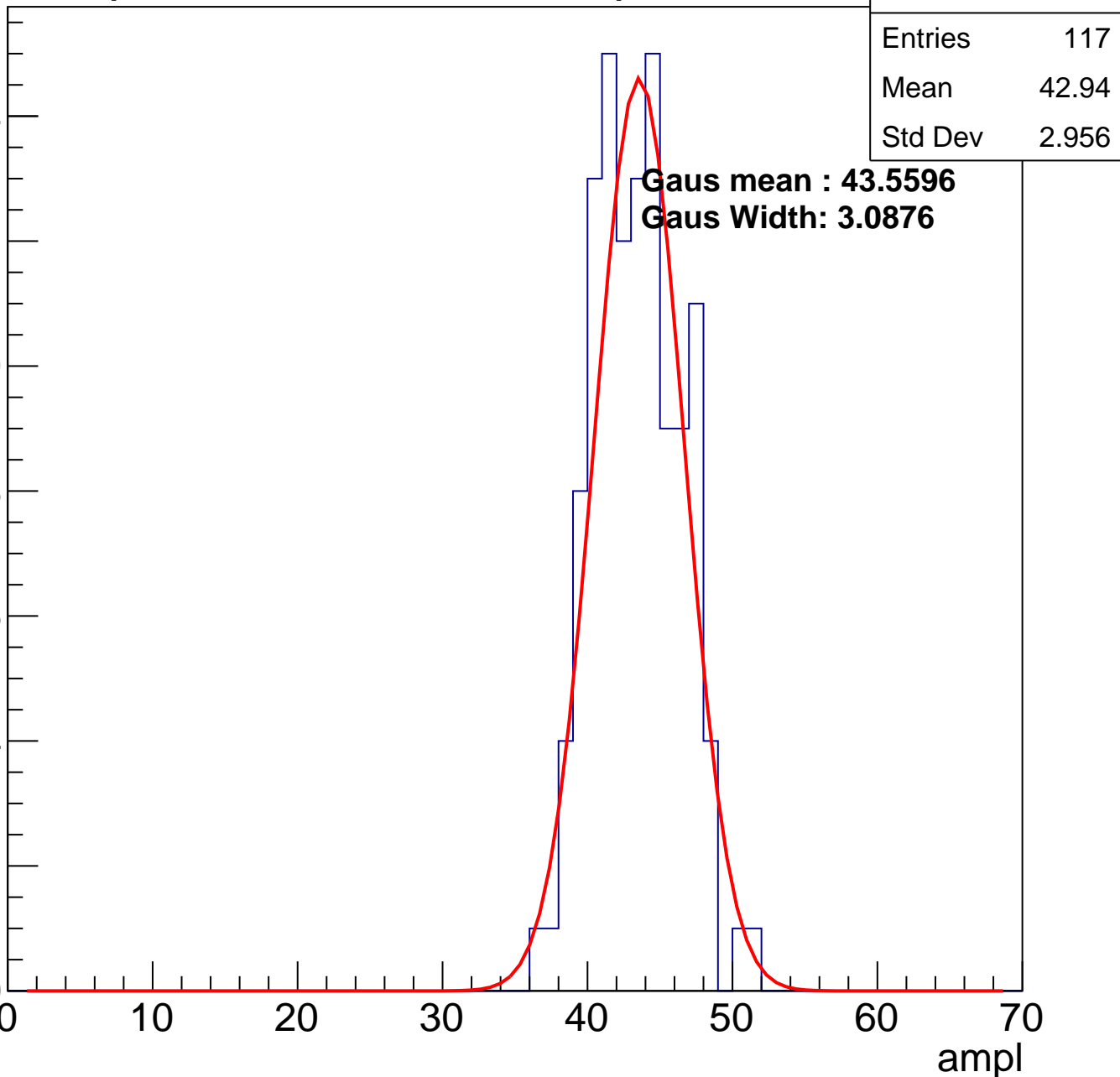
40

50

60

70

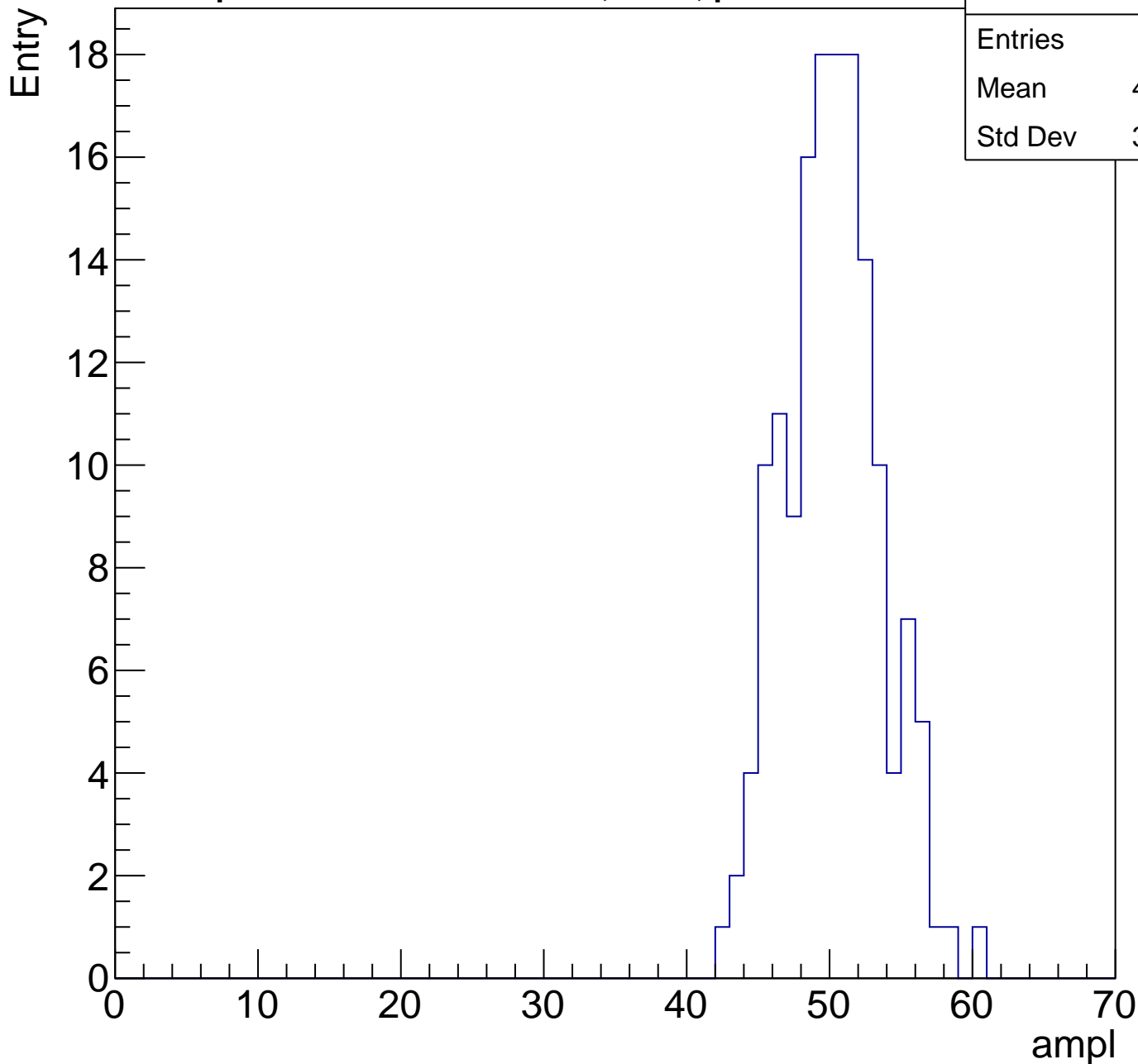
ampl



B1L001S, U21-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	49.77
Std Dev	3.349



B1L001S, U21-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

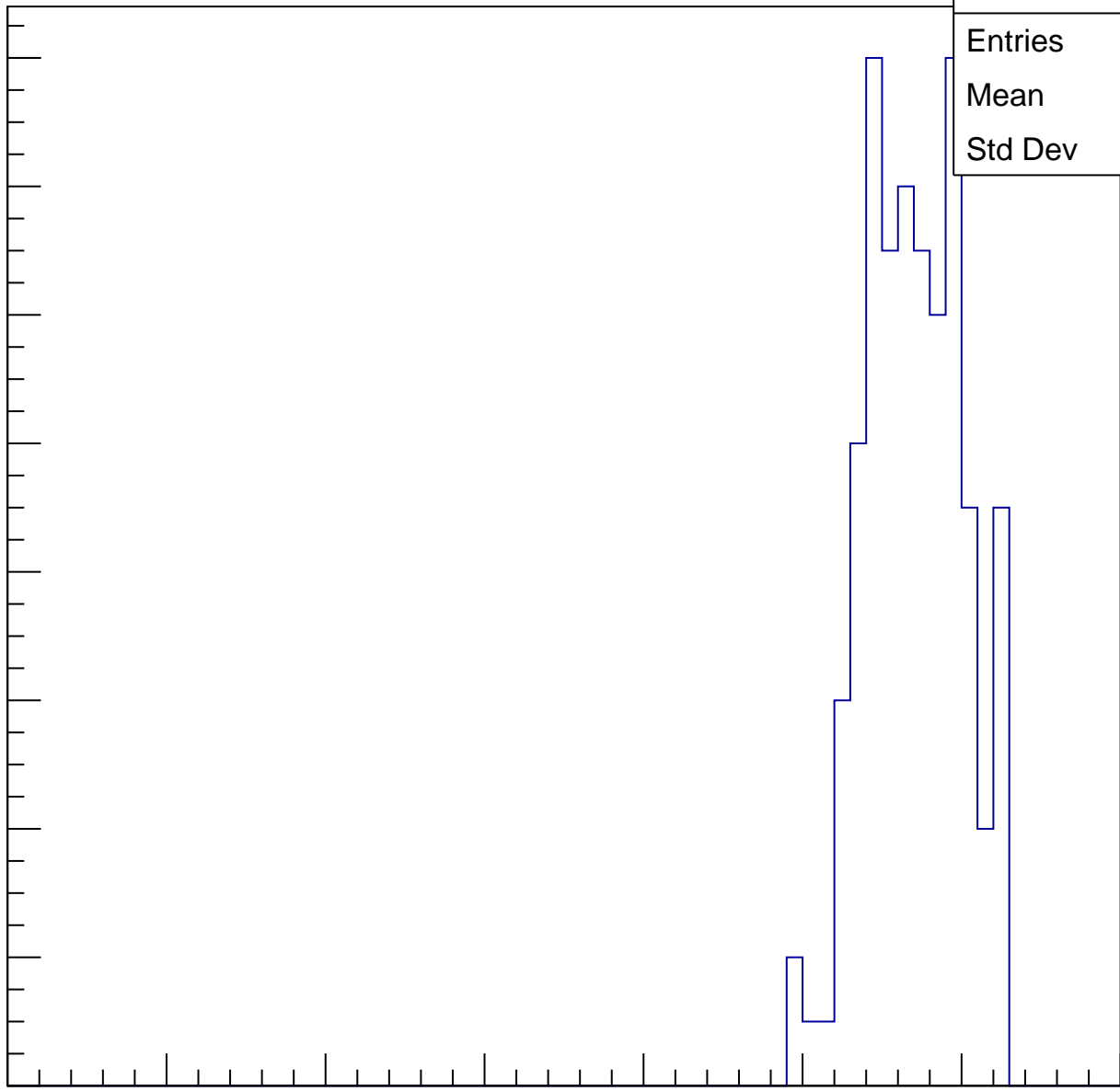
Entries	126
Mean	56.56
Std Dev	3.028

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch57, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	73
Mean	59.95
Std Dev	7.346

Entry

20
18
16
14
12
10
8
6
4
2
0

0

10

20

30

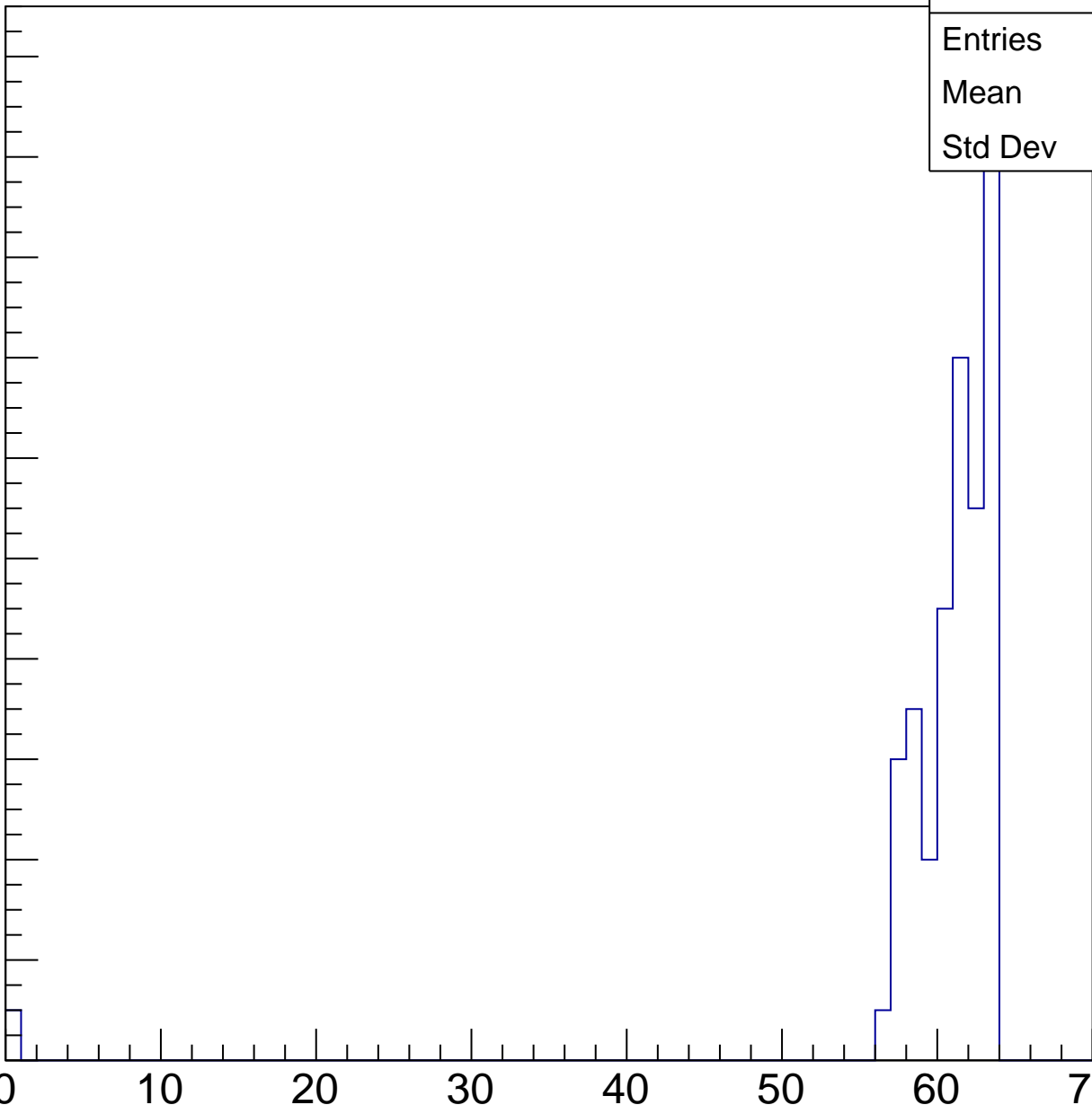
40

50

60

70

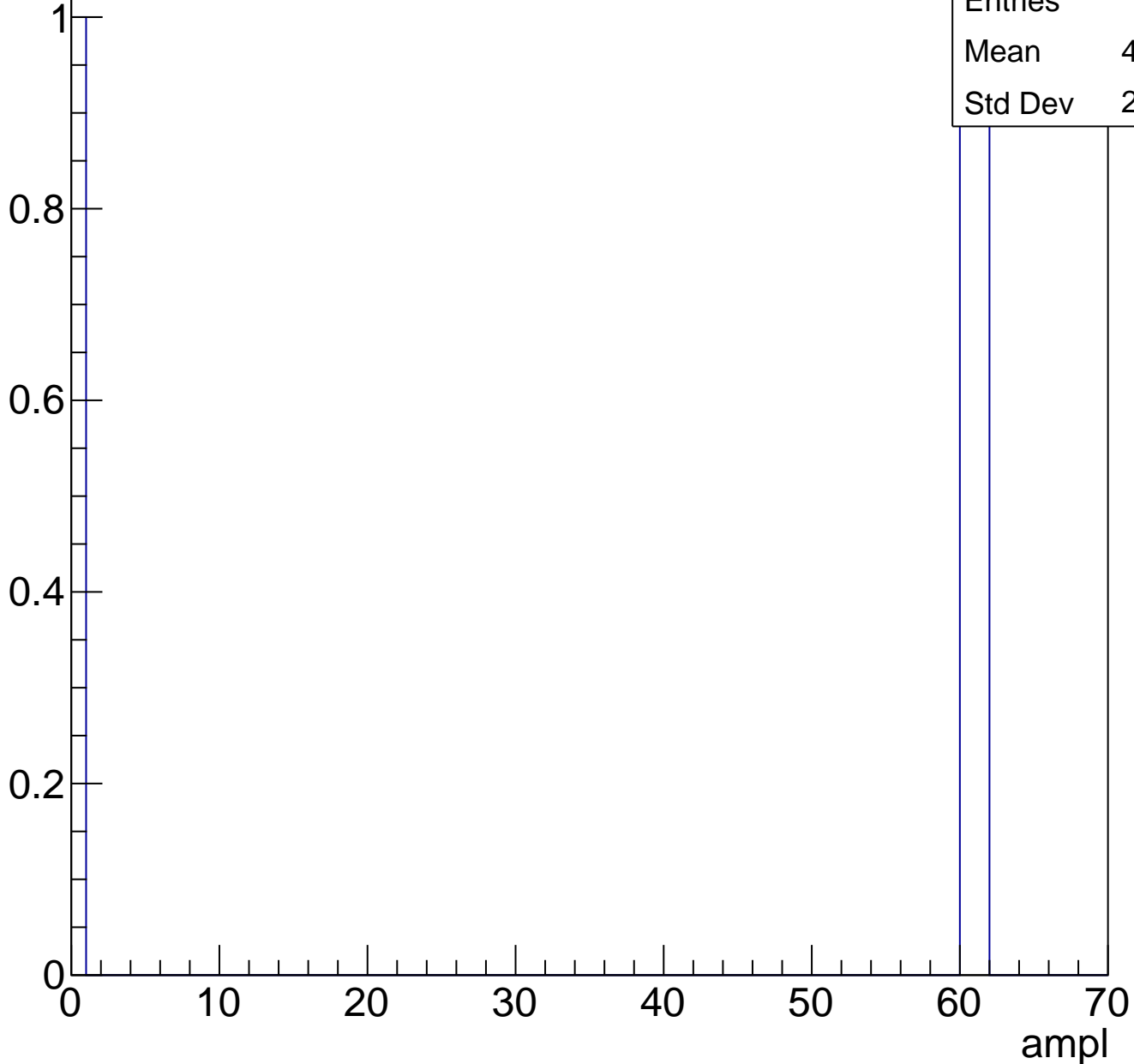
ampl



B1L001S, U21-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

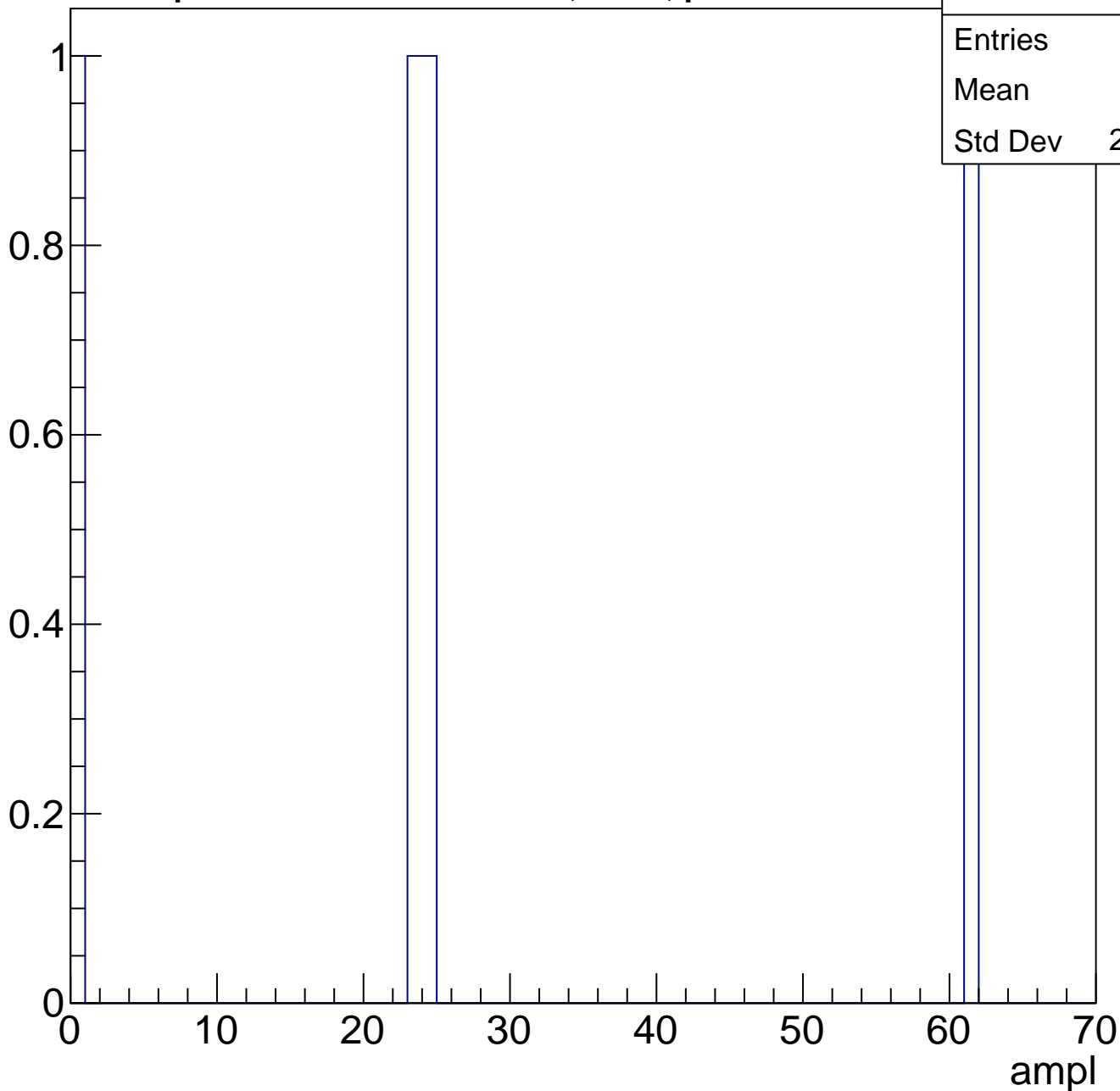
Entry



B1L001S, U21-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	27
Std Dev	21.85

B1L001S, U21-ch58, adc0

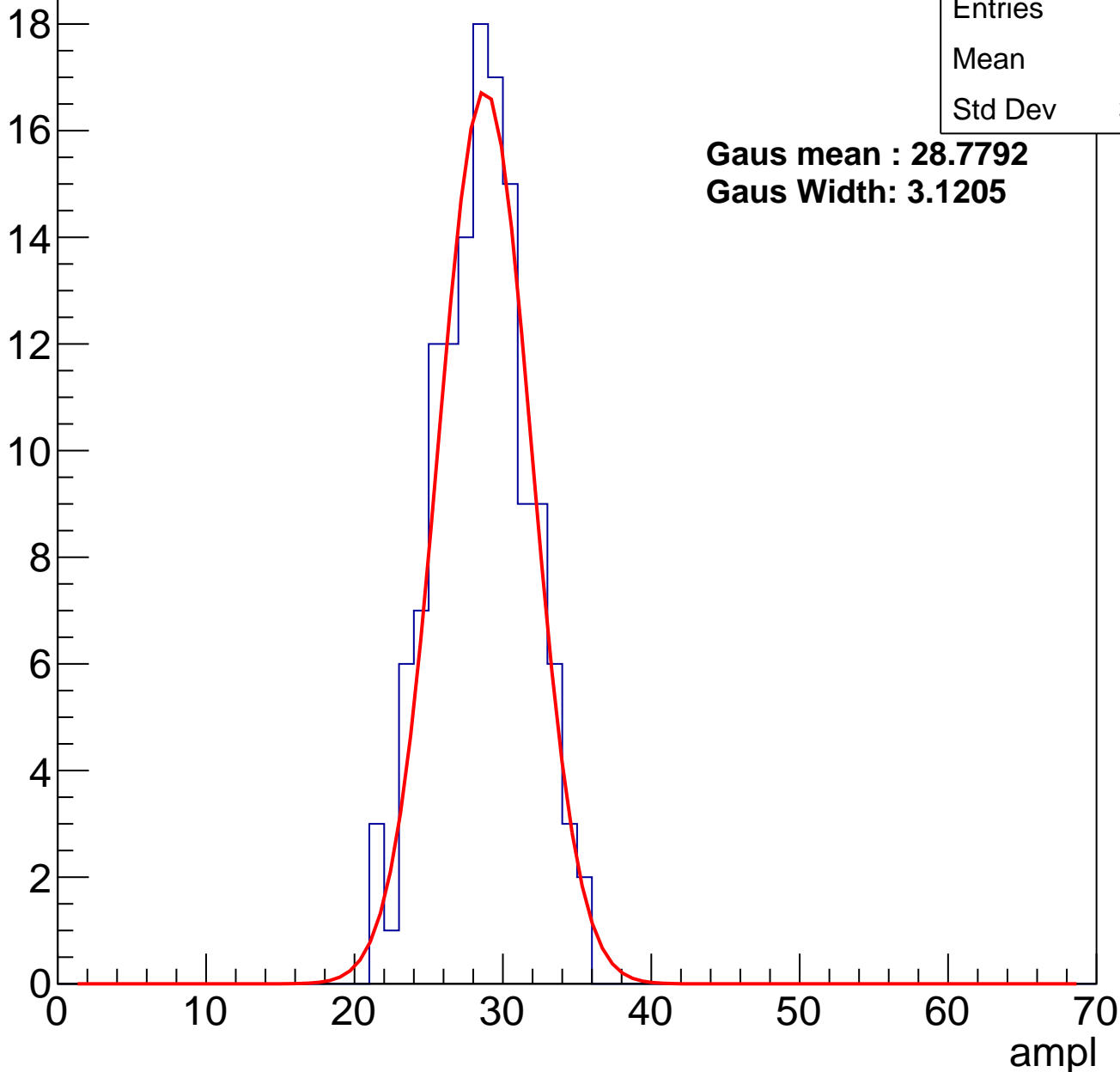
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.1
Std Dev	3.081

Gaus mean : 28.7792

Gaus Width: 3.1205

Entry



B1L001S, U21-ch58, adc1

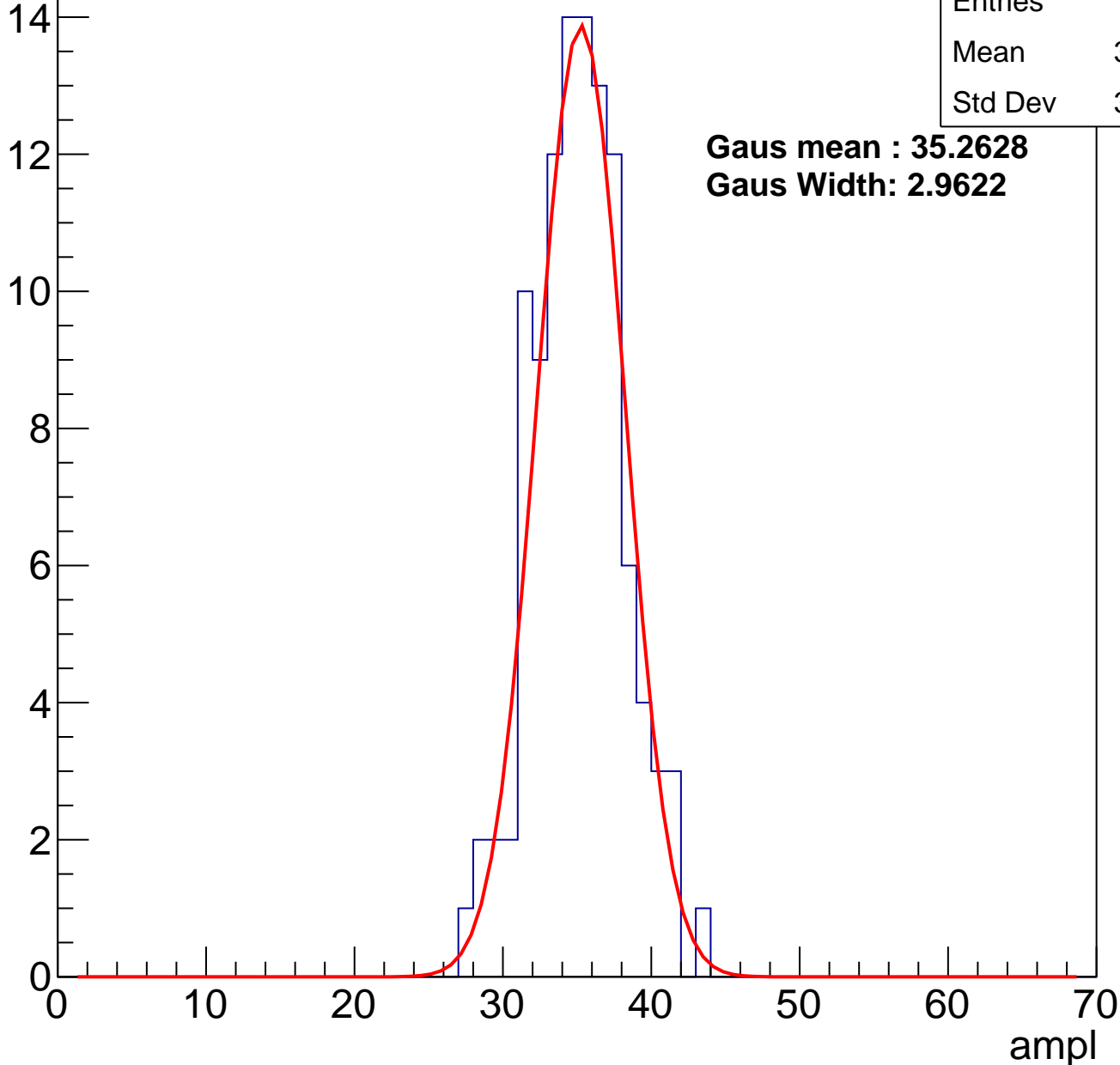
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	34.66
Std Dev	3.043

Gaus mean : 35.2628

Gaus Width: 2.9622

Entry



B1L001S, U21-ch58, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

164

Mean

41.24

Std Dev

3.781

Gaus mean : 41.6825

Gaus Width: 3.9344

0

10

20

30

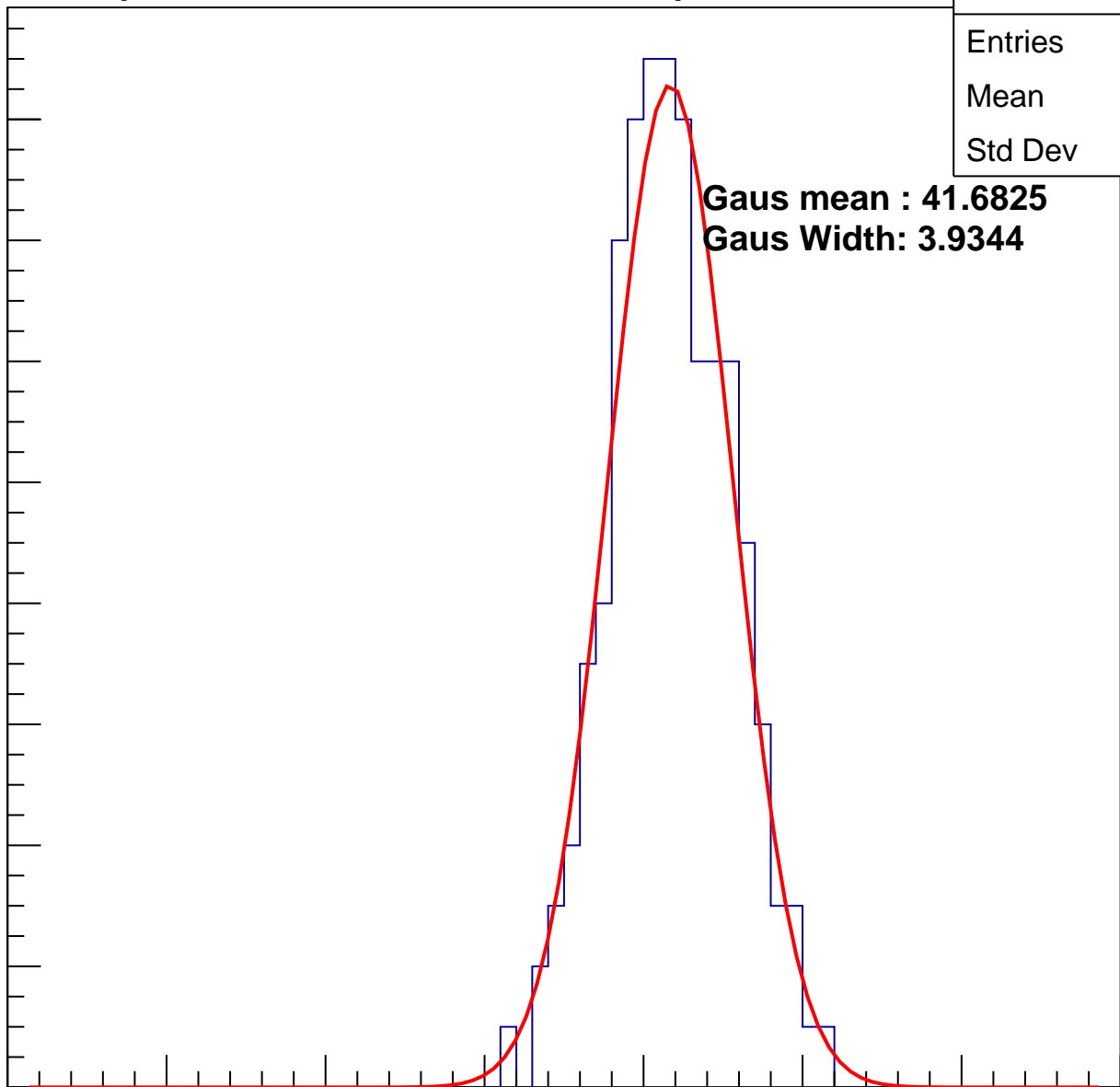
40

50

60

70

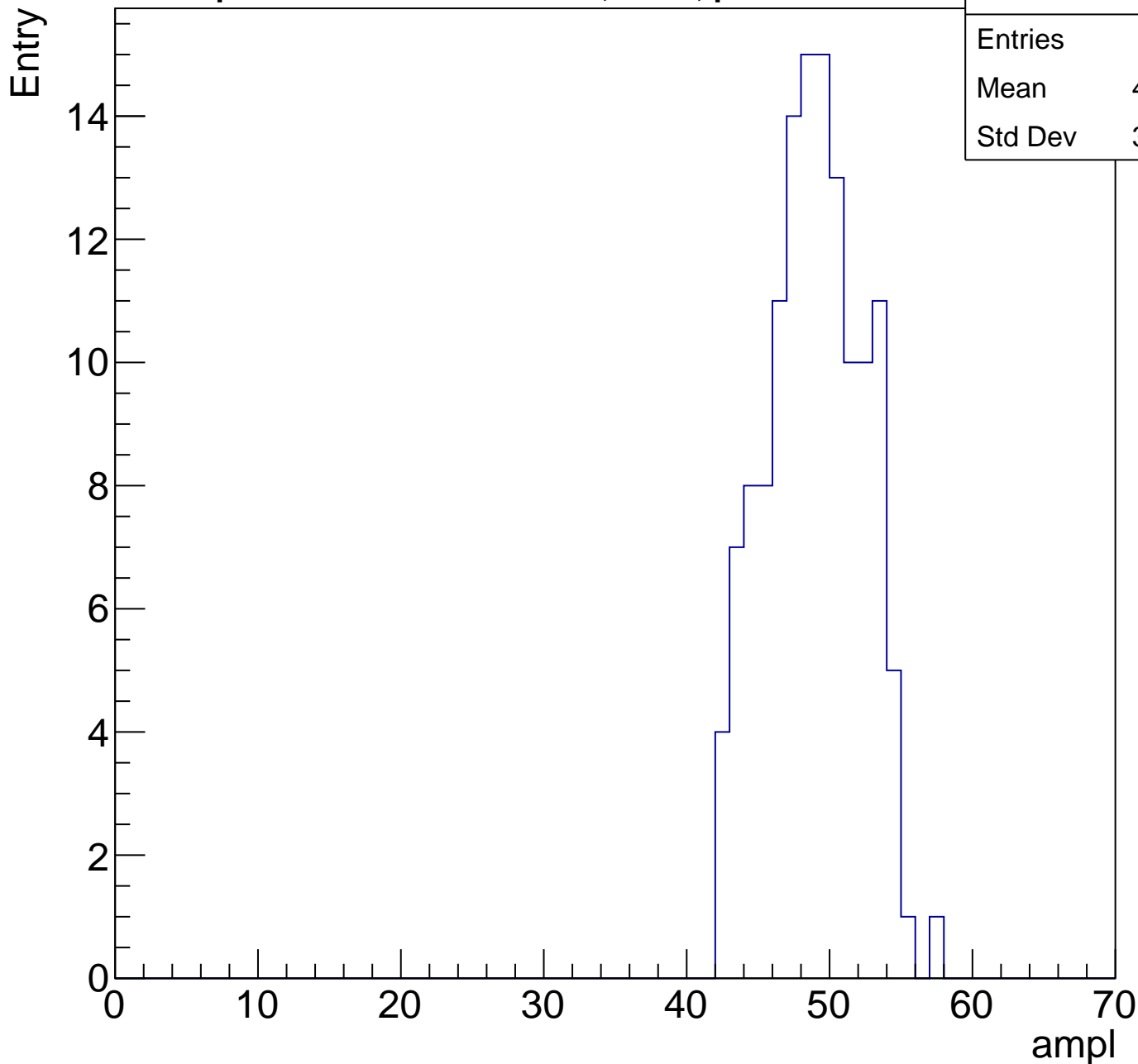
ampl



B1L001S, U21-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	48.46
Std Dev	3.303



B1L001S, U21-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	54.93
Std Dev	3.133

Entry

14
12
10
8
6
4
2
0

0

10

20

30

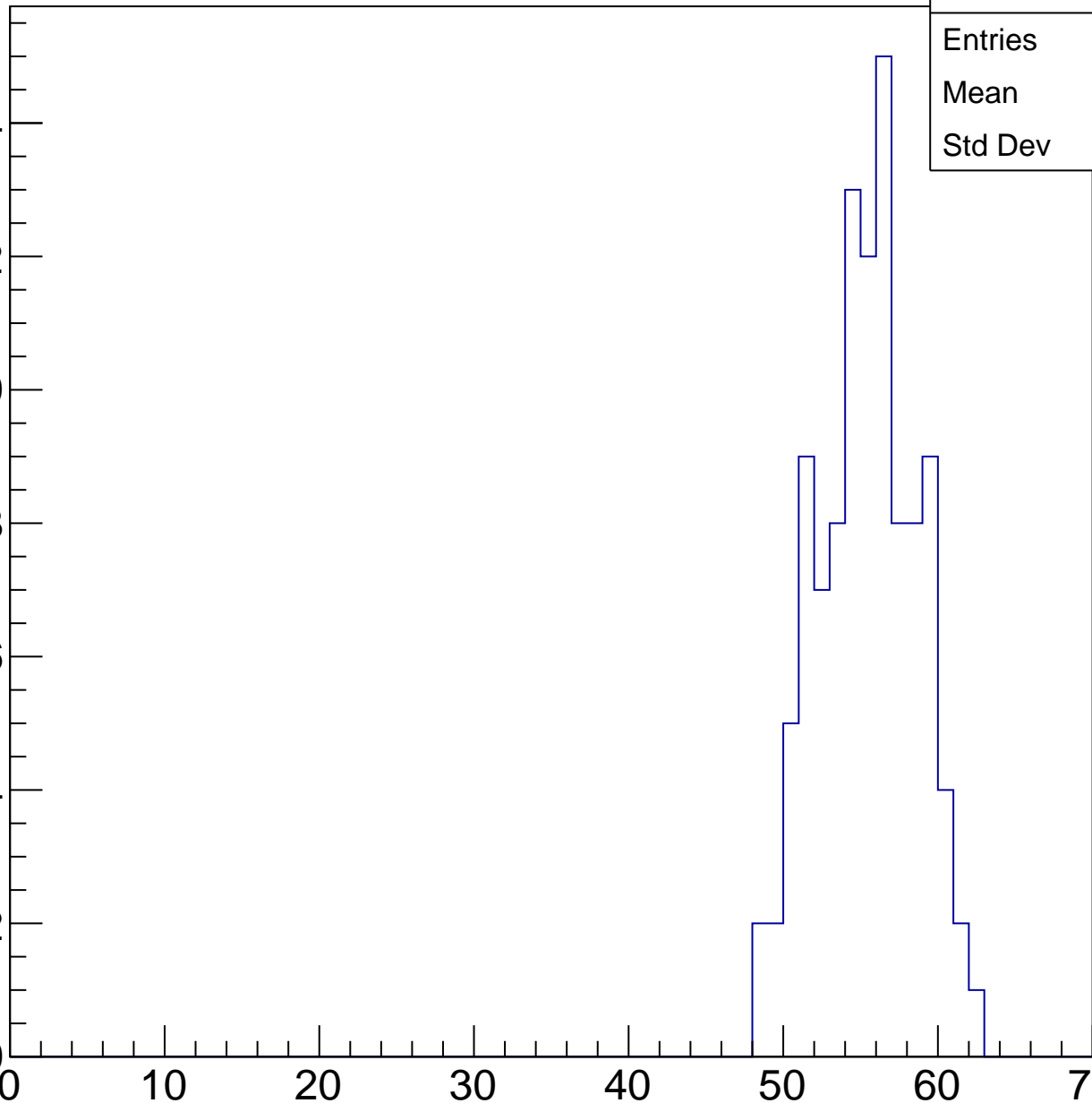
40

50

60

70

ampl



B1L001S, U21-ch58, adc5

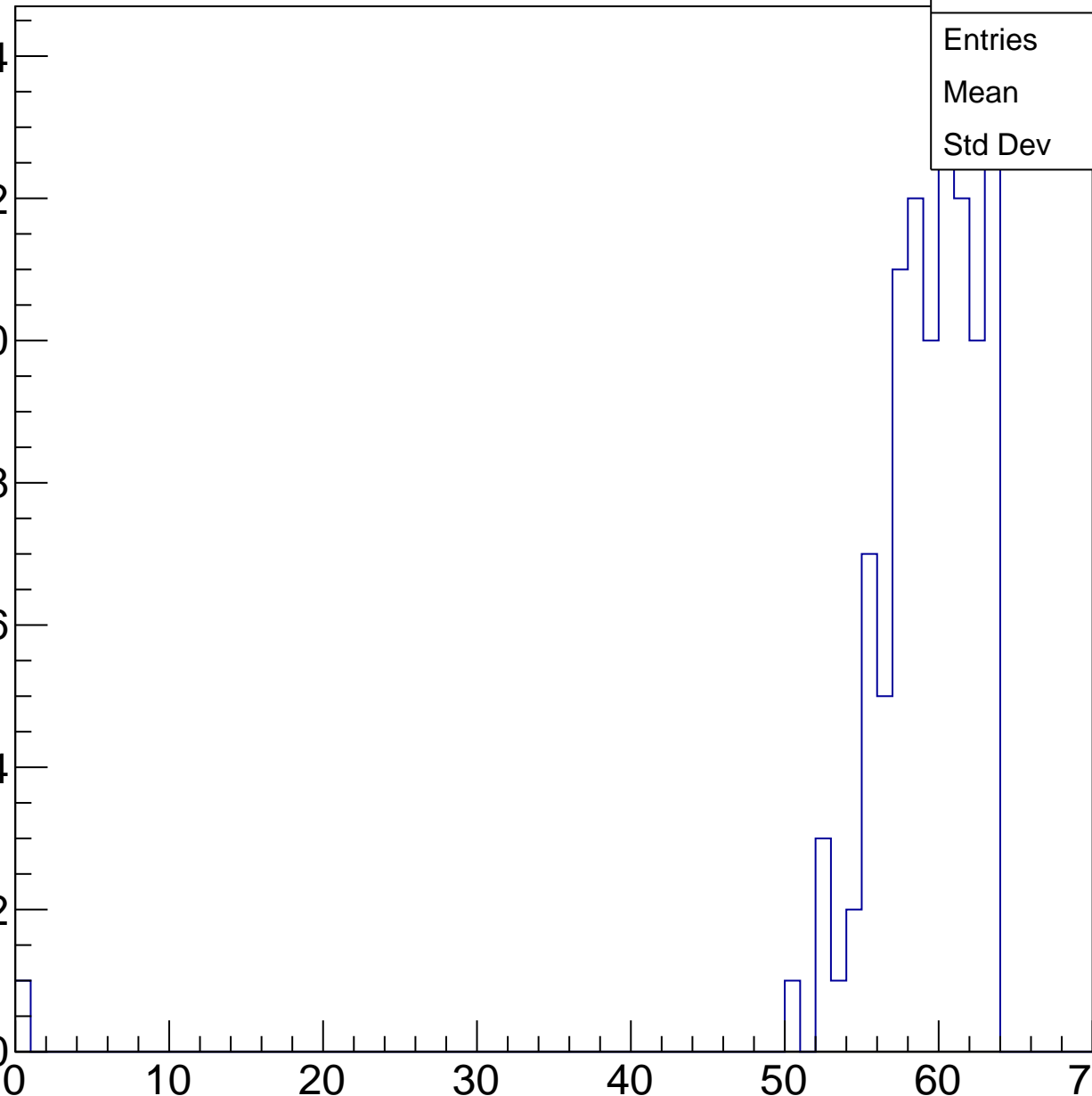
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	58.39
Std Dev	6.516

ampl

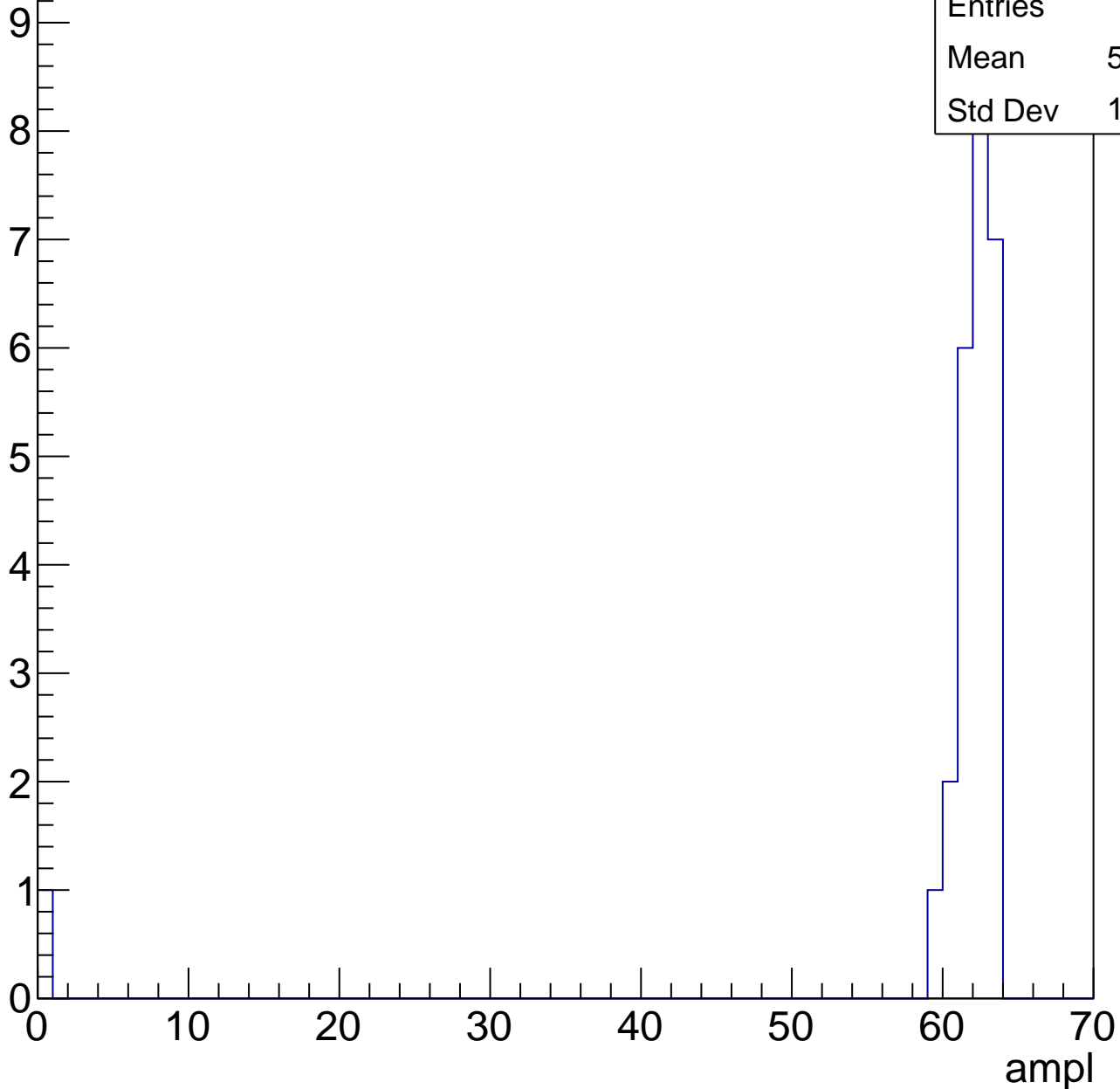


B1L001S, U21-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	59.38
Std Dev	11.92



B1L001S, U21-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

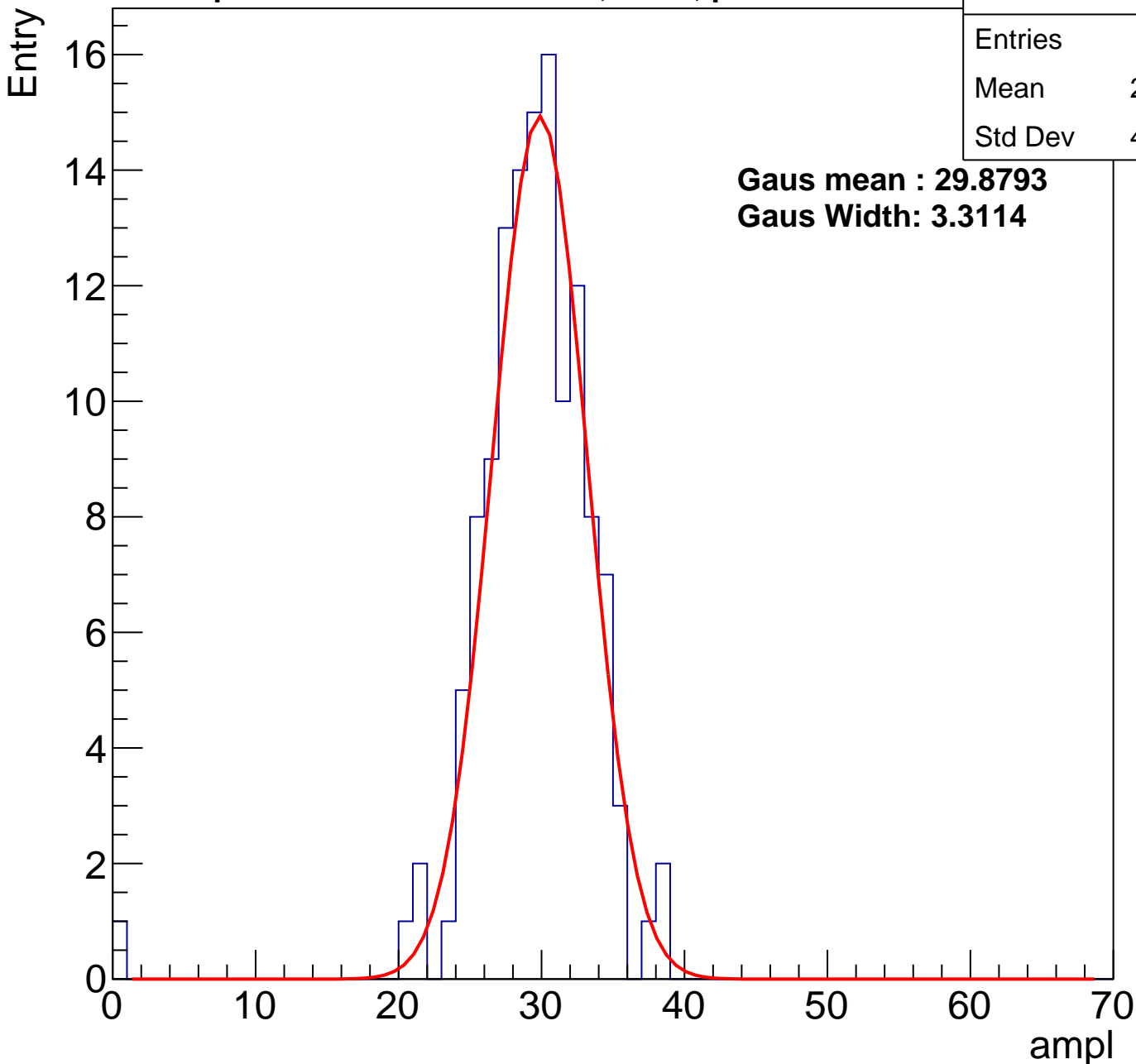
B1L001S, U21-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	28.99
Std Dev	4.227

Gaus mean : 29.8793

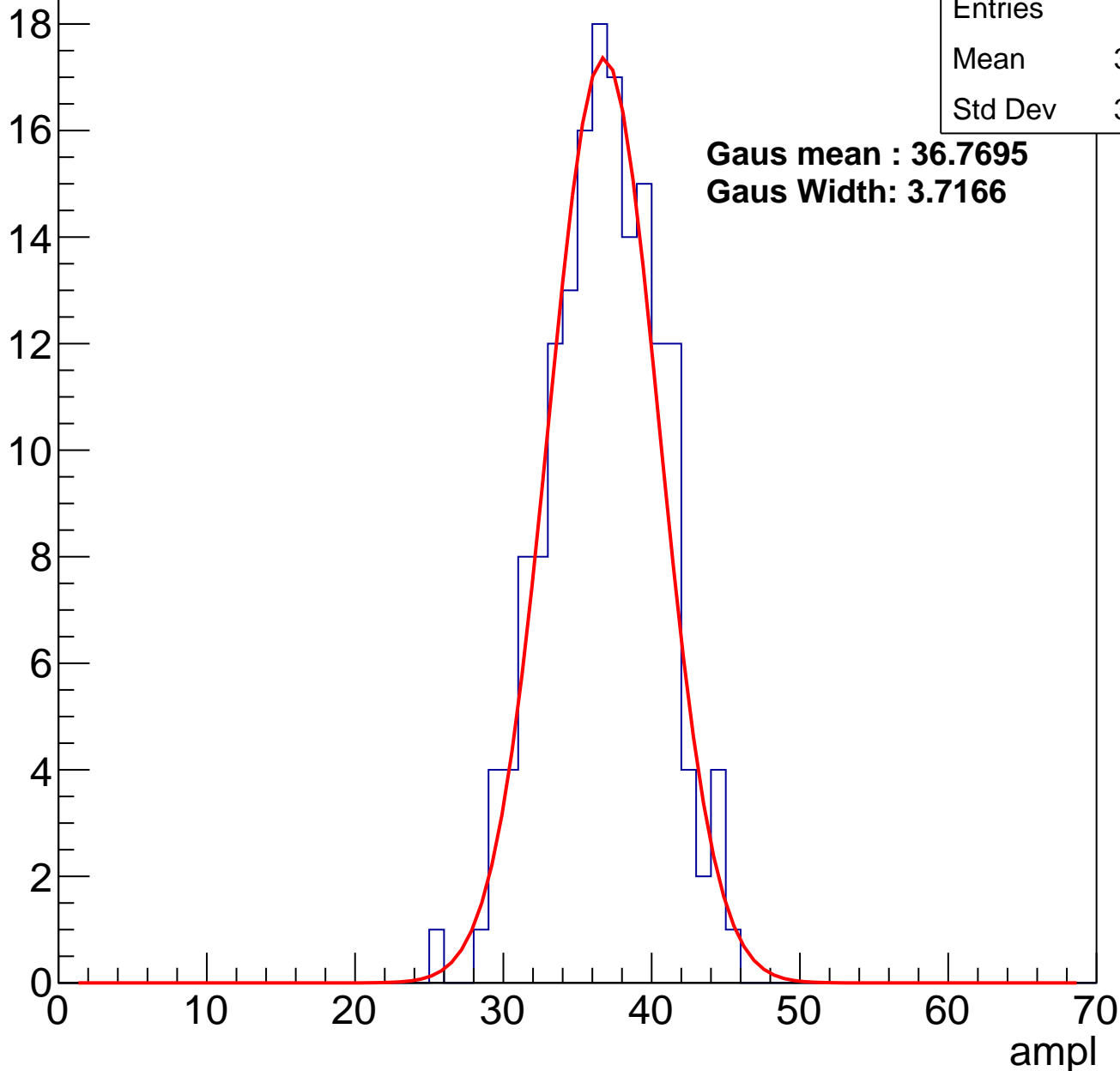
Gaus Width: 3.3114



B1L001S, U21-ch59, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

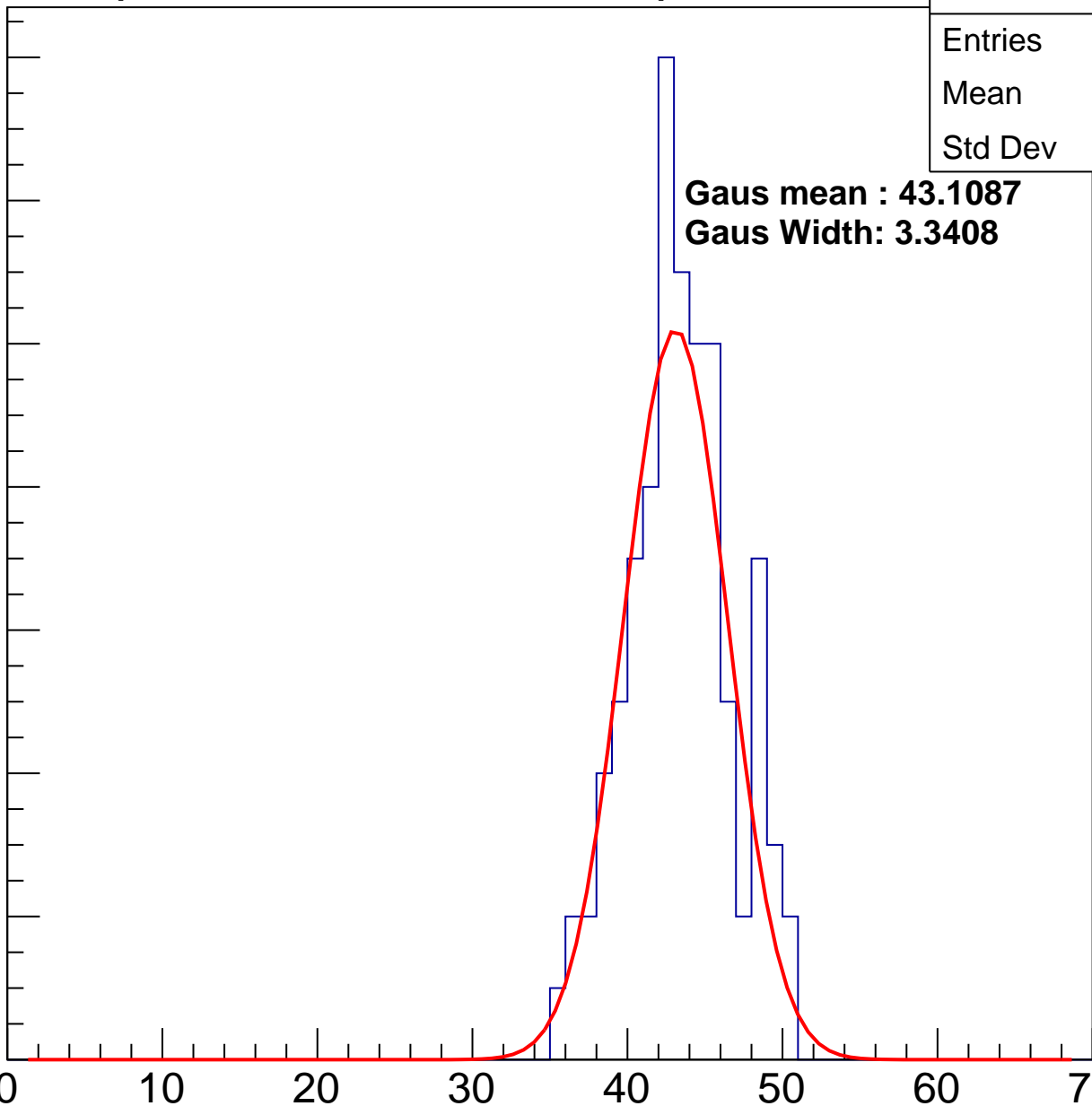
Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	42.95
Std Dev	3.329

Gaus mean : 43.1087
Gaus Width: 3.3408

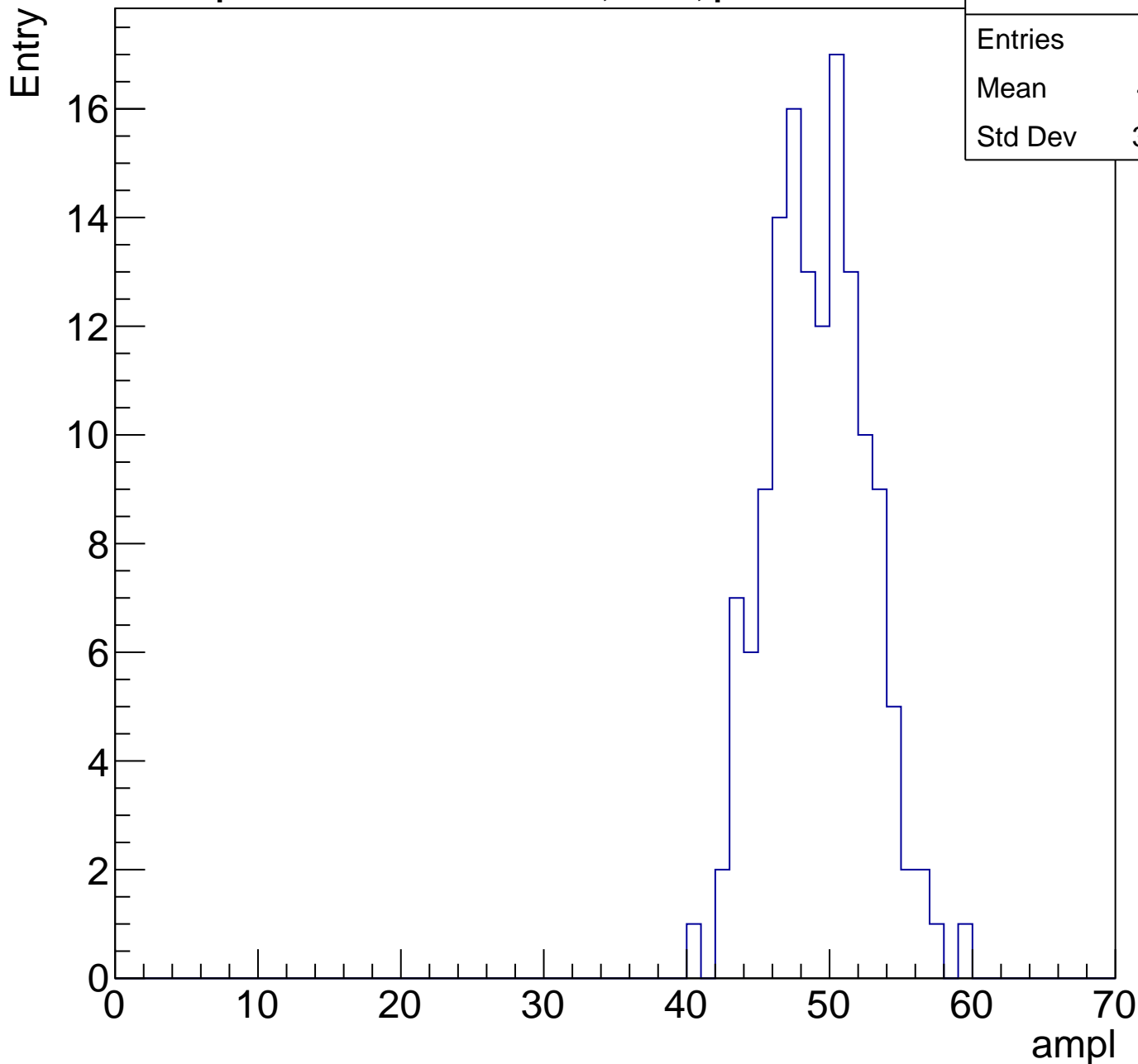
0 10 20 30 40 50 60 70
ampl



B1L001S, U21-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	48.71
Std Dev	3.463



B1L001S, U21-ch59, adc4

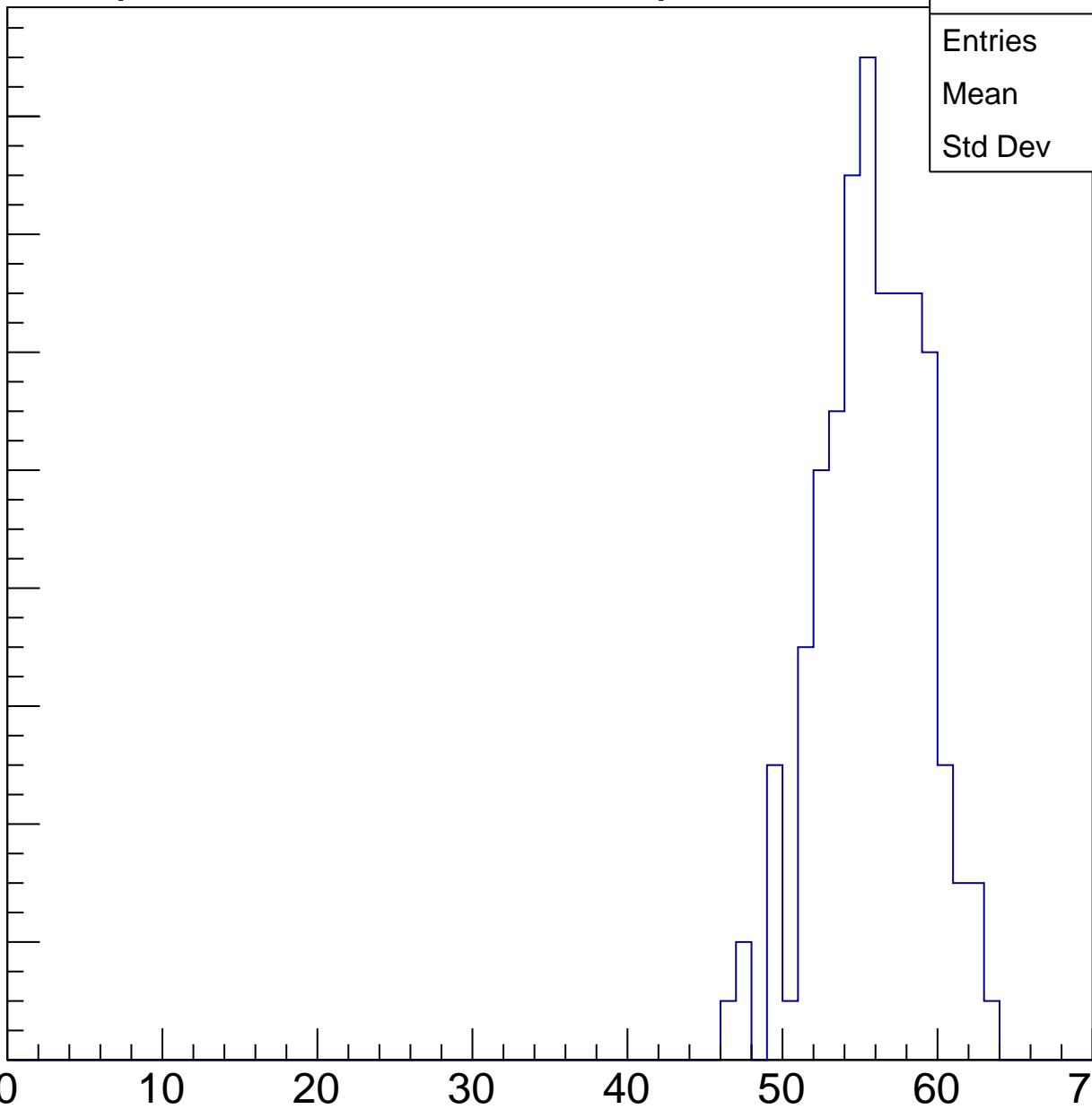
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	55.33
Std Dev	3.347

ampl



B1L001S, U21-ch59, adc5

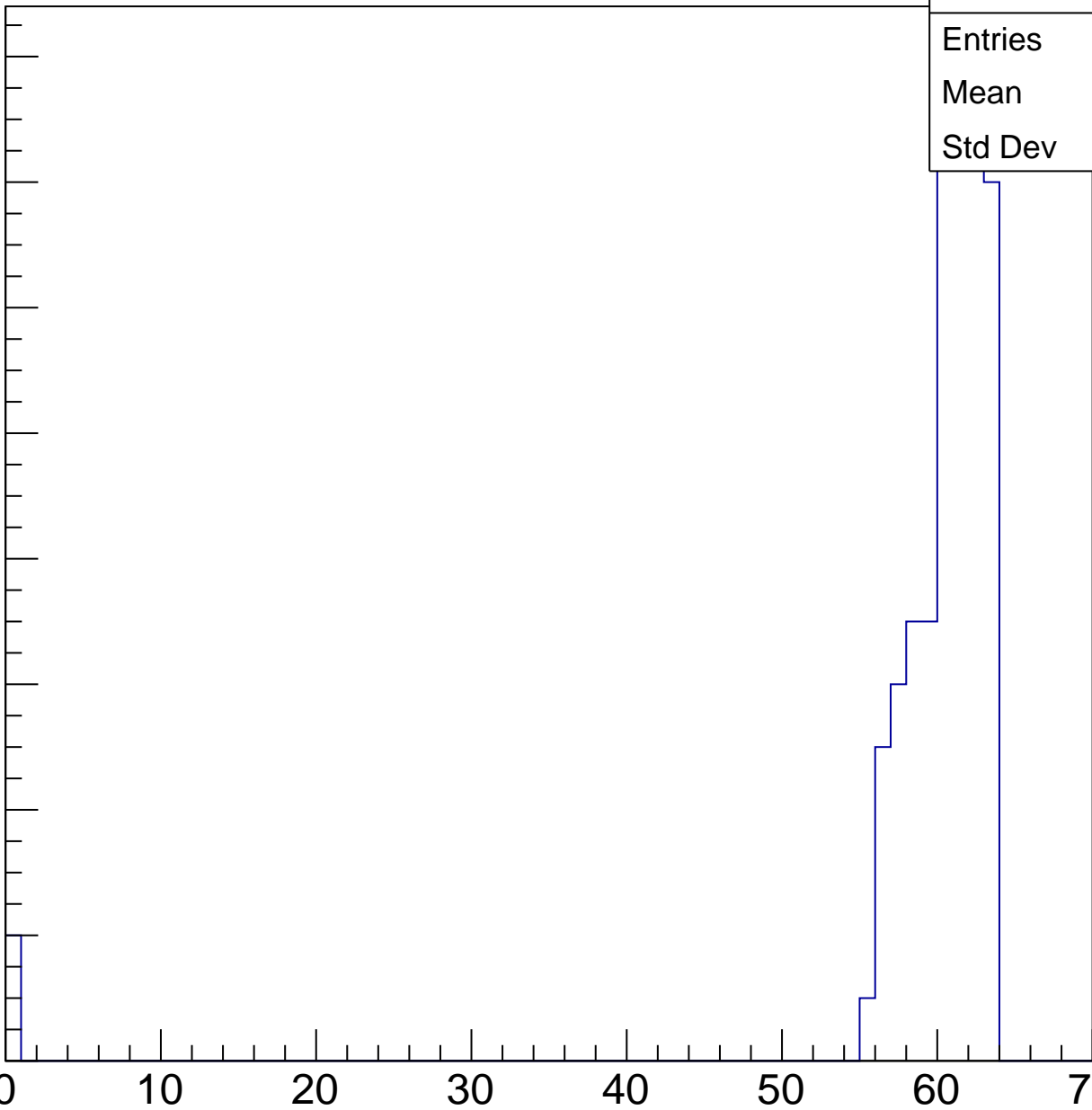
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.94
Std Dev	9.18

ampl

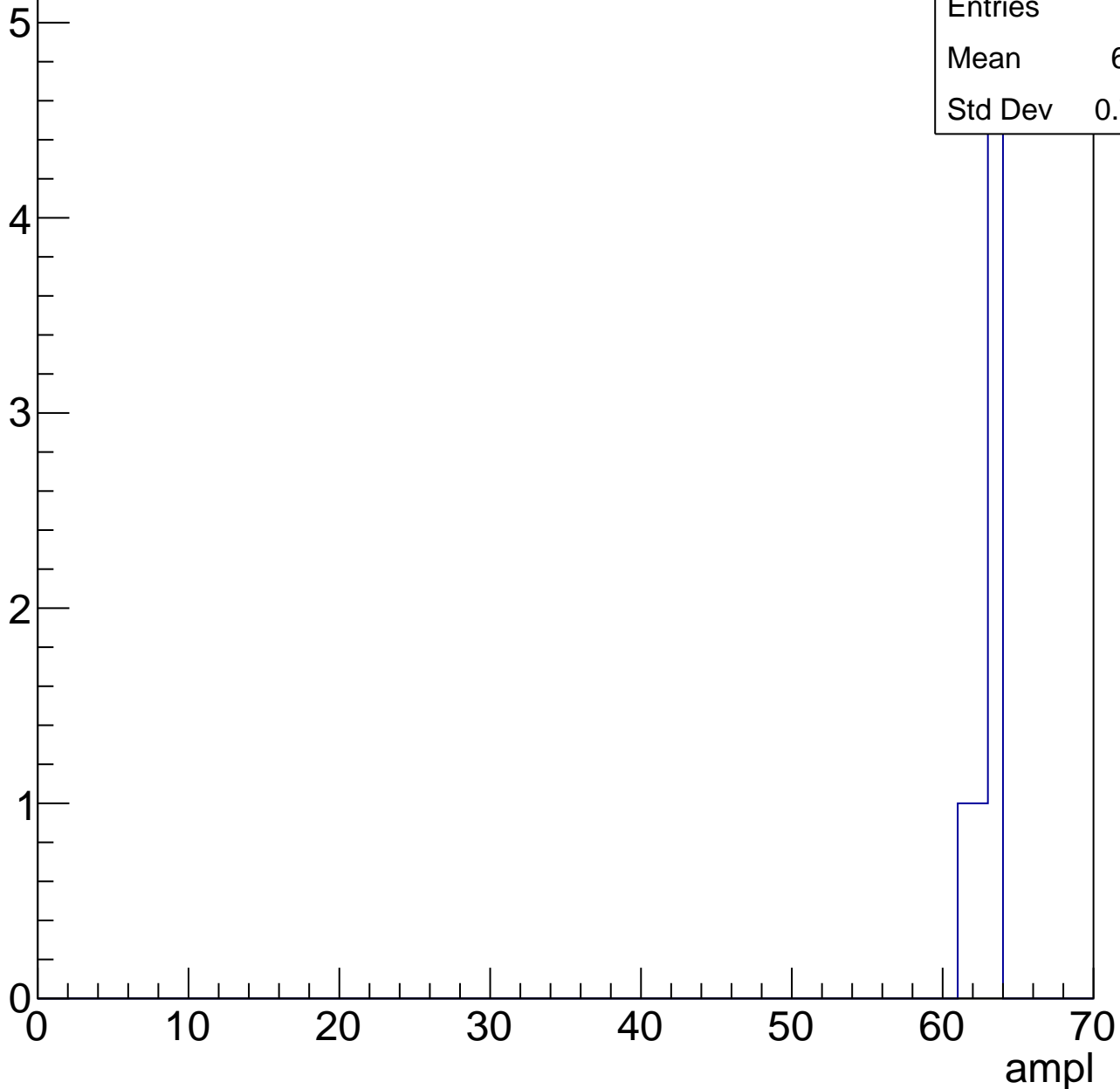


B1L001S, U21-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B1L001S, U21-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch60, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	28.53
Std Dev	2.99

Gaus mean : 29.1657

Gaus Width: 3.3147

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U21-ch60, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	34.67
Std Dev	3.267

Gaus mean : 35.0563

Gaus Width: 3.2756

Entry

14

12

10

8

6

4

2

0

0

10

20

30

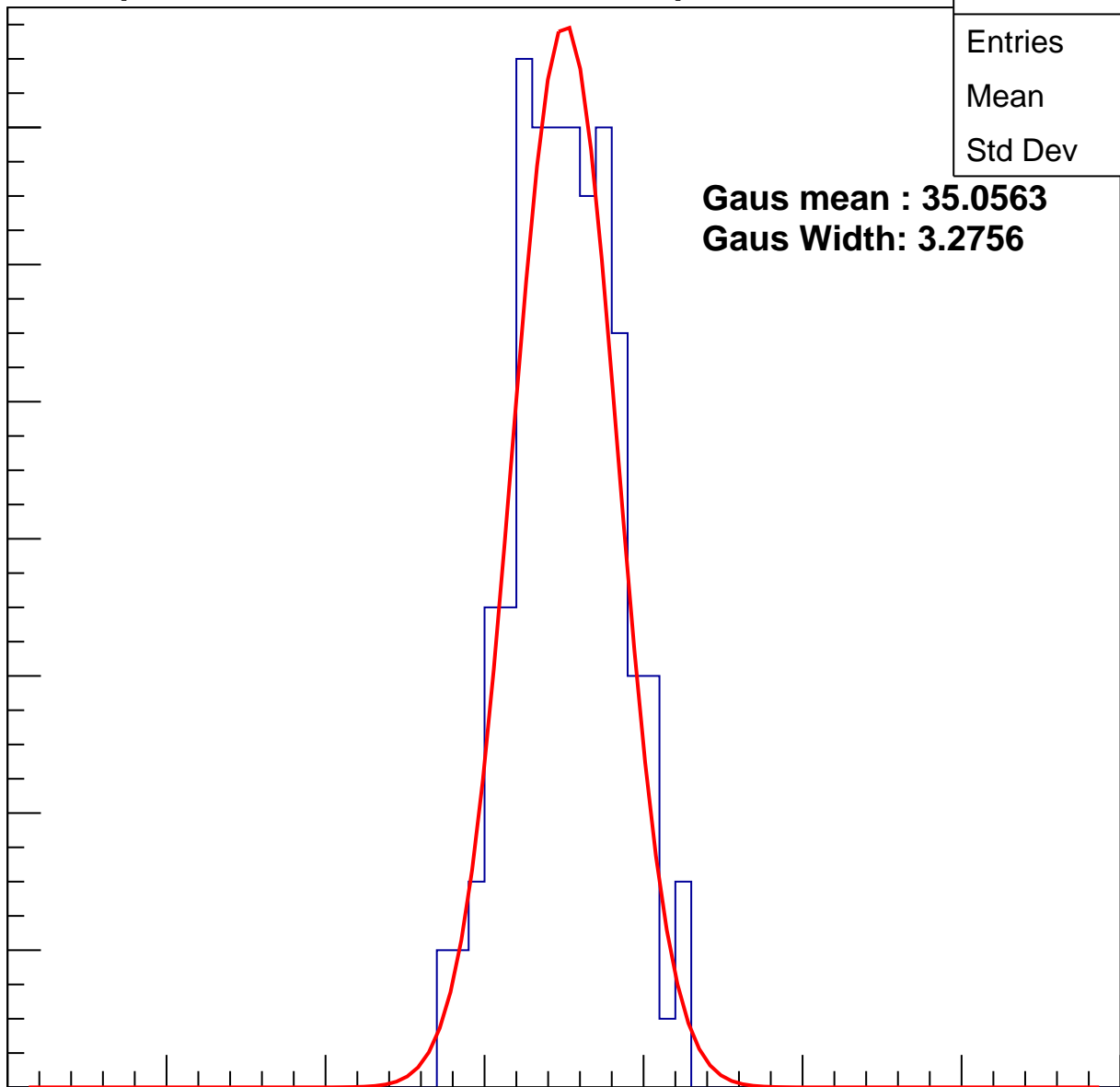
40

50

60

70

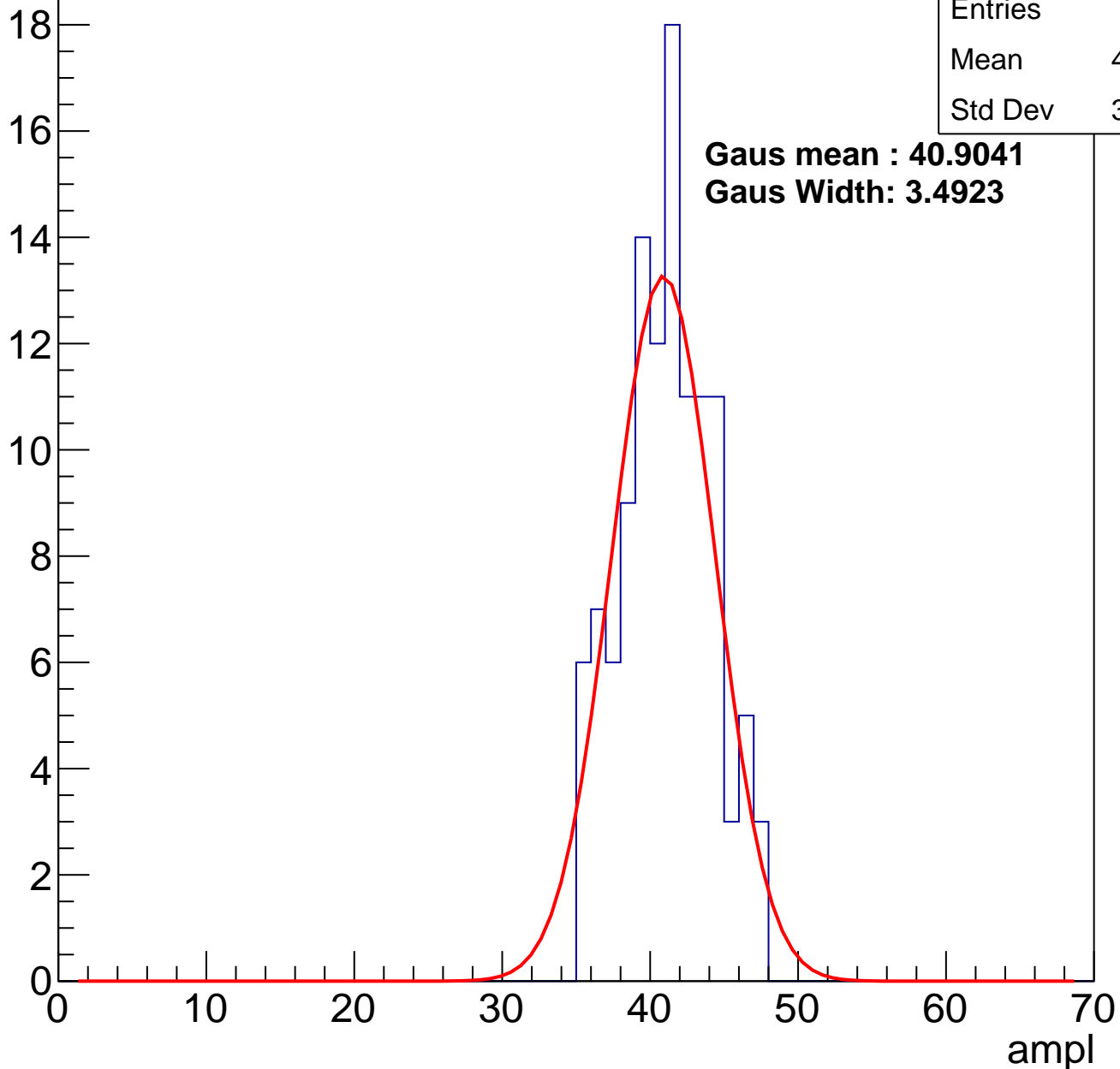
ampl



B1L001S, U21-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

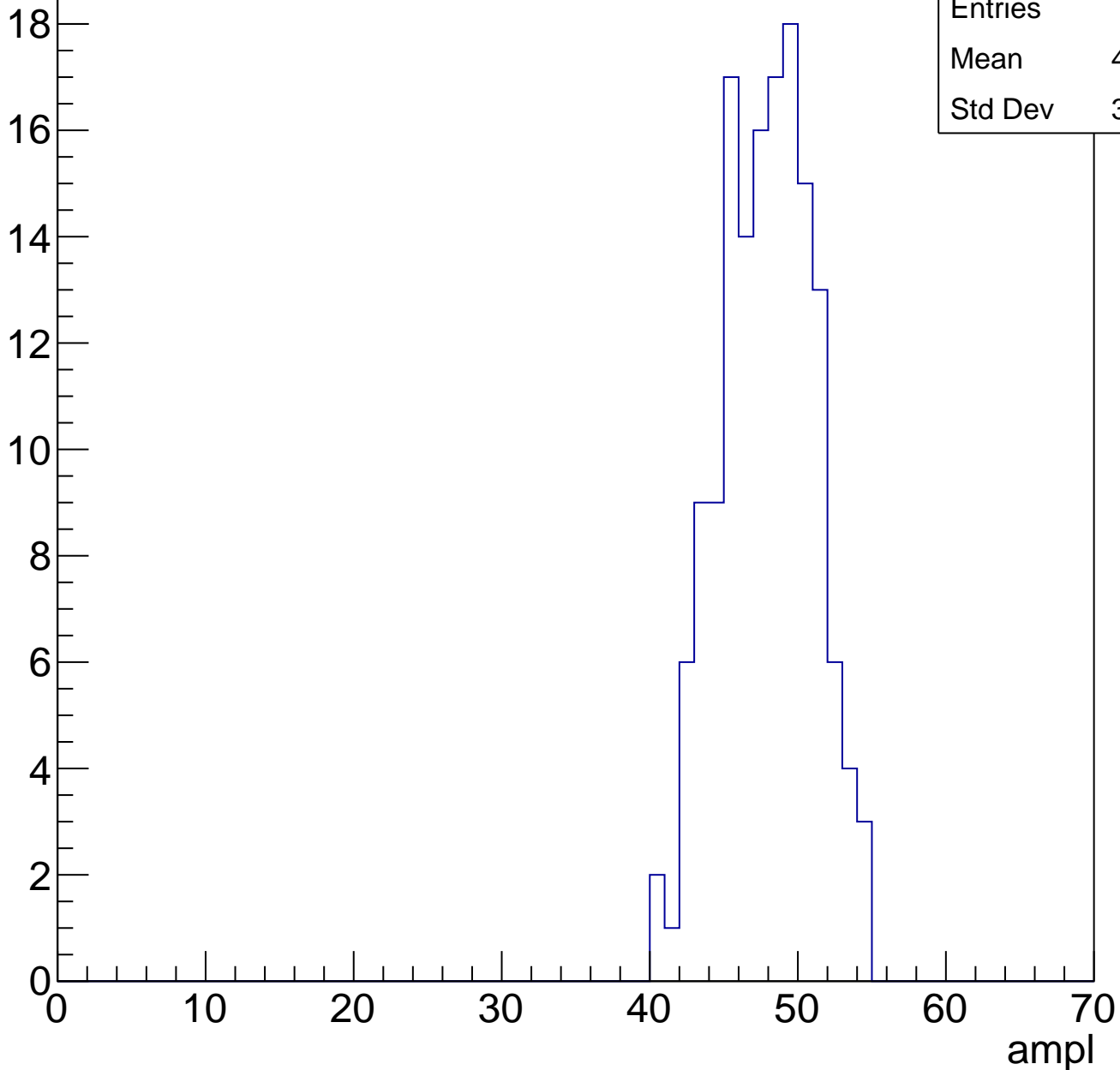


B1L001S, U21-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	47.43
Std Dev	3.095

Entry



B1L001S, U21-ch60, adc4

calib_packv5_042523_0143.root, FC#2, port C2

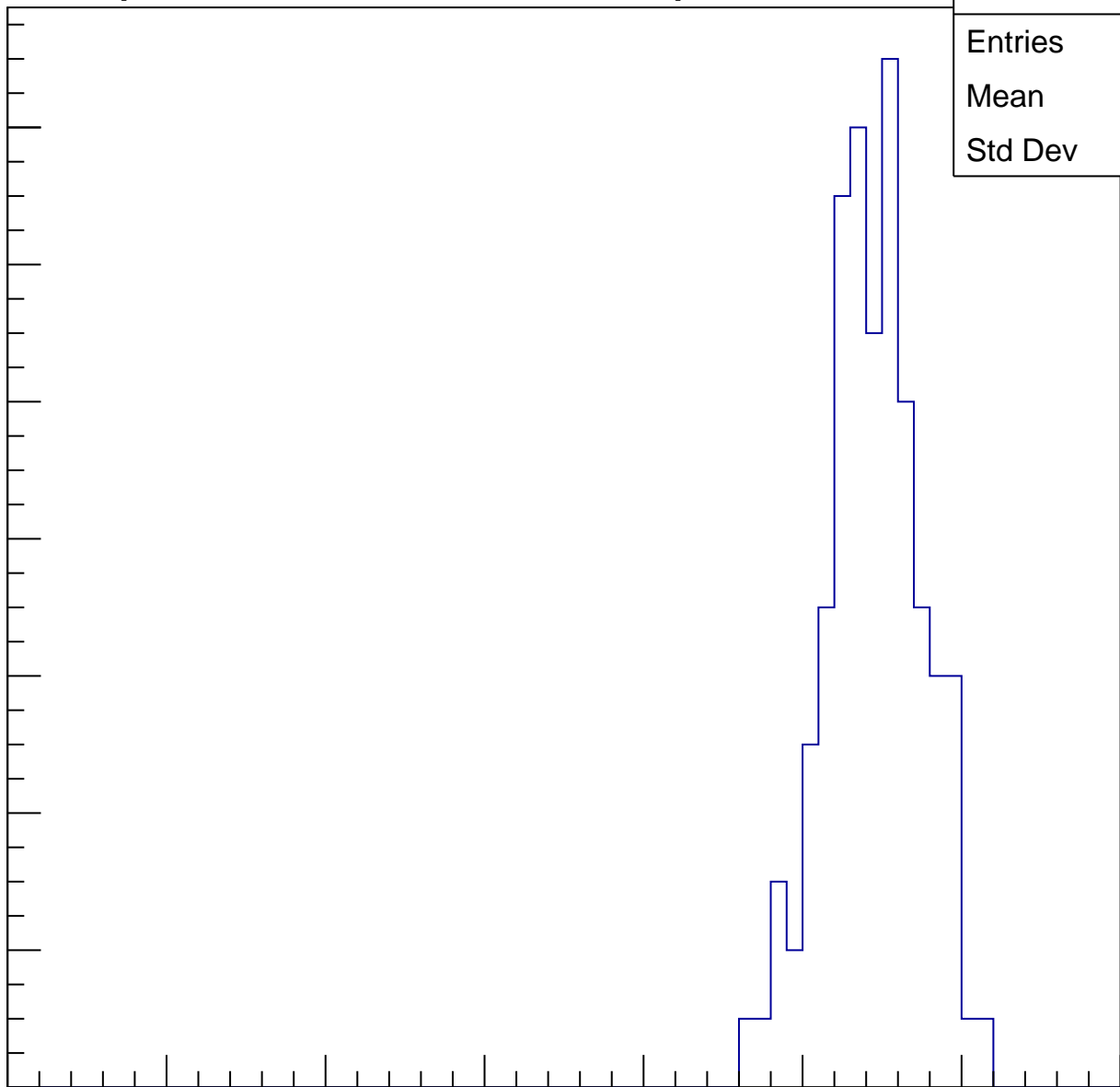
Entries	103
Mean	53.99
Std Dev	3.003

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch60, adc5

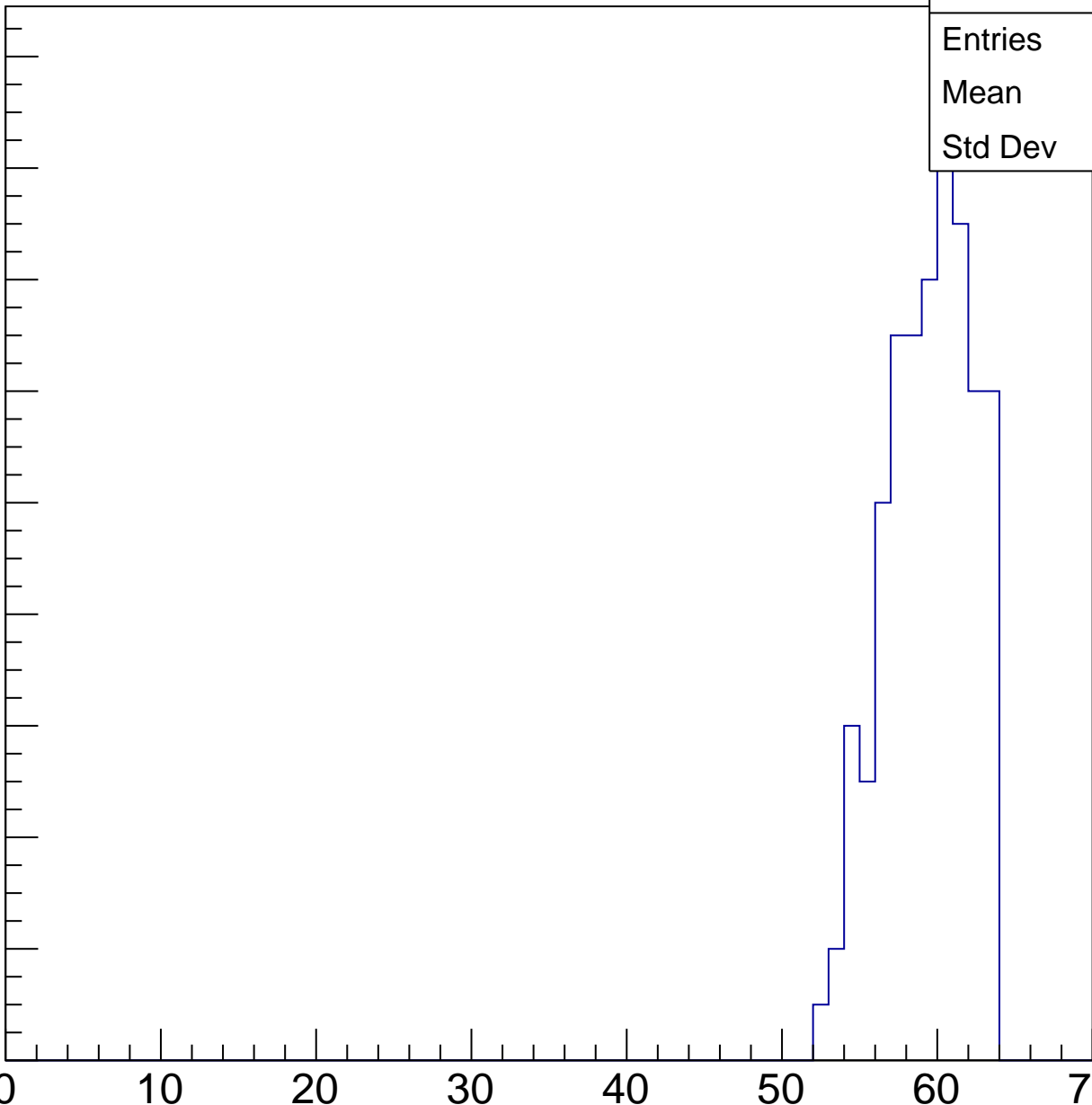
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	121
Mean	58.95
Std Dev	2.702

ampl

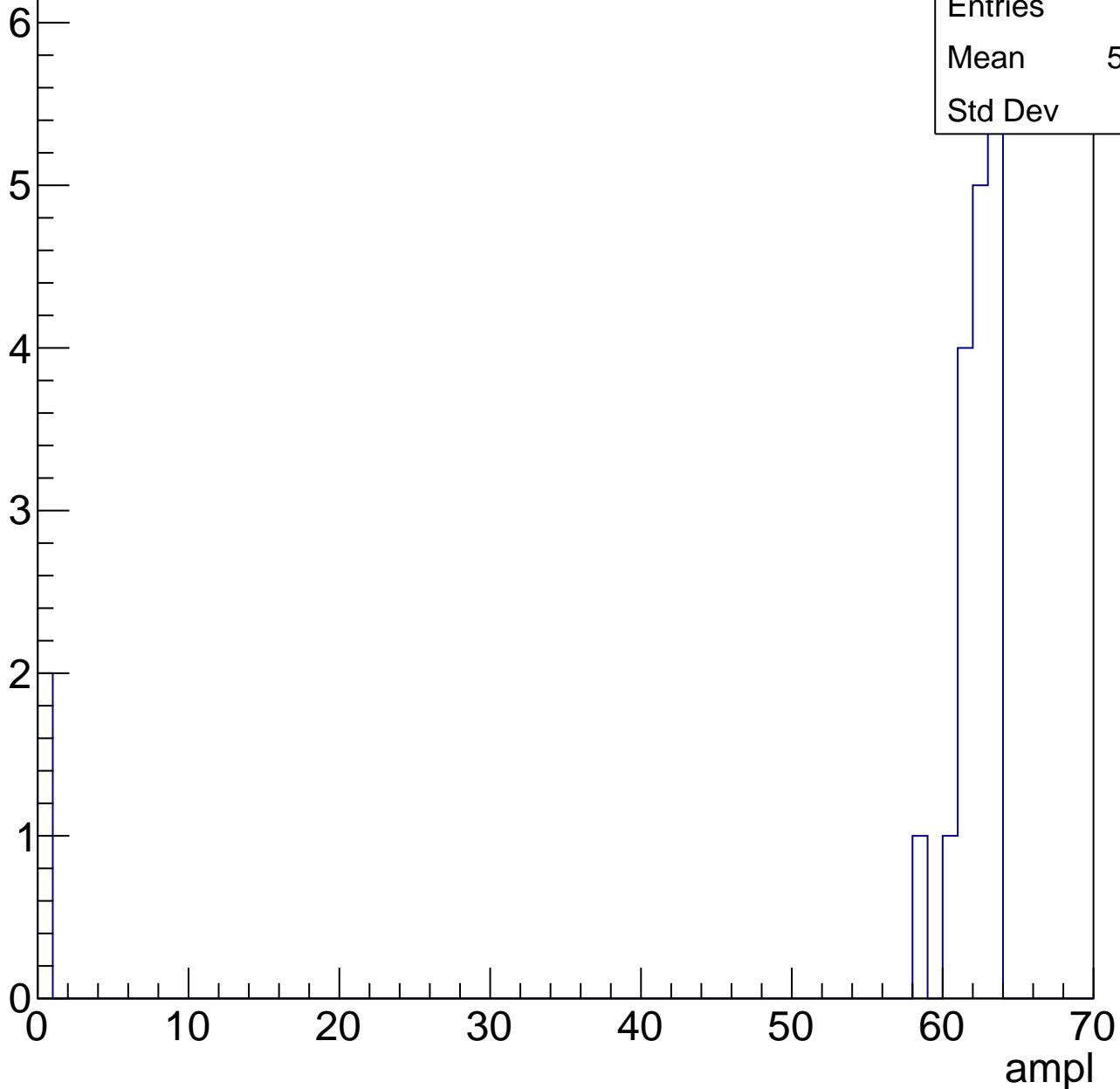


B1L001S, U21-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

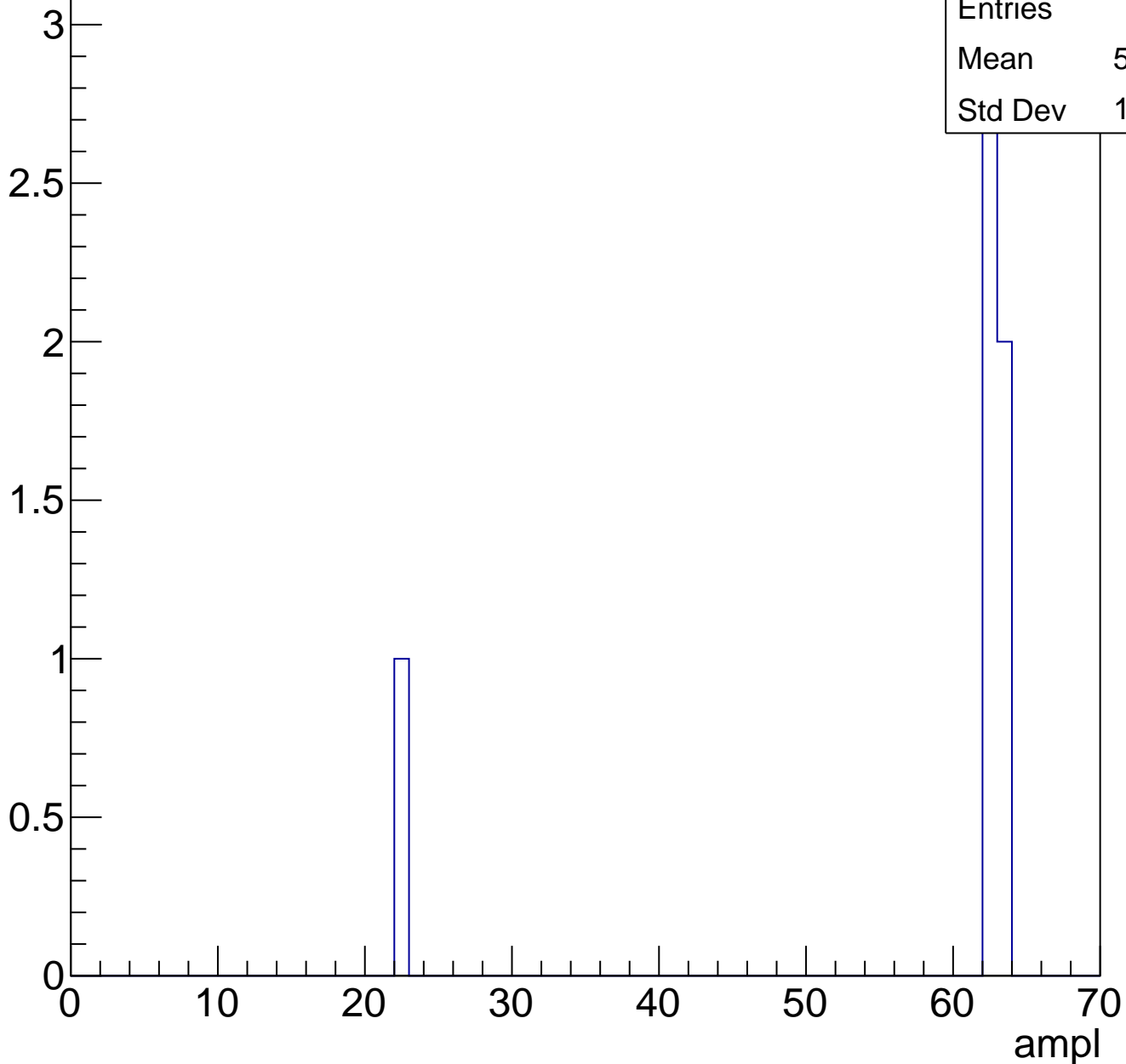
Entries	19
Mean	55.26
Std Dev	19



B1L001S, U21-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch61, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	30.53
Std Dev	3.382

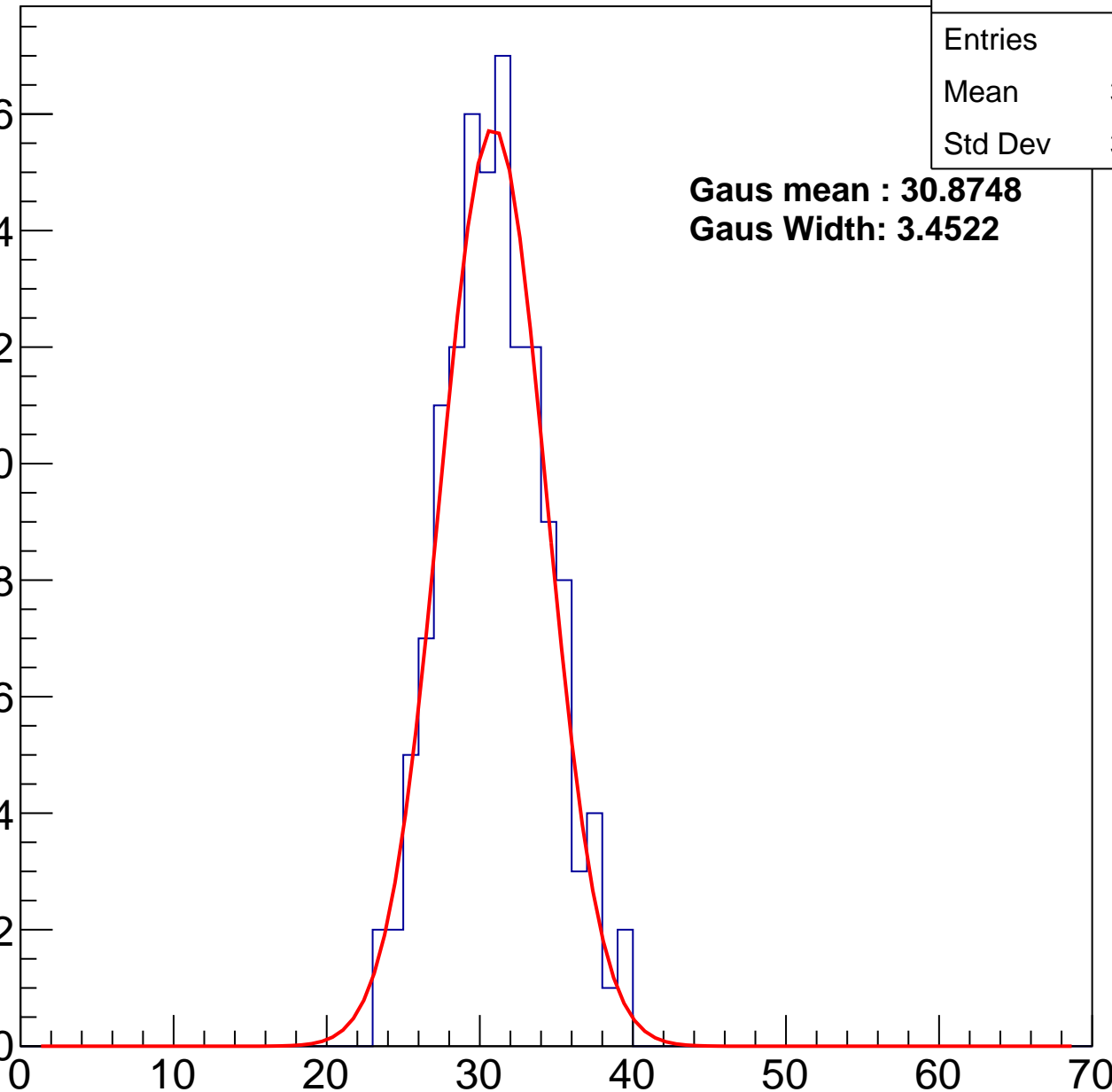
Gaus mean : 30.8748

Gaus Width: 3.4522

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch61, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	36.12
Std Dev	3.04

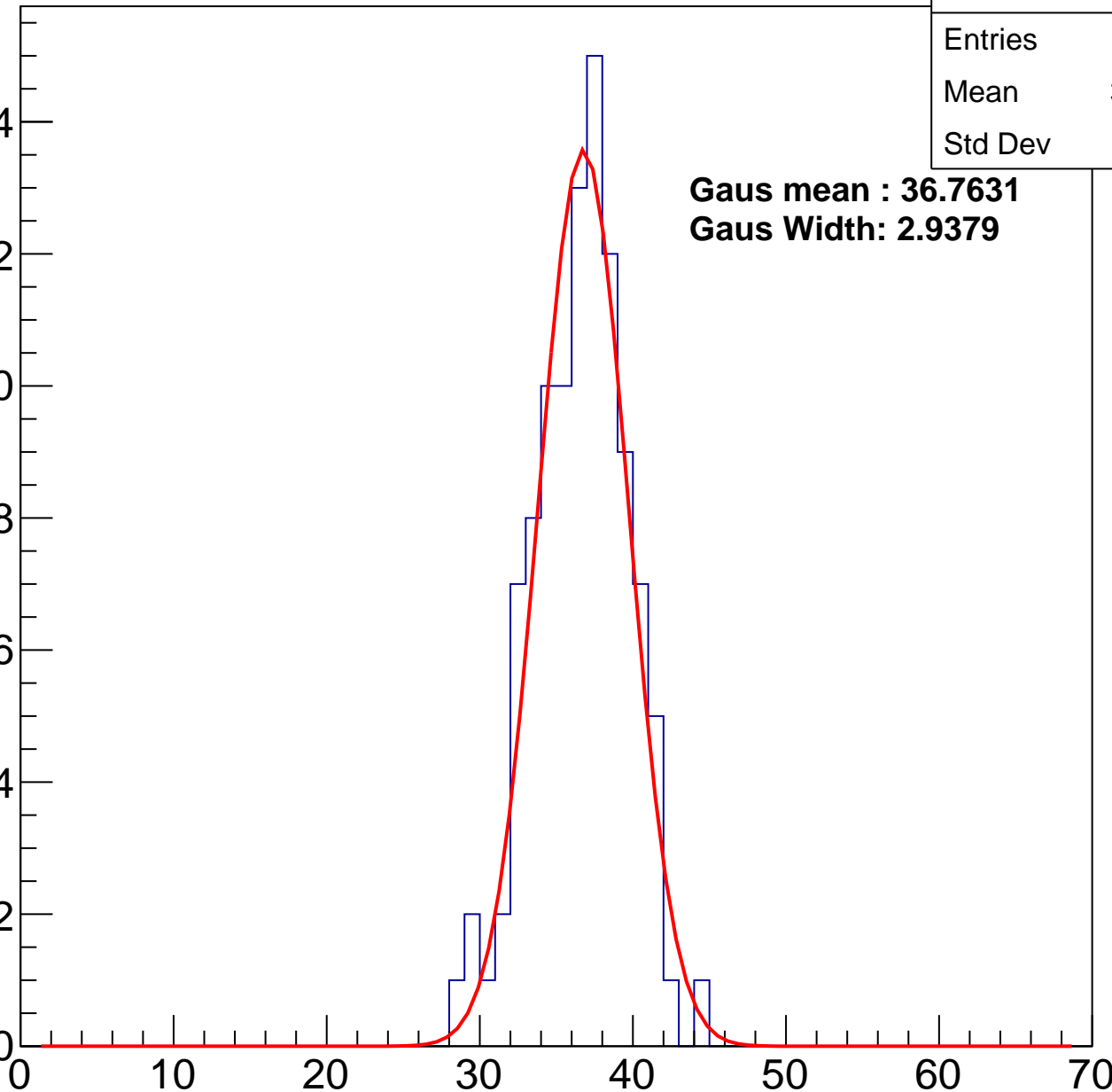
Gaus mean : 36.7631

Gaus Width: 2.9379

Entry

14
12
10
8
6
4
2
0

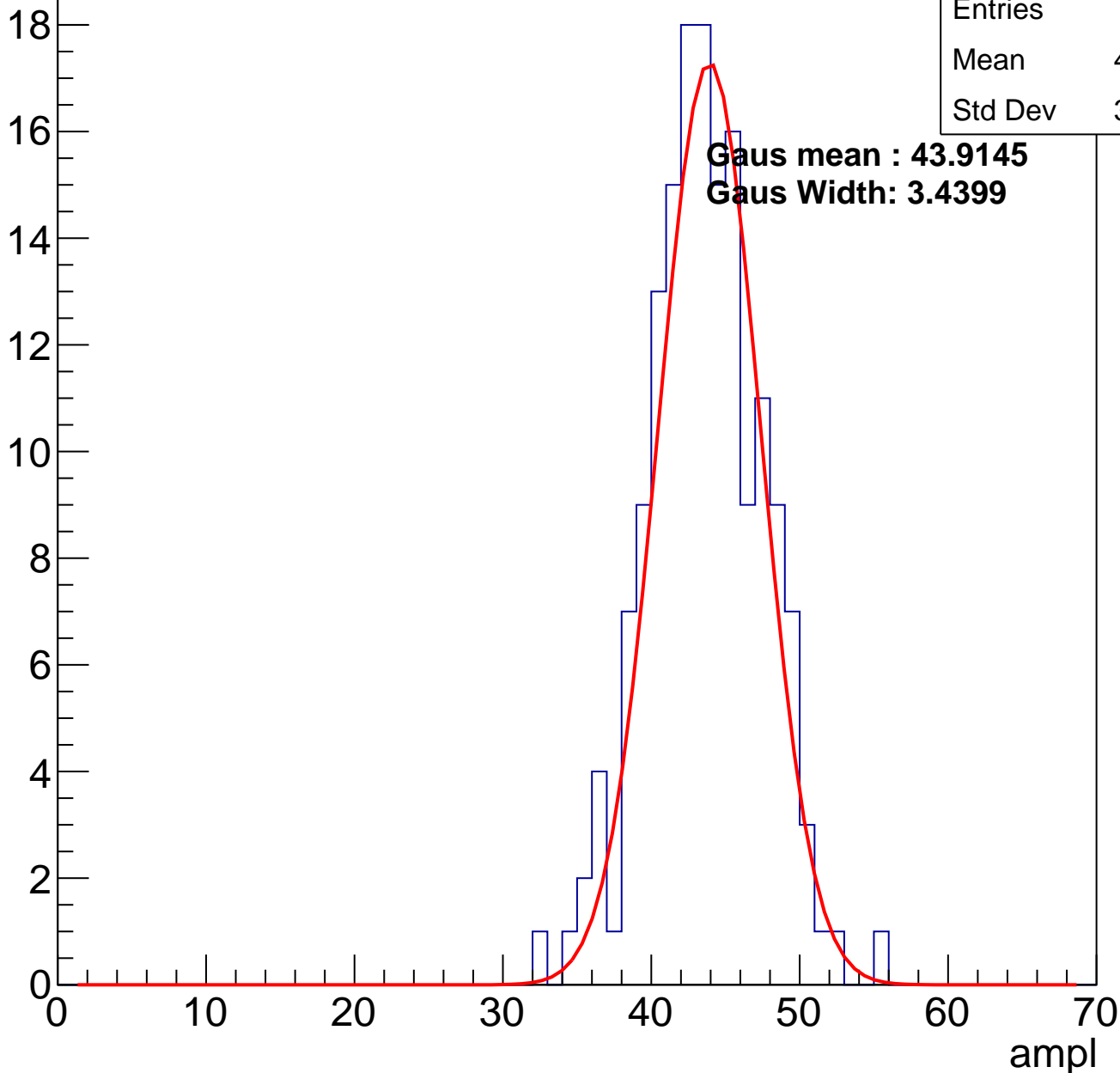
ampl



B1L001S, U21-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

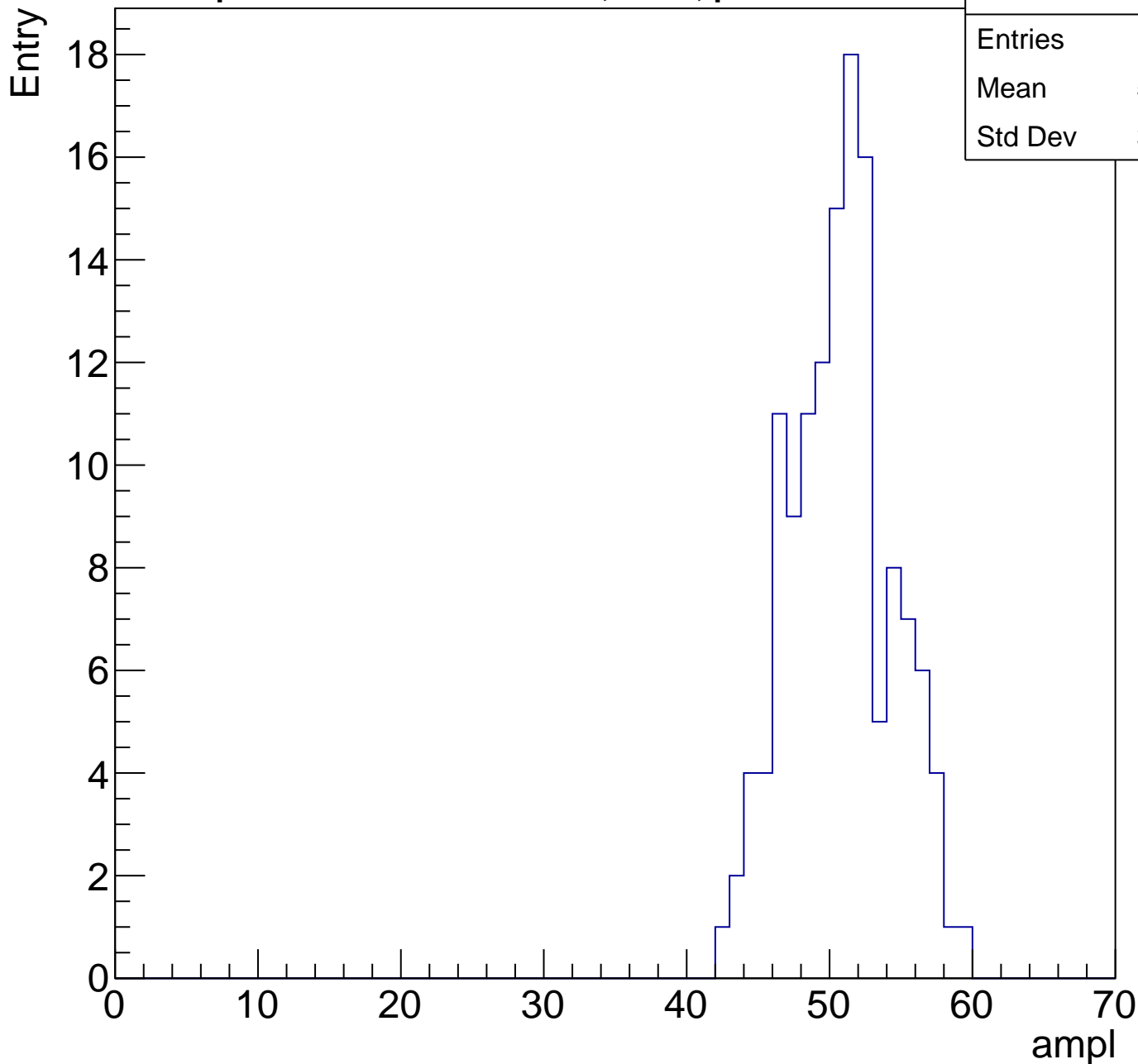
Entry



B1L001S, U21-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

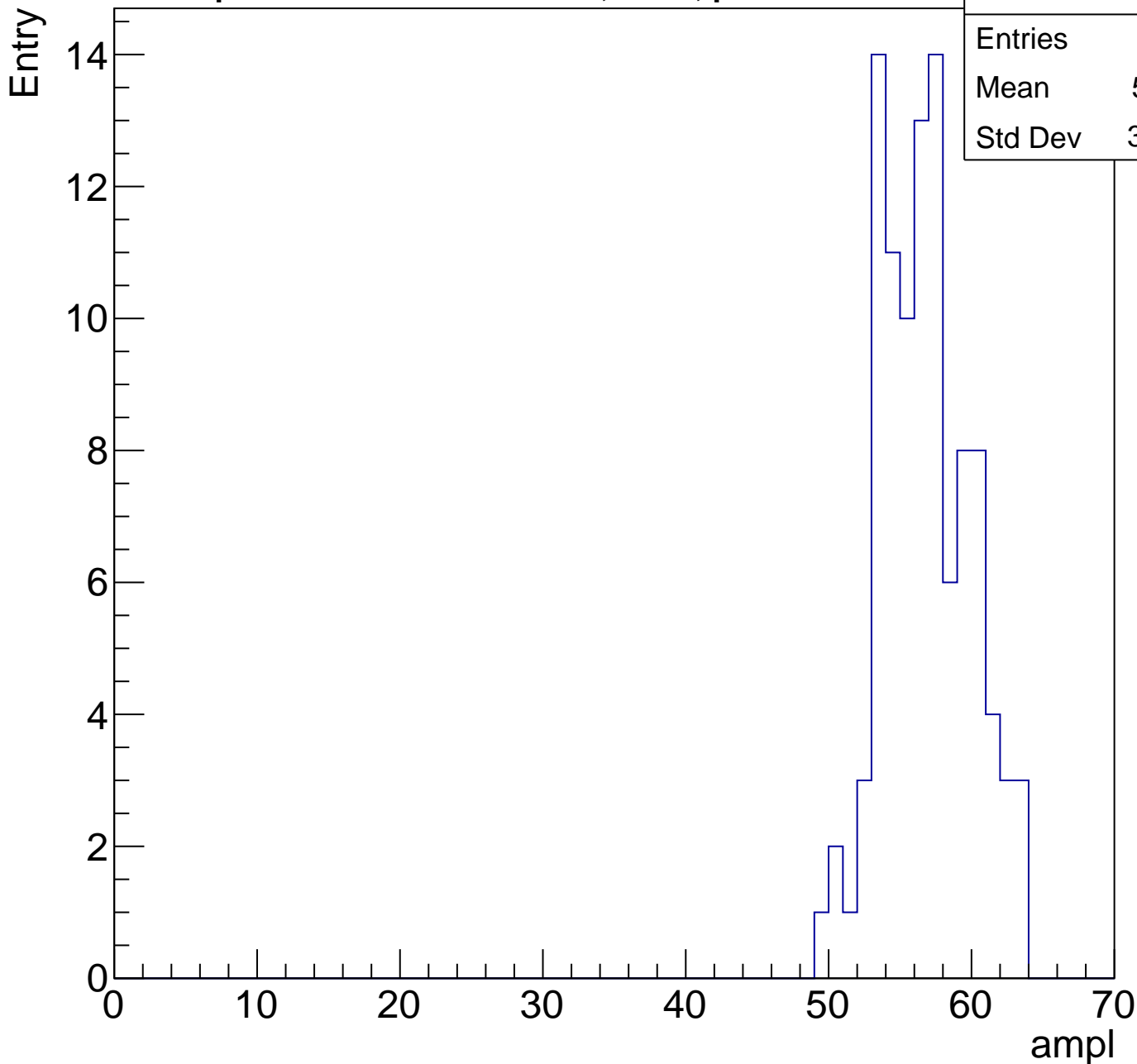
Entries	135
Mean	50.31
Std Dev	3.541



B1L001S, U21-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	56.31
Std Dev	3.079



B1L001S, U21-ch61, adc5

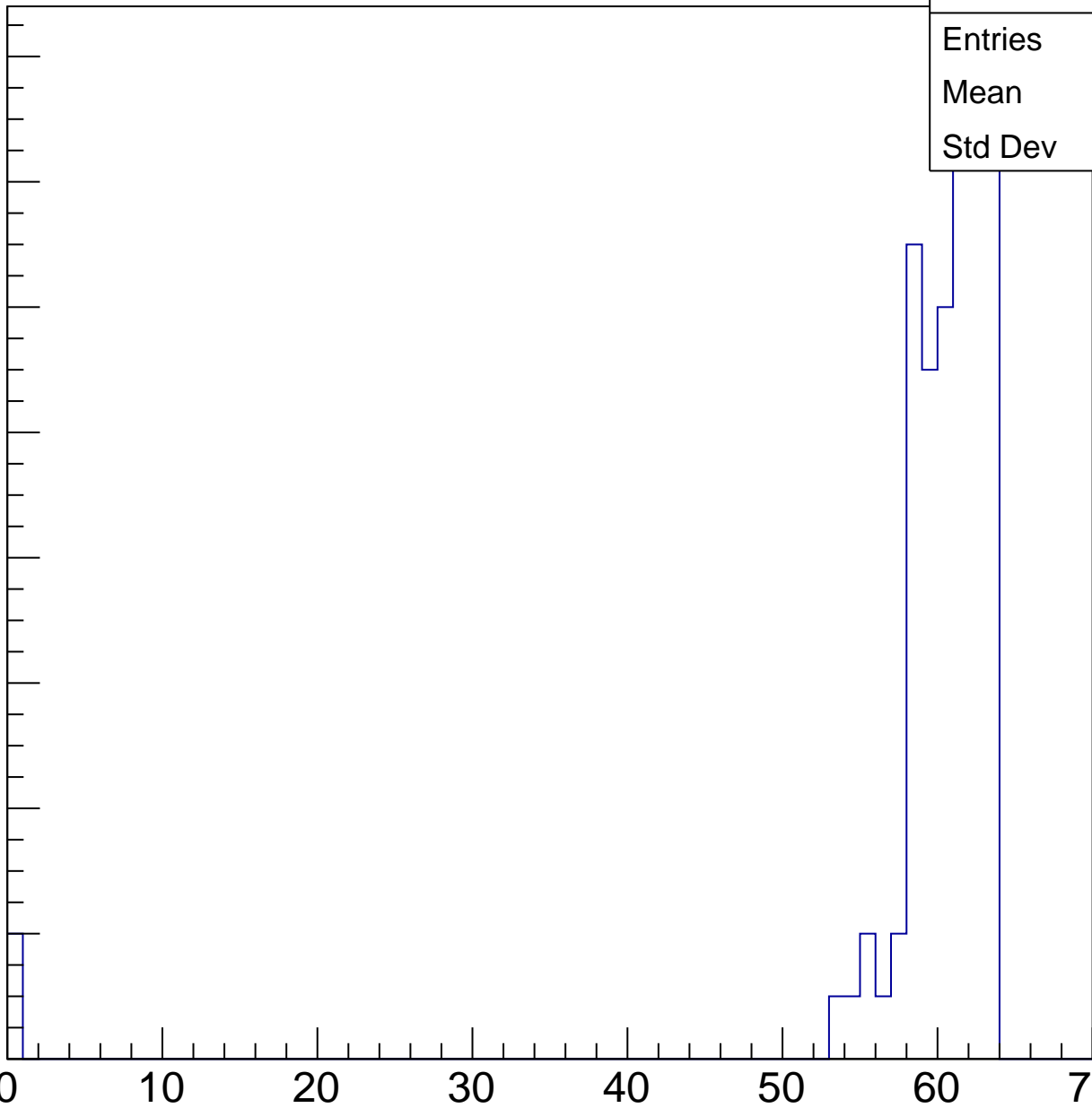
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.95
Std Dev	9.056

ampl



B1L001S, U21-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

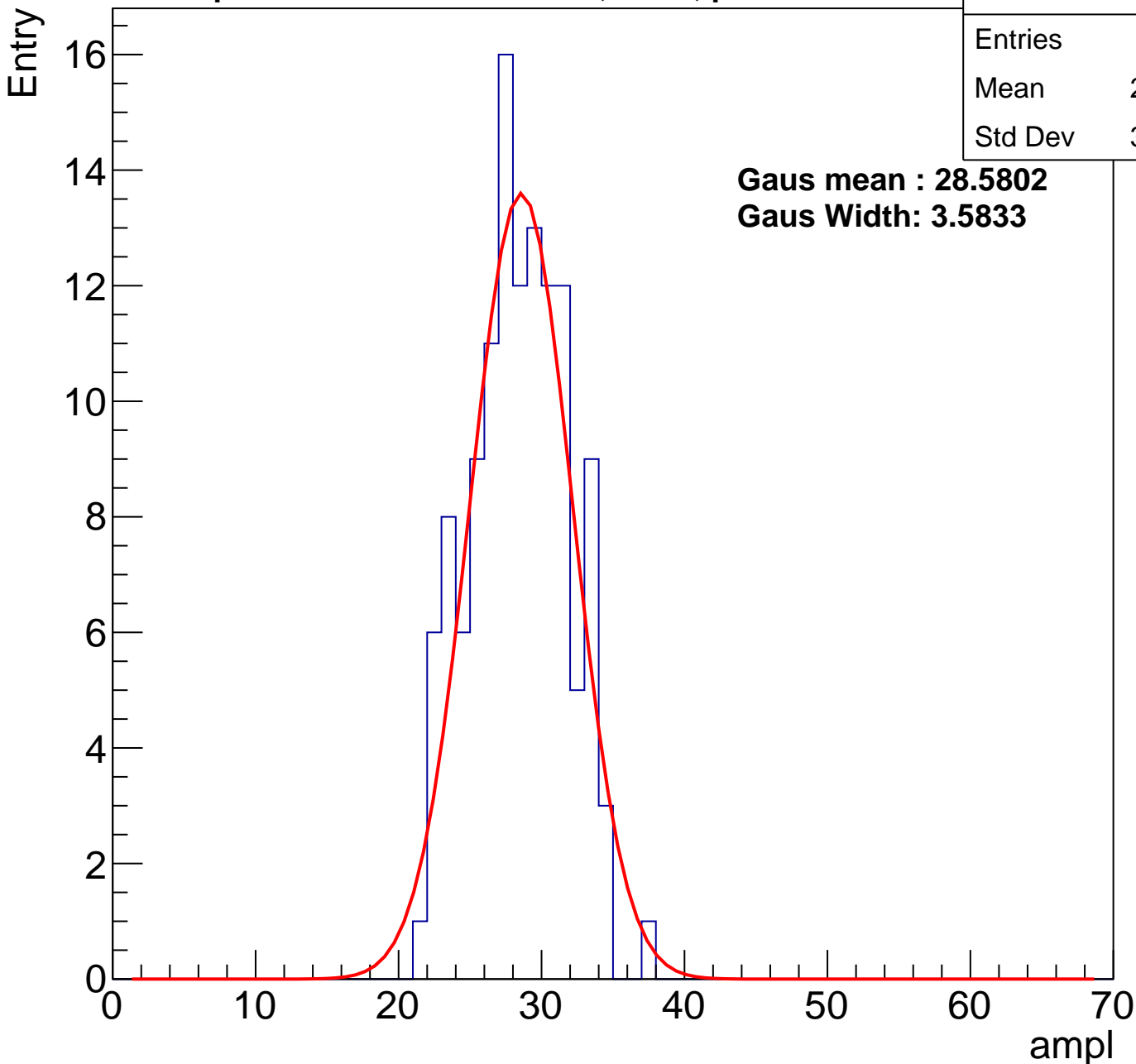
B1L001S, U21-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	27.94
Std Dev	3.317

Gaus mean : 28.5802

Gaus Width: 3.5833



B1L001S, U21-ch62, adc1

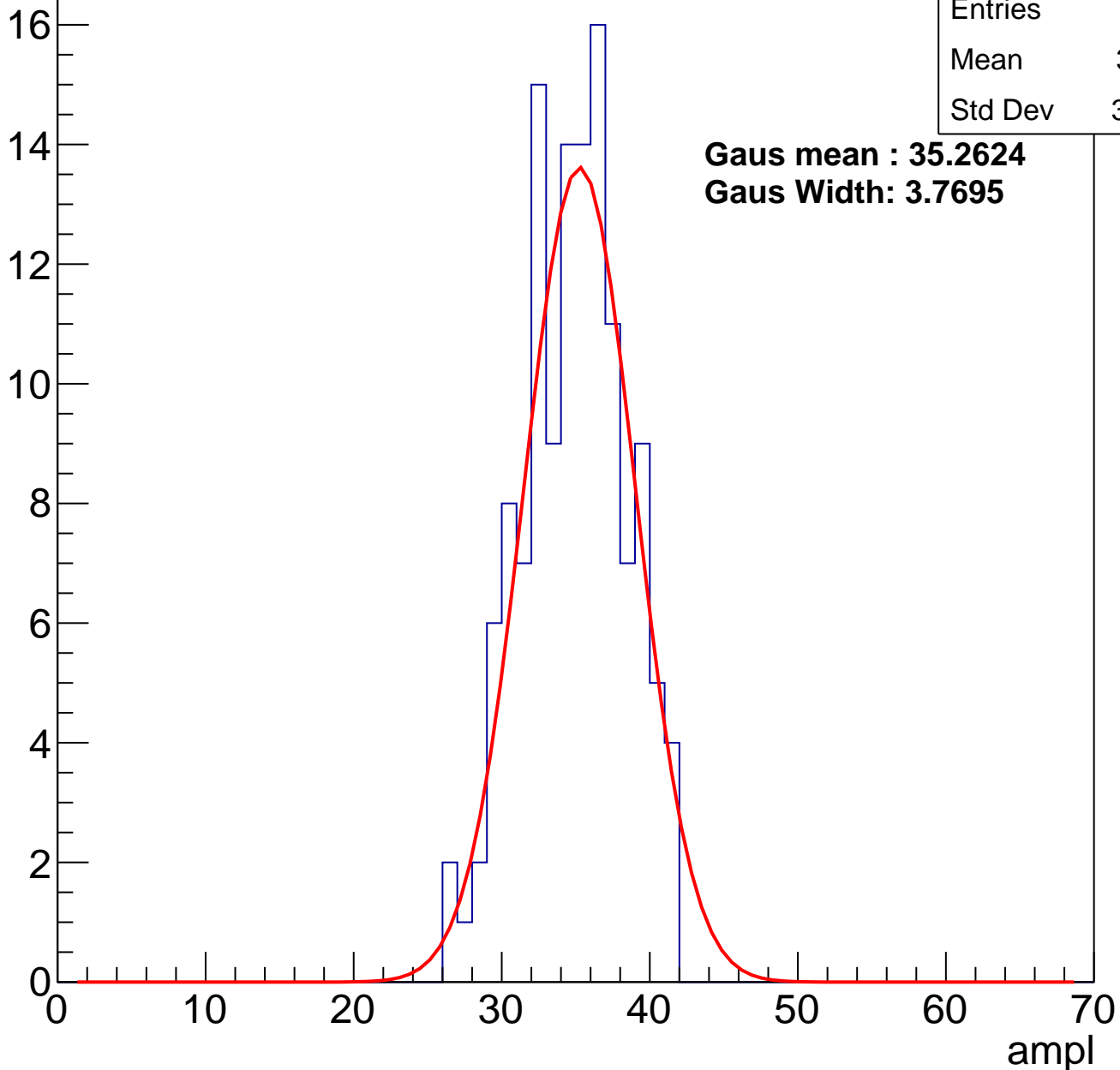
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	34.41
Std Dev	3.428

Gaus mean : 35.2624

Gaus Width: 3.7695

Entry



B1L001S, U21-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	40.91
Std Dev	3.607

Gaus mean : 41.2048

Gaus Width: 3.6022

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

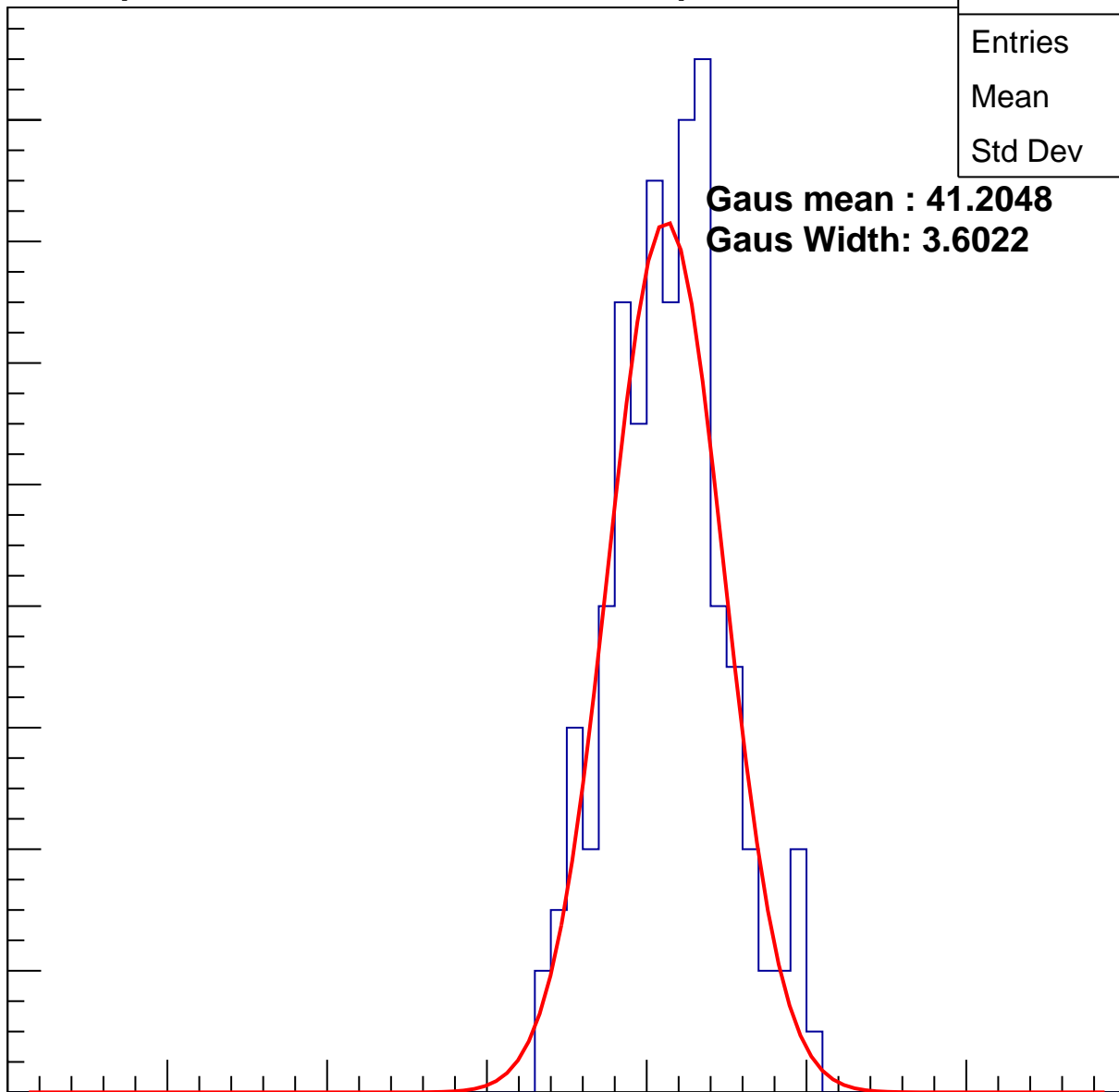
40

50

60

70

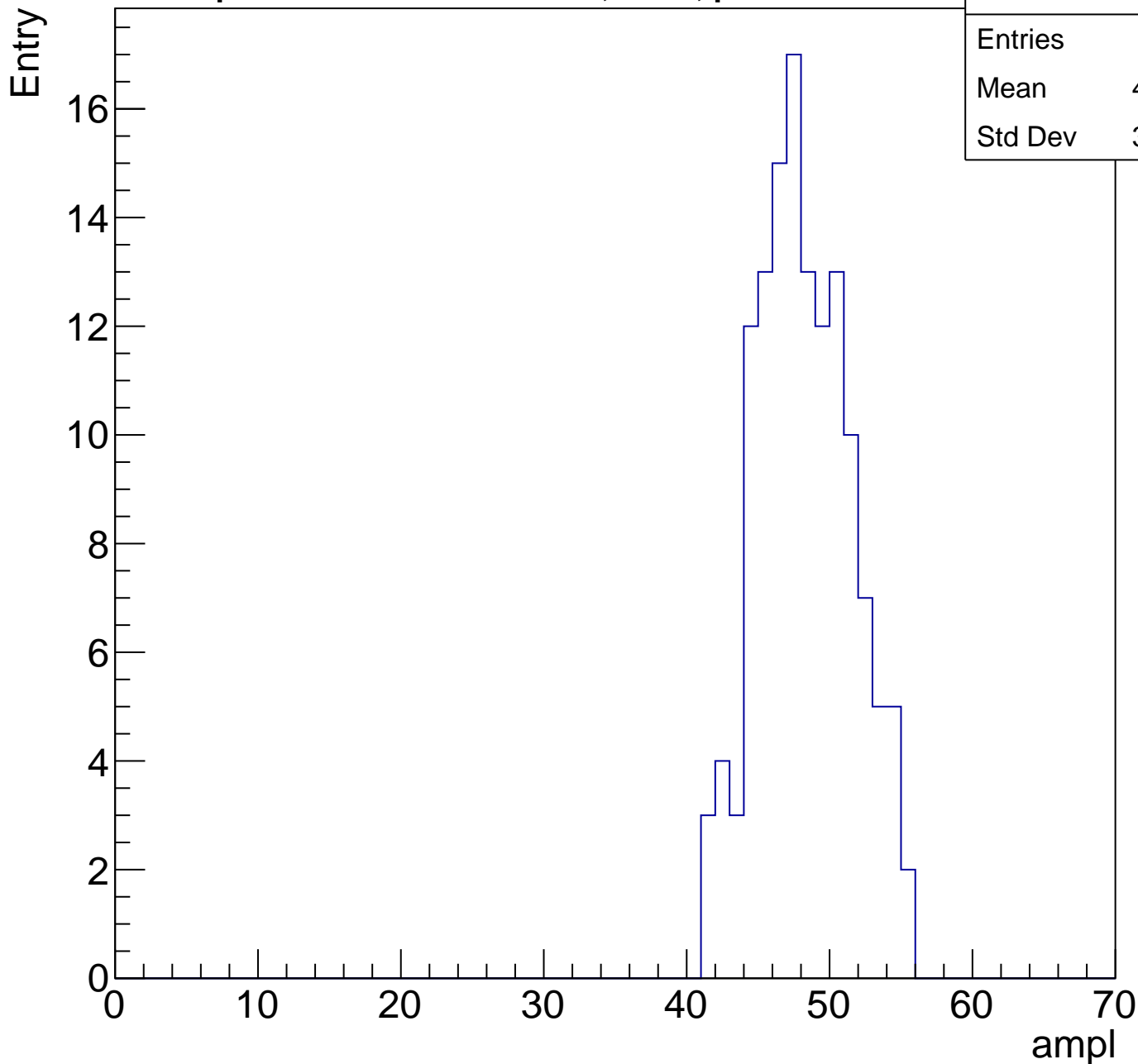
ampl



B1L001S, U21-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

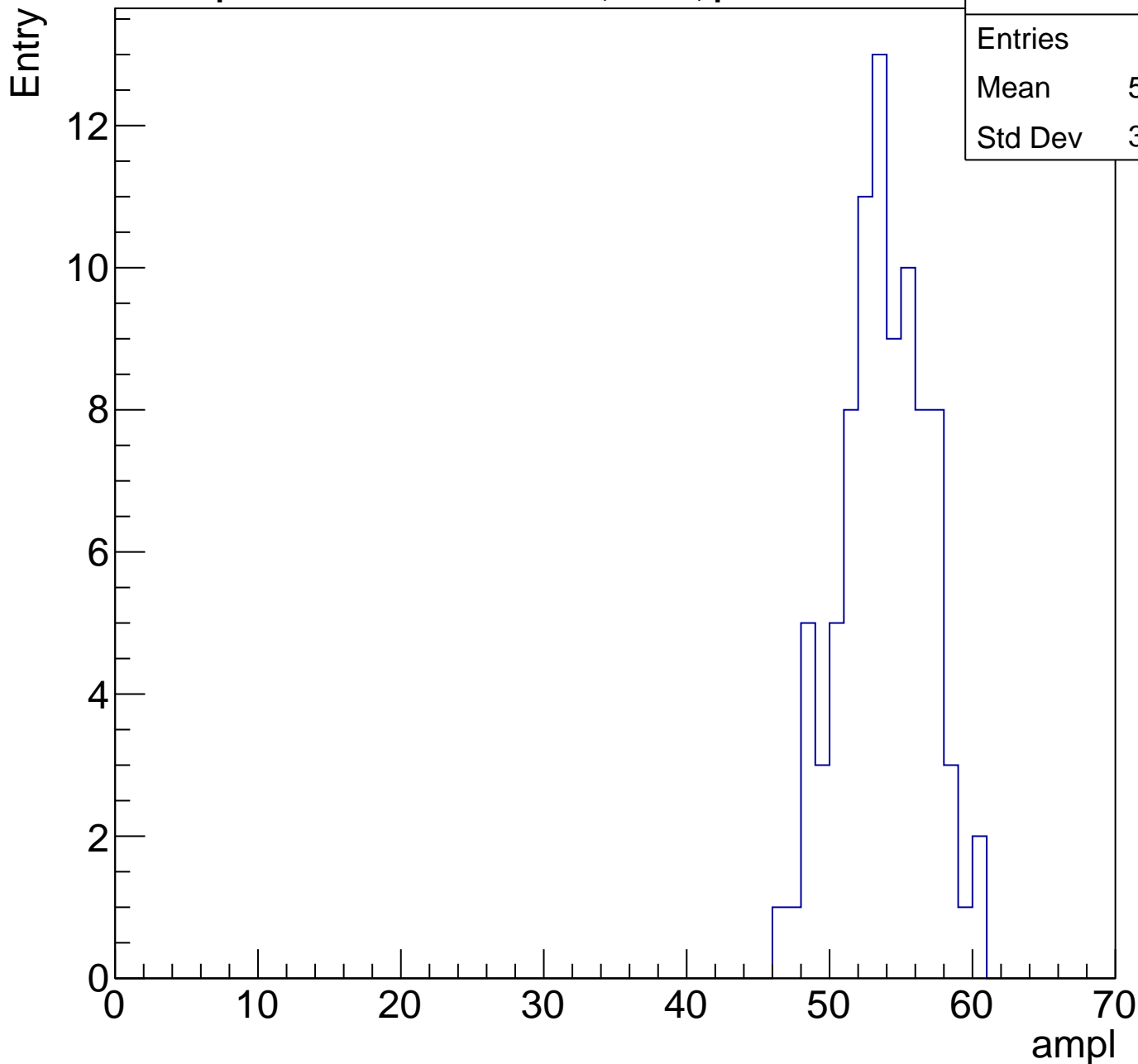
Entries	134
Mean	47.78
Std Dev	3.249



B1L001S, U21-ch62, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	88
Mean	53.32
Std Dev	3.006



B1L001S, U21-ch62, adc5

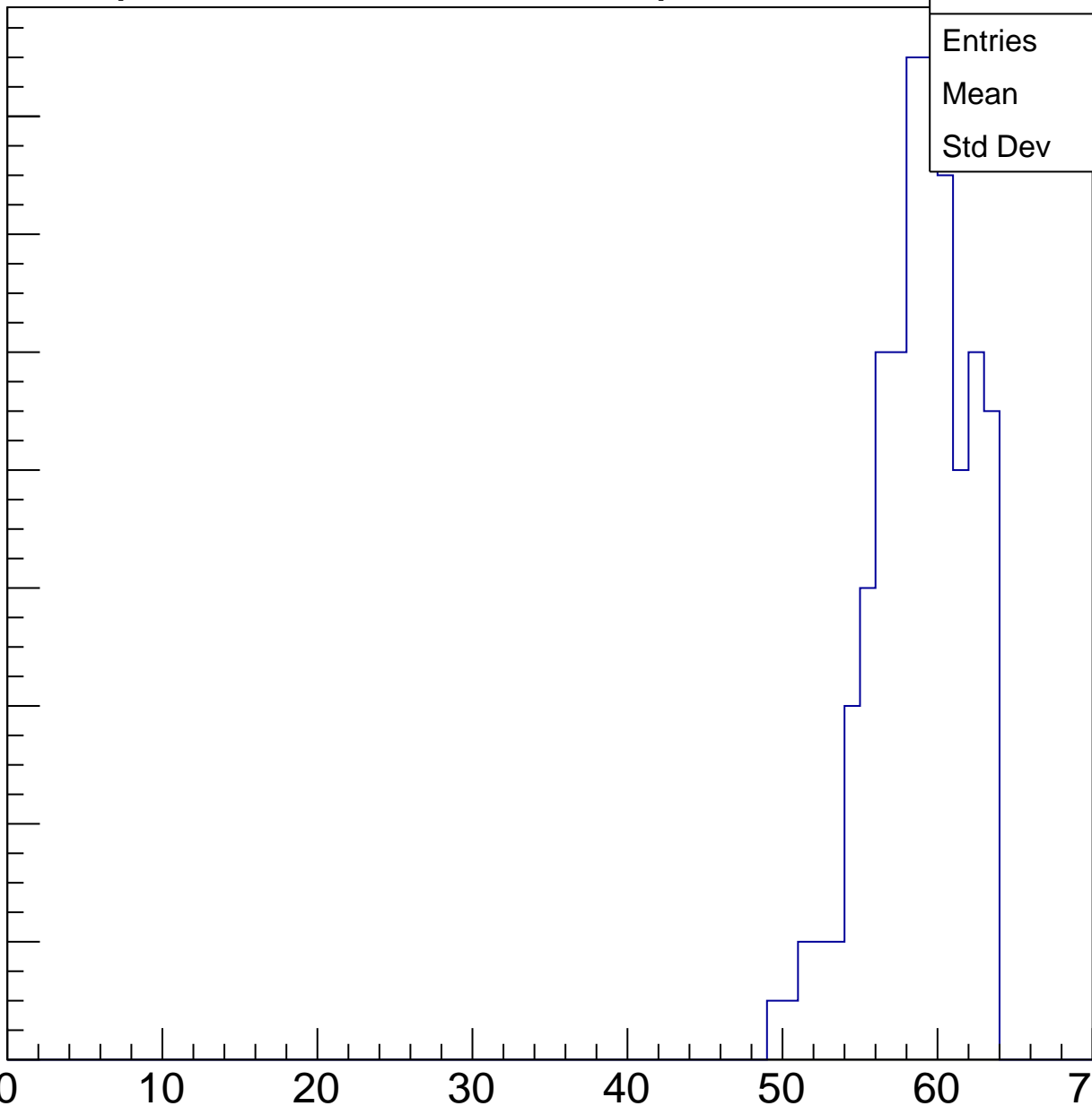
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	58.34
Std Dev	3.078

ampl

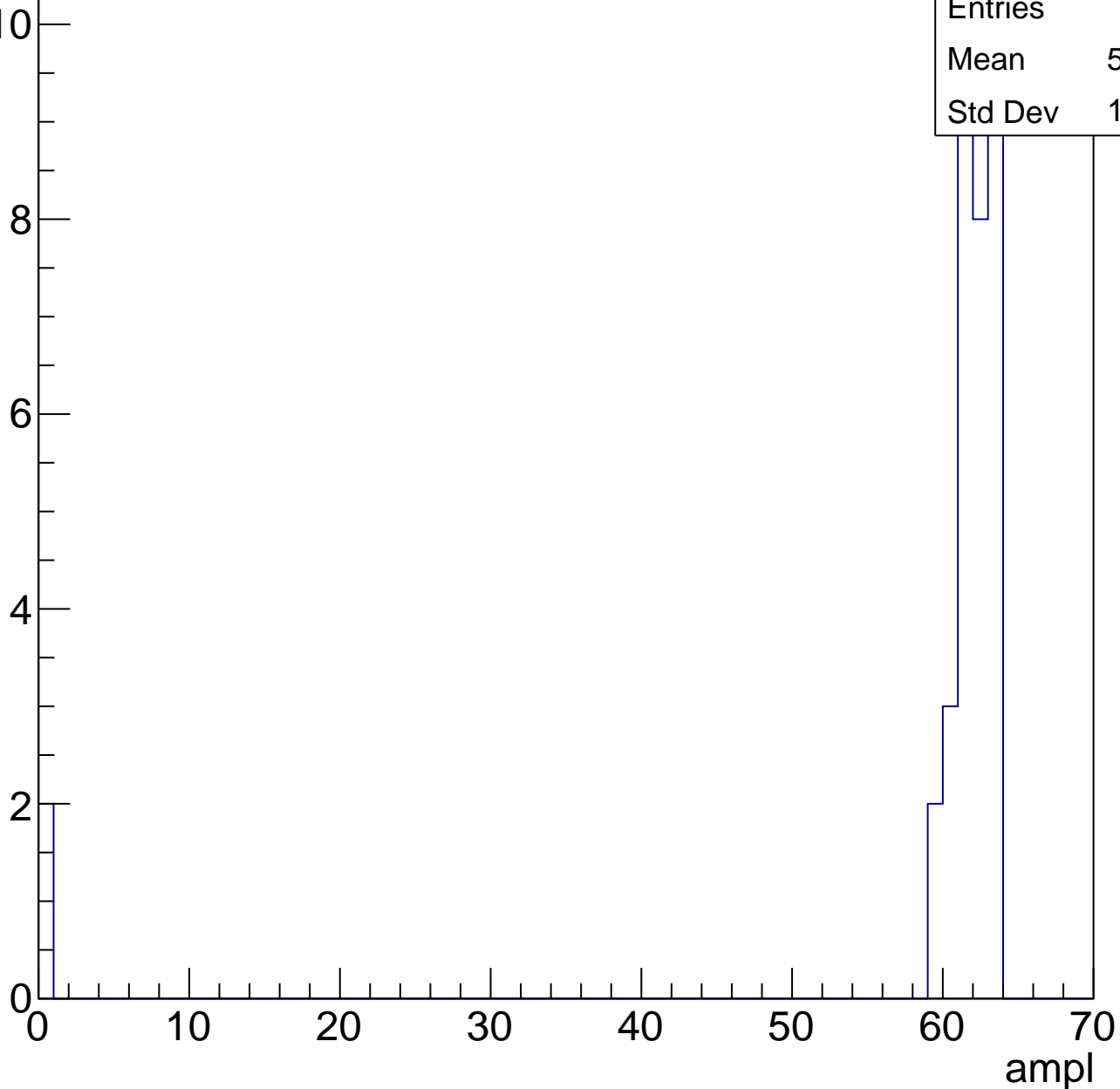


B1L001S, U21-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	34
Mean	57.97
Std Dev	14.54



B1L001S, U21-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	20.5
Std Dev	0.5

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch63, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

123

Mean

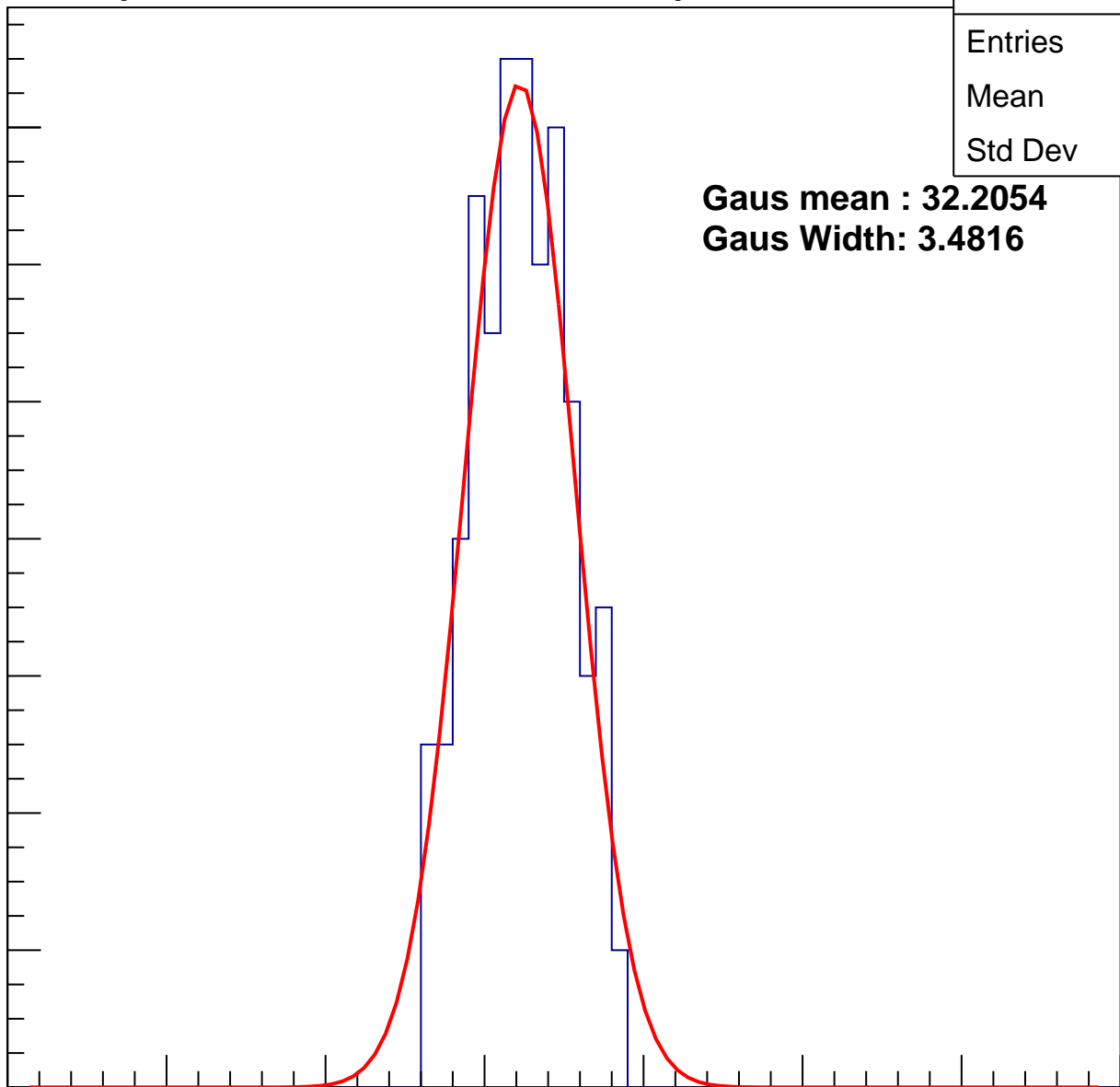
31.82

Std Dev

2.999

Gaus mean : 32.2054

Gaus Width: 3.4816



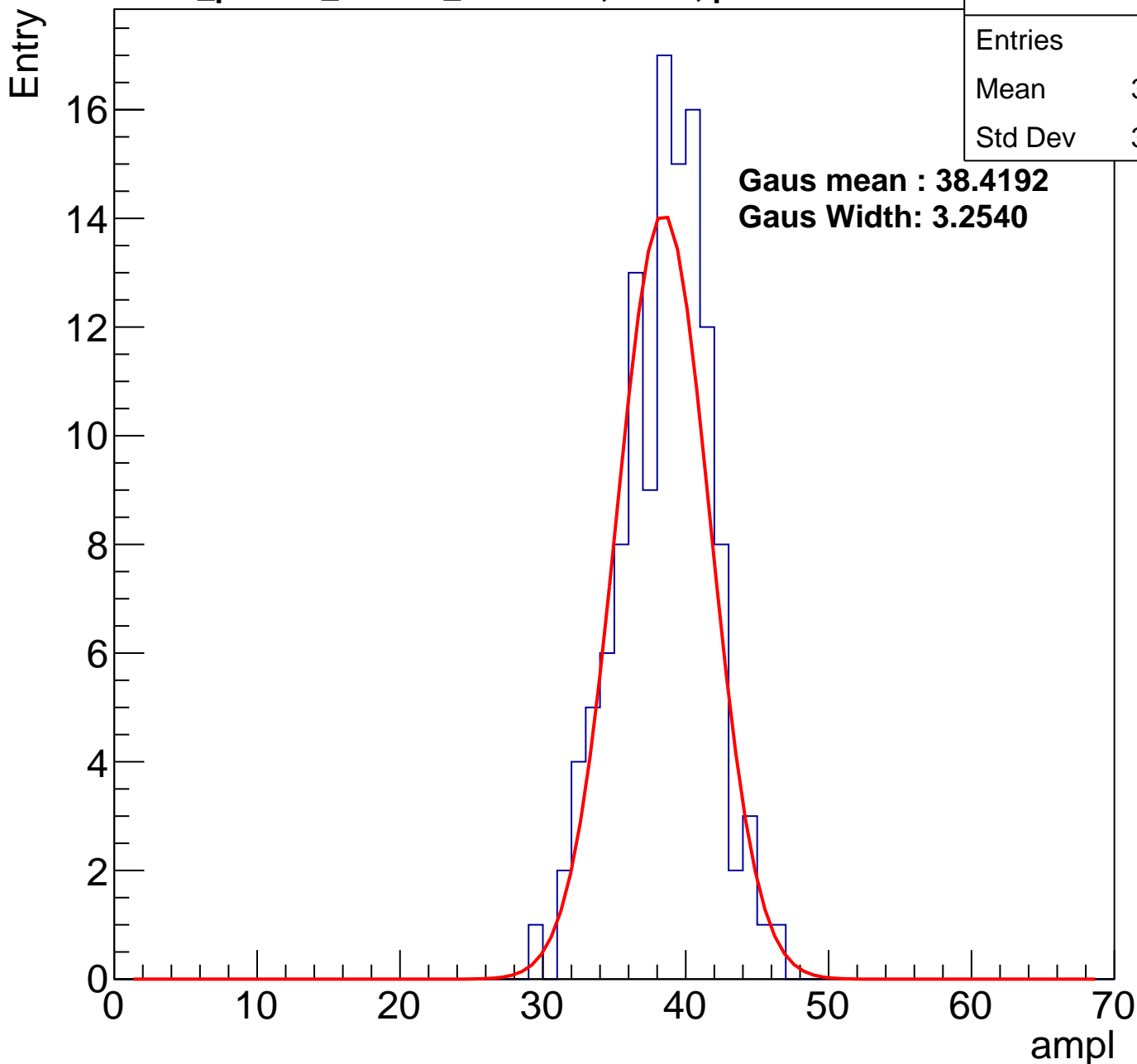
B1L001S, U21-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	38.02
Std Dev	3.204

Gaus mean : 38.4192

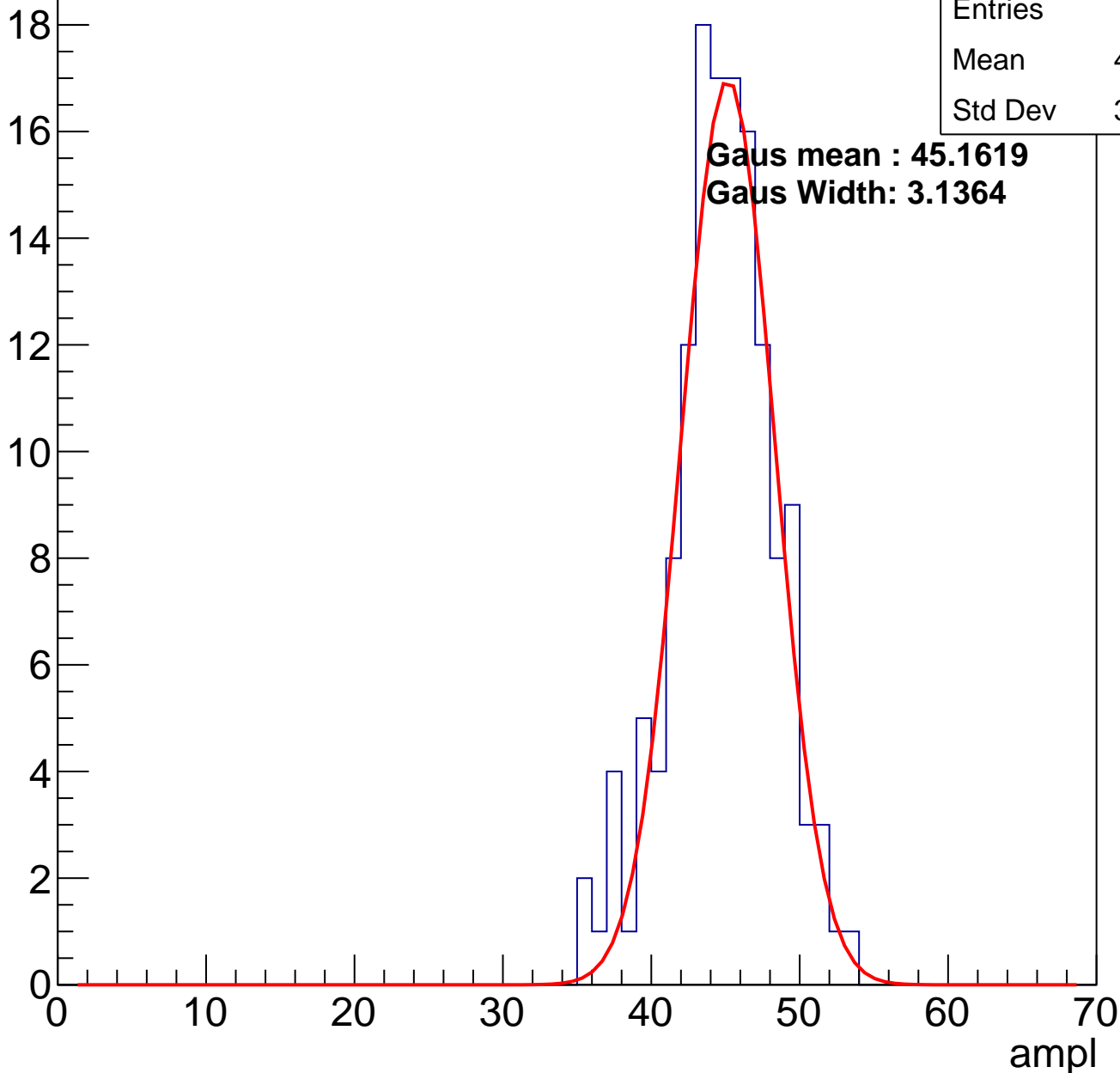
Gaus Width: 3.2540



B1L001S, U21-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

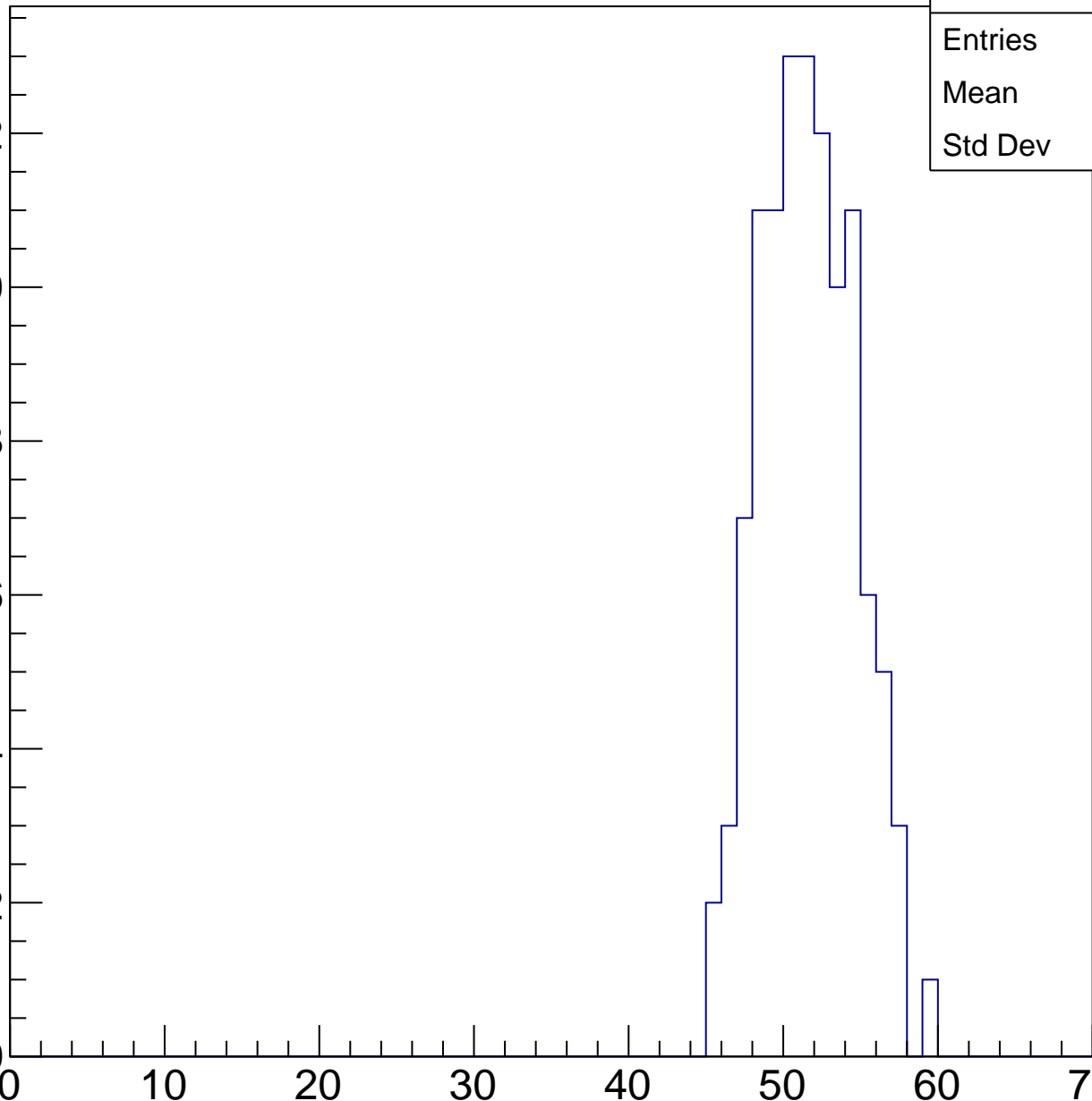
40

50

60

ampl

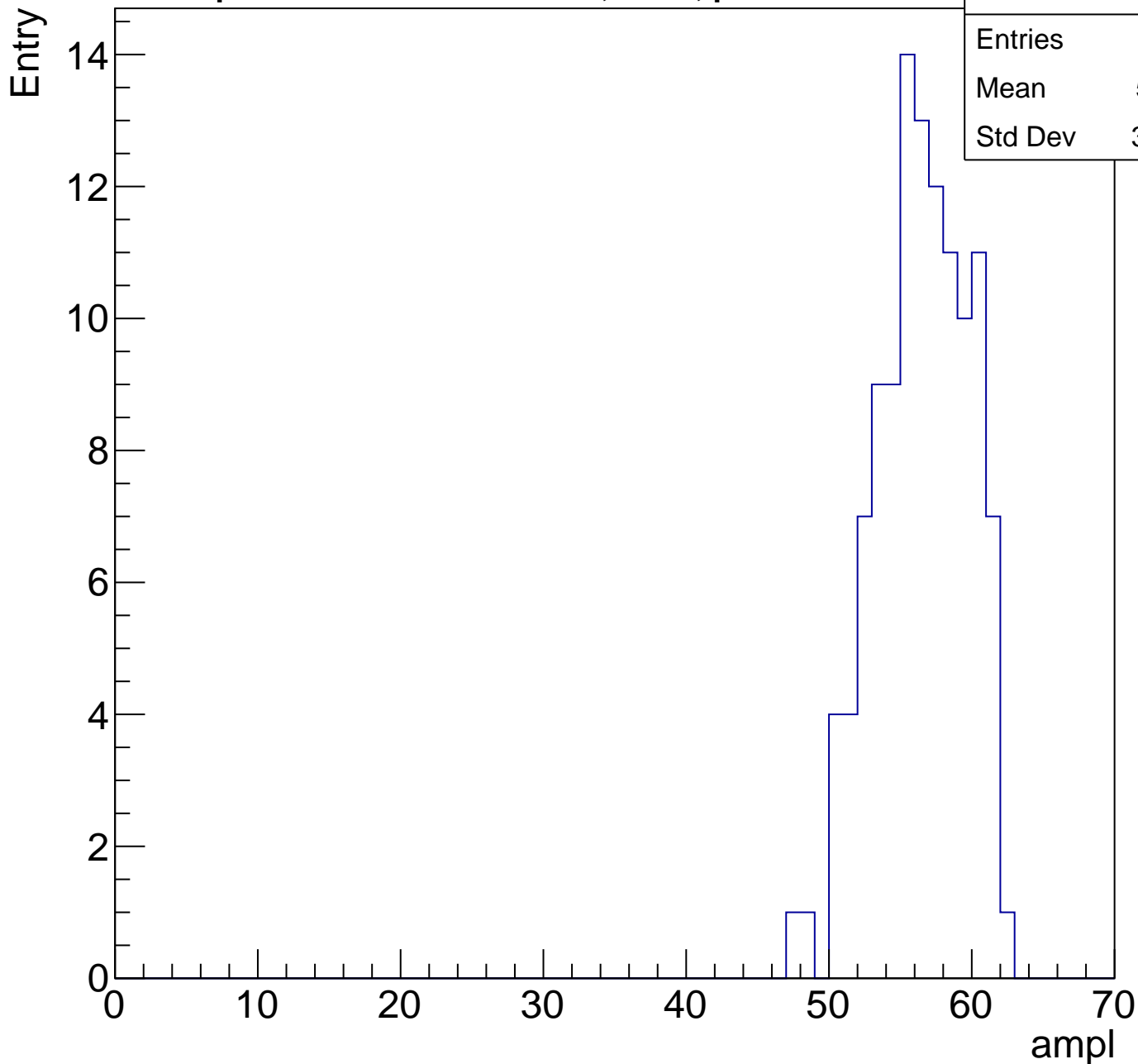
Entries	108
Mean	51.16
Std Dev	2.976



B1L001S, U21-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	56.01
Std Dev	3.197



B1L001S, U21-ch63, adc5

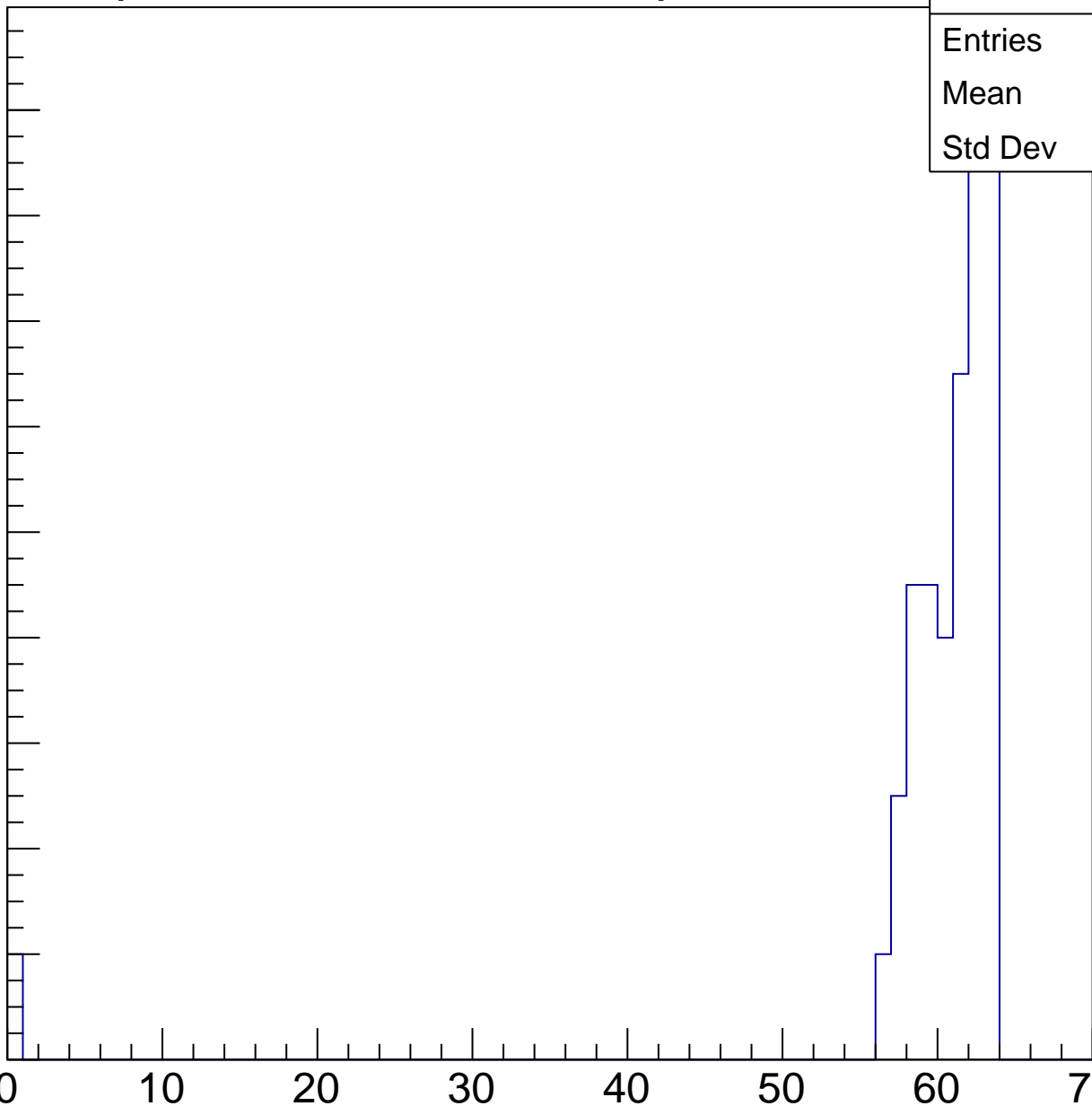
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.24
Std Dev	9.408

ampl



B1L001S, U21-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

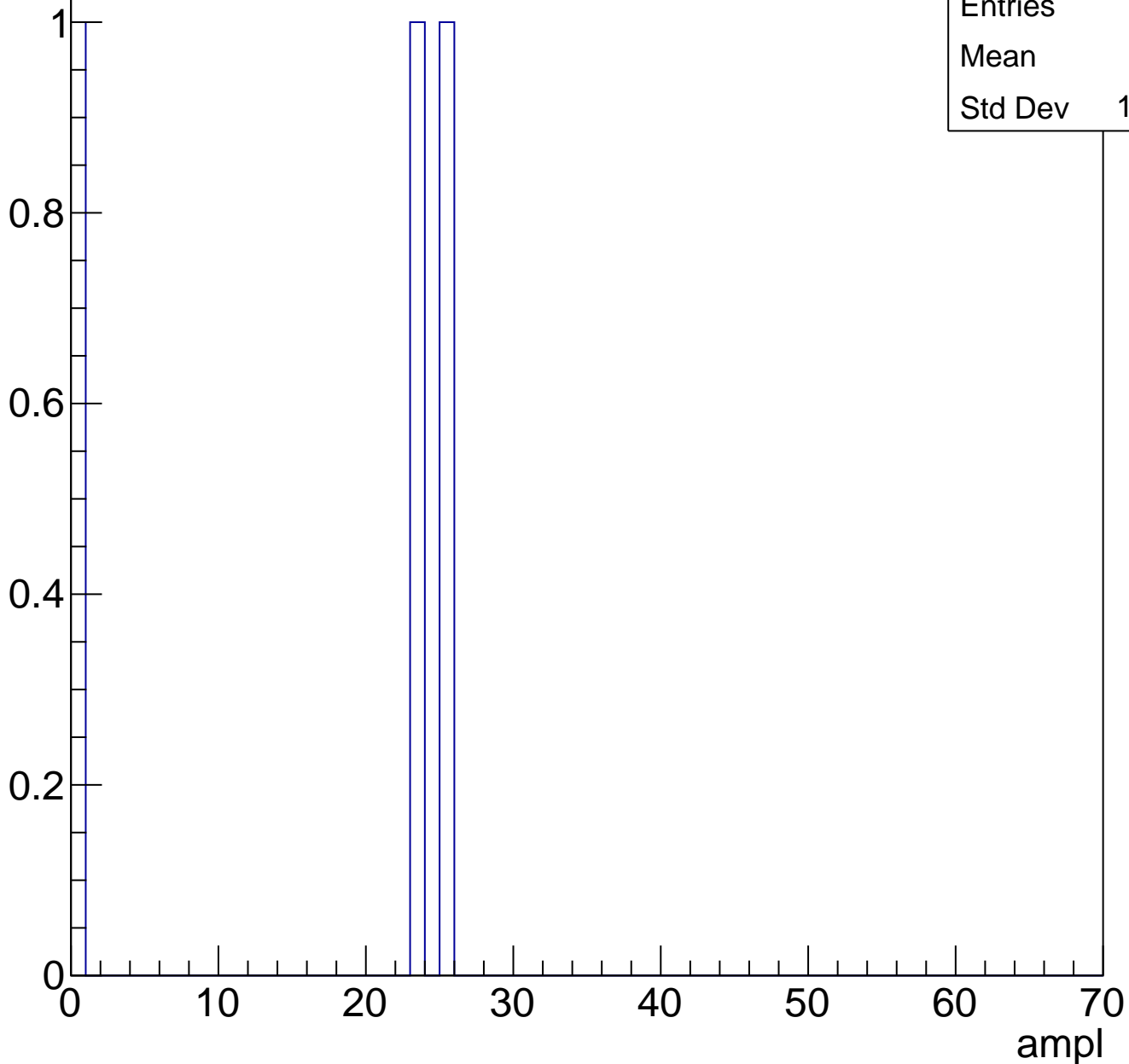
Entry



B1L001S, U21-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	31.27
Std Dev	3.269

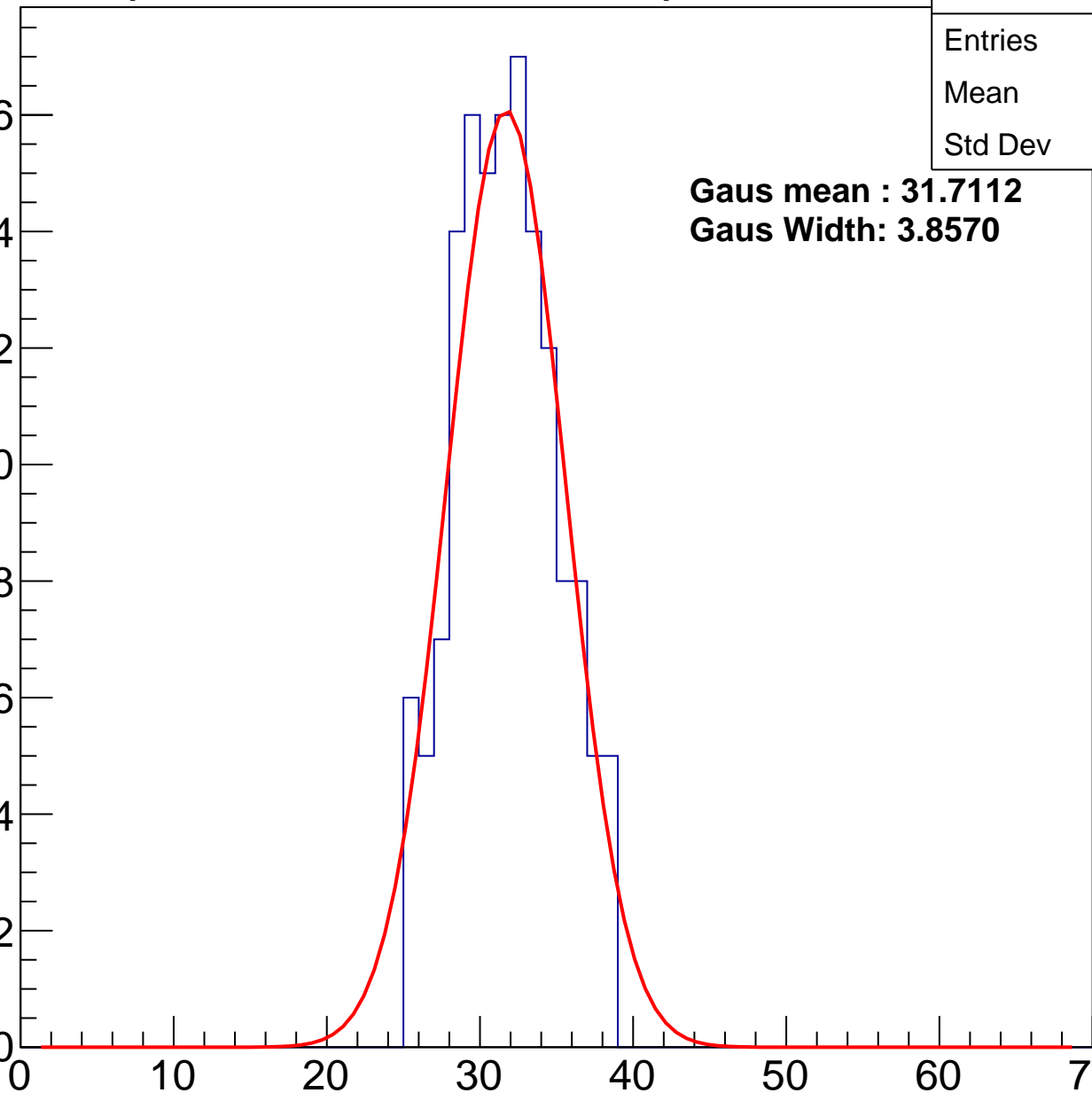
Gaus mean : 31.7112

Gaus Width: 3.8570

Entry

16
14
12
10
8
6
4
2
0

ampl

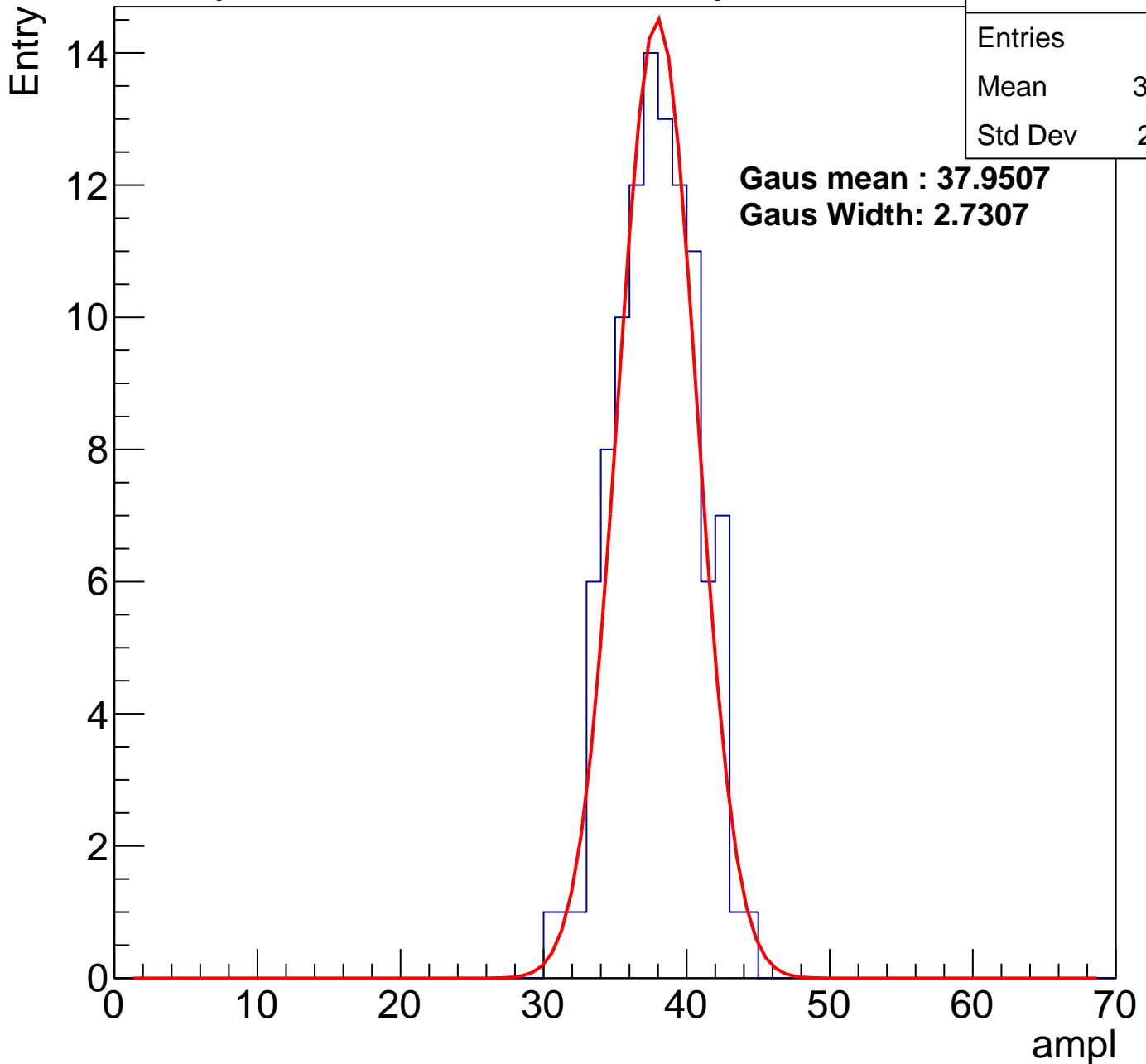


B1L001S, U21-ch64, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	37.42
Std Dev	2.821

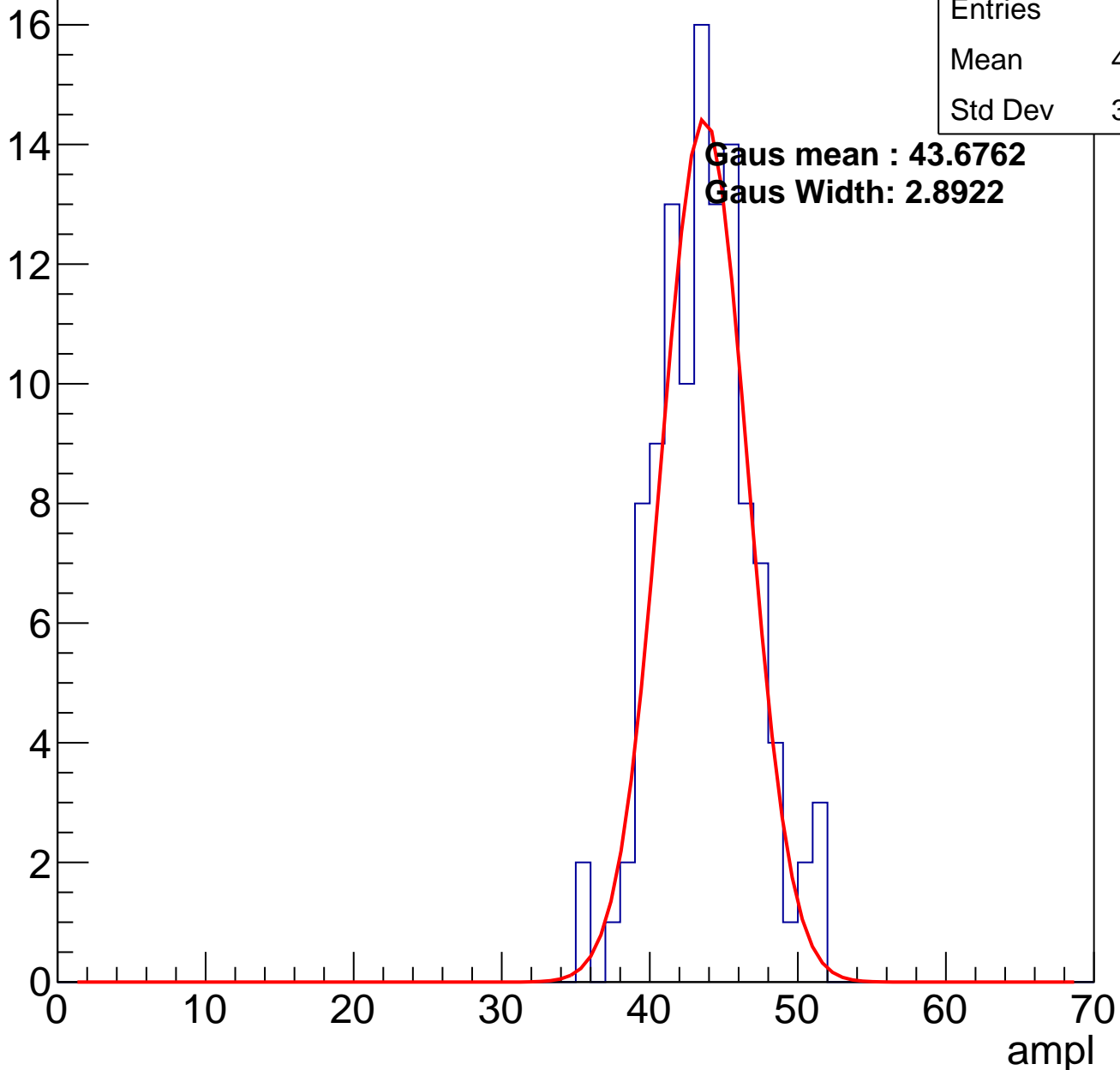
Gaus mean : 37.9507
Gaus Width: 2.7307



B1L001S, U21-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

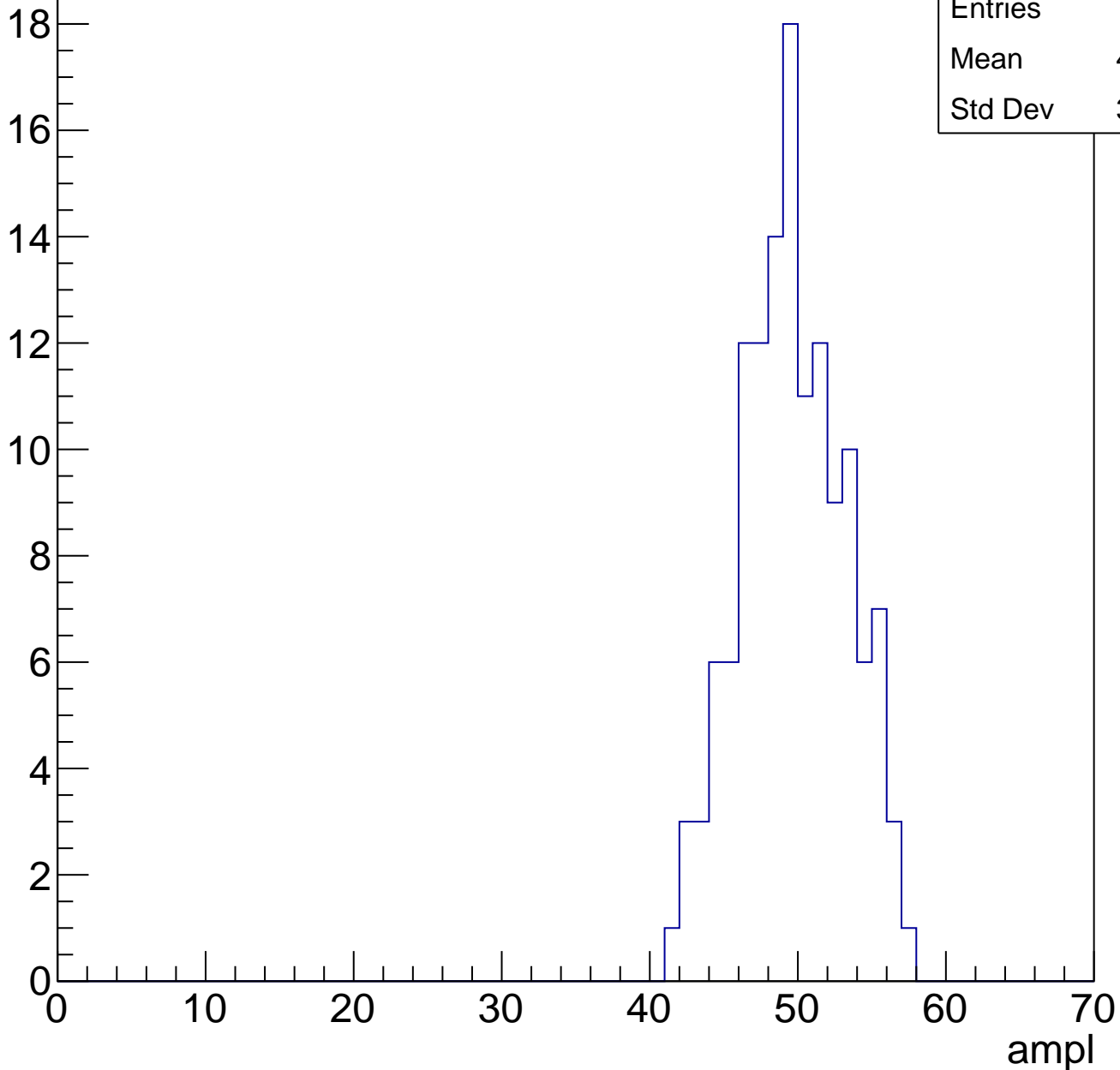
Entry



B1L001S, U21-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	134
Mean	49.21
Std Dev	3.501

B1L001S, U21-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

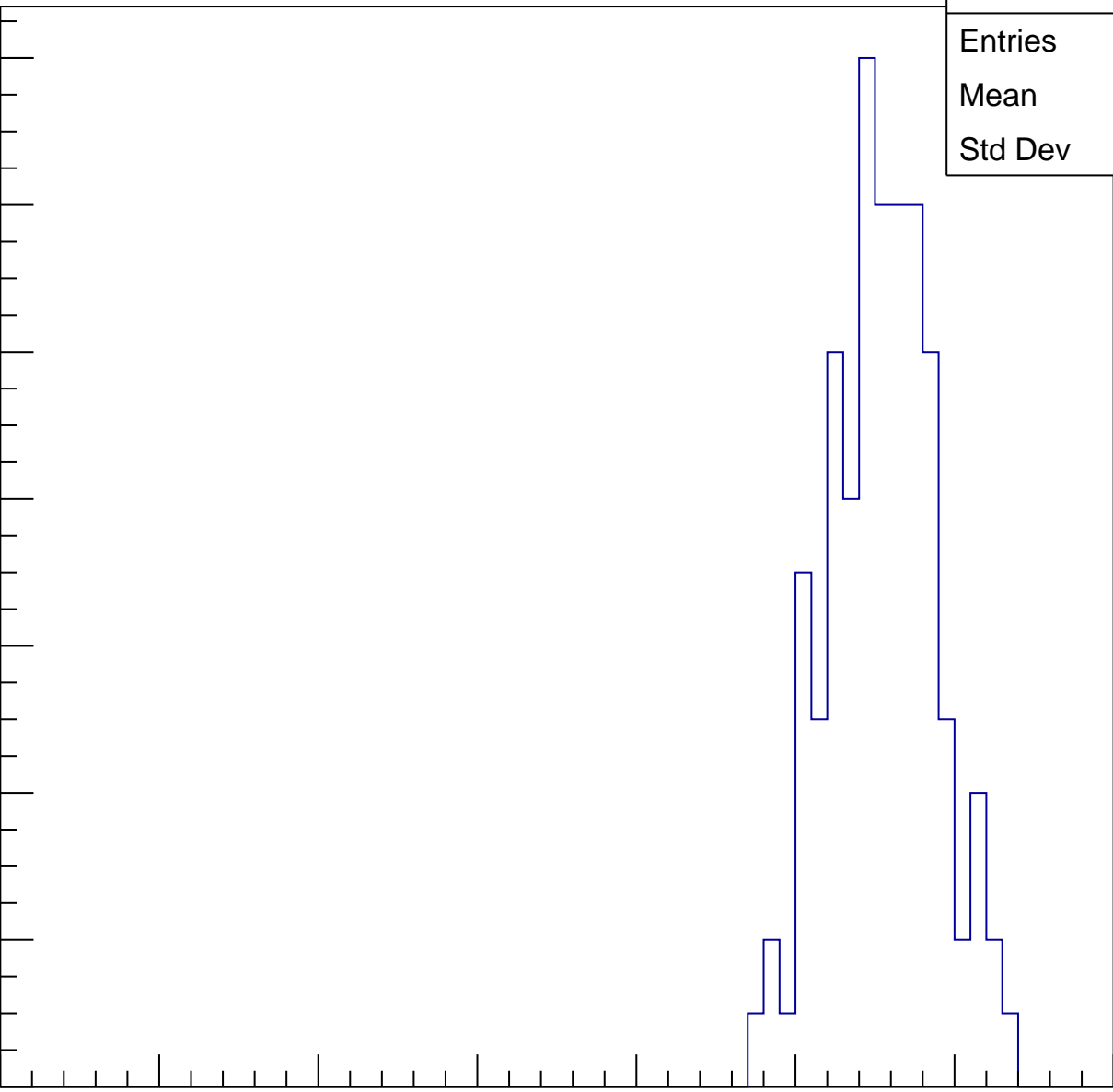
Entries	108
Mean	54.99
Std Dev	3.298

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch64, adc5

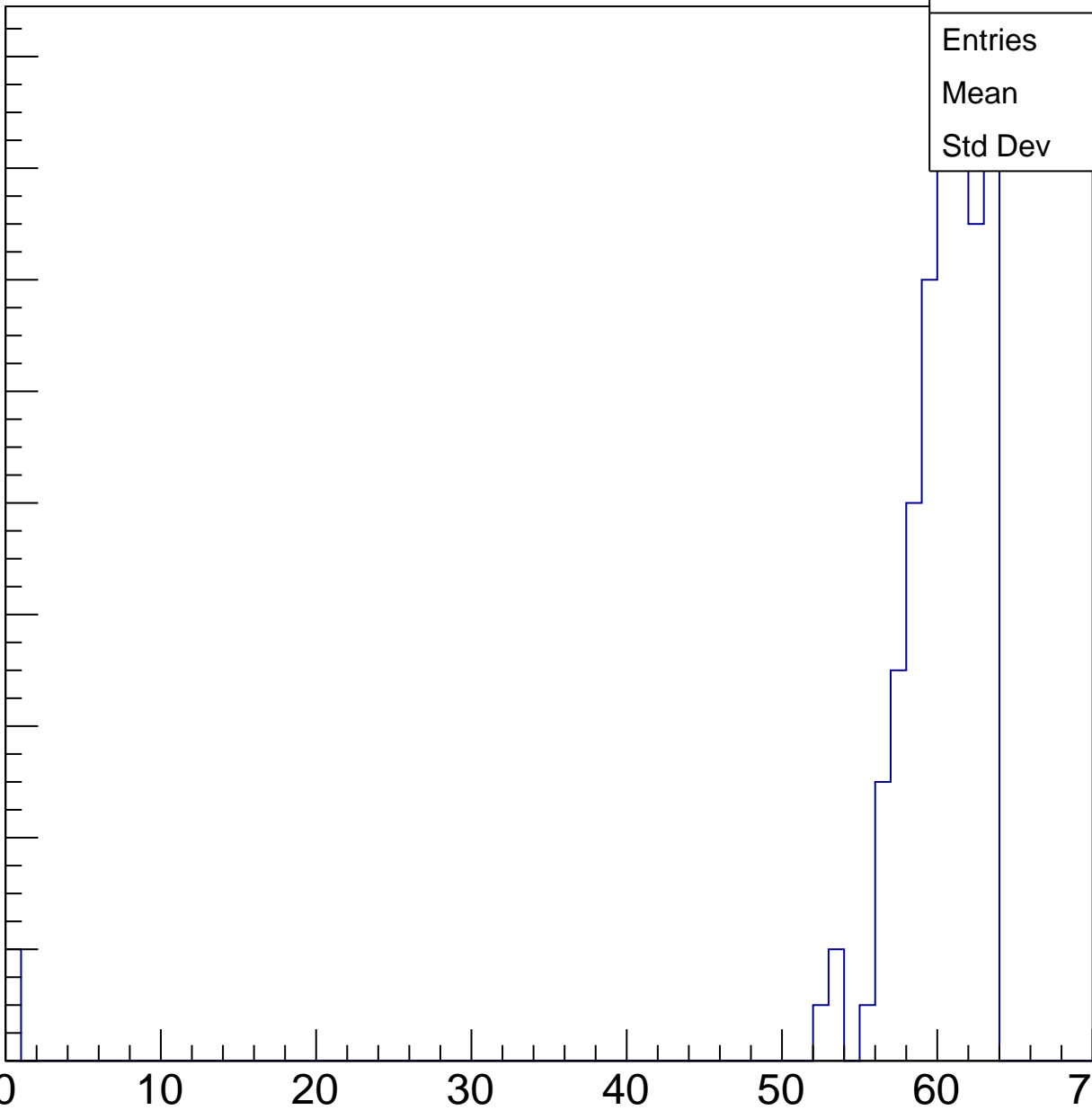
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.86
Std Dev	8.474

ampl



B1L001S, U21-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61
Std Dev	1.414

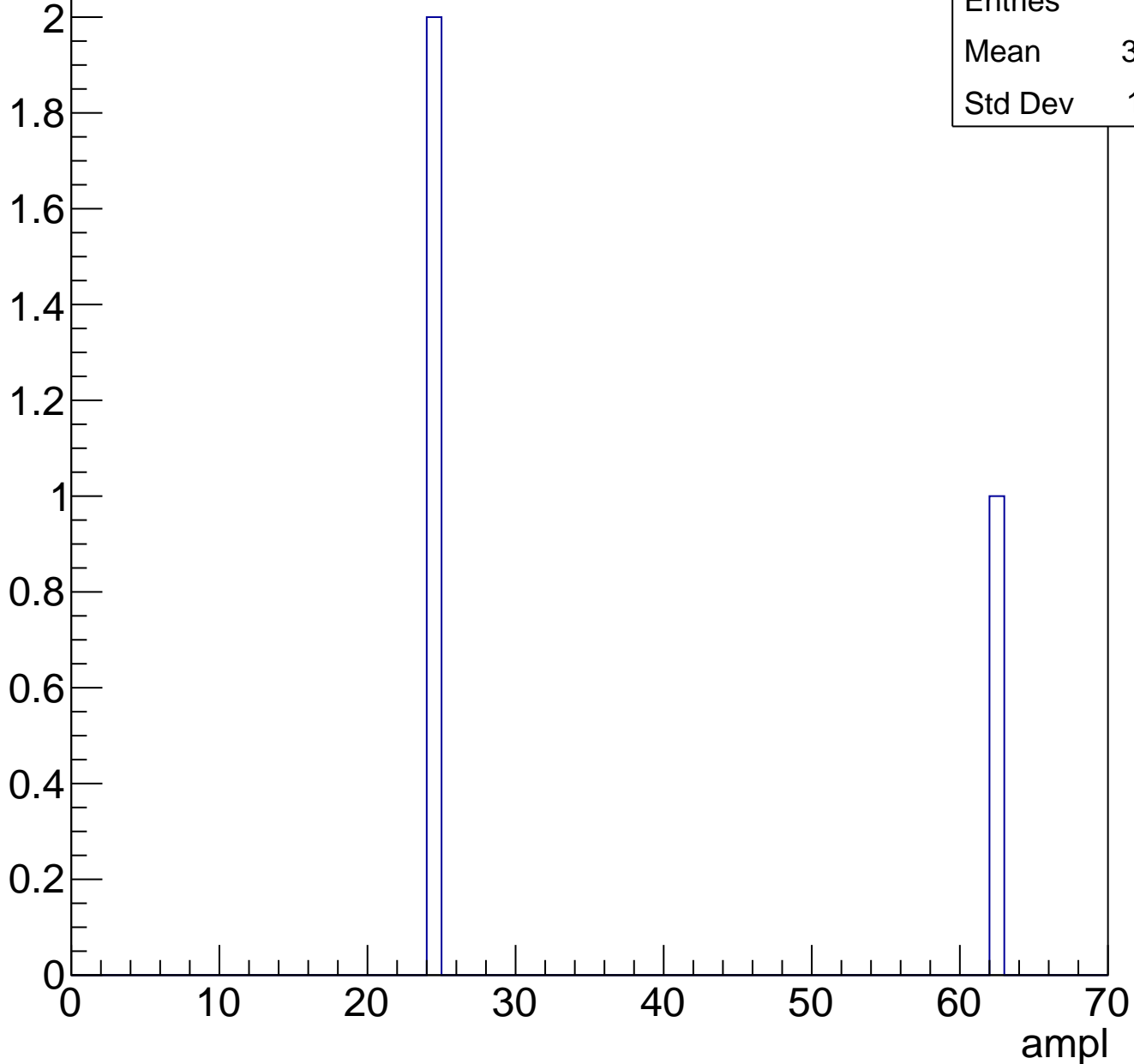
ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	36.67
Std Dev	17.91

B1L001S, U21-ch65, adc0

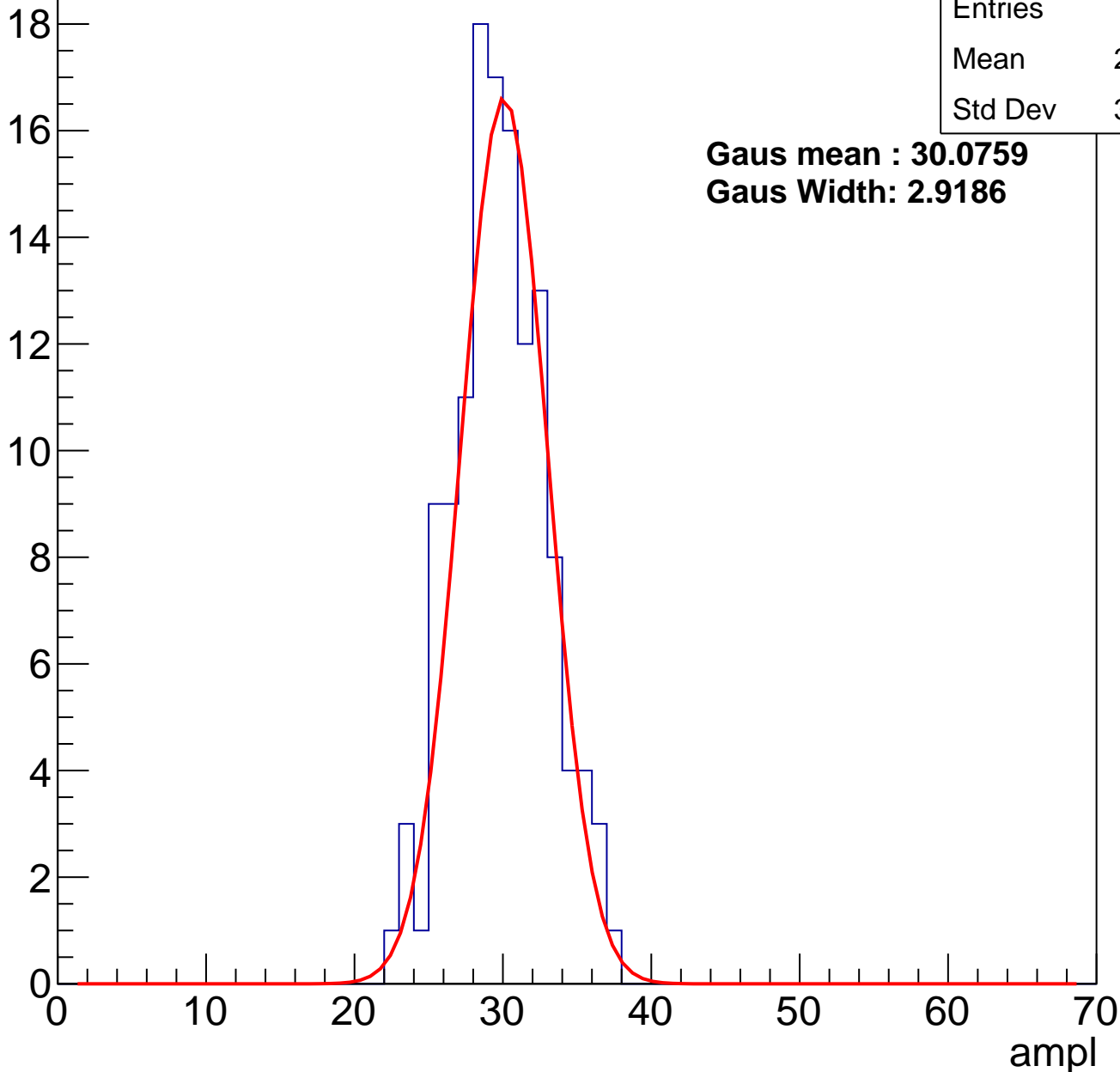
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	29.39
Std Dev	3.055

Gaus mean : 30.0759

Gaus Width: 2.9186

Entry



B1L001S, U21-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	36.22
Std Dev	3.471

Gaus mean : 36.4327

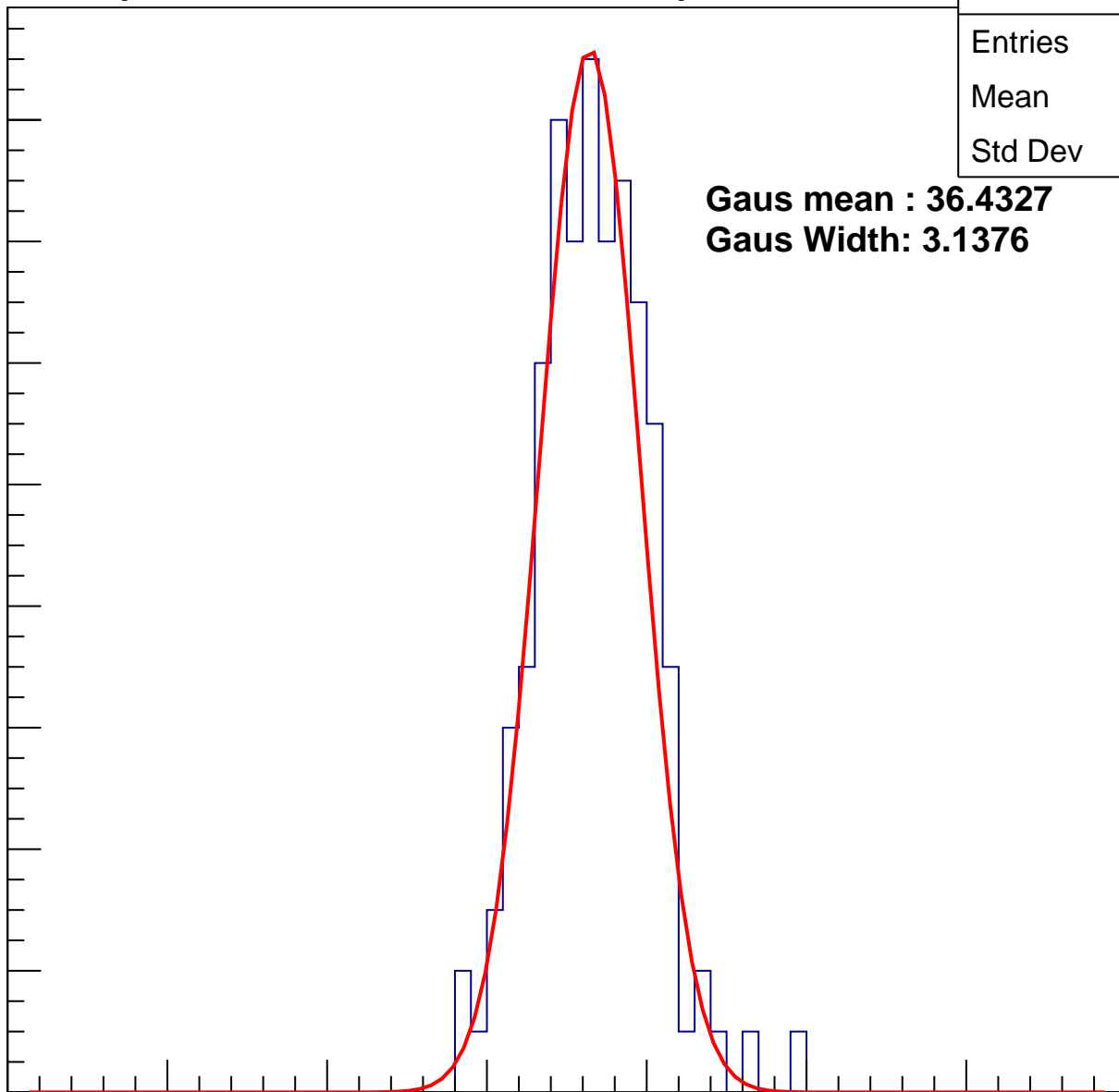
Gaus Width: 3.1376

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch65, adc2

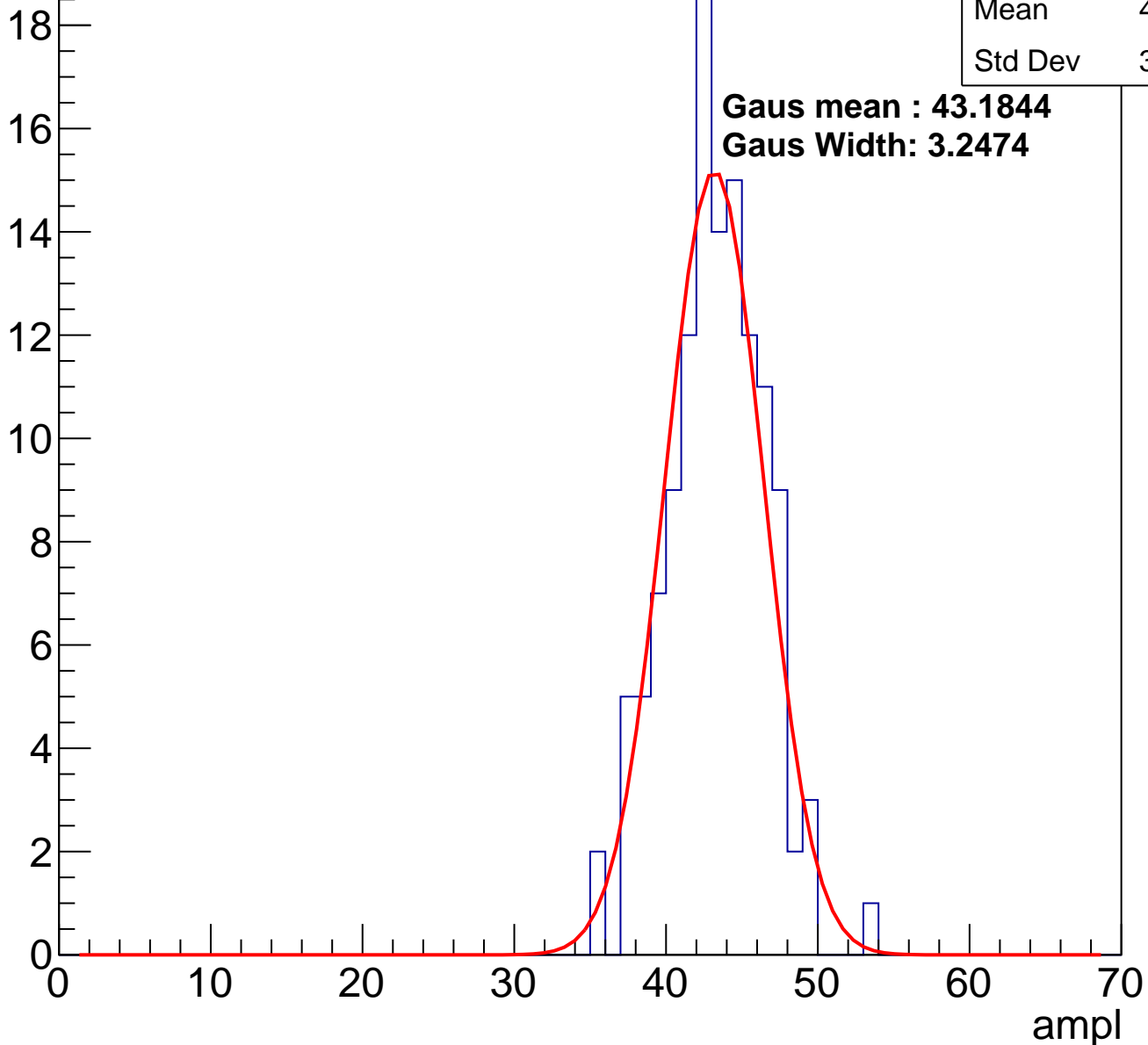
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	42.82
Std Dev	3.153

Gaus mean : 43.1844

Gaus Width: 3.2474

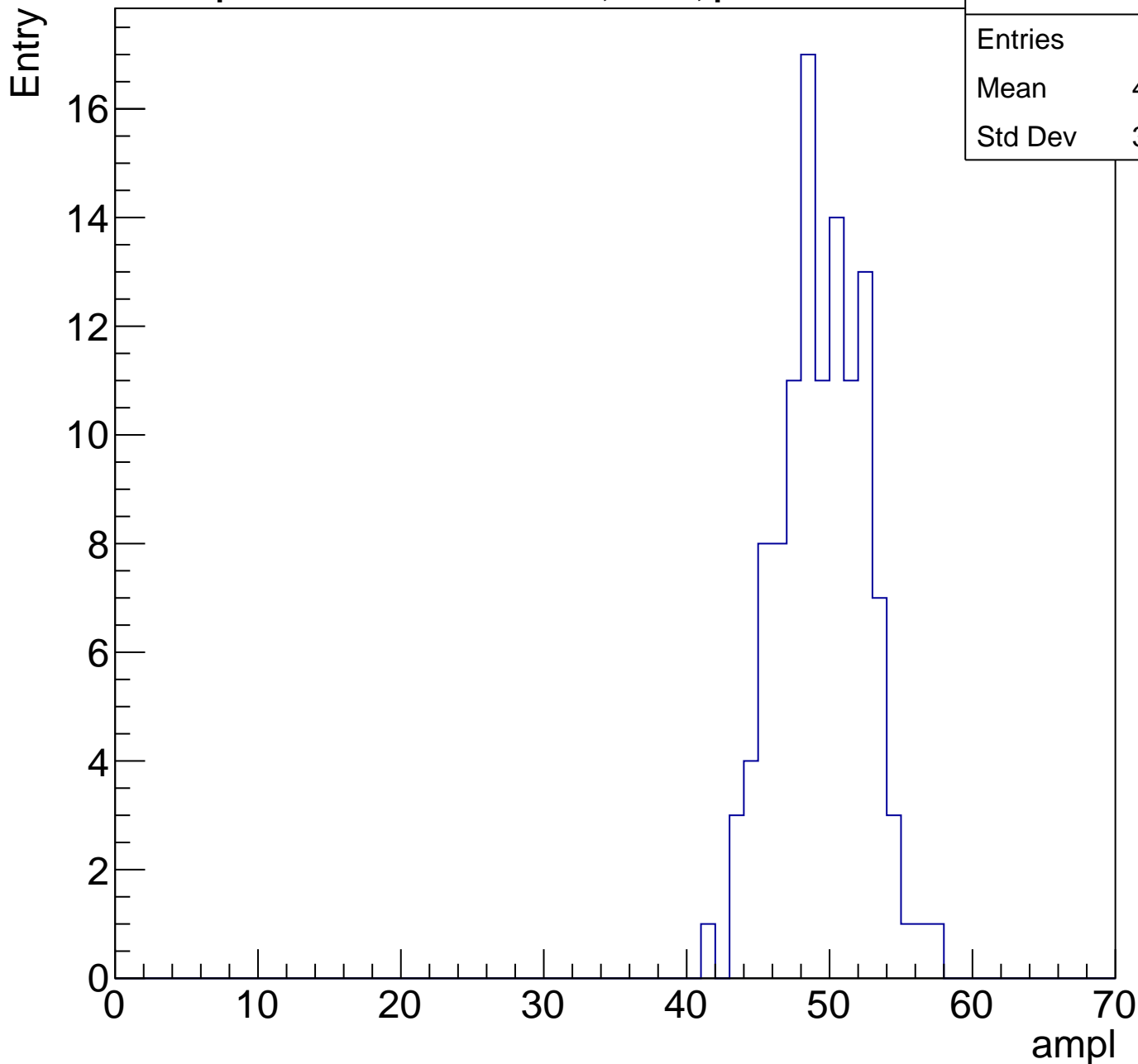
Entry



B1L001S, U21-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	48.98
Std Dev	3.026

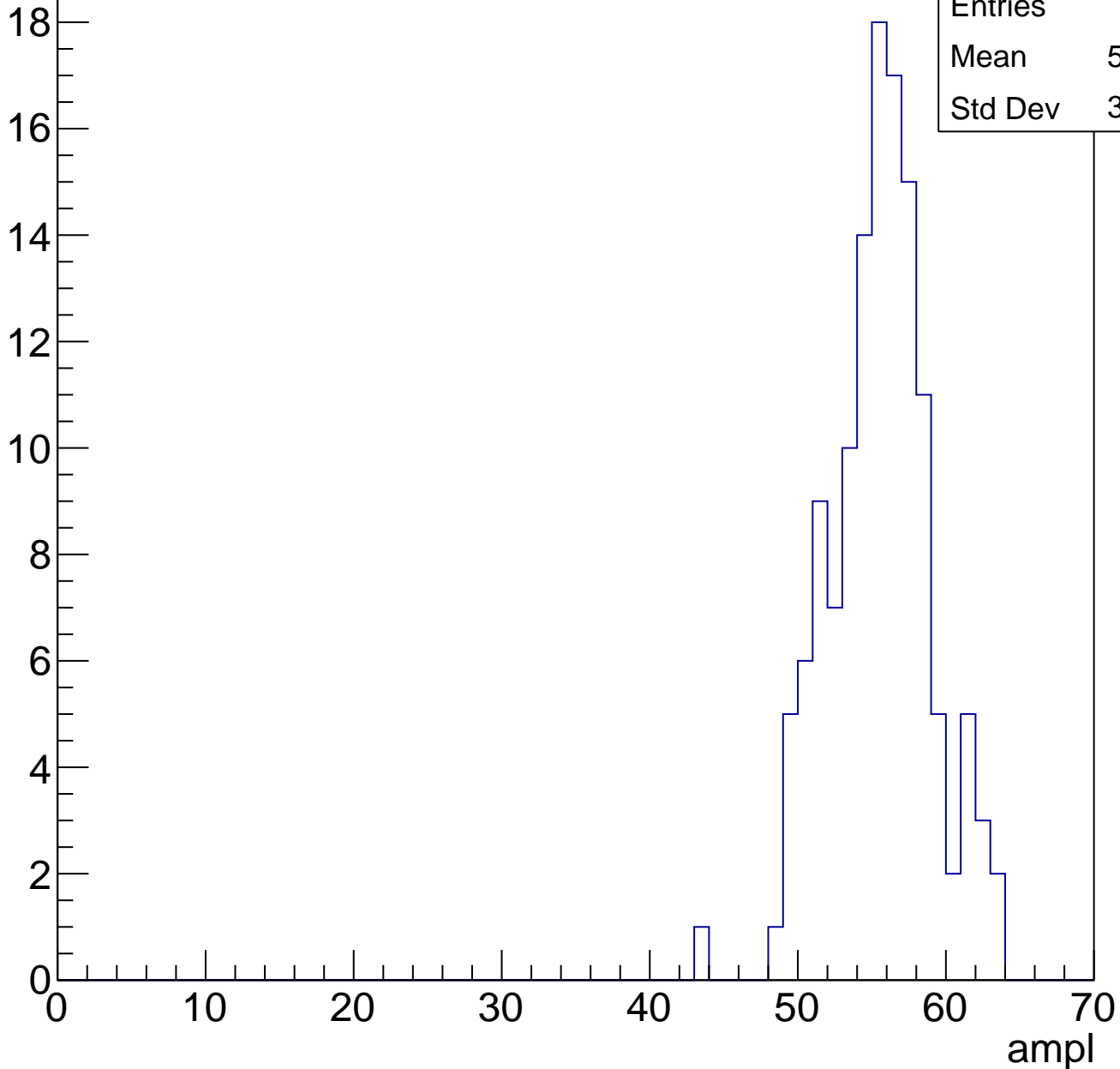


B1L001S, U21-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	55.05
Std Dev	3.434

Entry



B1L001S, U21-ch65, adc5

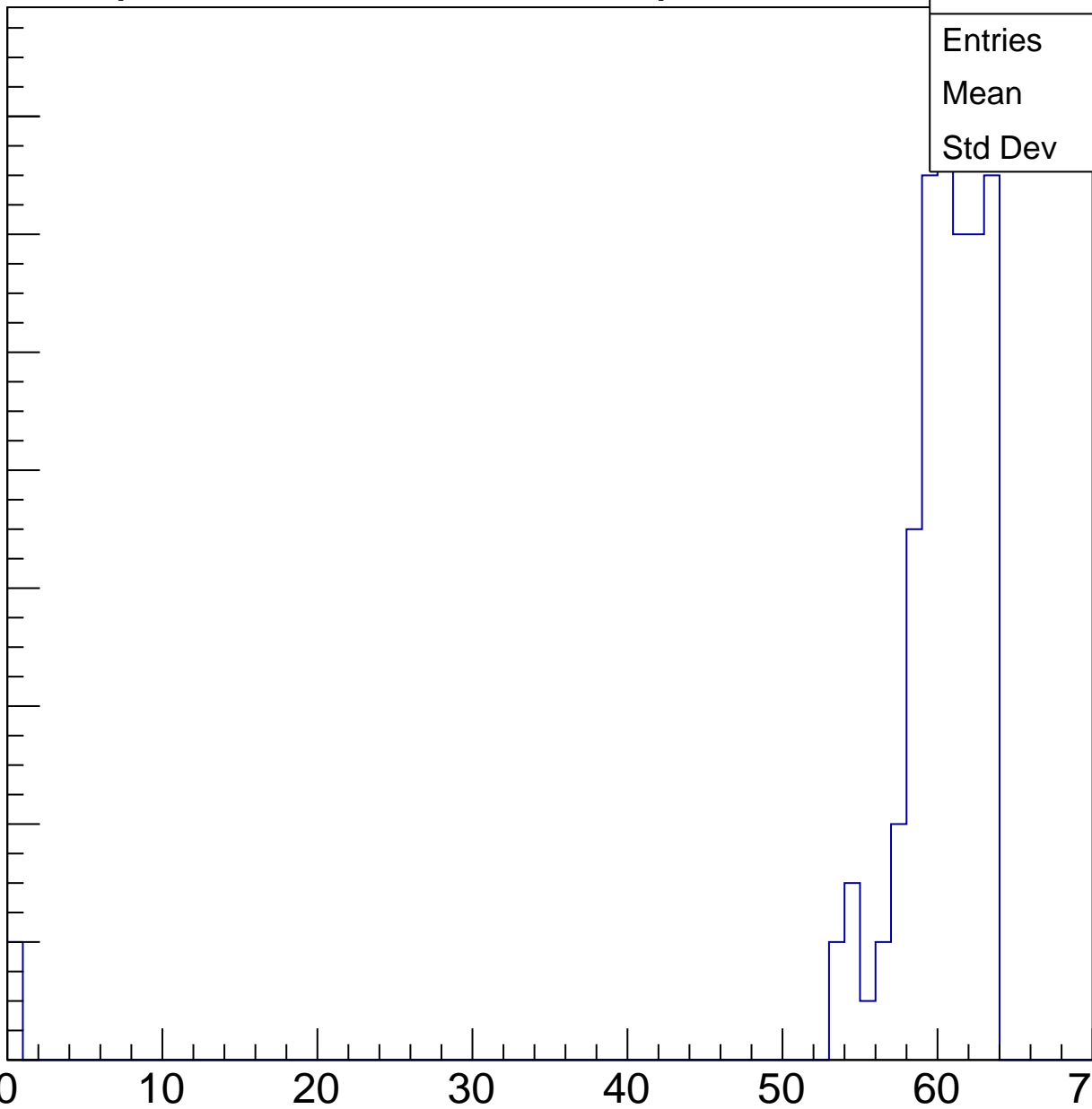
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.74
Std Dev	8.807

ampl



B1L001S, U21-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62

Std Dev

1

B1L001S, U21-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	21
Std Dev	0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch66, adc0

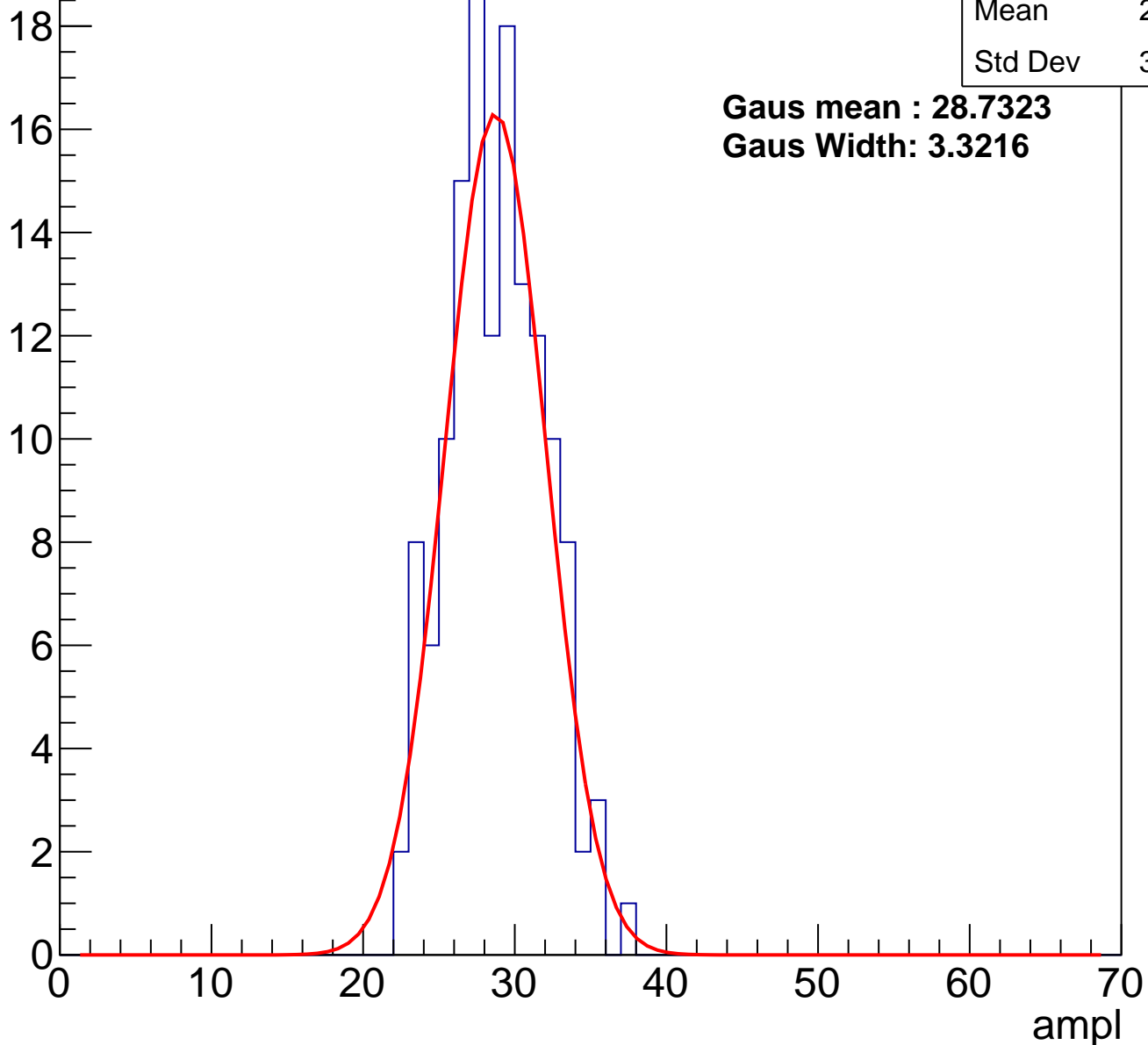
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	28.34
Std Dev	3.127

Gaus mean : 28.7323

Gaus Width: 3.3216

Entry



B1L001S, U21-ch66, adc1

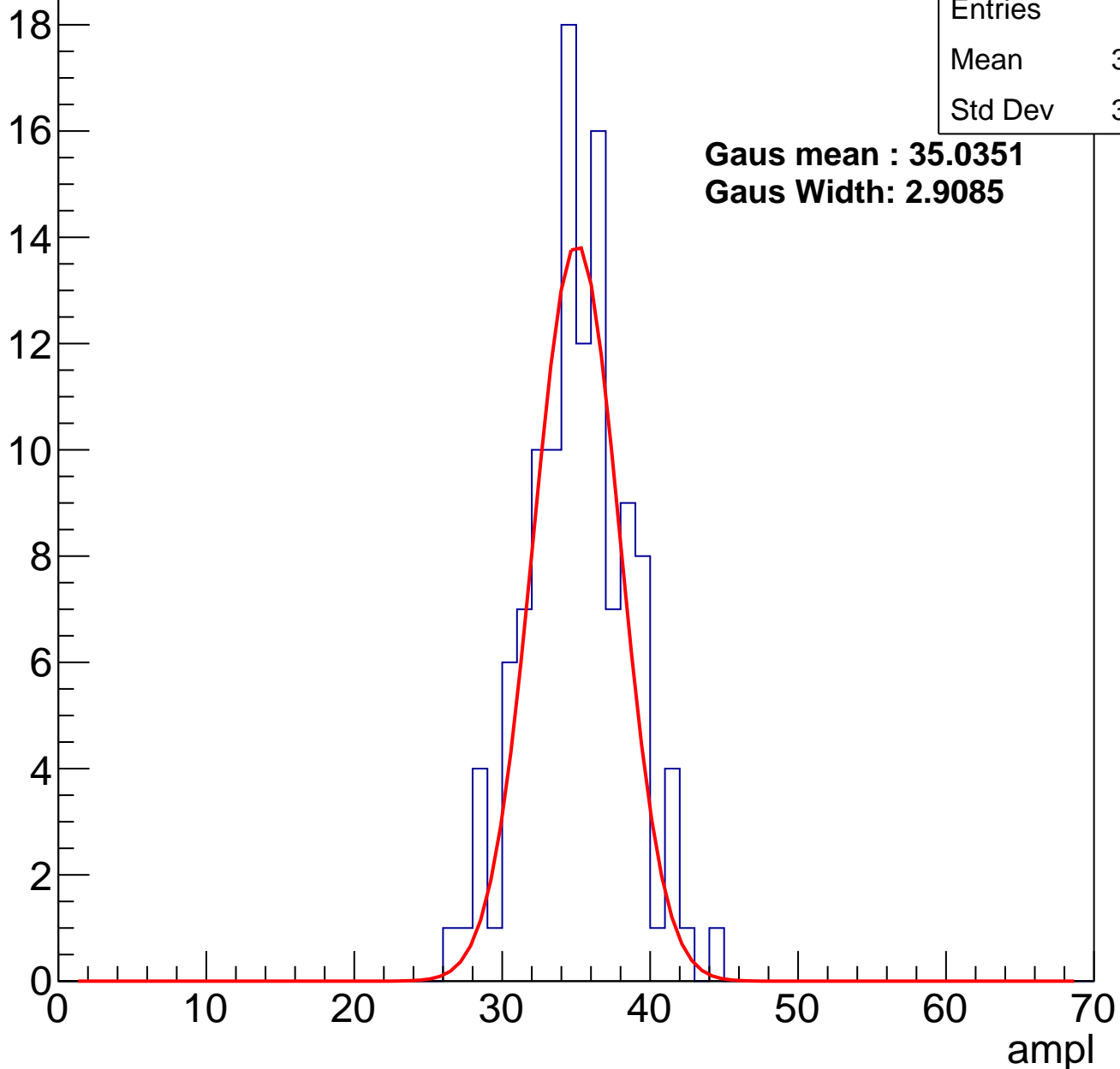
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	34.63
Std Dev	3.363

Gaus mean : 35.0351

Gaus Width: 2.9085

Entry



B1L001S, U21-ch66, adc2

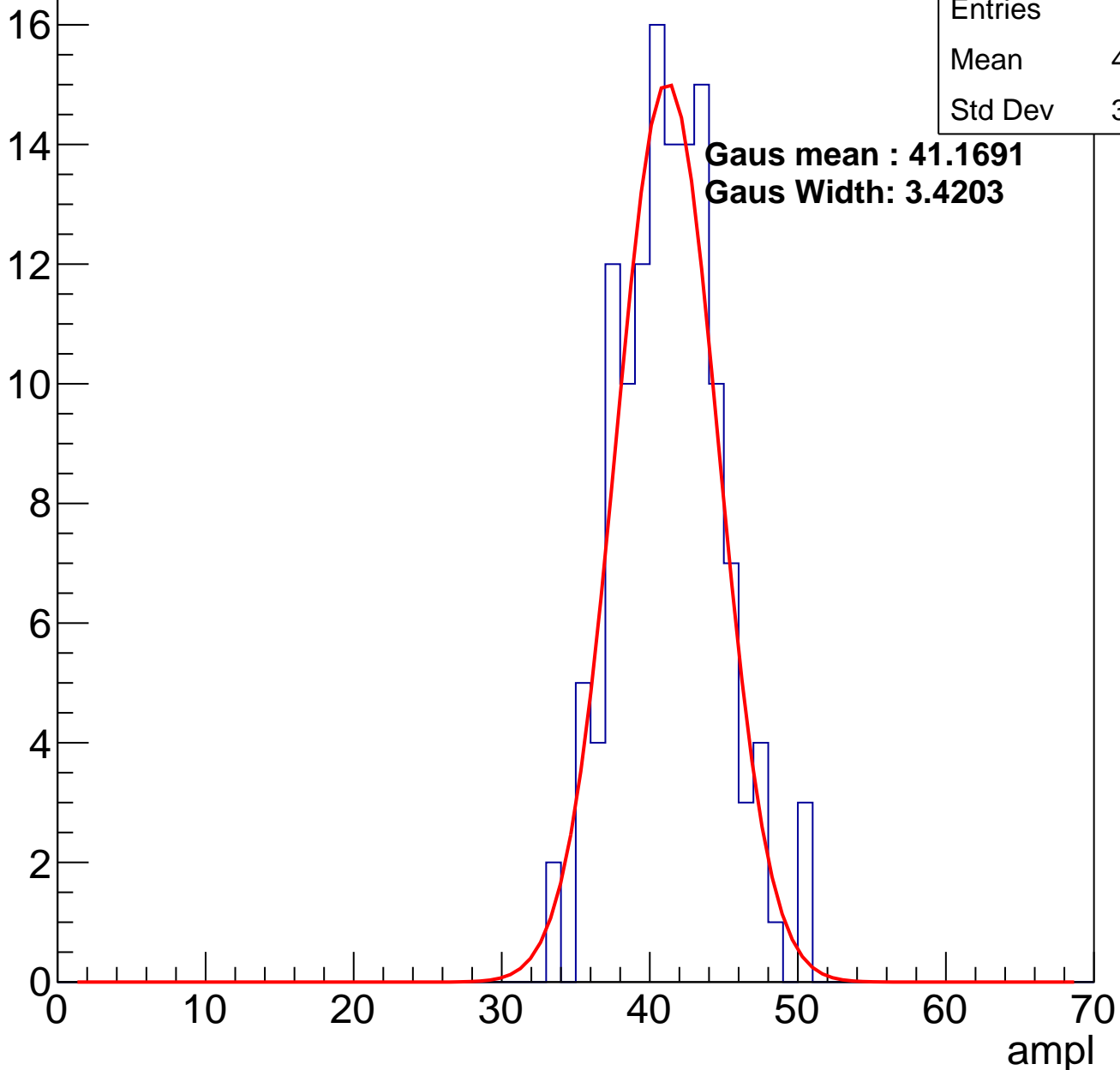
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	40.93
Std Dev	3.416

Gaus mean : 41.1691

Gaus Width: 3.4203

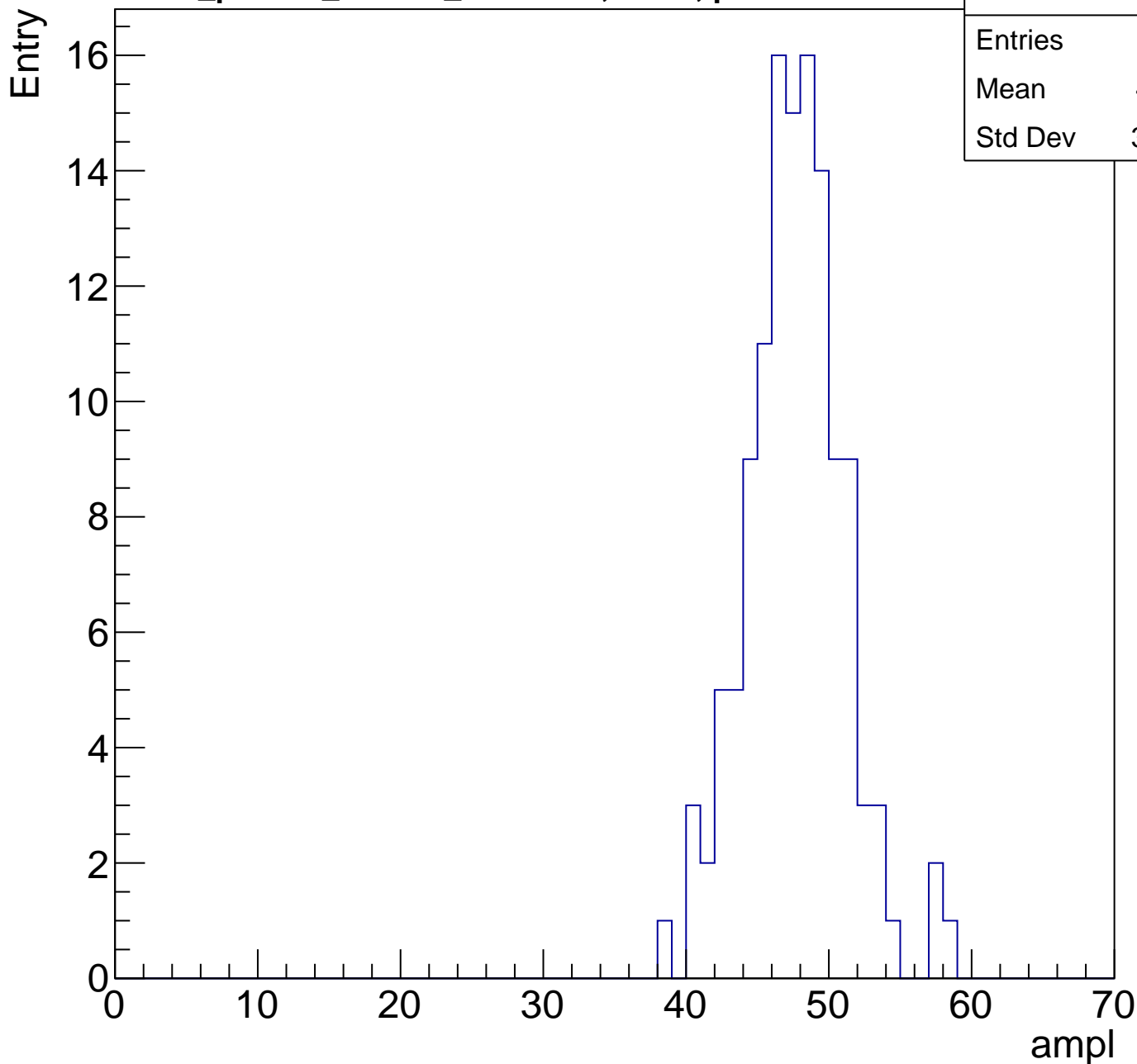
Entry



B1L001S, U21-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

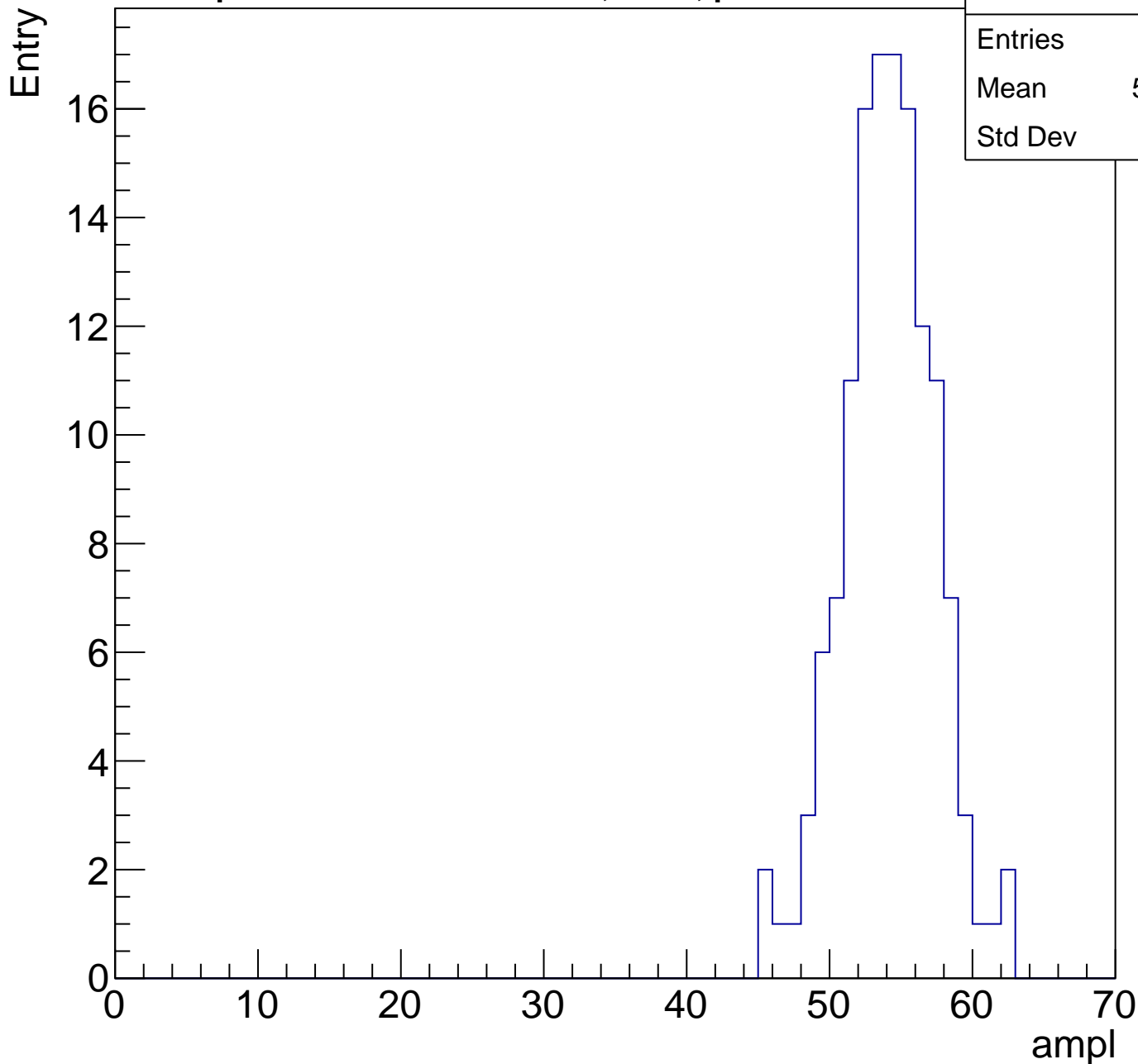
Entries	125
Mean	47.21
Std Dev	3.453



B1L001S, U21-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	53.66
Std Dev	3.2



B1L001S, U21-ch66, adc5

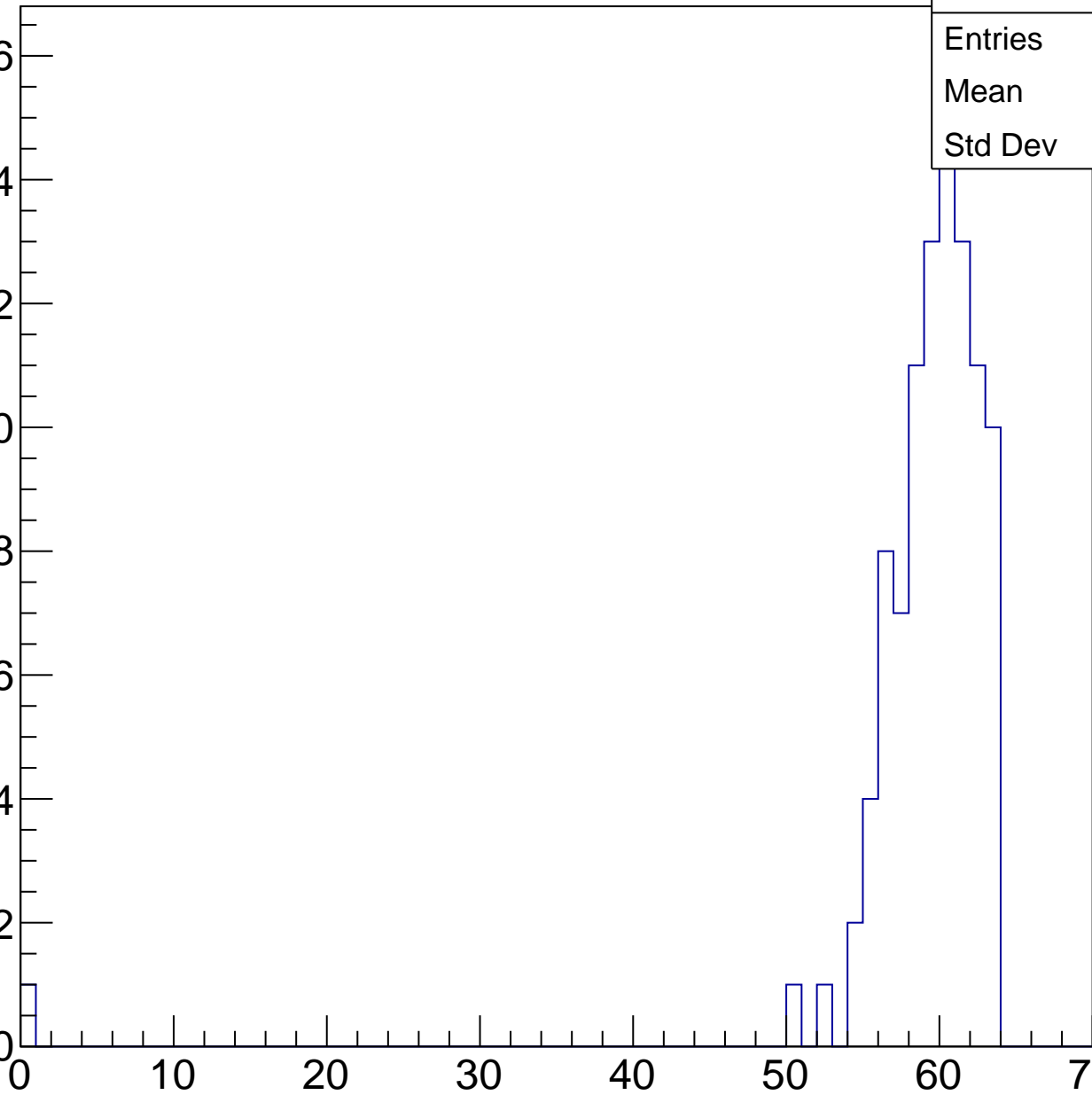
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.64
Std Dev	6.508

ampl

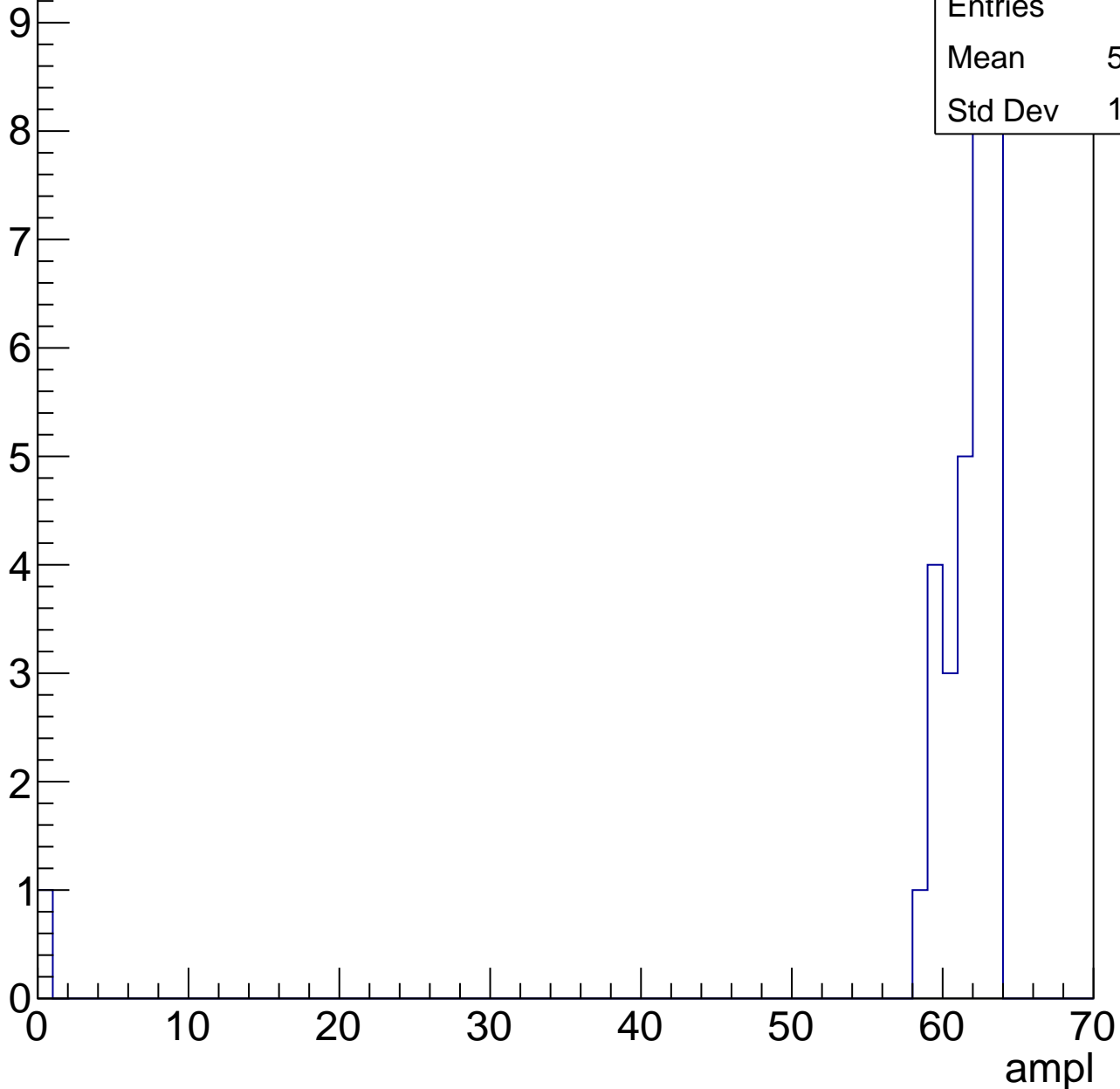


B1L001S, U21-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	31
Mean	59.42
Std Dev	10.95



B1L001S, U21-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	34.67
Std Dev	20.04

ampl

B1L001S, U21-ch67, adc0

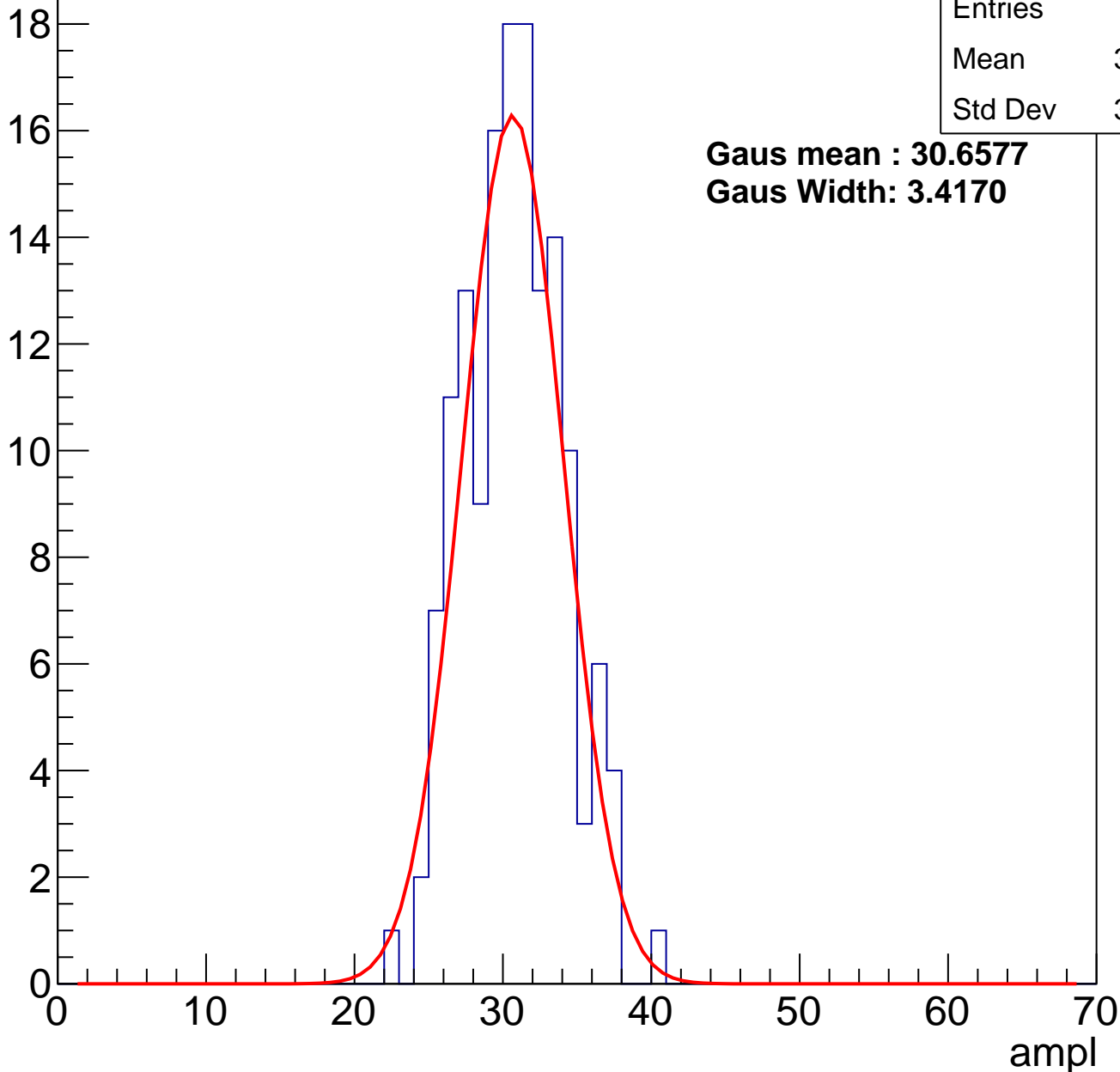
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	30.29
Std Dev	3.296

Gaus mean : 30.6577

Gaus Width: 3.4170

Entry



B1L001S, U21-ch67, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	37.29
Std Dev	3.326

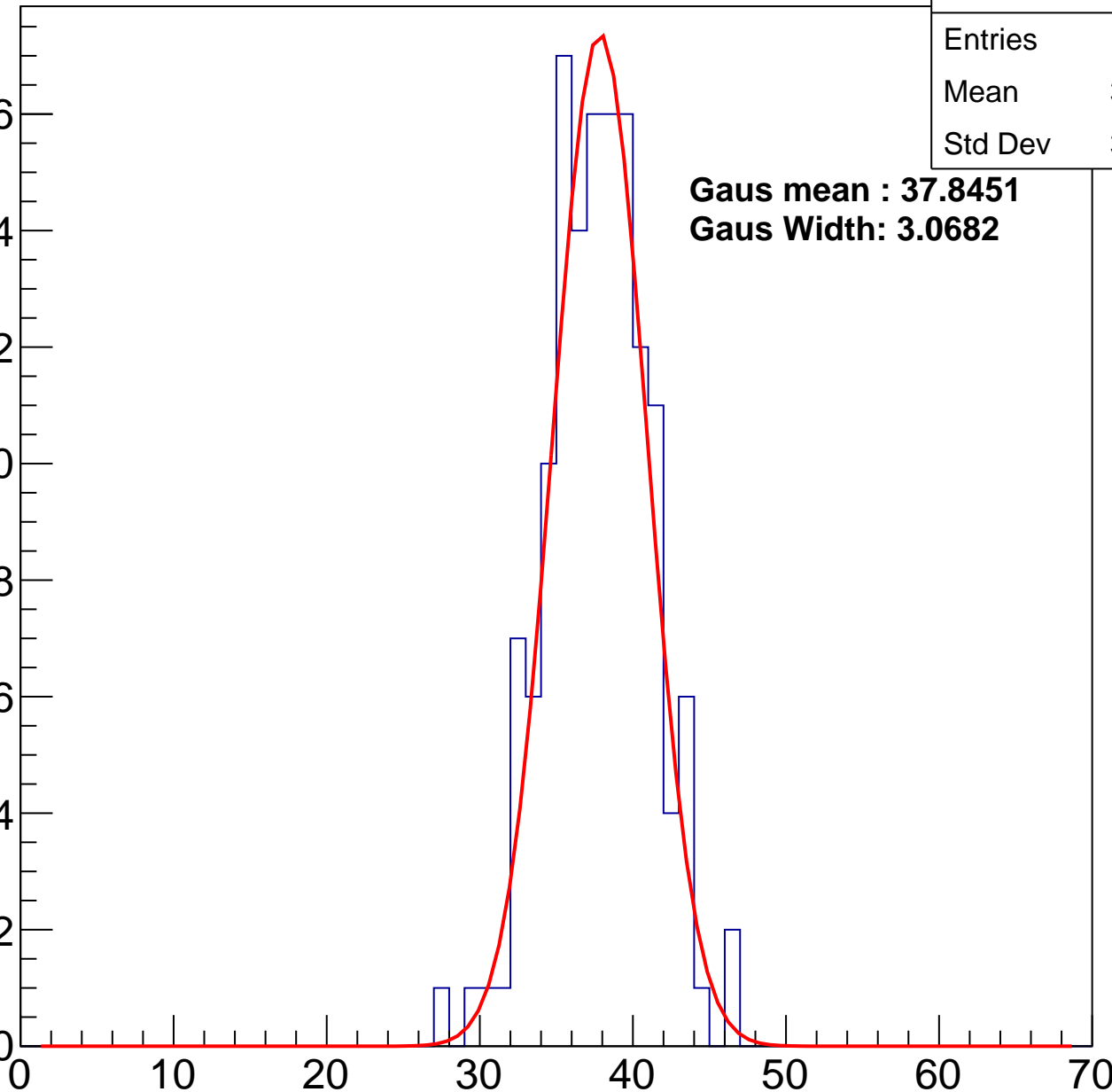
Gaus mean : 37.8451

Gaus Width: 3.0682

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	43.9
Std Dev	3.142

Gaus mean : 44.3667

Gaus Width: 3.1552

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

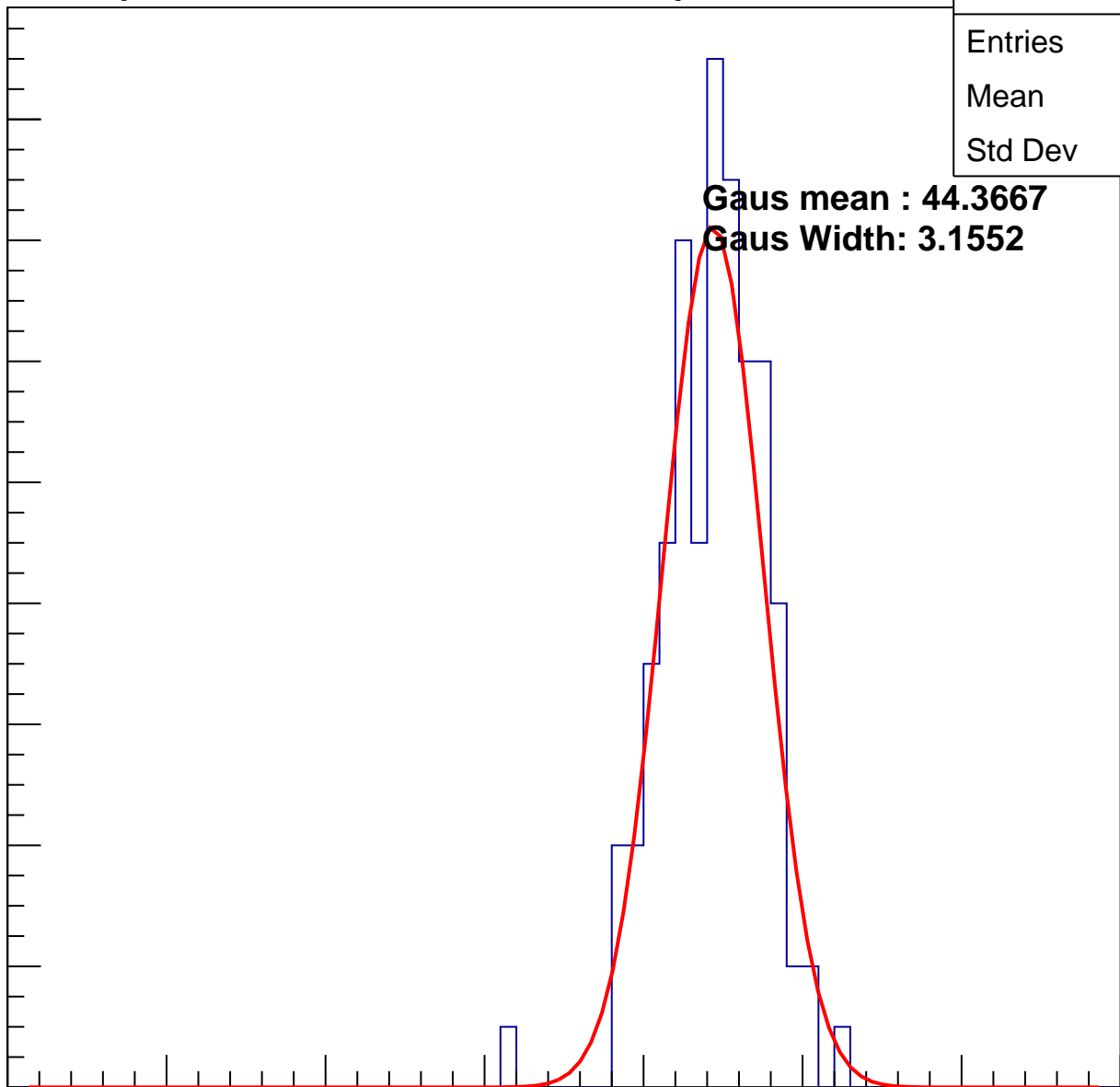
40

50

60

70

ampl



B1L001S, U21-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

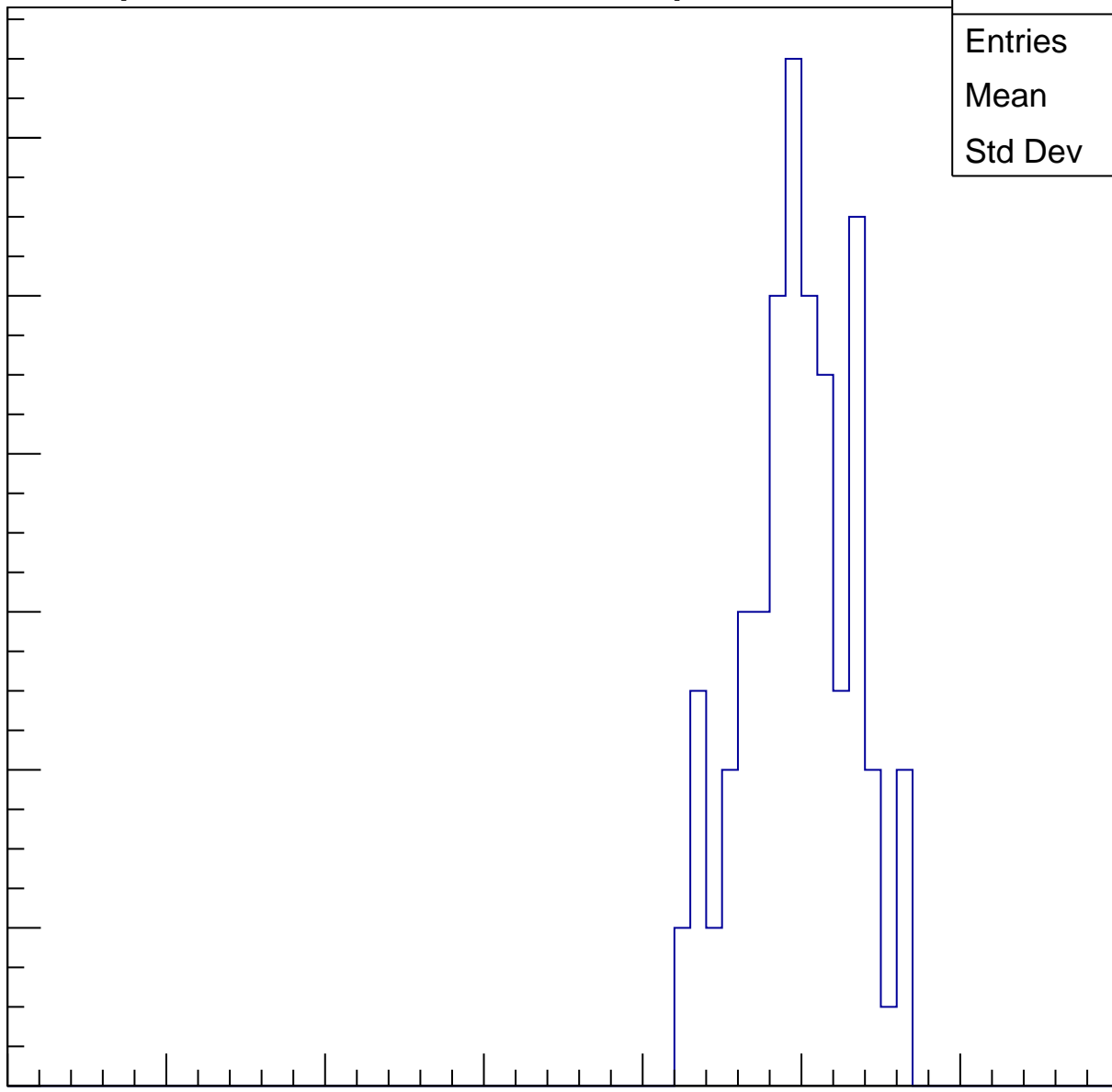
Entries	92
Mean	49.34
Std Dev	3.408

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

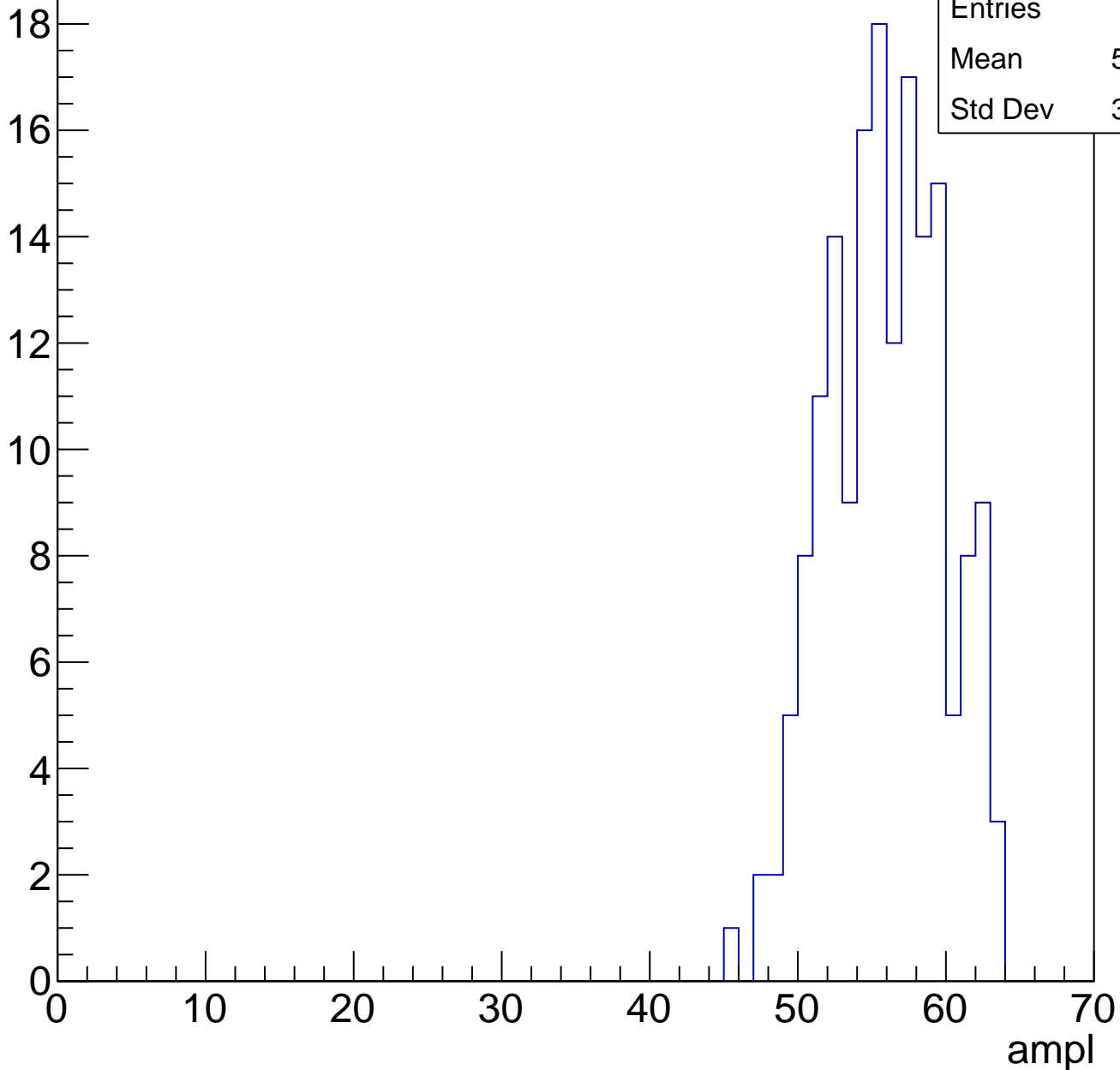


B1L001S, U21-ch67, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	55.46
Std Dev	3.858

Entry



B1L001S, U21-ch67, adc5

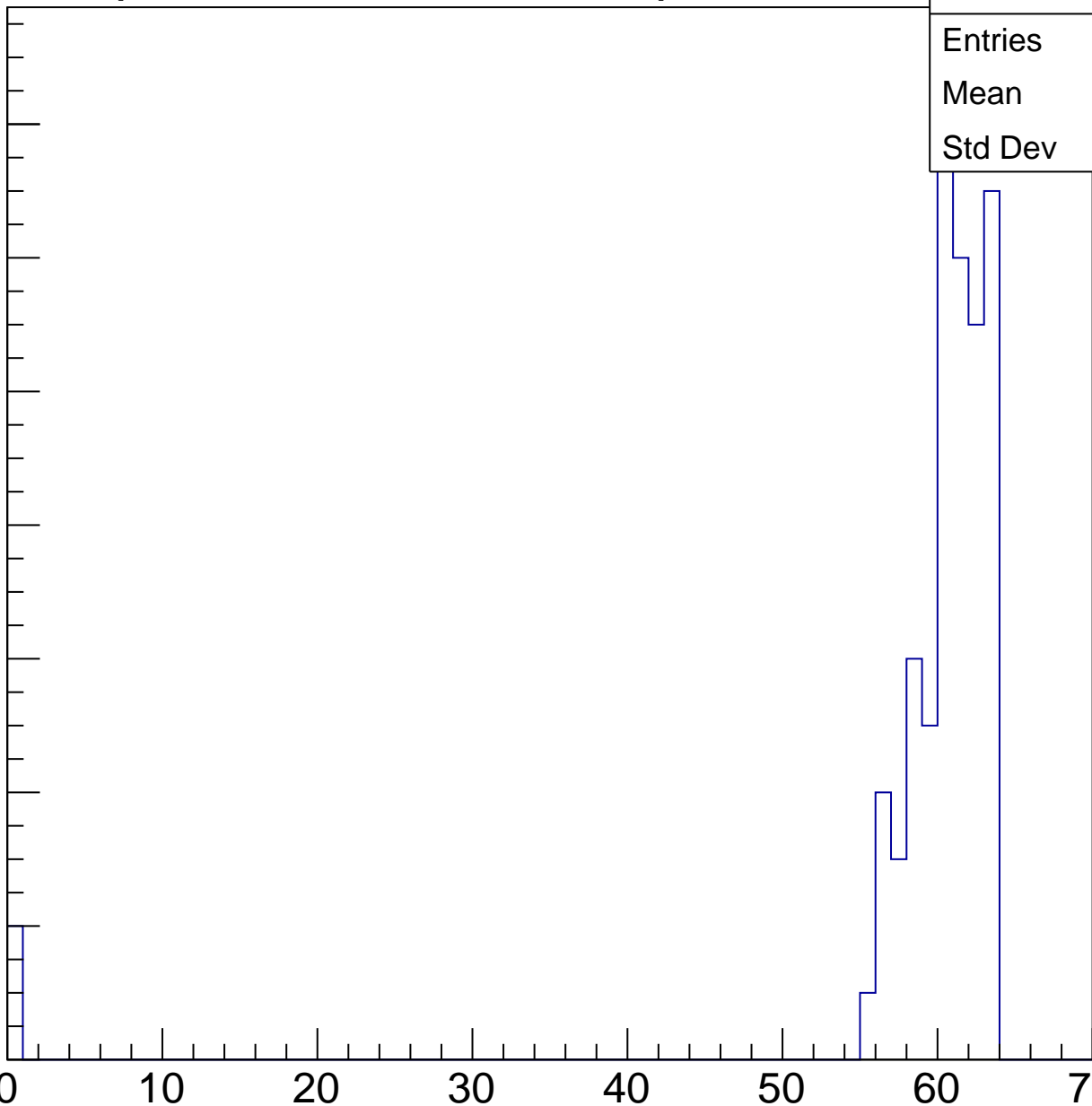
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.69
Std Dev	10.13

ampl



B1L001S, U21-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

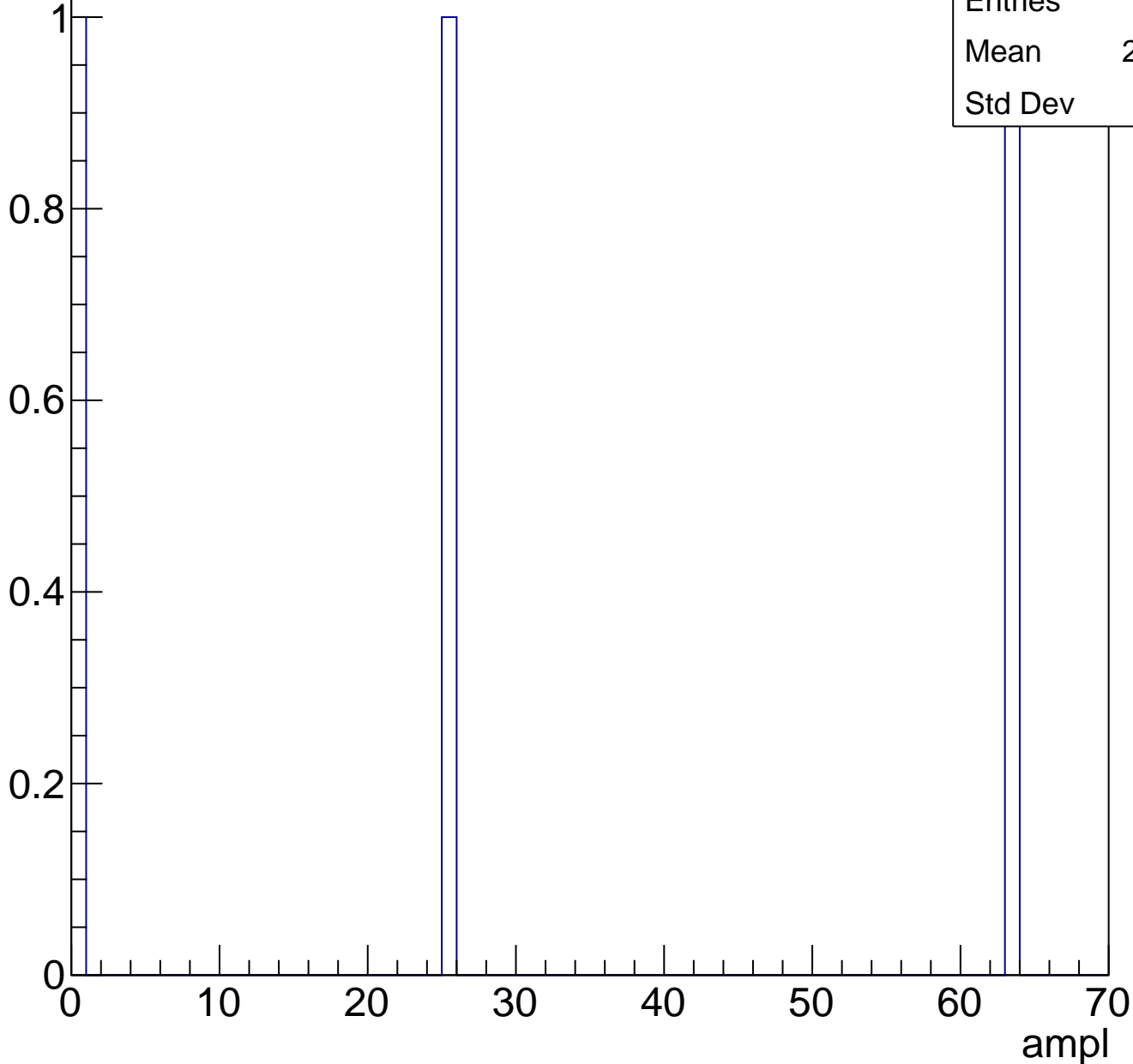
Entry



B1L001S, U21-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	29.33
Std Dev	25.9

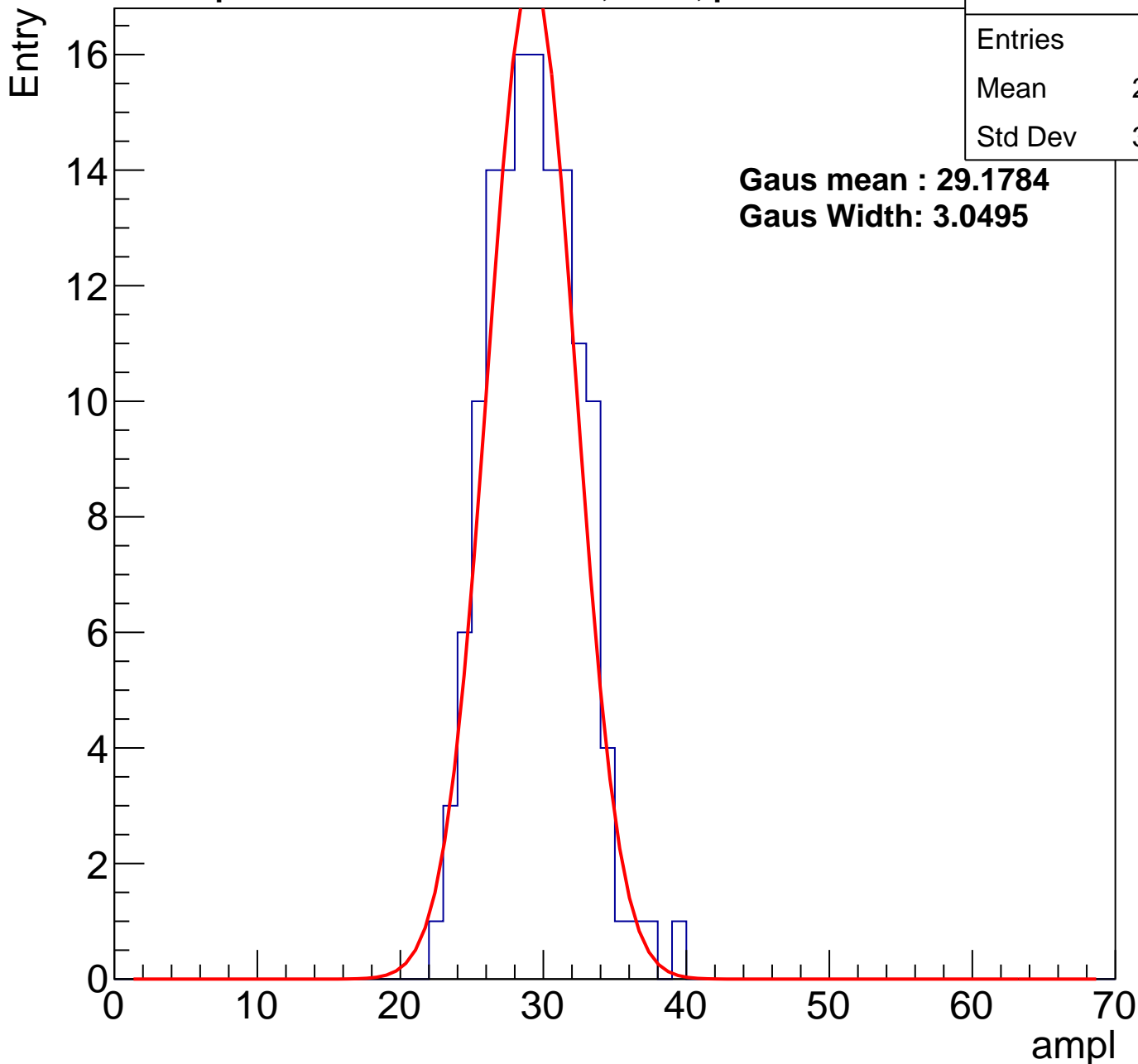
B1L001S, U21-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.89
Std Dev	3.129

Gaus mean : 29.1784

Gaus Width: 3.0495



B1L001S, U21-ch68, adc1

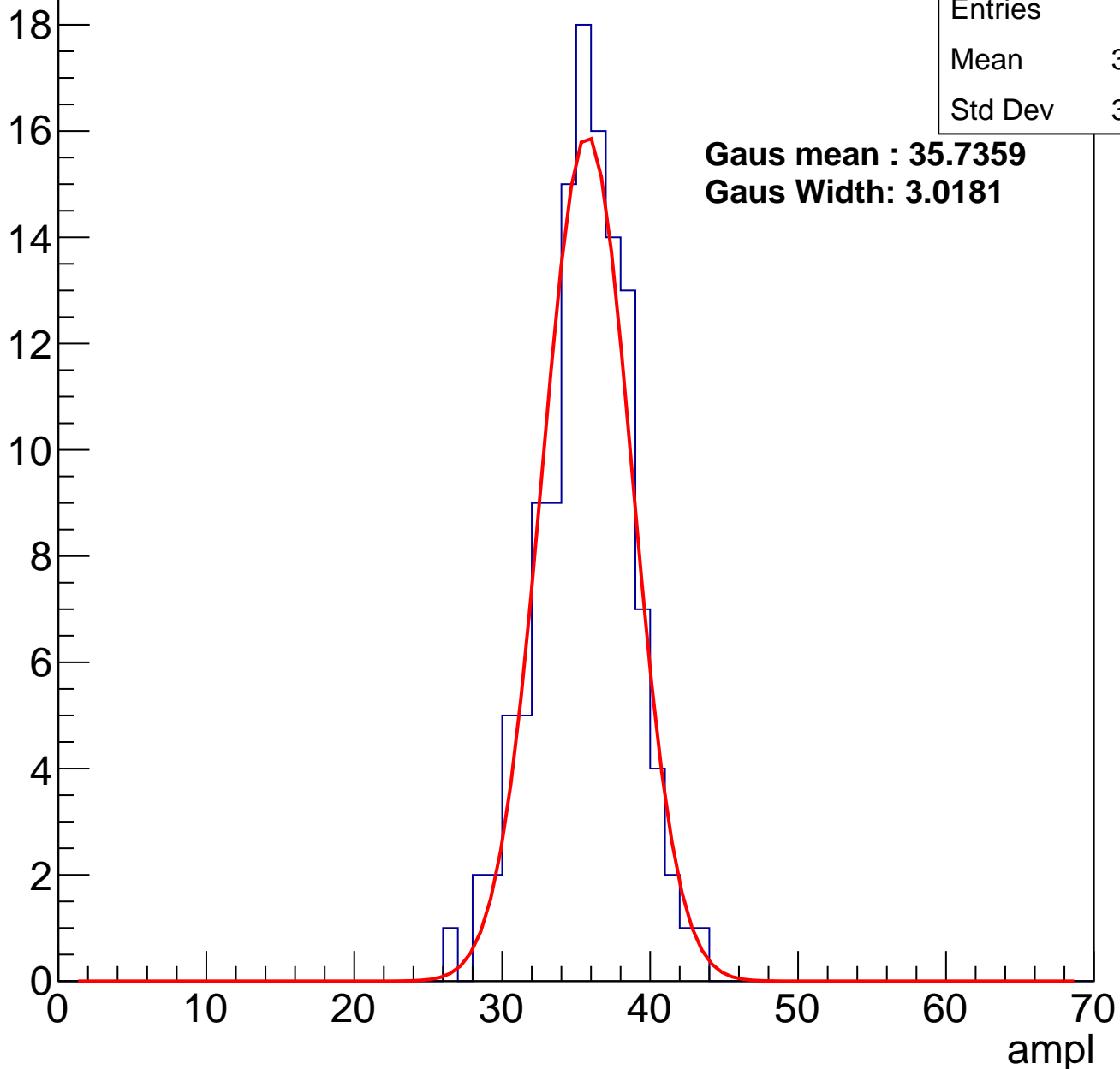
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	35.15
Std Dev	3.074

Gaus mean : 35.7359

Gaus Width: 3.0181

Entry



B1L001S, U21-ch68, adc2

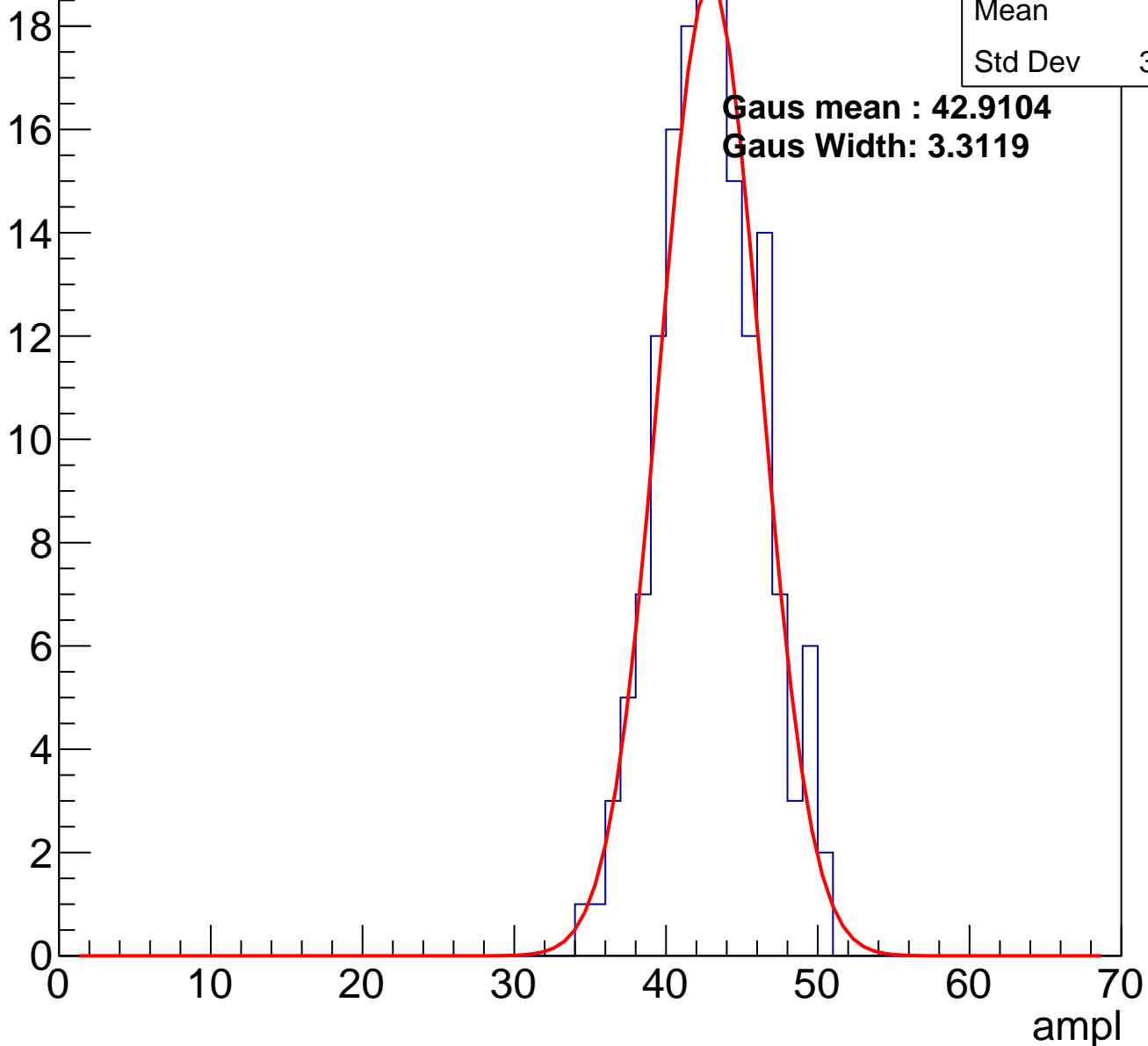
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	42.5
Std Dev	3.283

Gaus mean : 42.9104

Gaus Width: 3.3119

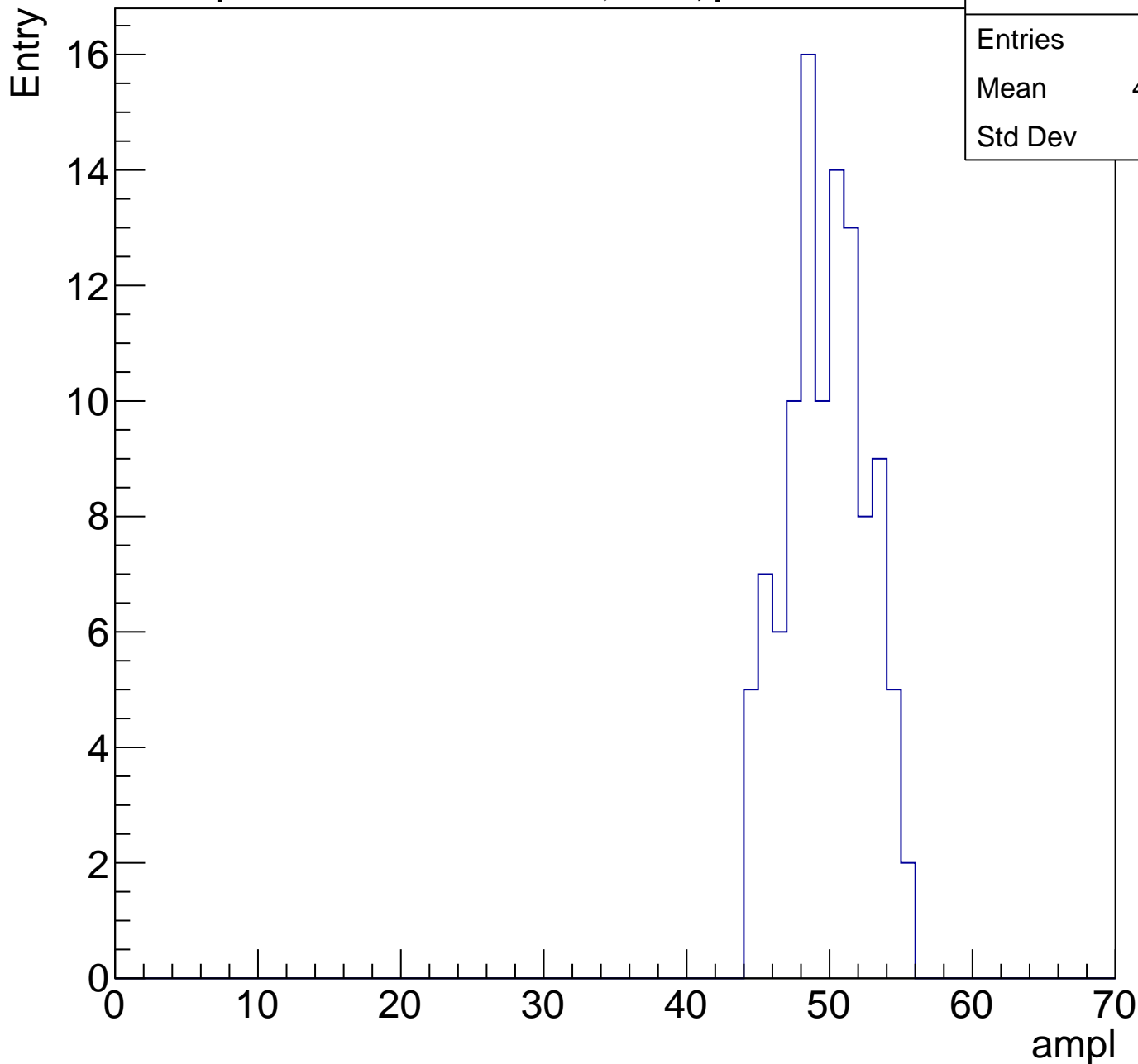
Entry



B1L001S, U21-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	49.29
Std Dev	2.79



B1L001S, U21-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	55.01
Std Dev	3.462

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

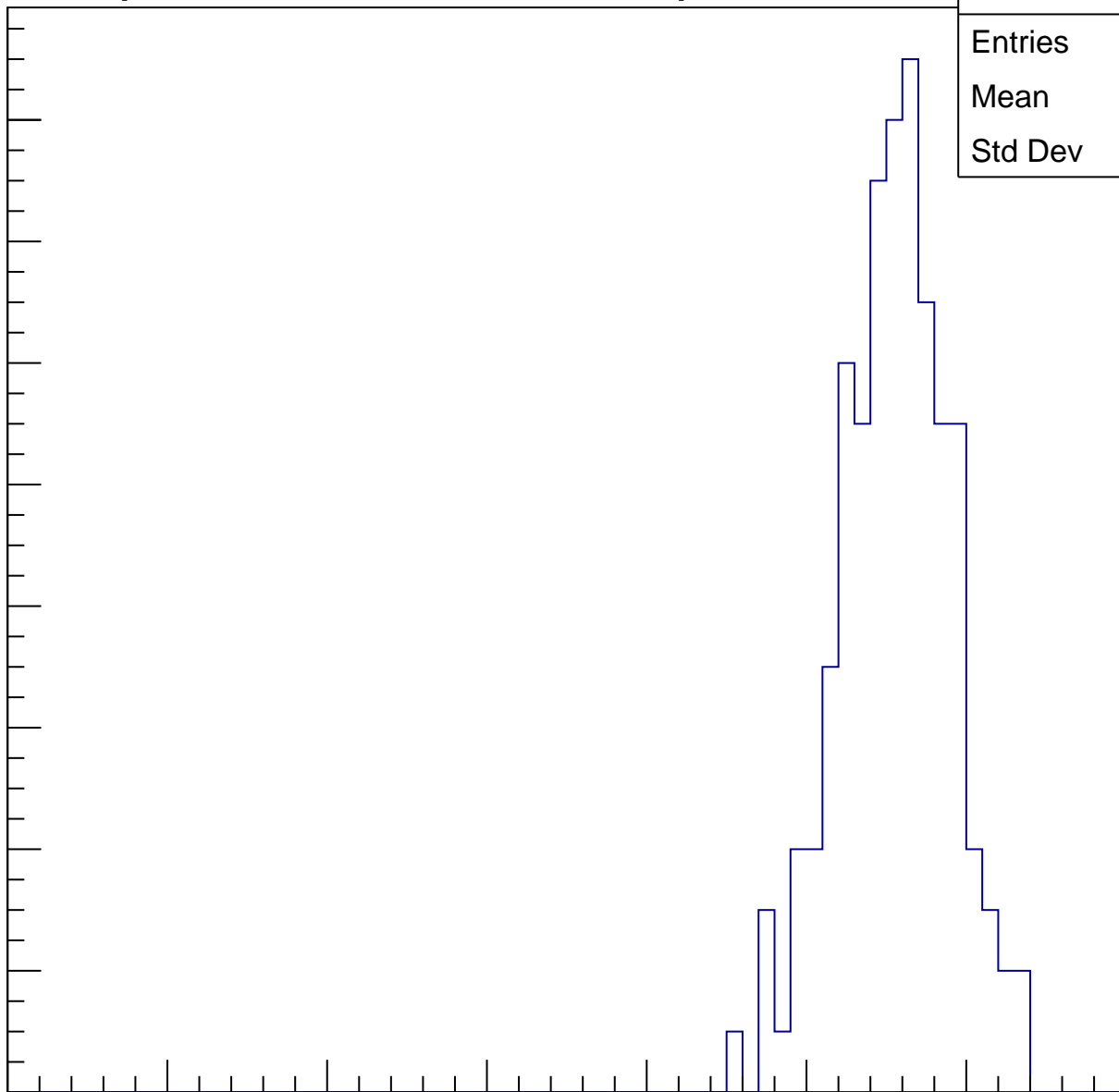
40

50

60

70

ampl



B1L001S, U21-ch68, adc5

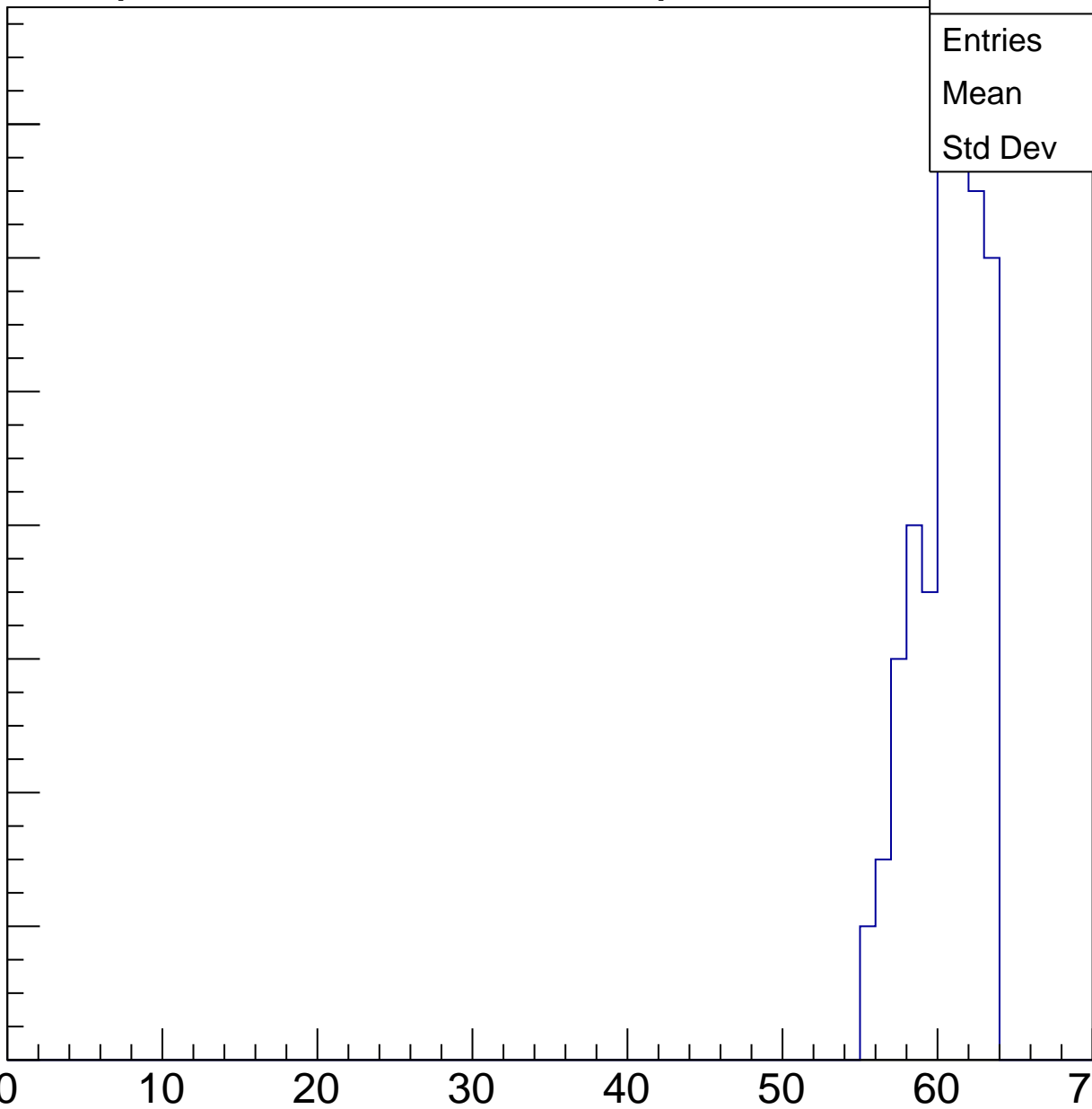
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	60.17
Std Dev	2.132

ampl

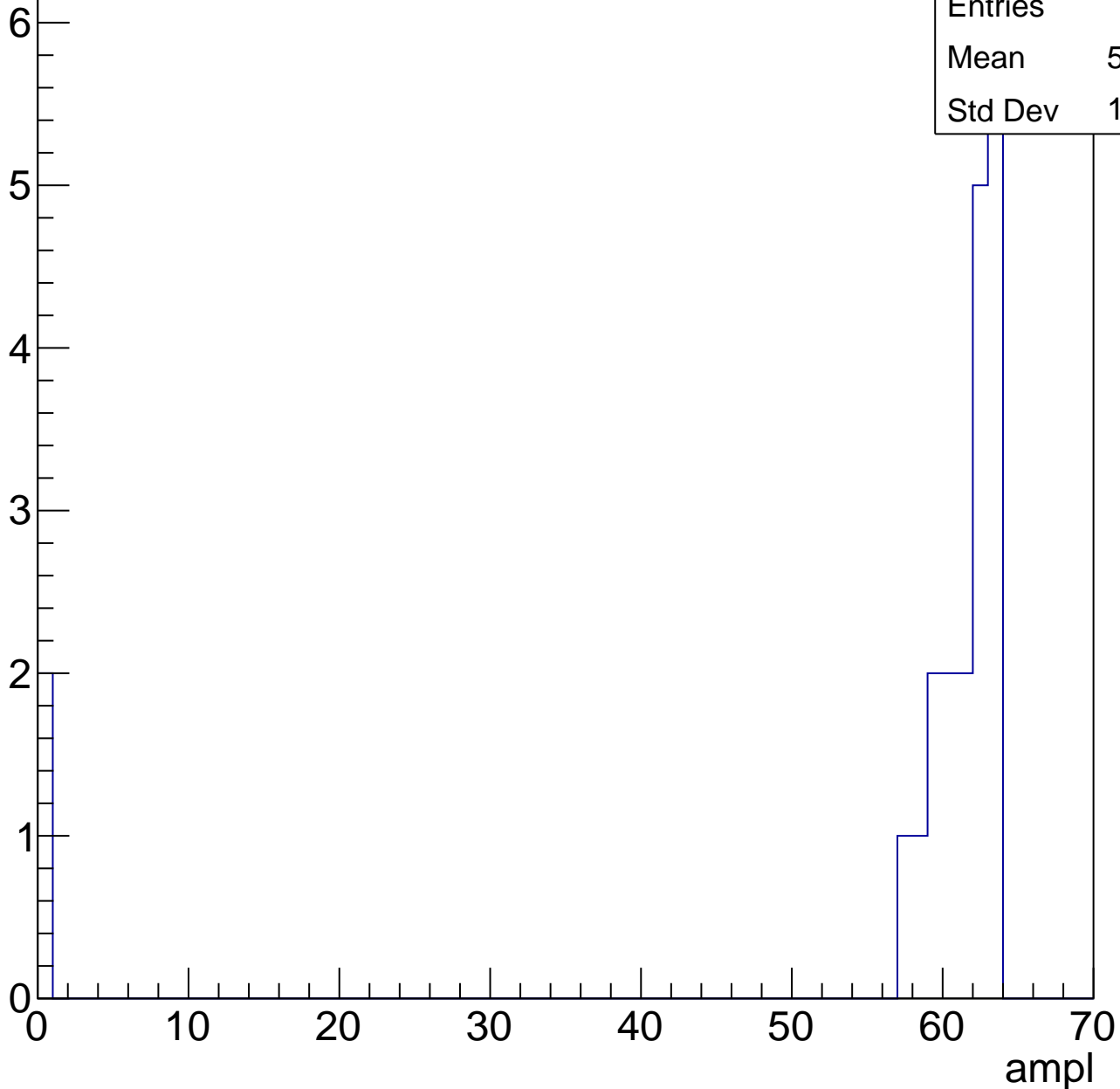


B1L001S, U21-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

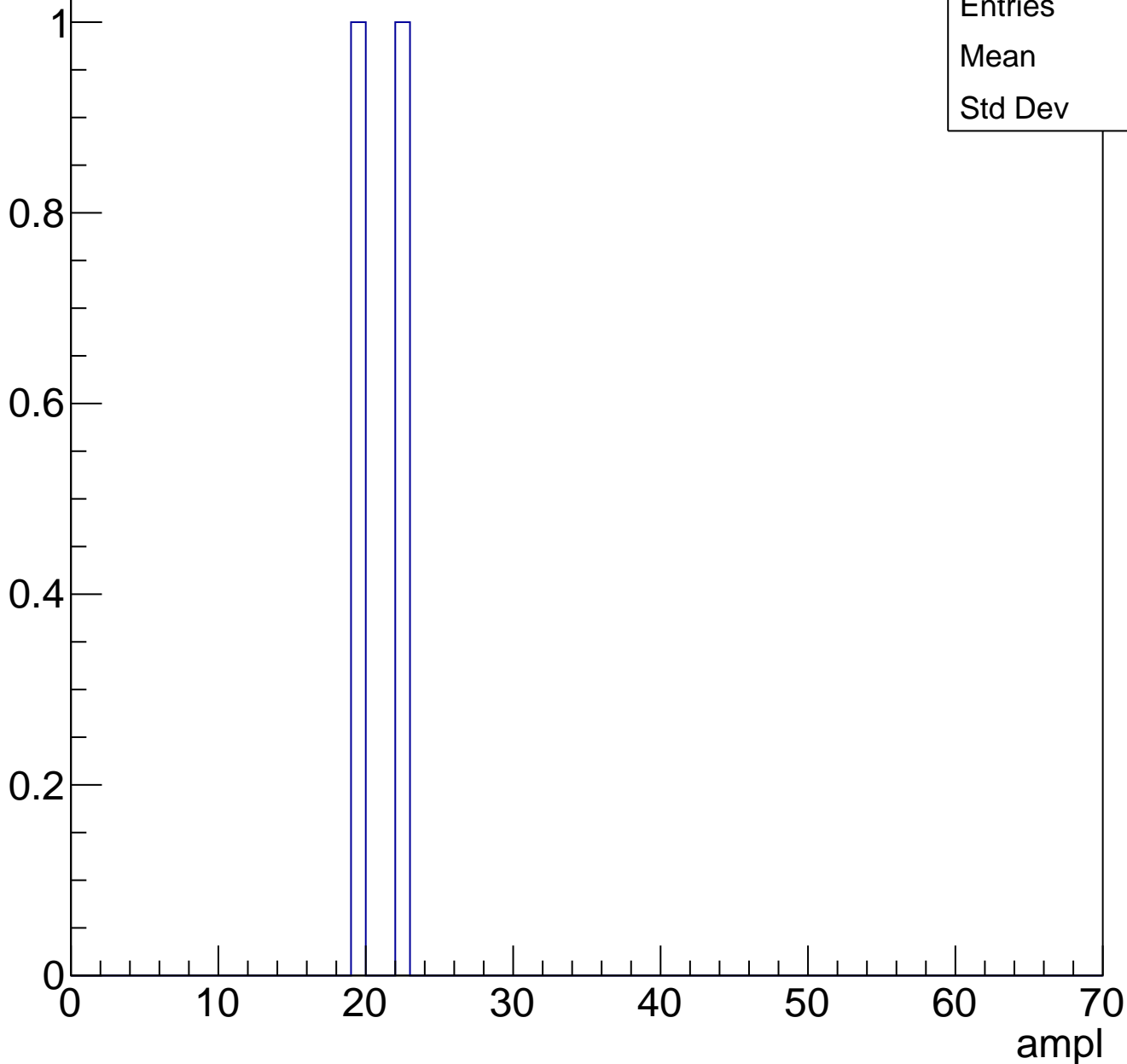
Entries	21
Mean	55.38
Std Dev	18.05



B1L001S, U21-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch69, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	28.18
Std Dev	2.989

Gaus mean : 28.5955

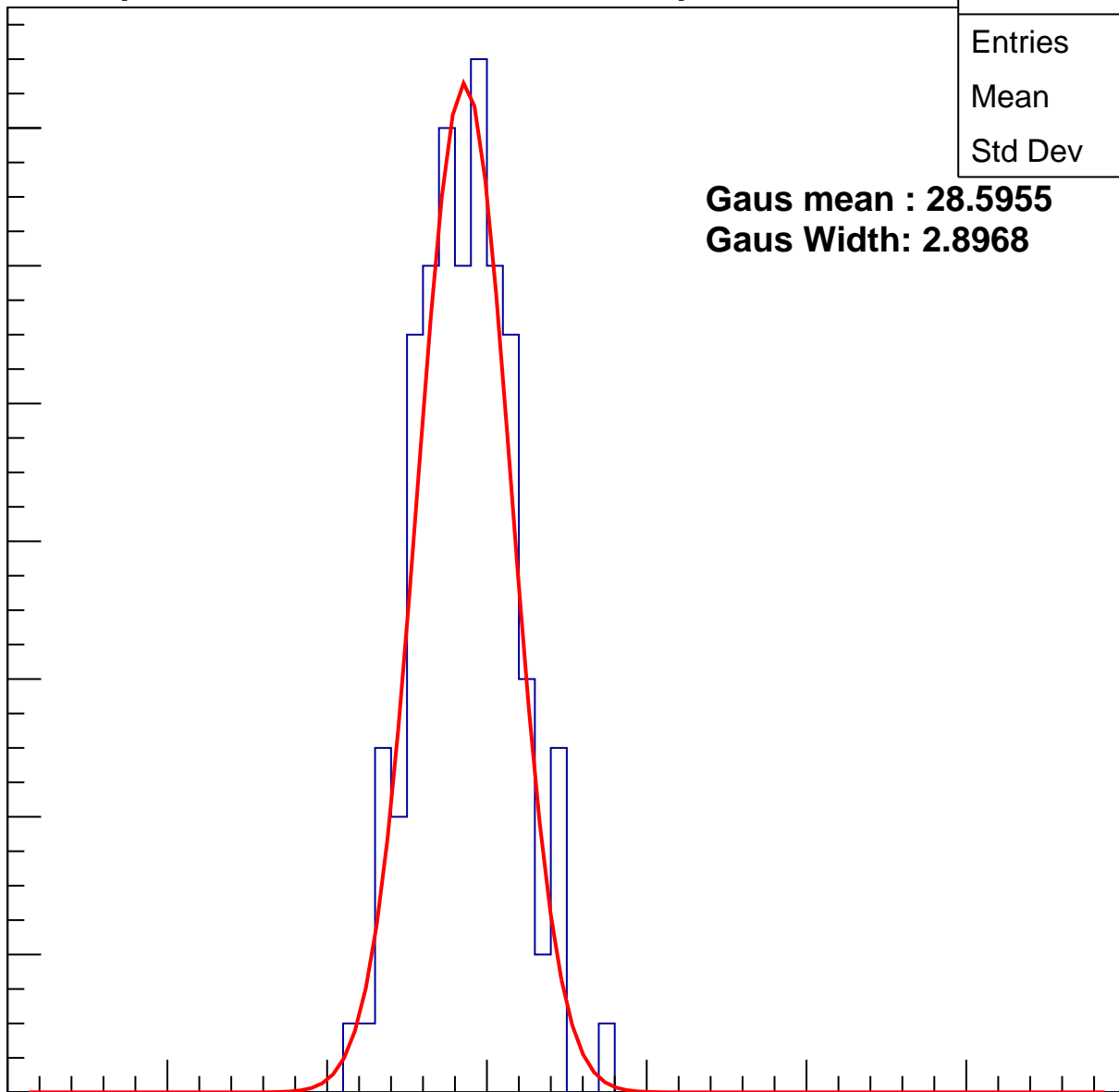
Gaus Width: 2.8968

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

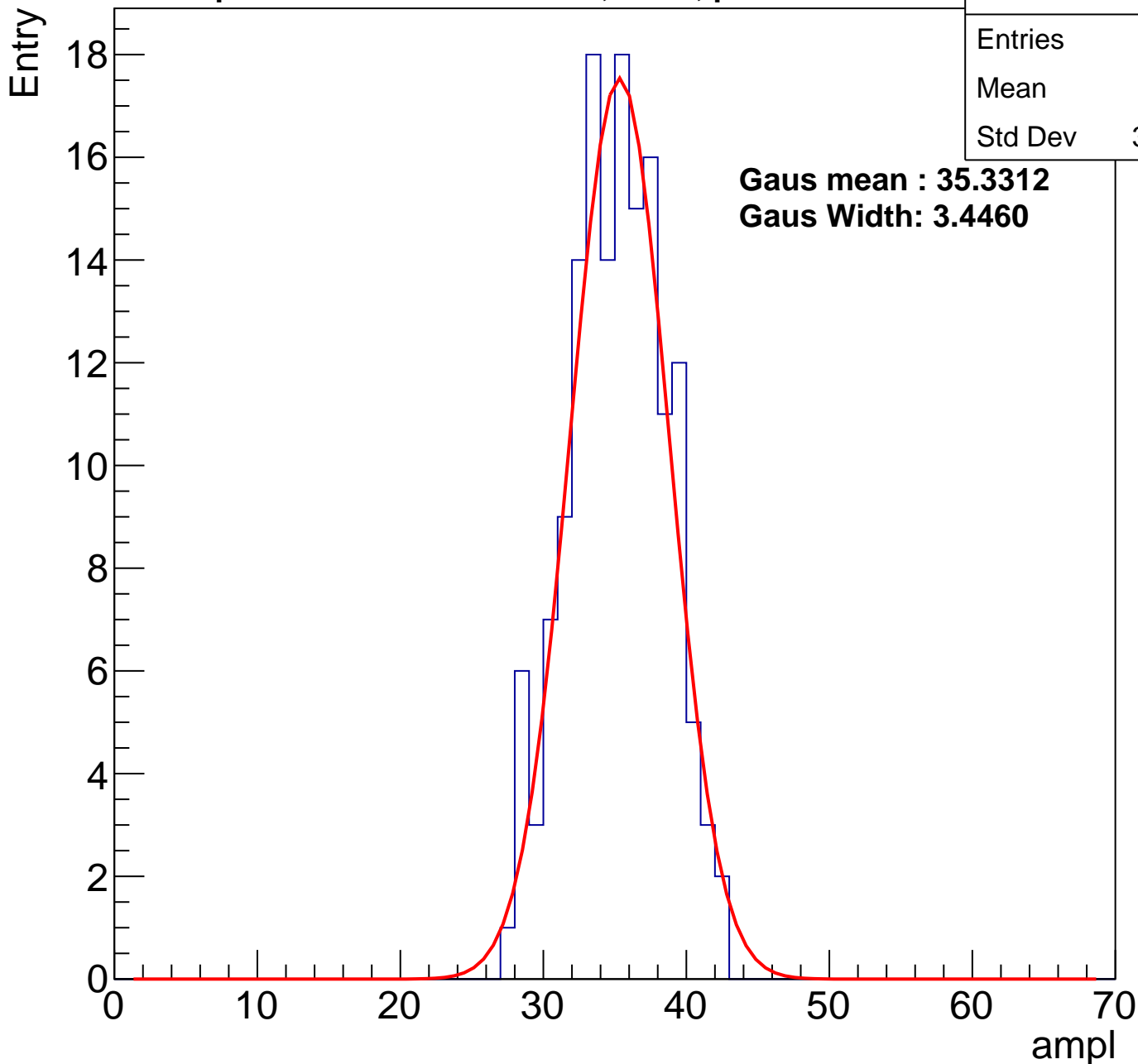


B1L001S, U21-ch69, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	34.7
Std Dev	3.307

Gaus mean : 35.3312
Gaus Width: 3.4460



B1L001S, U21-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

41.96

Std Dev

3.259

Gaus mean : 42.4436

Gaus Width: 3.1908

0

10

20

30

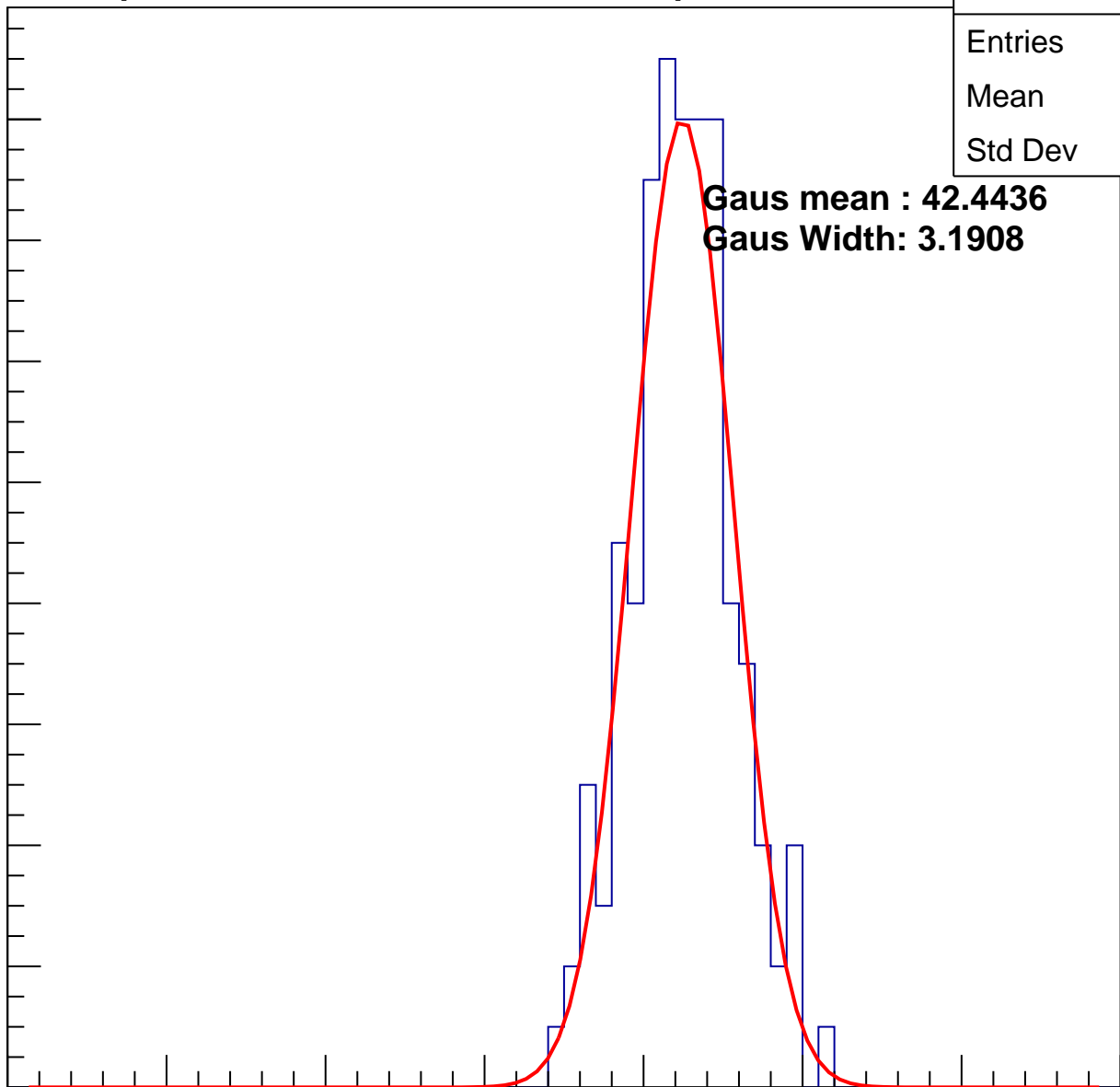
40

50

60

70

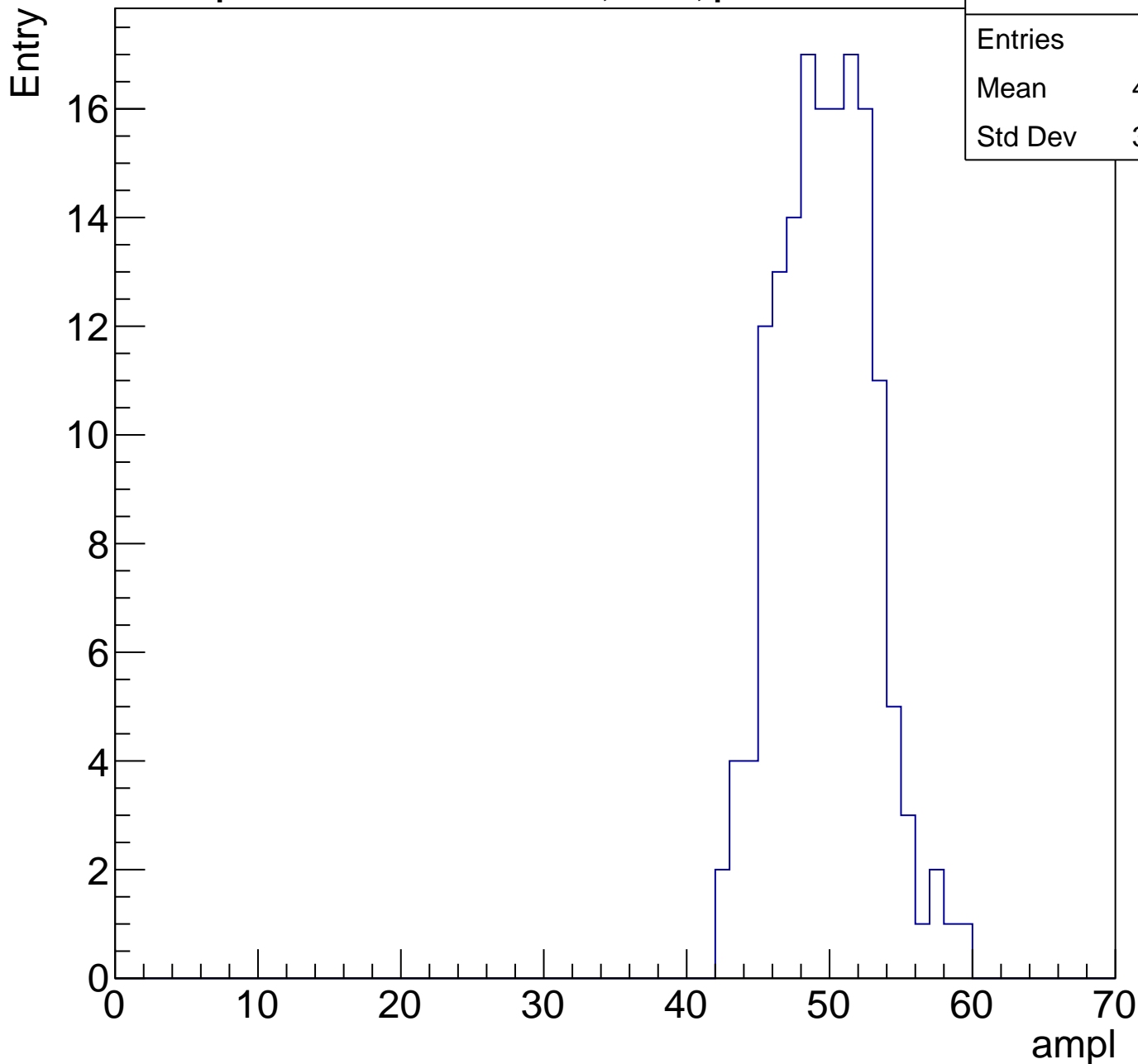
ampl



B1L001S, U21-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	49.24
Std Dev	3.322



B1L001S, U21-ch69, adc4

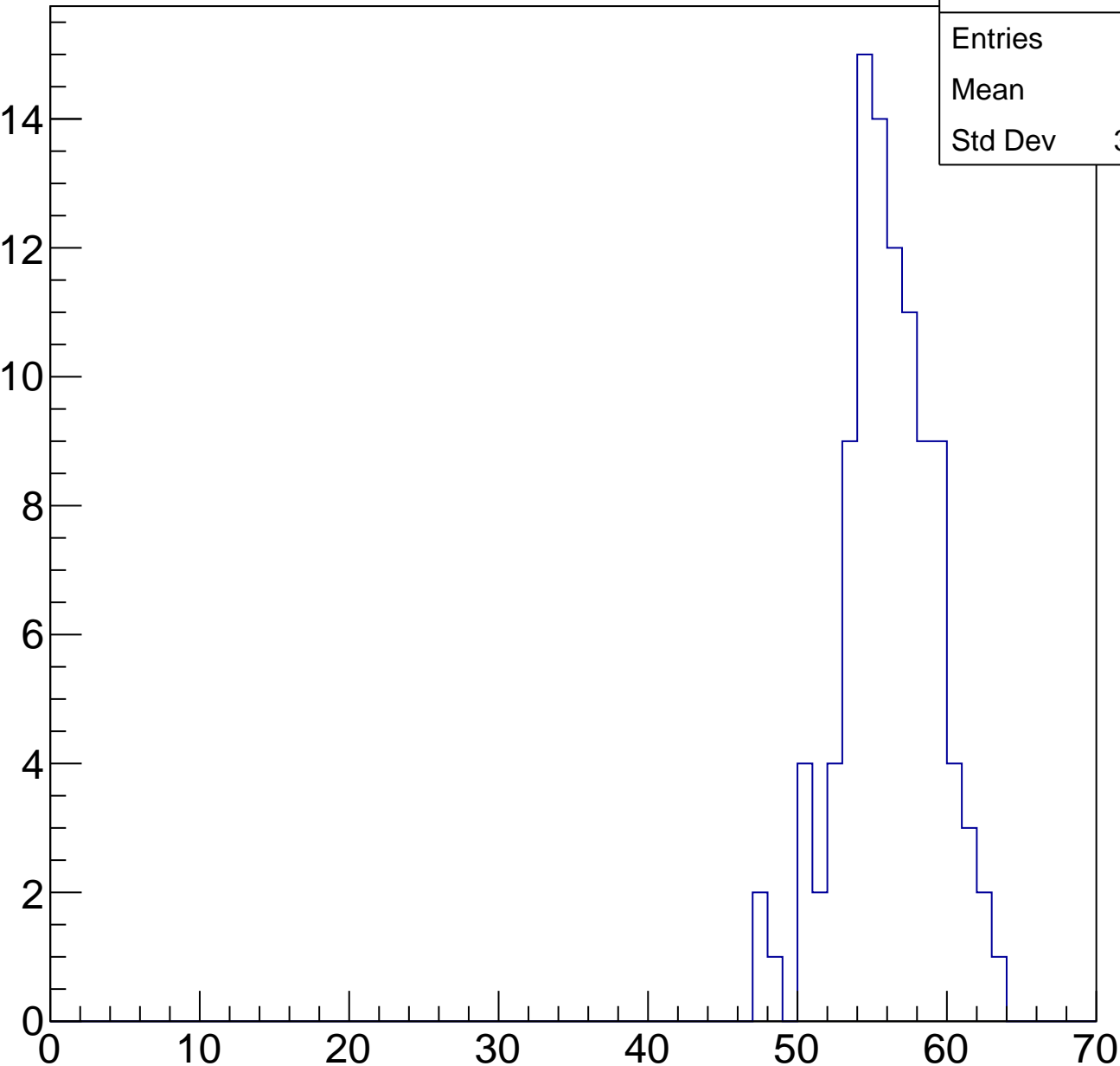
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	55.6
Std Dev	3.148

ampl



B1L001S, U21-ch69, adc5

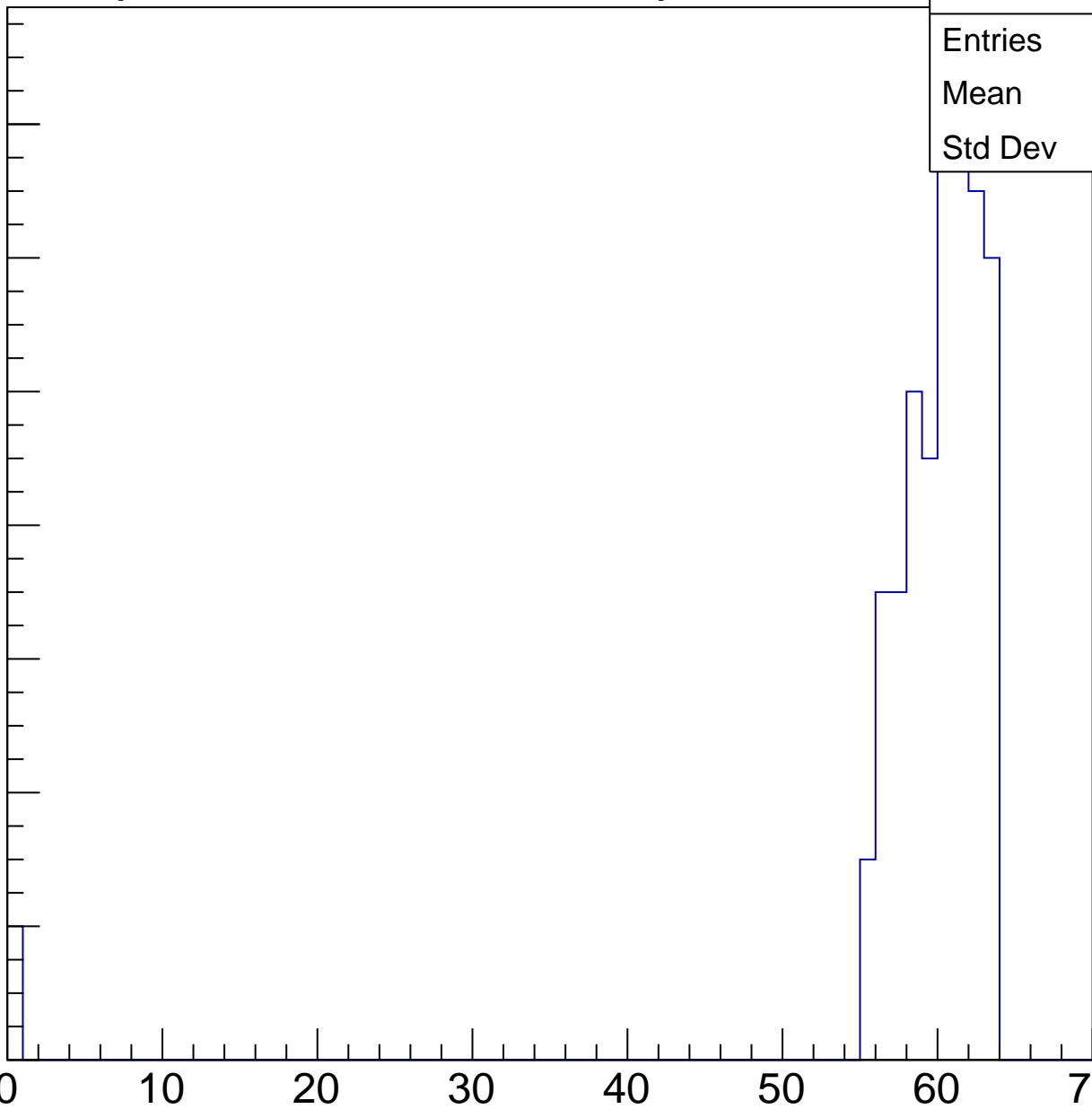
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.52
Std Dev	9.013

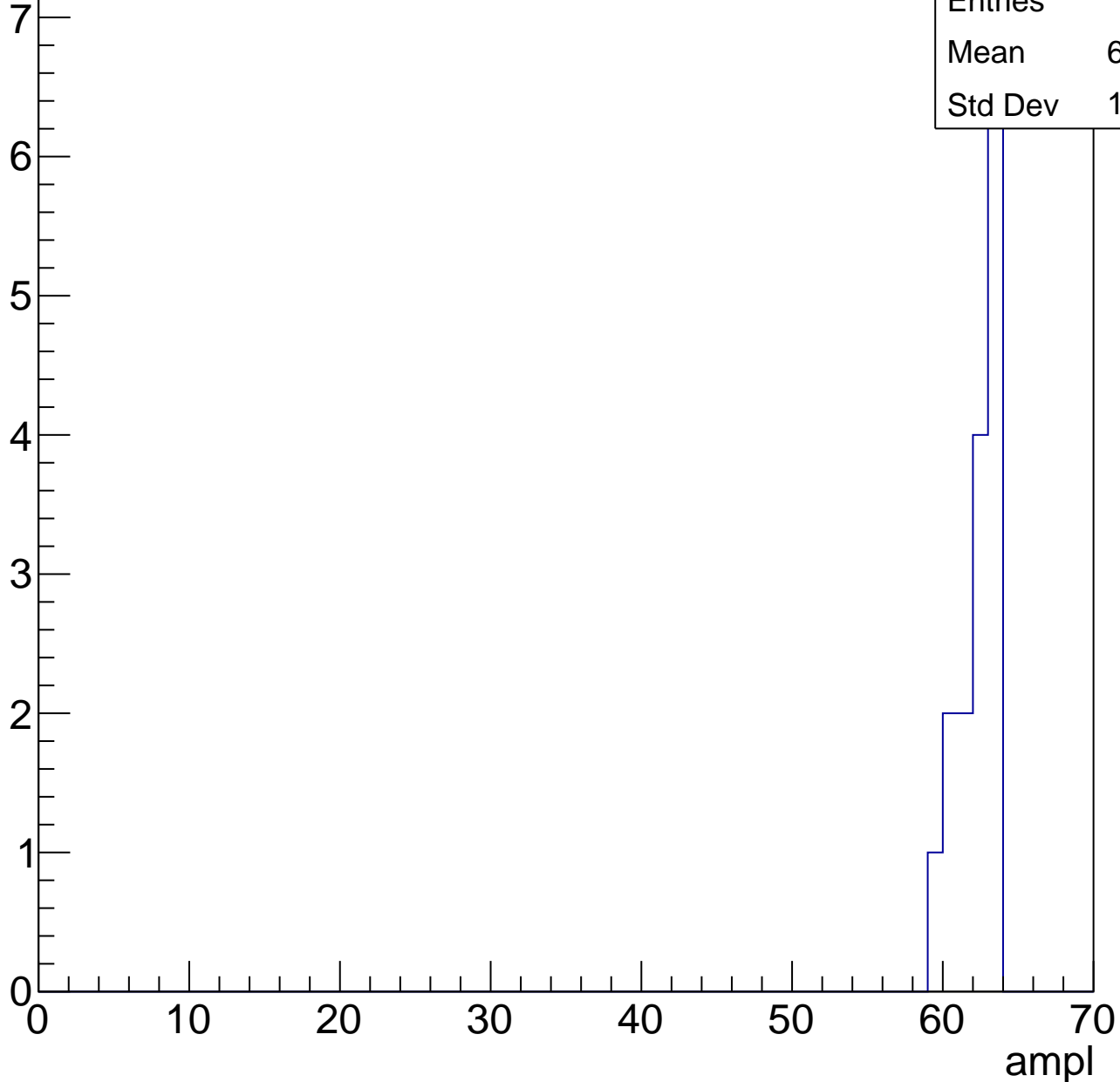
ampl



B1L001S, U21-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

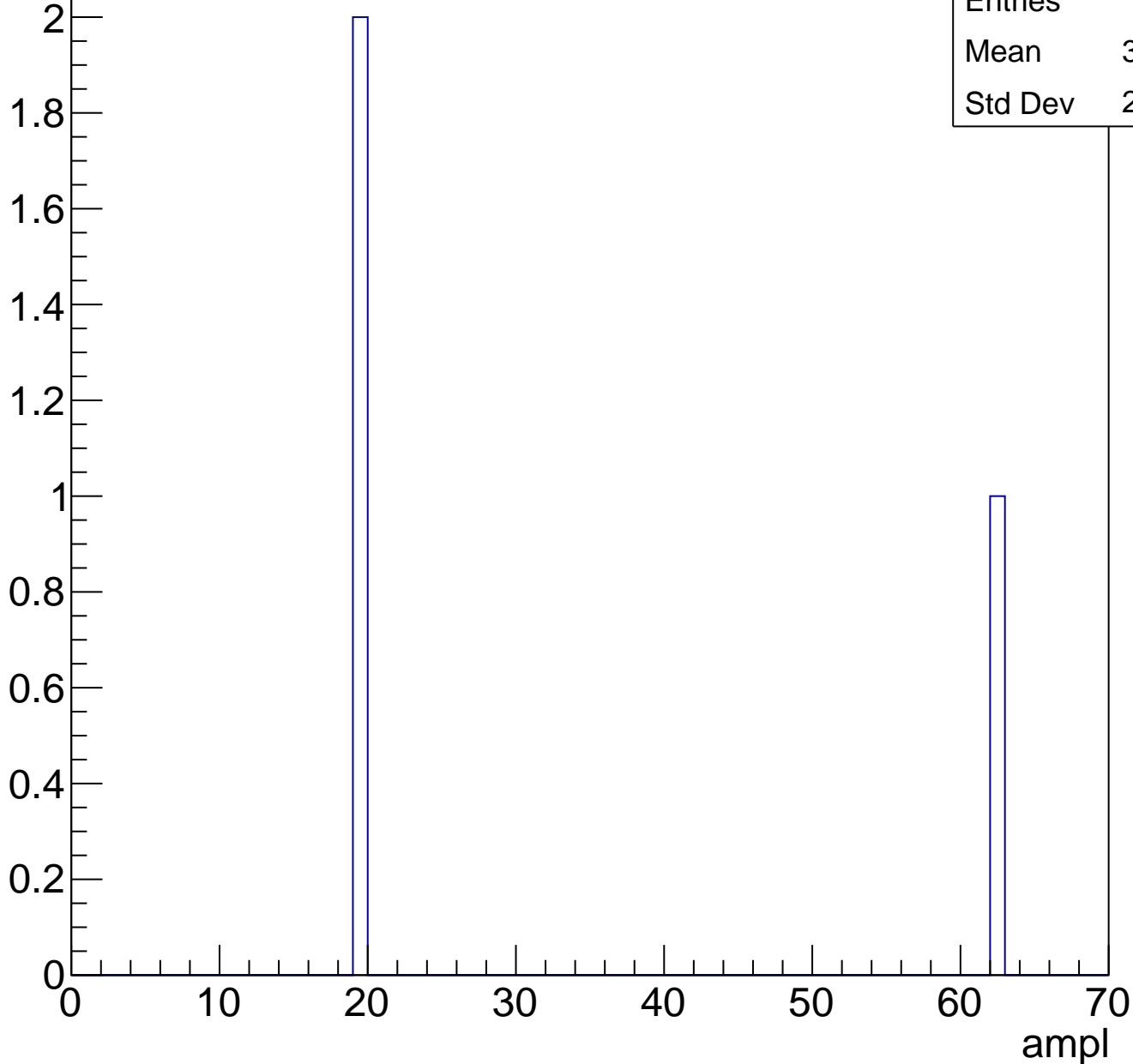
Entry



B1L001S, U21-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch70, adc0

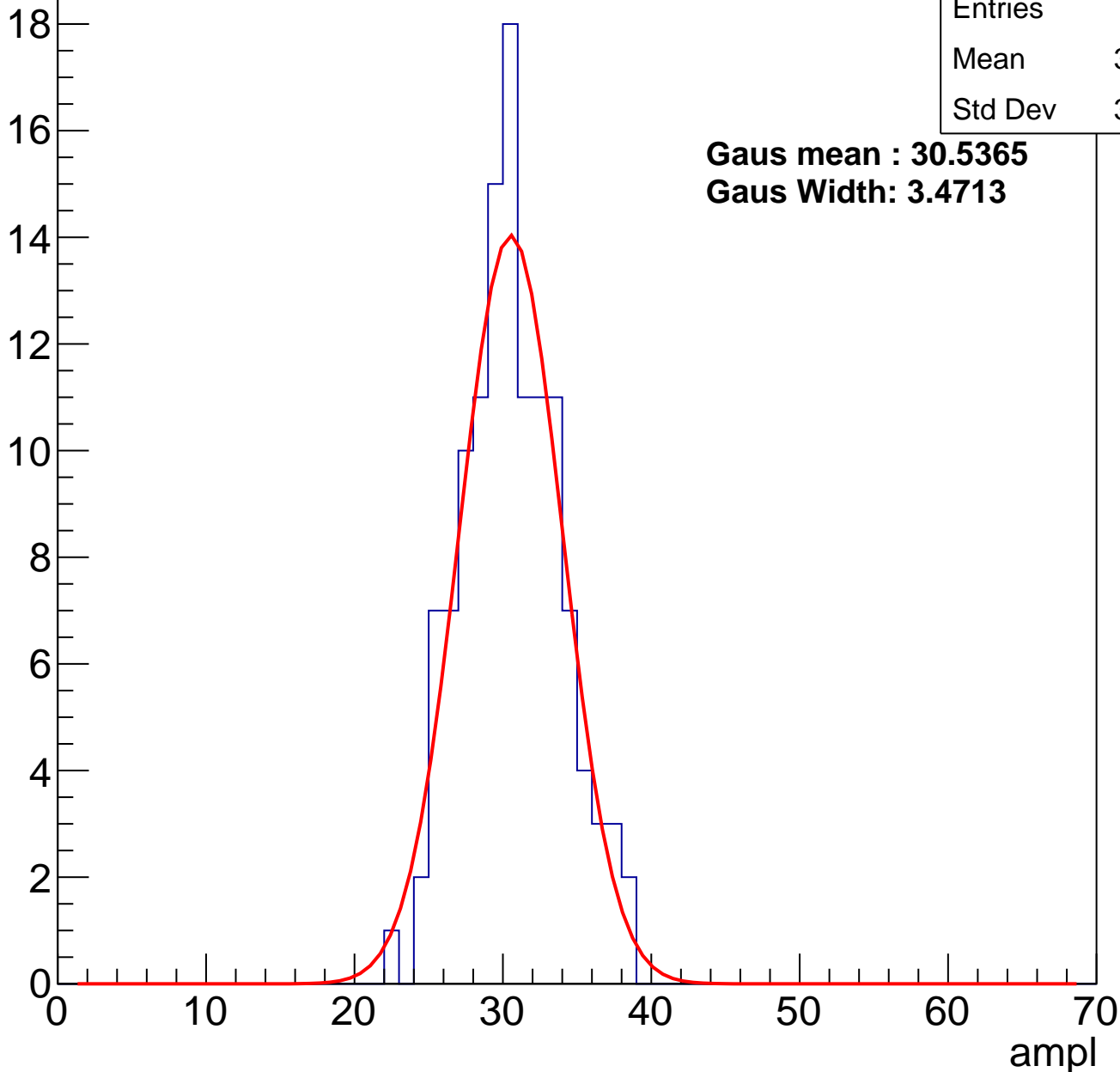
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	30.15
Std Dev	3.274

Gaus mean : 30.5365

Gaus Width: 3.4713

Entry



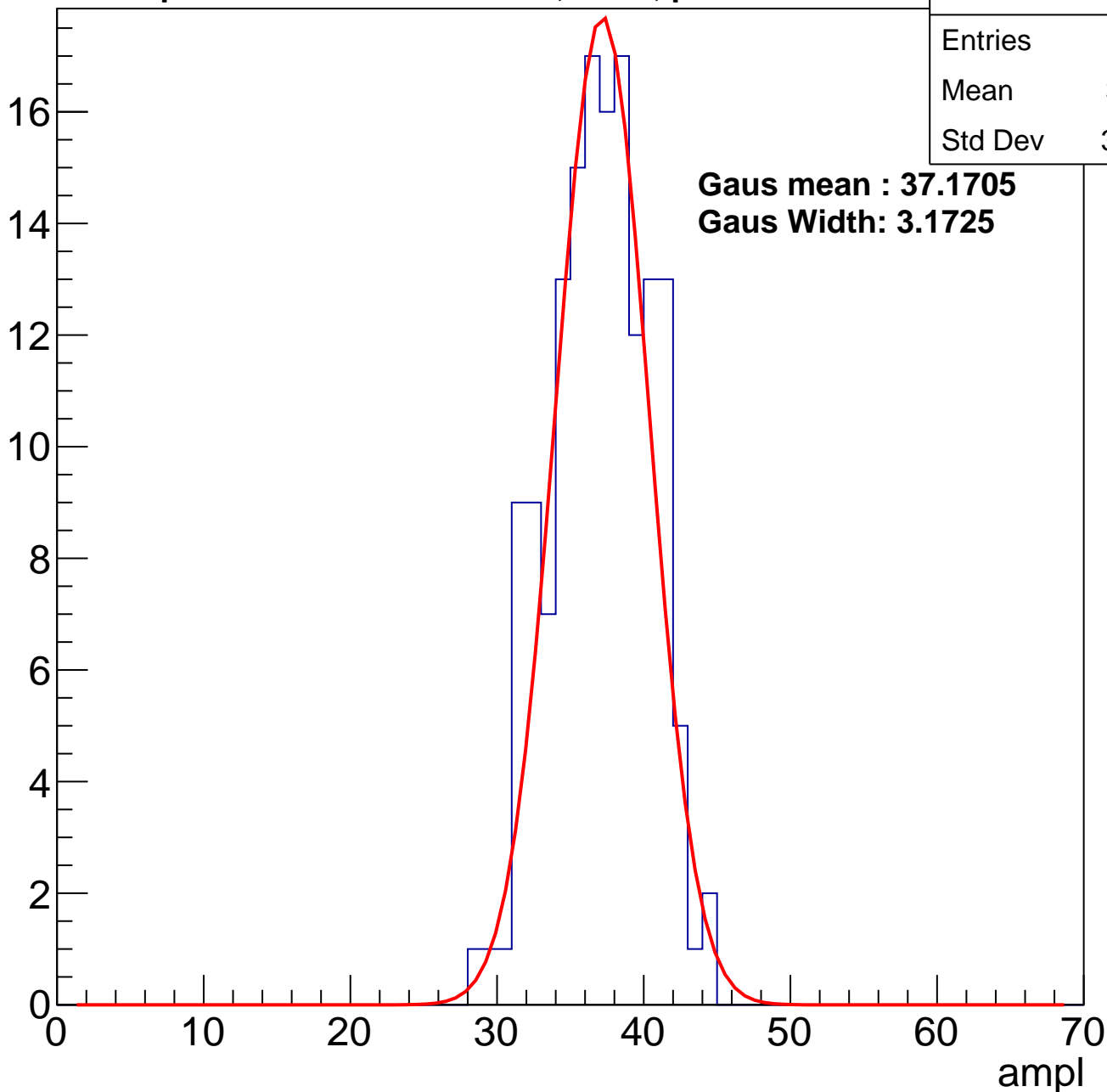
B1L001S, U21-ch70, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	36.61
Std Dev	3.319

Gaus mean : 37.1705
Gaus Width: 3.1725

Entry



B1L001S, U21-ch70, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	43.66
Std Dev	3.095

Gaus mean : 44.3977

Gaus Width: 2.8101

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

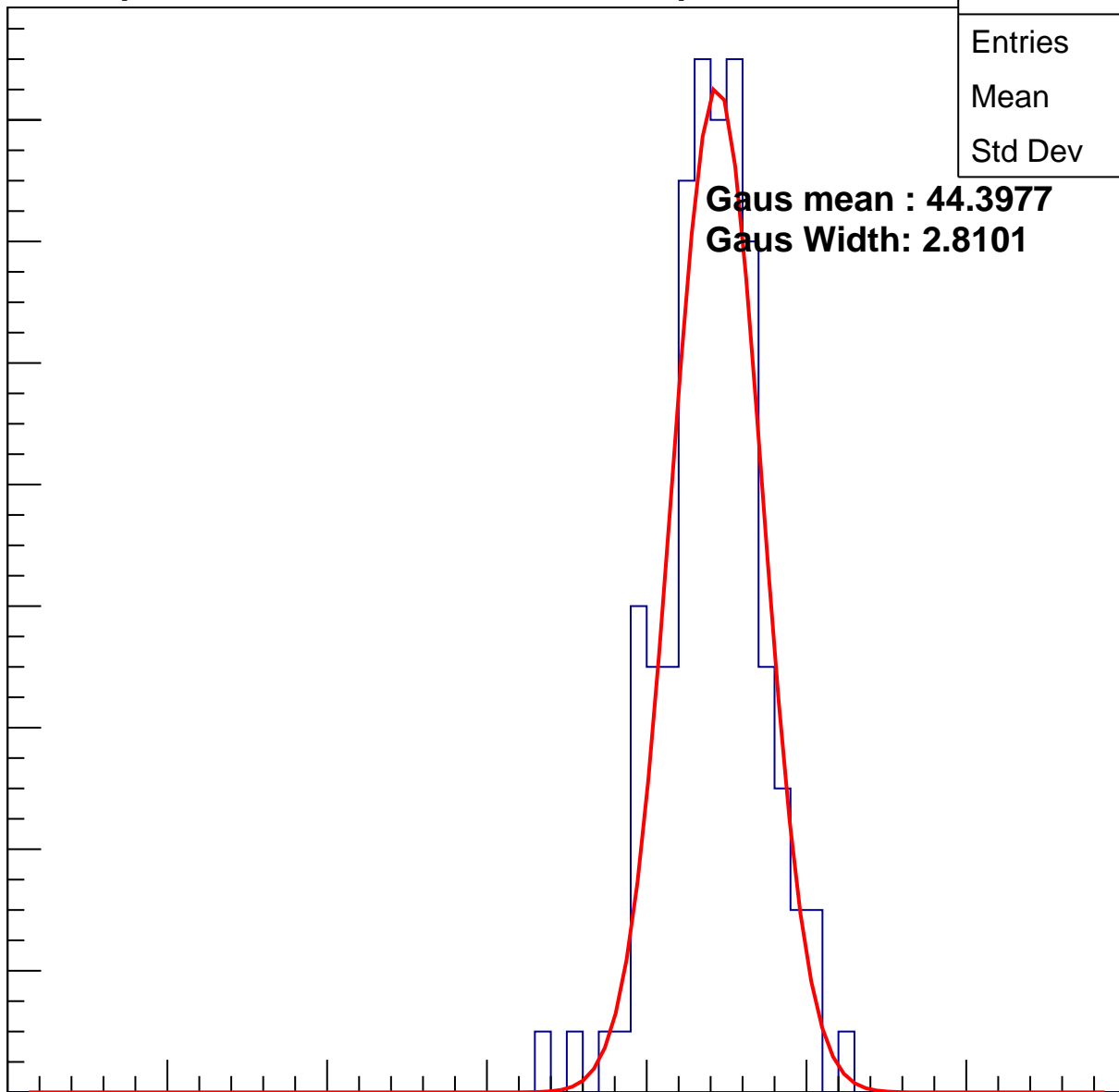
40

50

60

70

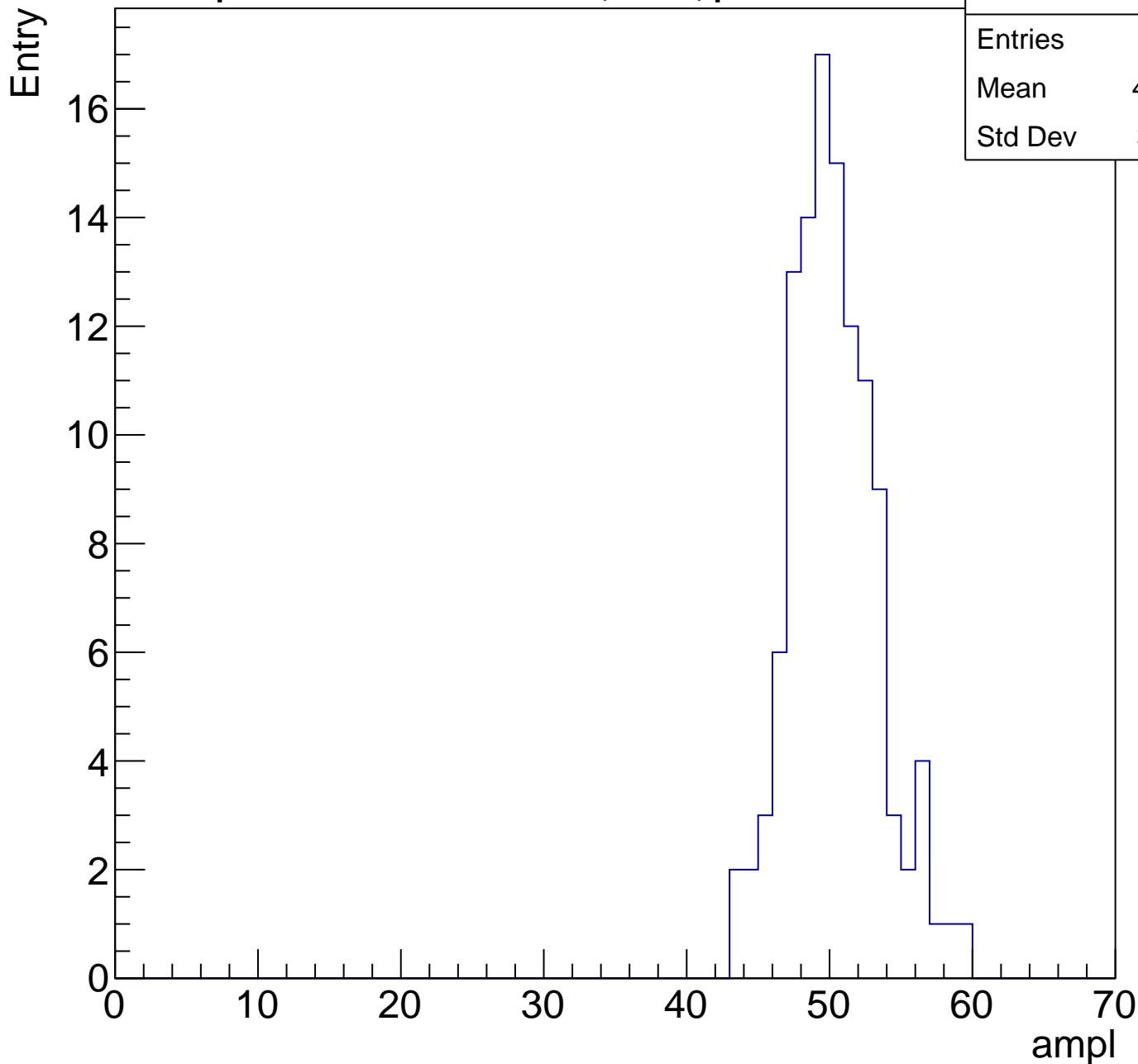
ampl



B1L001S, U21-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

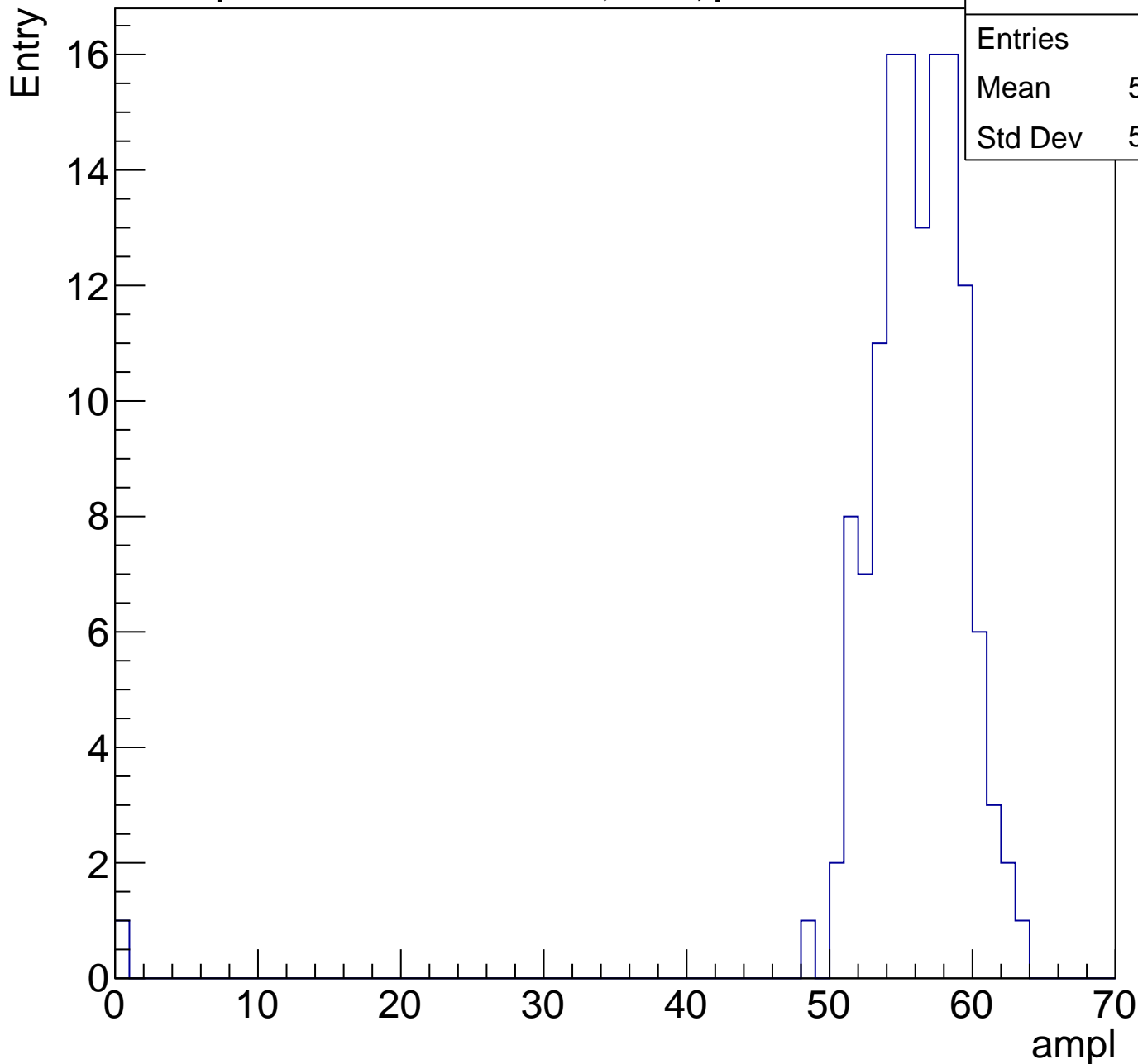
Entries	116
Mean	49.84
Std Dev	3.081



B1L001S, U21-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	55.37
Std Dev	5.656



B1L001S, U21-ch70, adc5

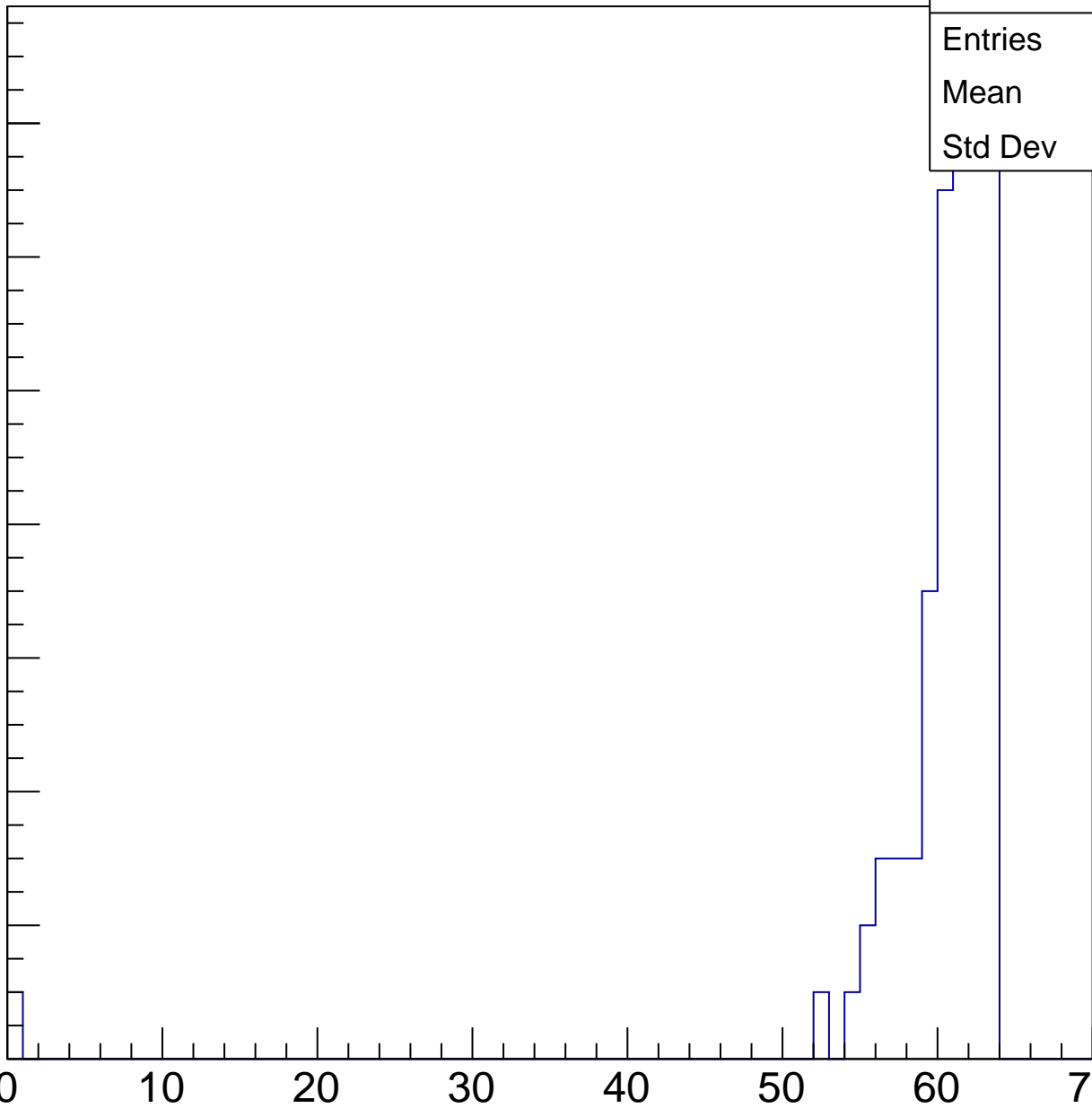
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.63
Std Dev	7.199

ampl



B1L001S, U21-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

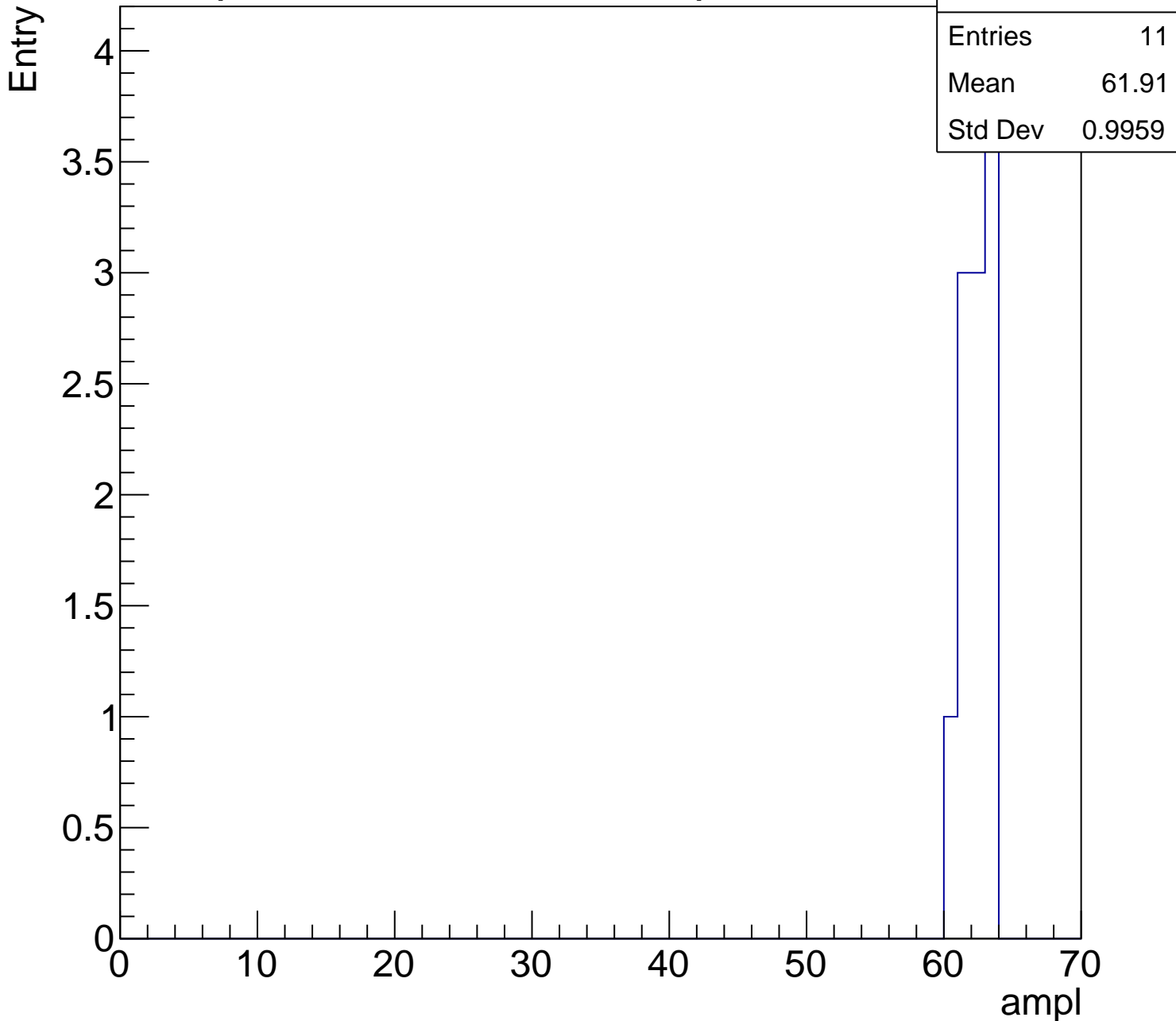
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	61.91
Std Dev	0.9959

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L001S, U21-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	29.65
Std Dev	4.099

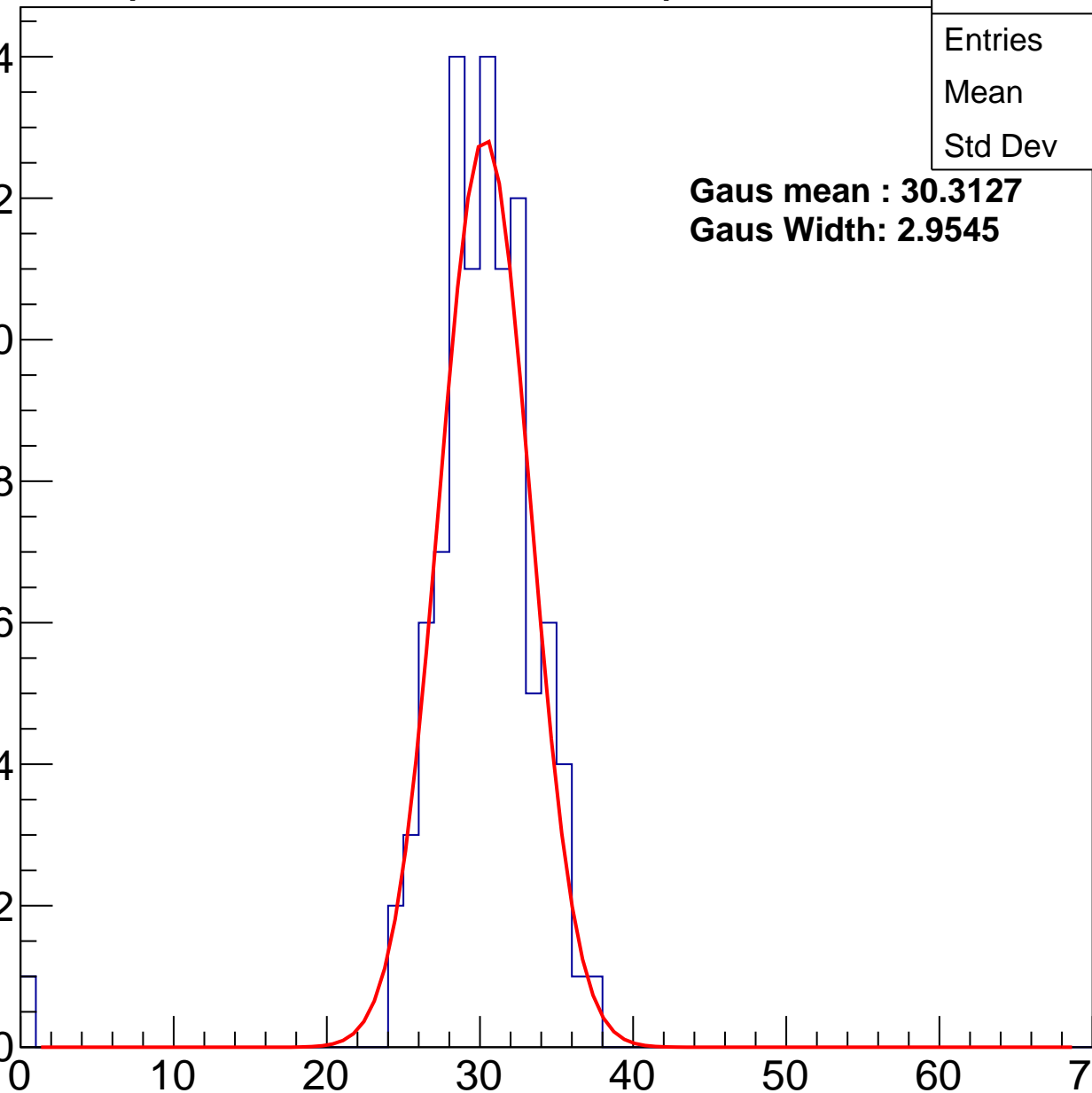
Gaus mean : 30.3127

Gaus Width: 2.9545

Entry

14
12
10
8
6
4
2
0

ampl



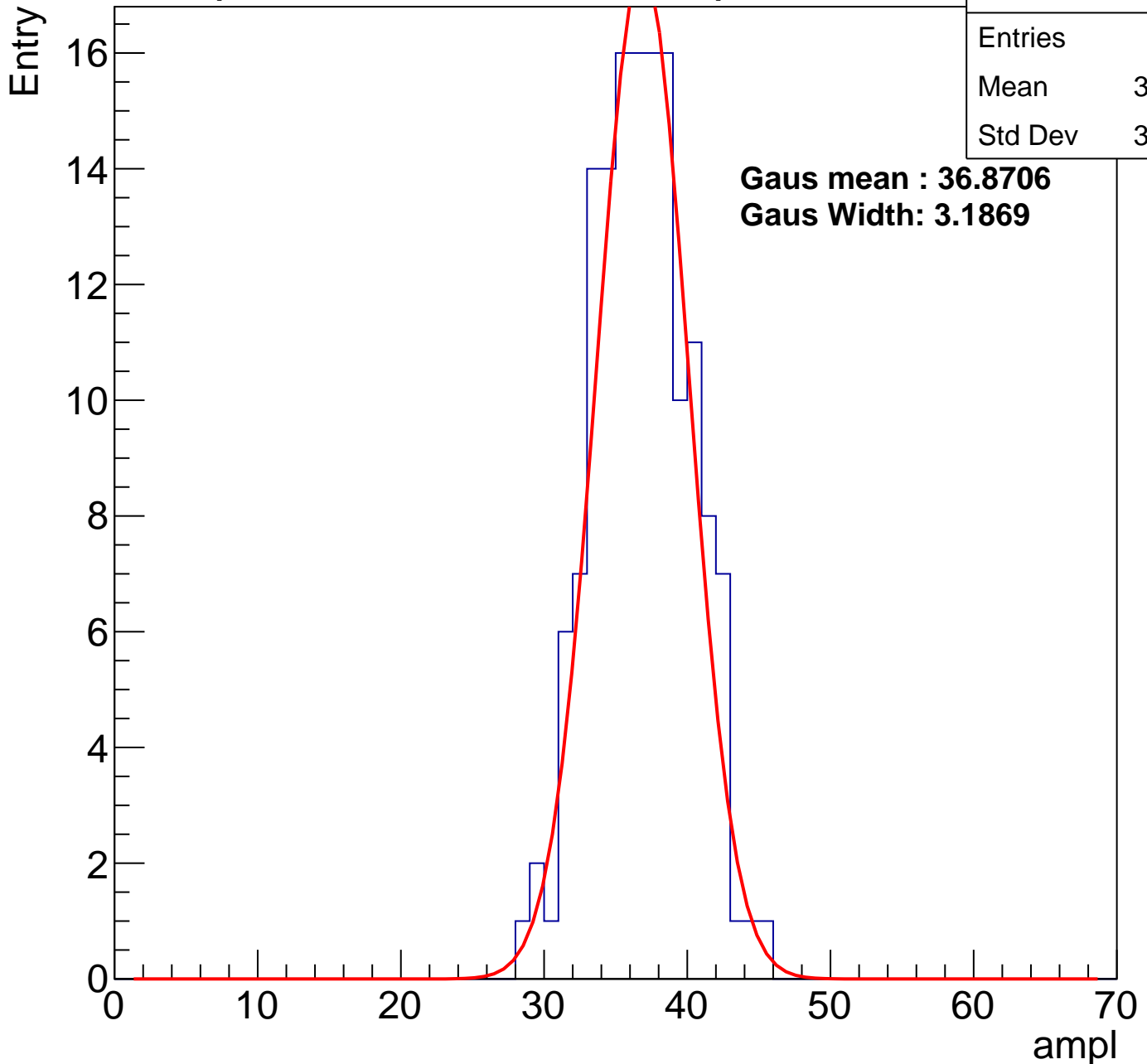
B1L001S, U21-ch71, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.38
Std Dev	3.317

Gaus mean : 36.8706

Gaus Width: 3.1869



B1L001S, U21-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	43.66
Std Dev	3.489

Gaus mean : 44.0566

Gaus Width: 3.5225

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

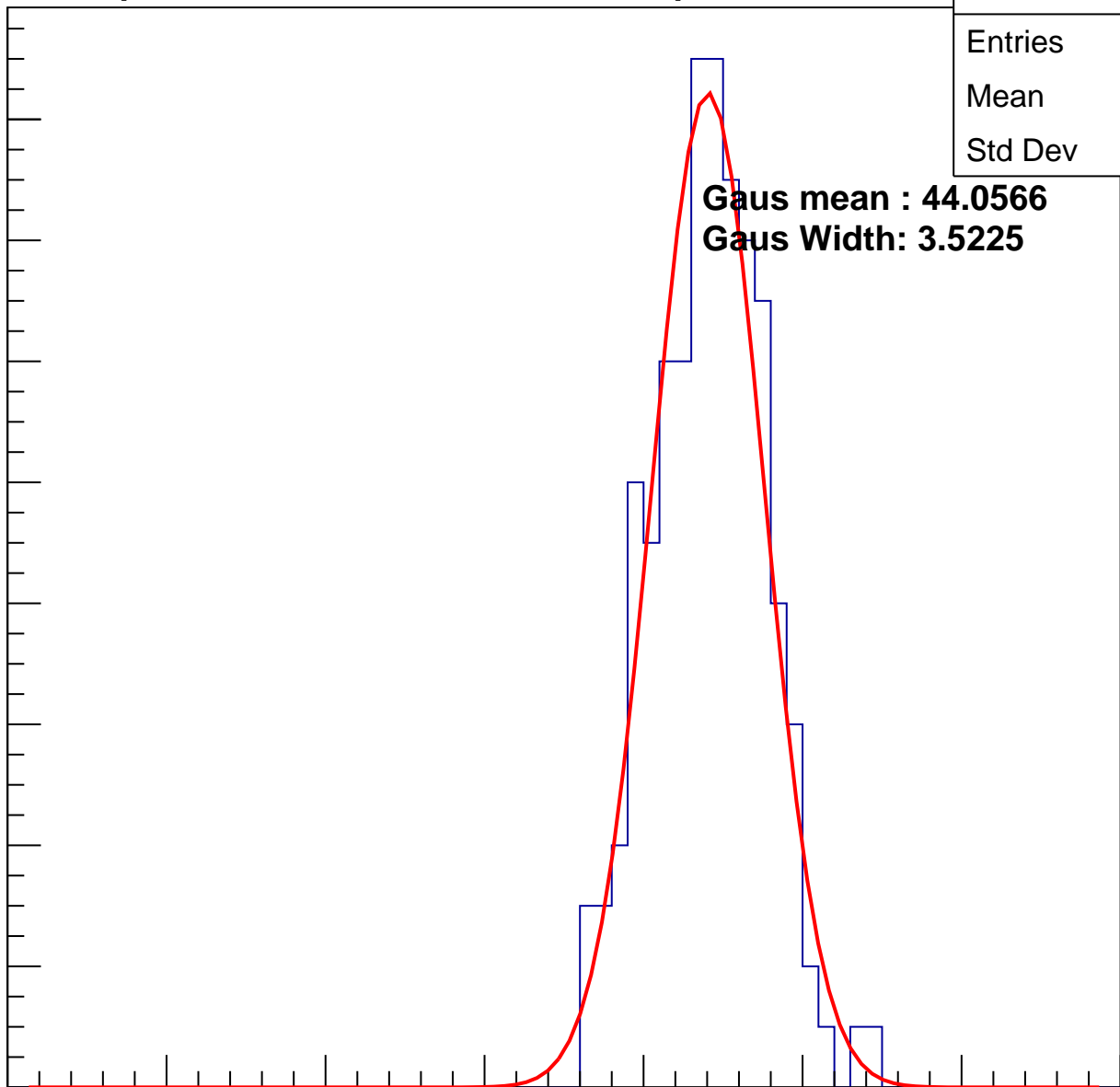
40

50

60

70

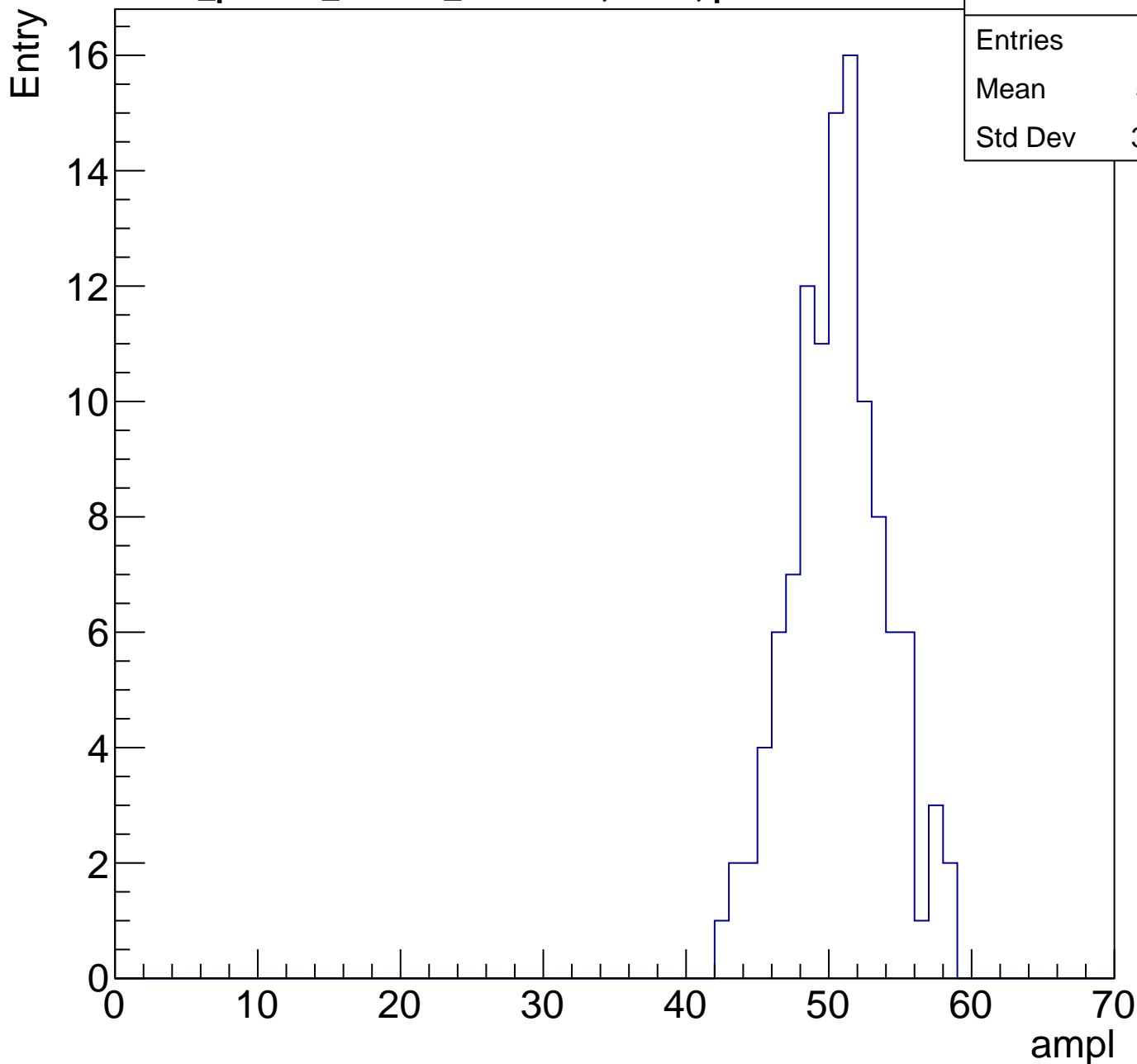
ampl



B1L001S, U21-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

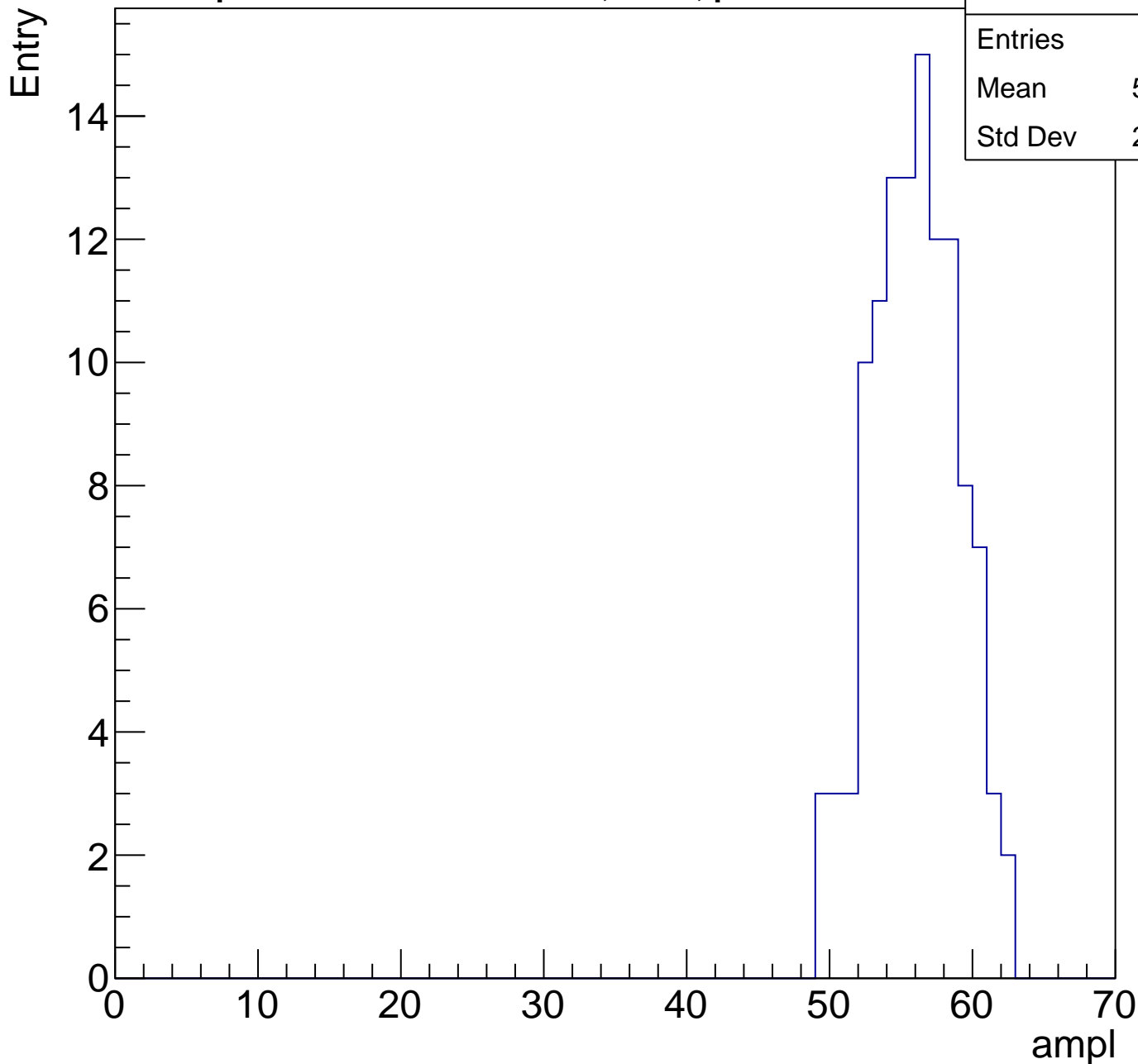
Entries	112
Mean	50.21
Std Dev	3.317



B1L001S, U21-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	55.56
Std Dev	2.982



B1L001S, U21-ch71, adc5

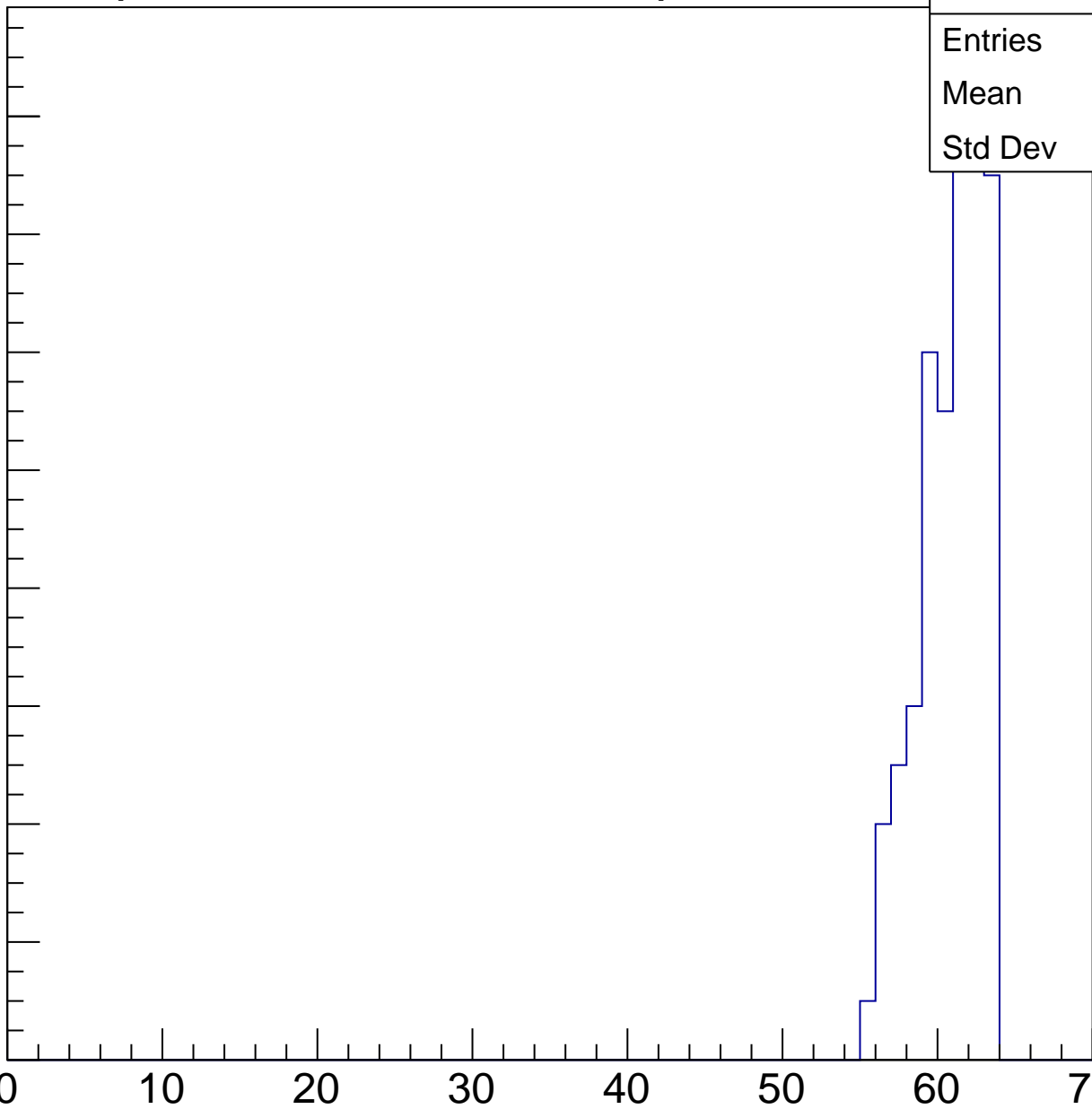
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	60.39
Std Dev	2.07

ampl

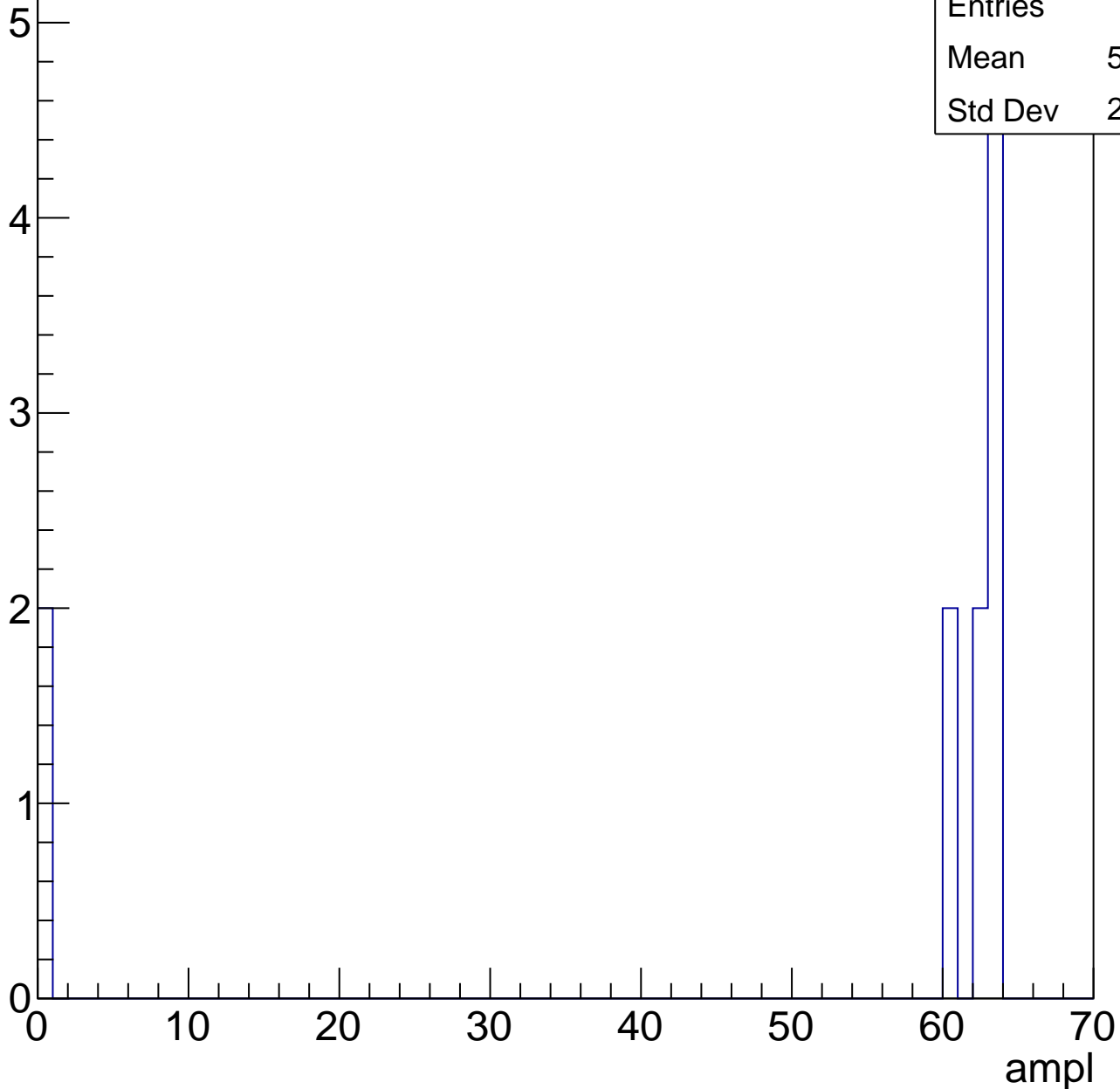


B1L001S, U21-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	50.82
Std Dev	23.98



B1L001S, U21-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	22
Std Dev	1

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch72, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.75
Std Dev	3.168

Gaus mean : 31.1270

Gaus Width: 3.3749

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

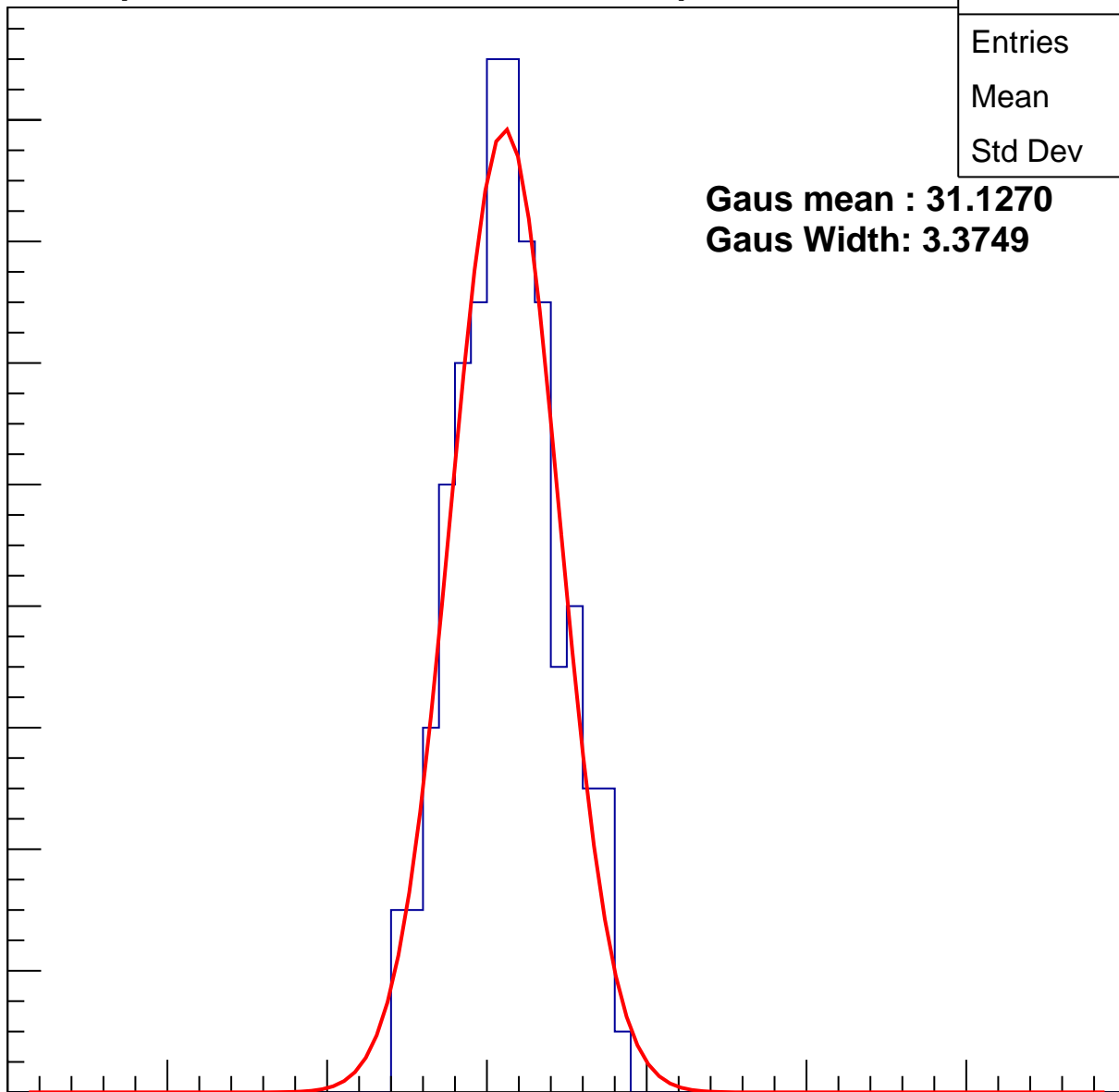
40

50

60

70

ampl



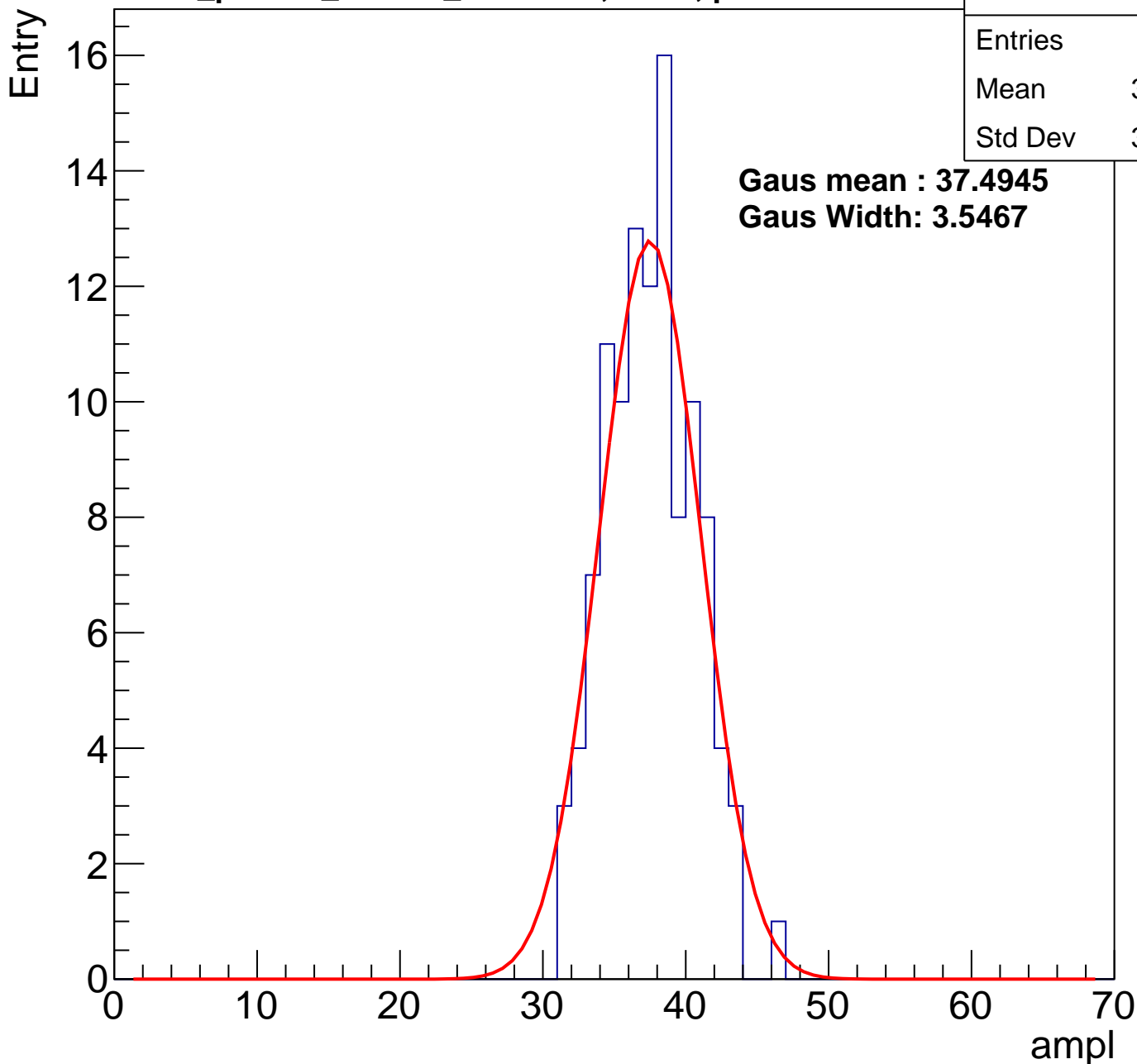
B1L001S, U21-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	37.08
Std Dev	3.054

Gaus mean : 37.4945

Gaus Width: 3.5467



B1L001S, U21-ch72, adc2

calib_packv5_042523_0143.root, FC#2, port C2

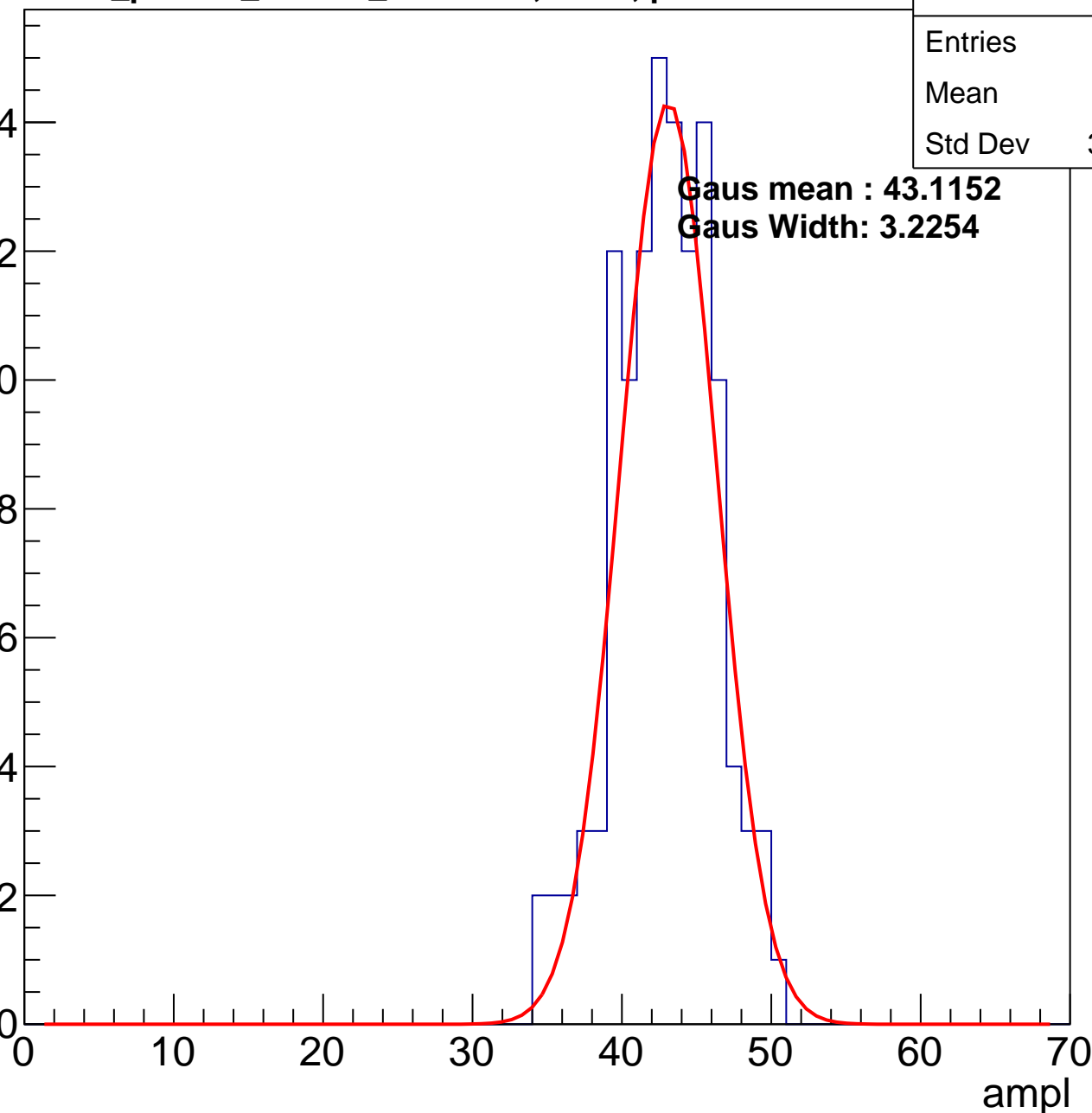
Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	42.41
Std Dev	3.299

Gaus mean : 43.1152

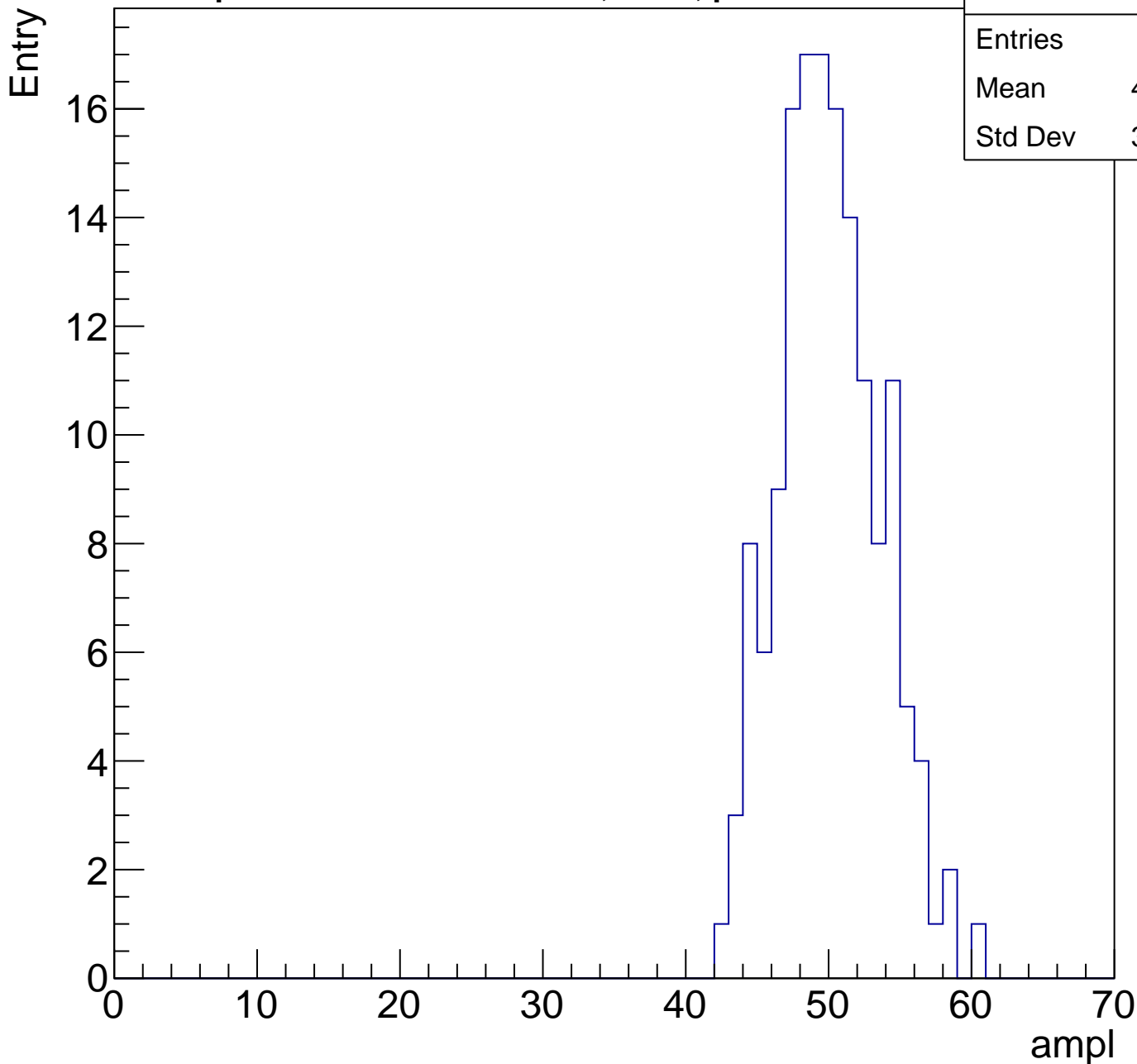
Gaus Width: 3.2254



B1L001S, U21-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

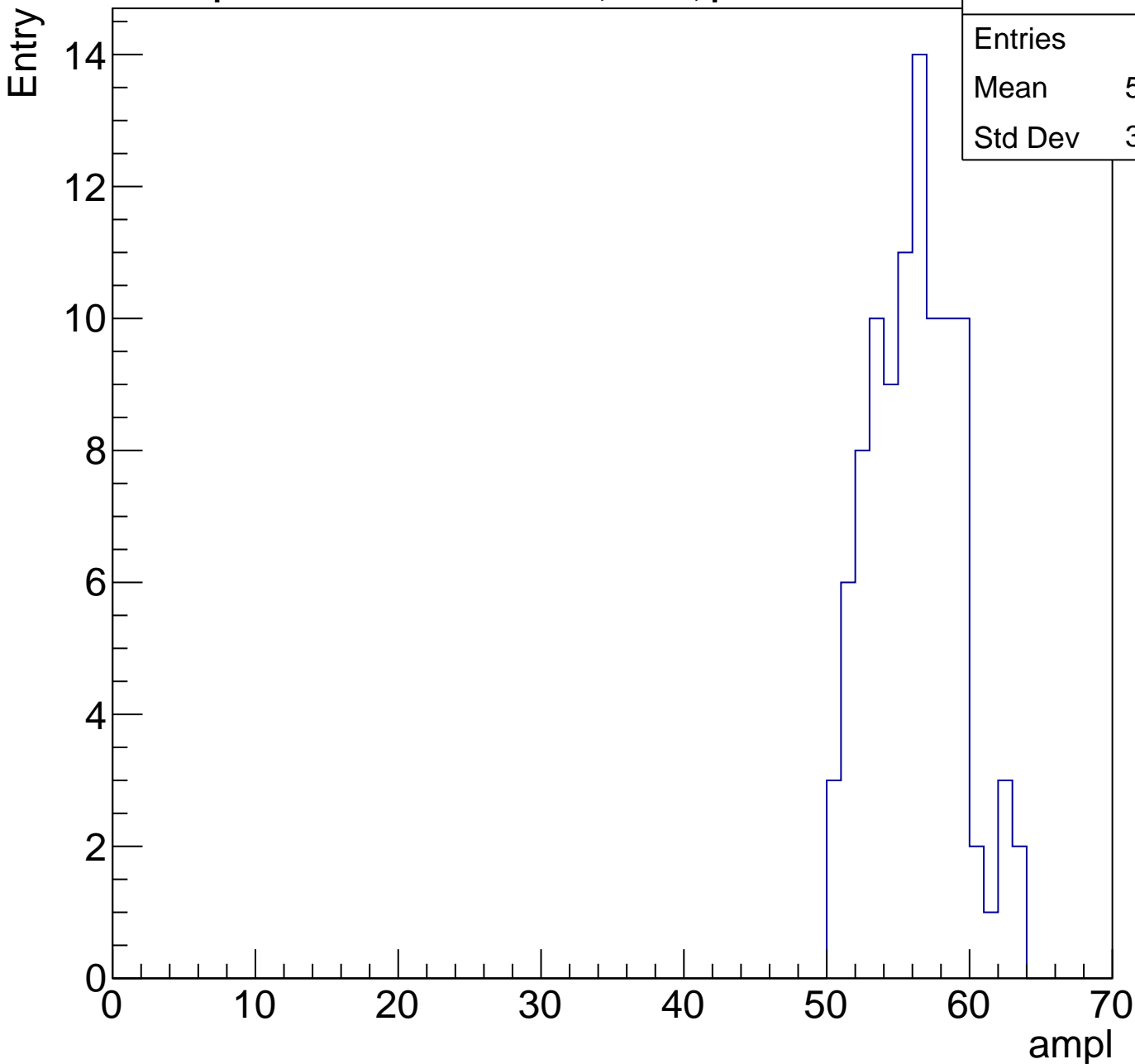
Entries	150
Mean	49.63
Std Dev	3.515



B1L001S, U21-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	55.66
Std Dev	3.039



B1L001S, U21-ch72, adc5

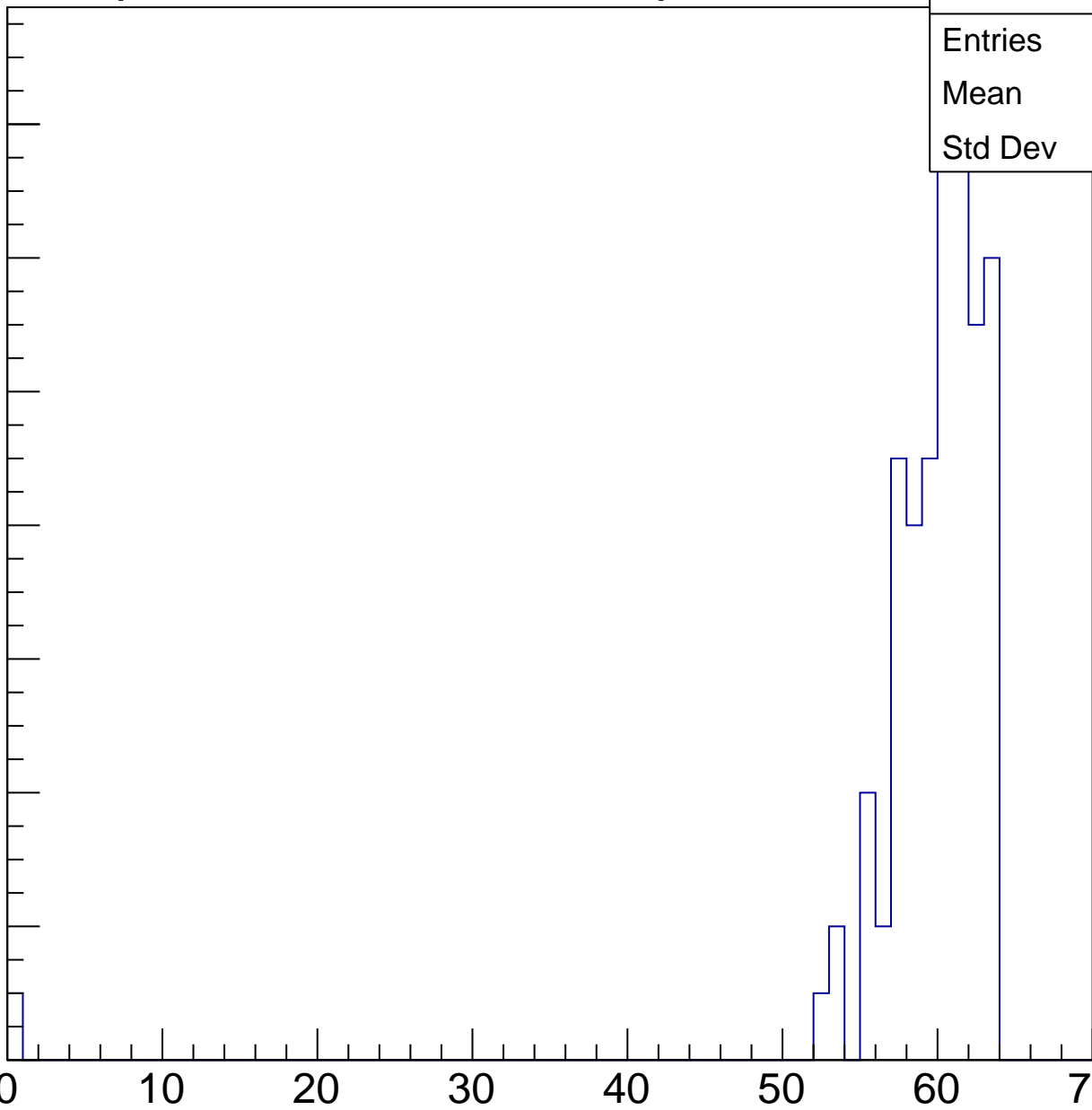
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	88
Mean	58.98
Std Dev	6.817

ampl

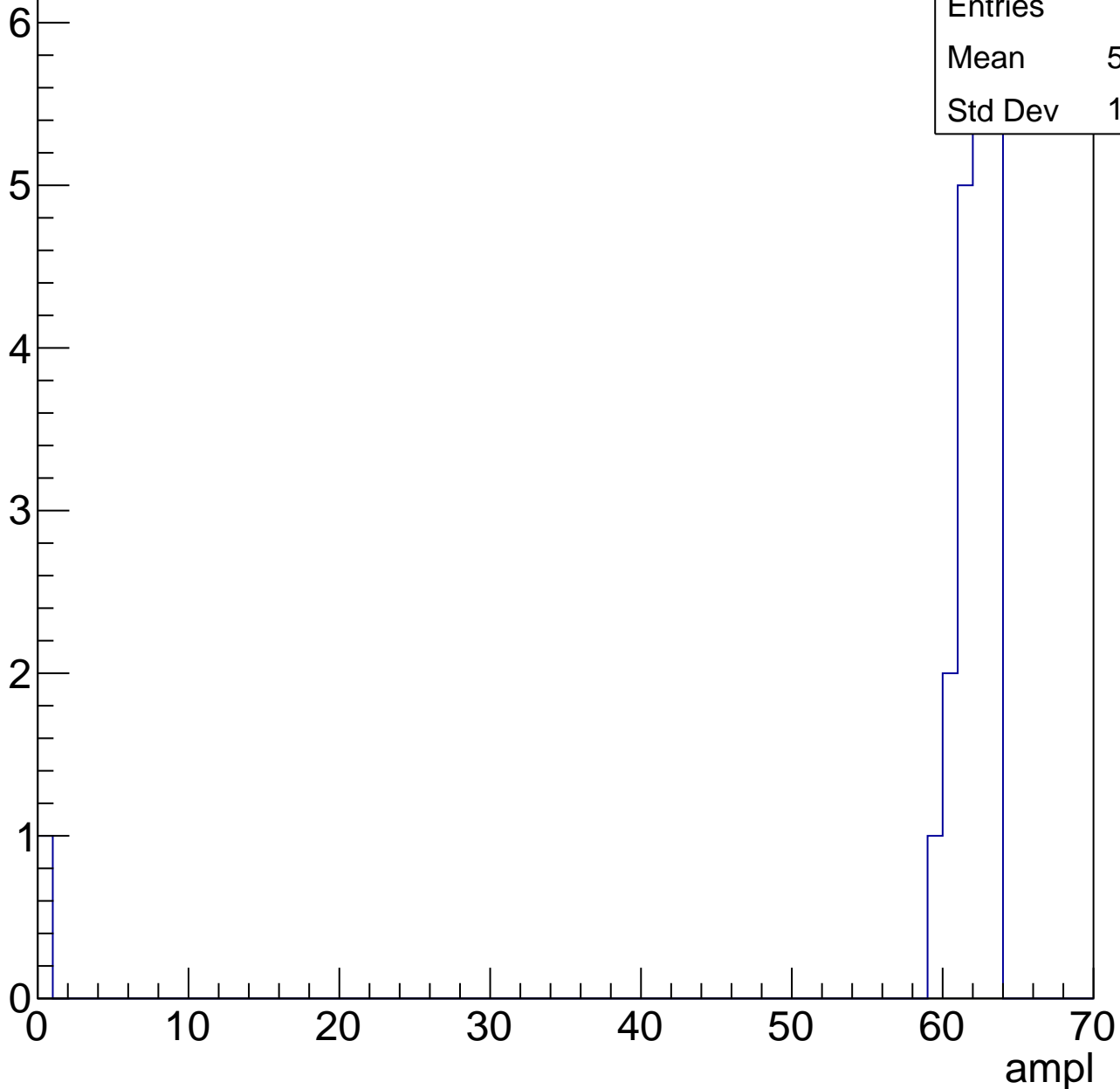


B1L001S, U21-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	58.76
Std Dev	13.19



B1L001S, U21-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.26
Std Dev	4.902

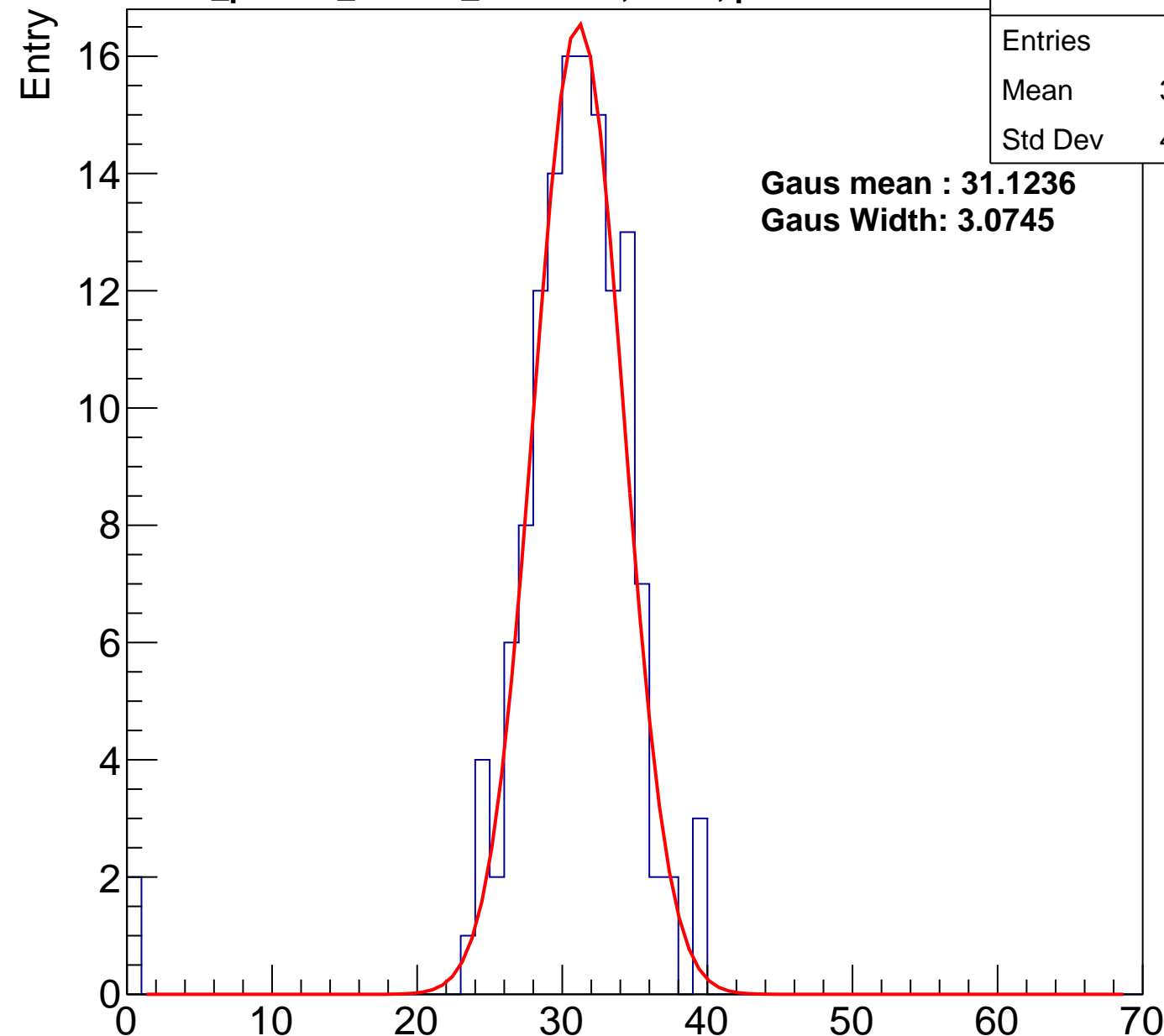
Gaus mean : 31.1236

Gaus Width: 3.0745

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch73, adc1

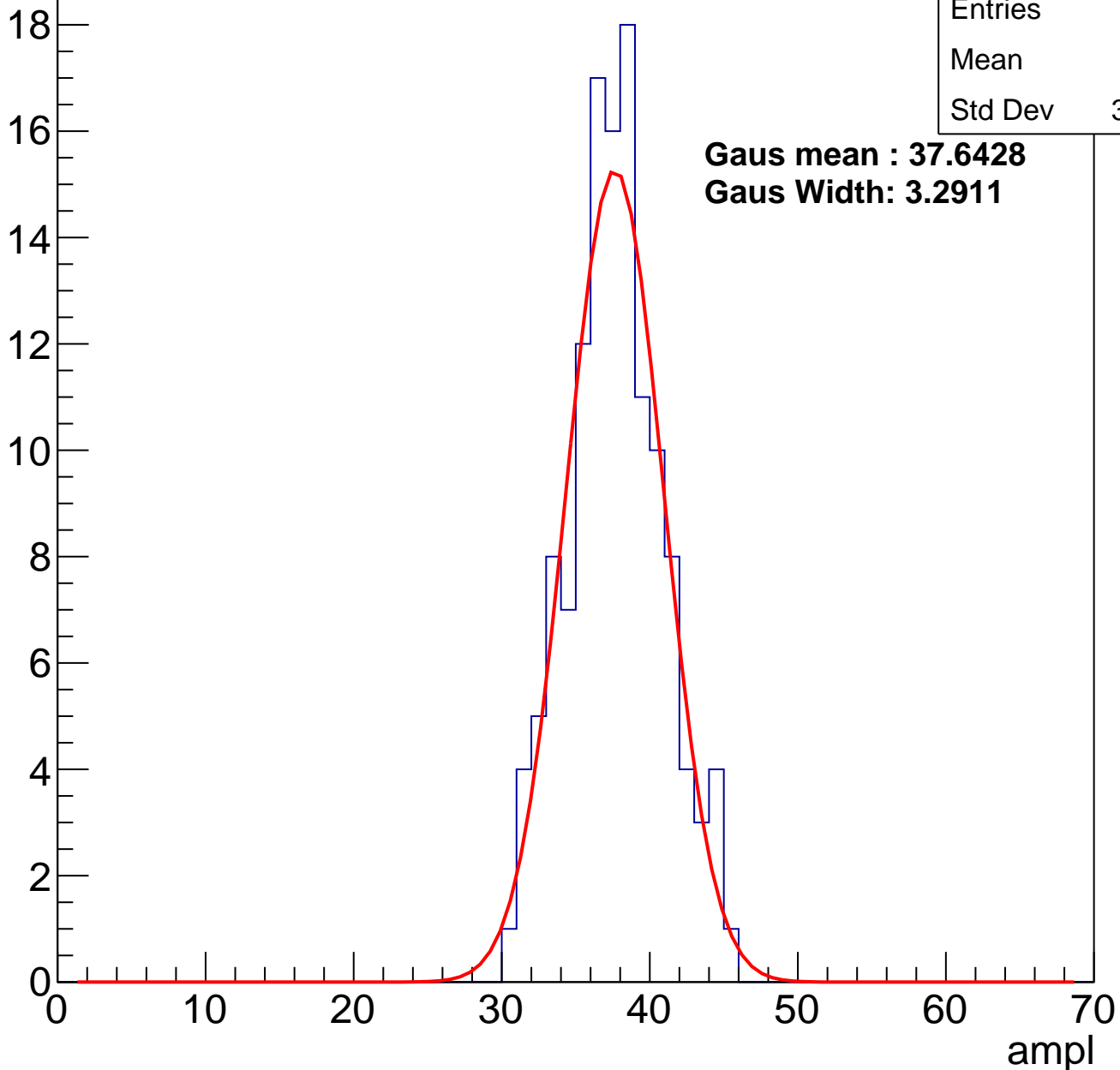
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	37.2
Std Dev	3.195

Gaus mean : 37.6428

Gaus Width: 3.2911

Entry



B1L001S, U21-ch73, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

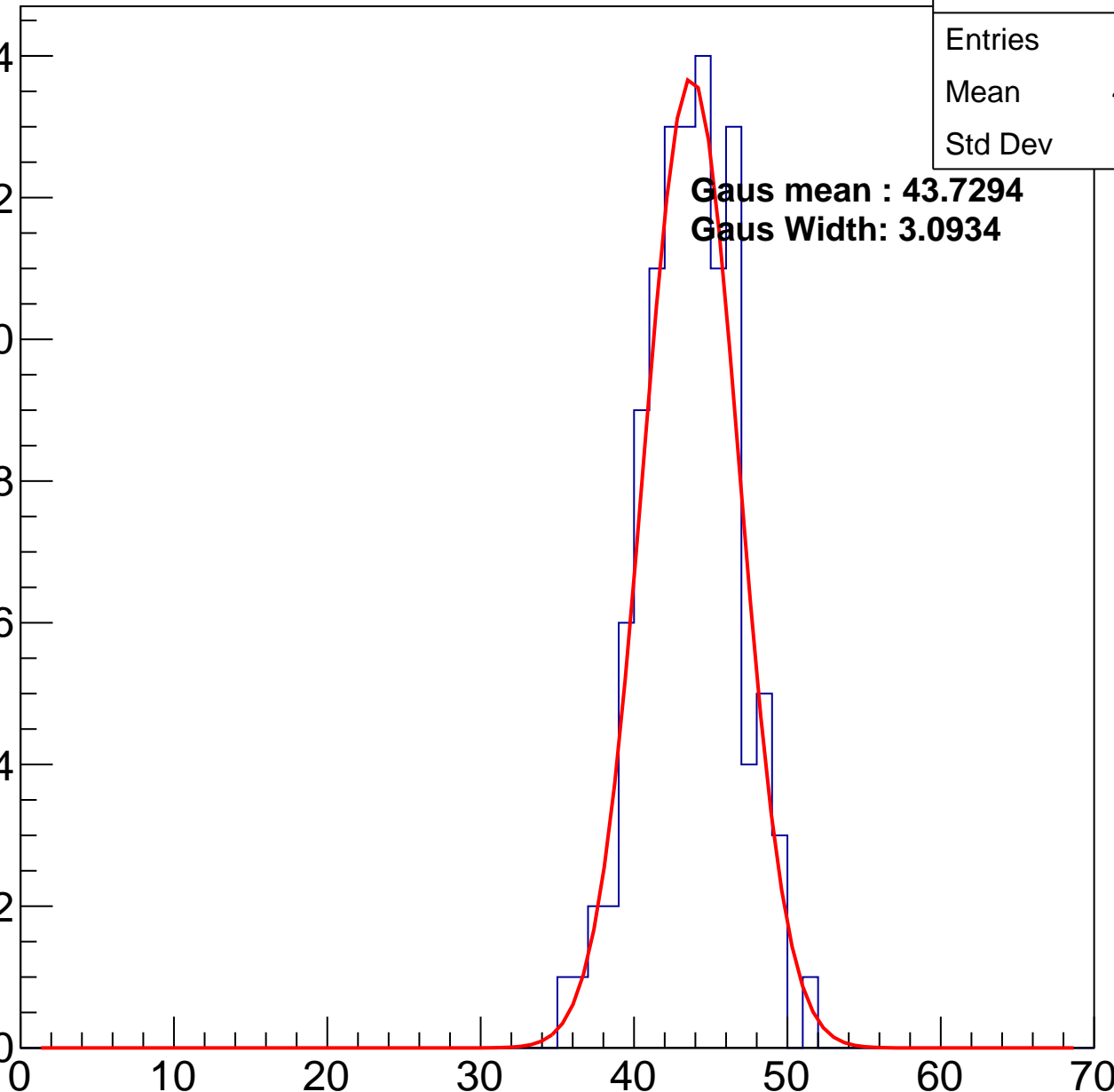
14
12
10
8
6
4
2
0

Entries	109
Mean	43.17
Std Dev	3.031

Gaus mean : 43.7294

Gaus Width: 3.0934

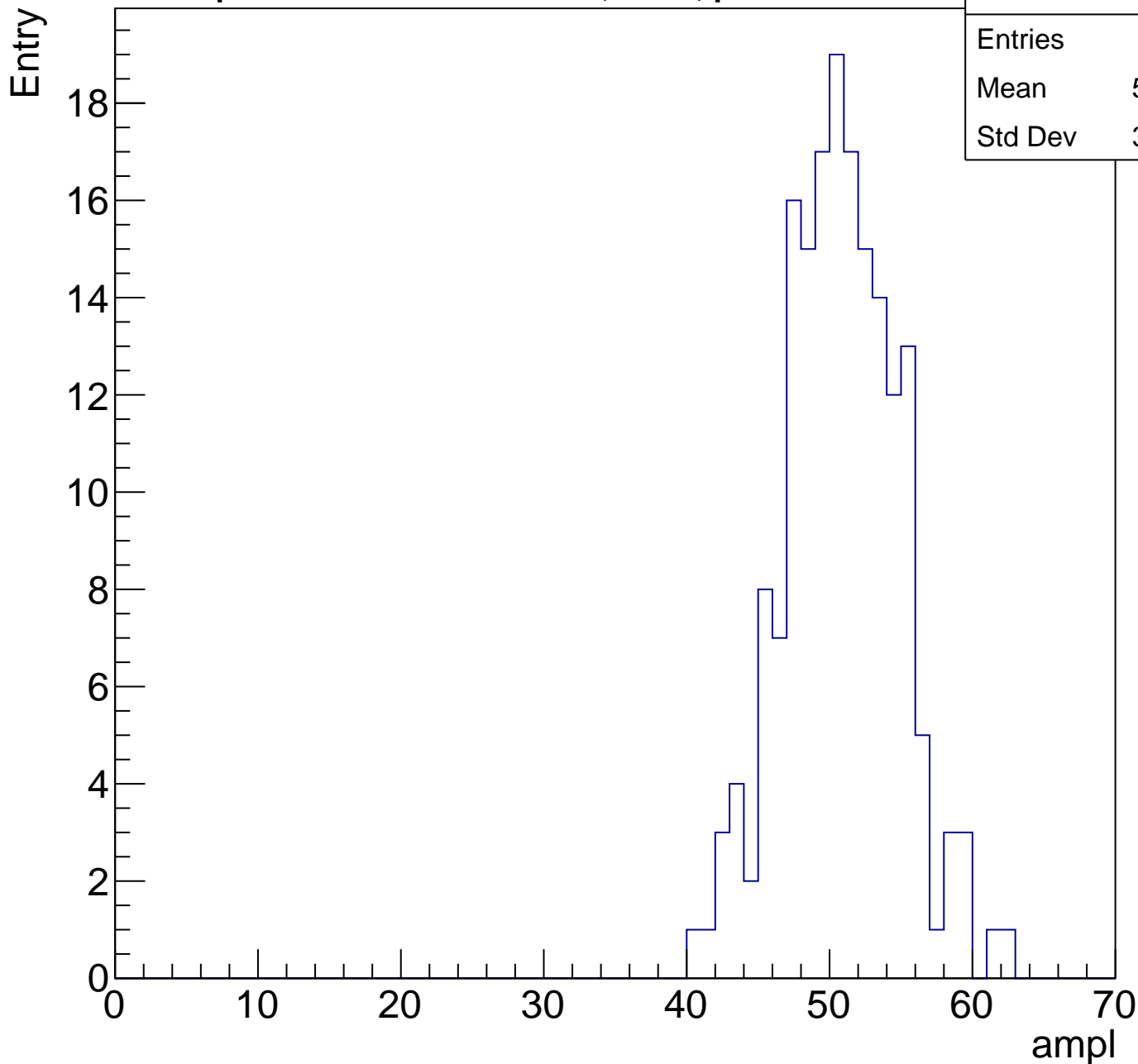
ampl



B1L001S, U21-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	178
Mean	50.38
Std Dev	3.965



B1L001S, U21-ch73, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	56.49
Std Dev	6.088

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

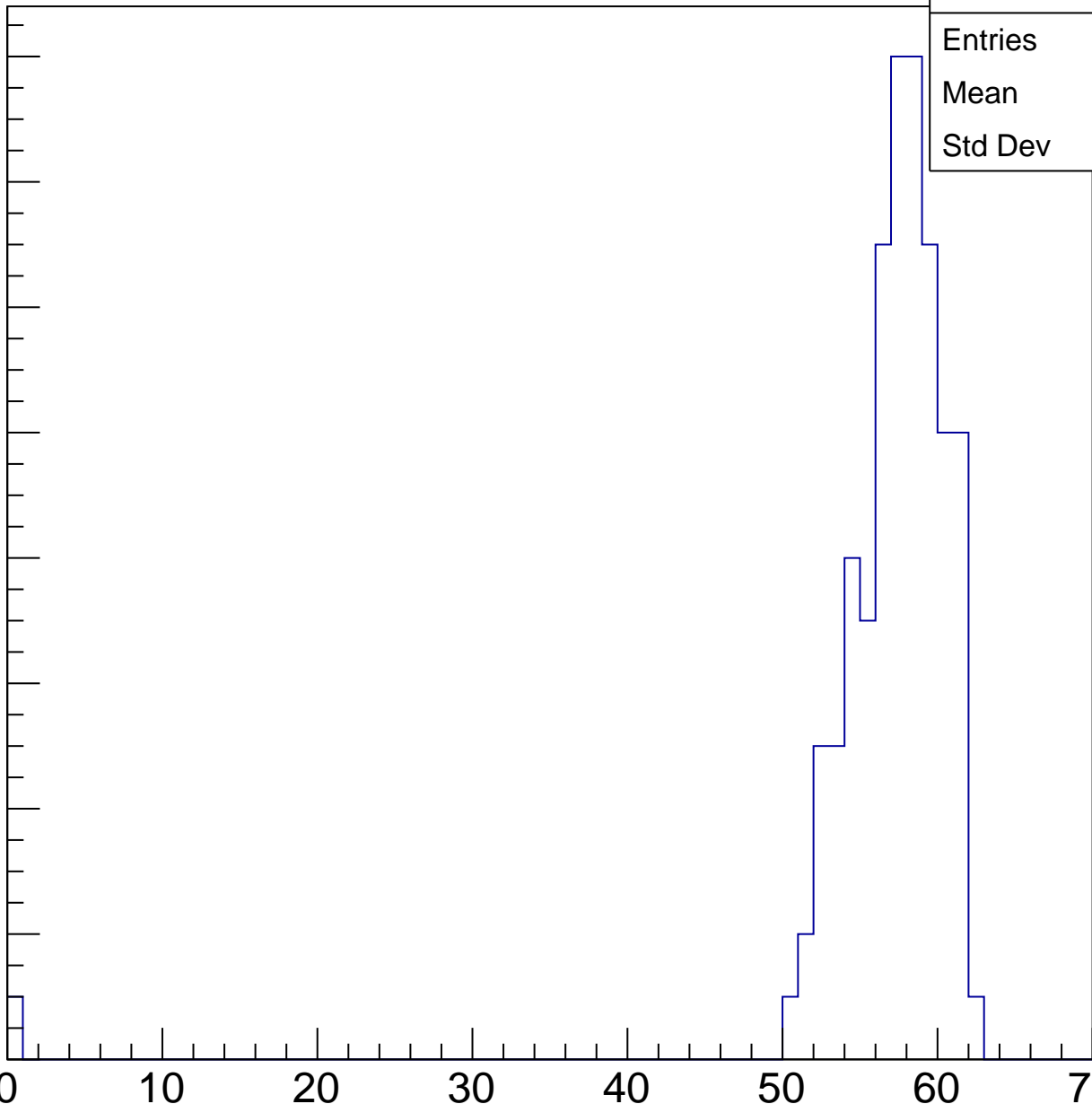
40

50

60

70

ampl



B1L001S, U21-ch73, adc5

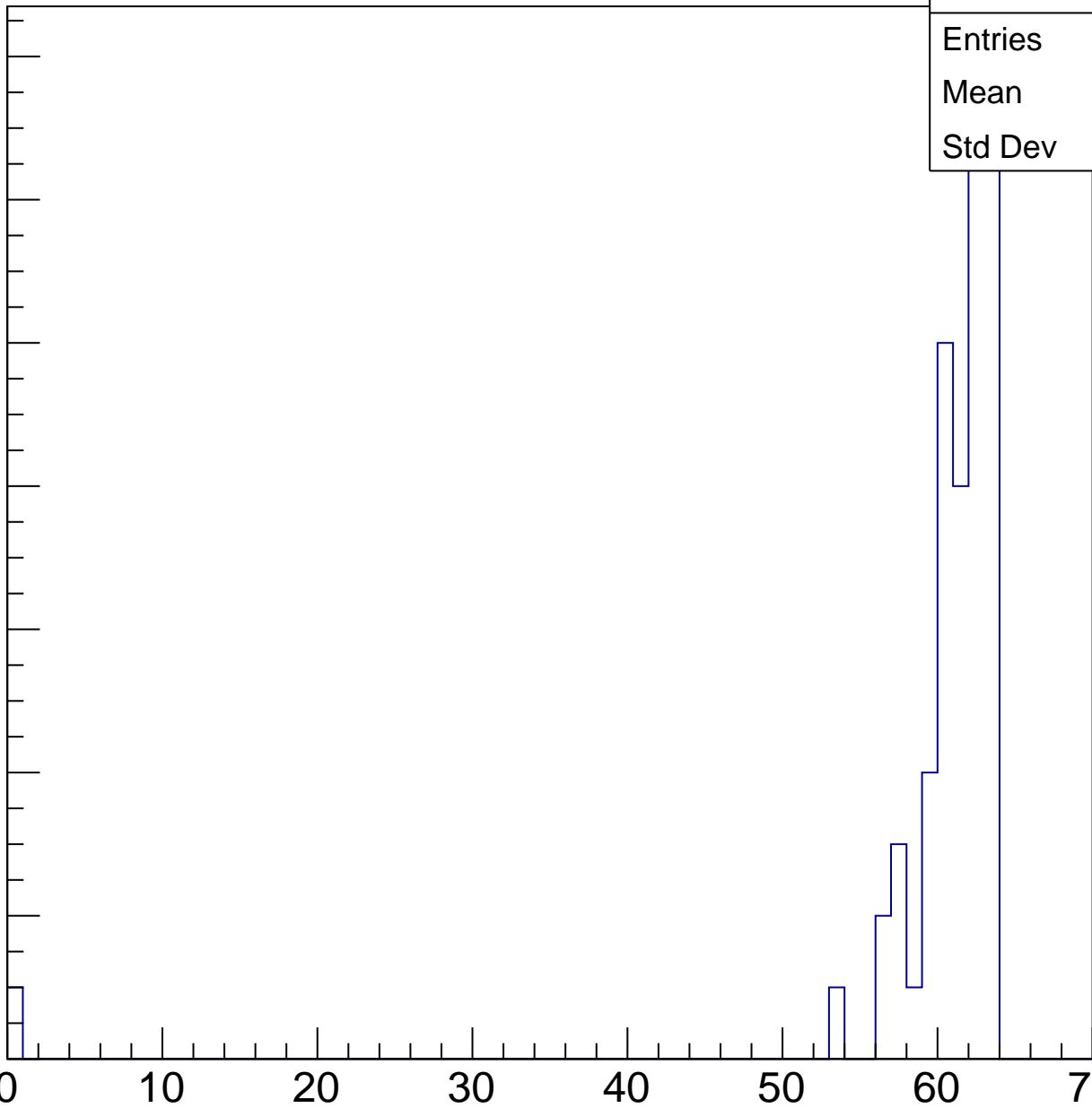
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	59.79
Std Dev	8.204

ampl

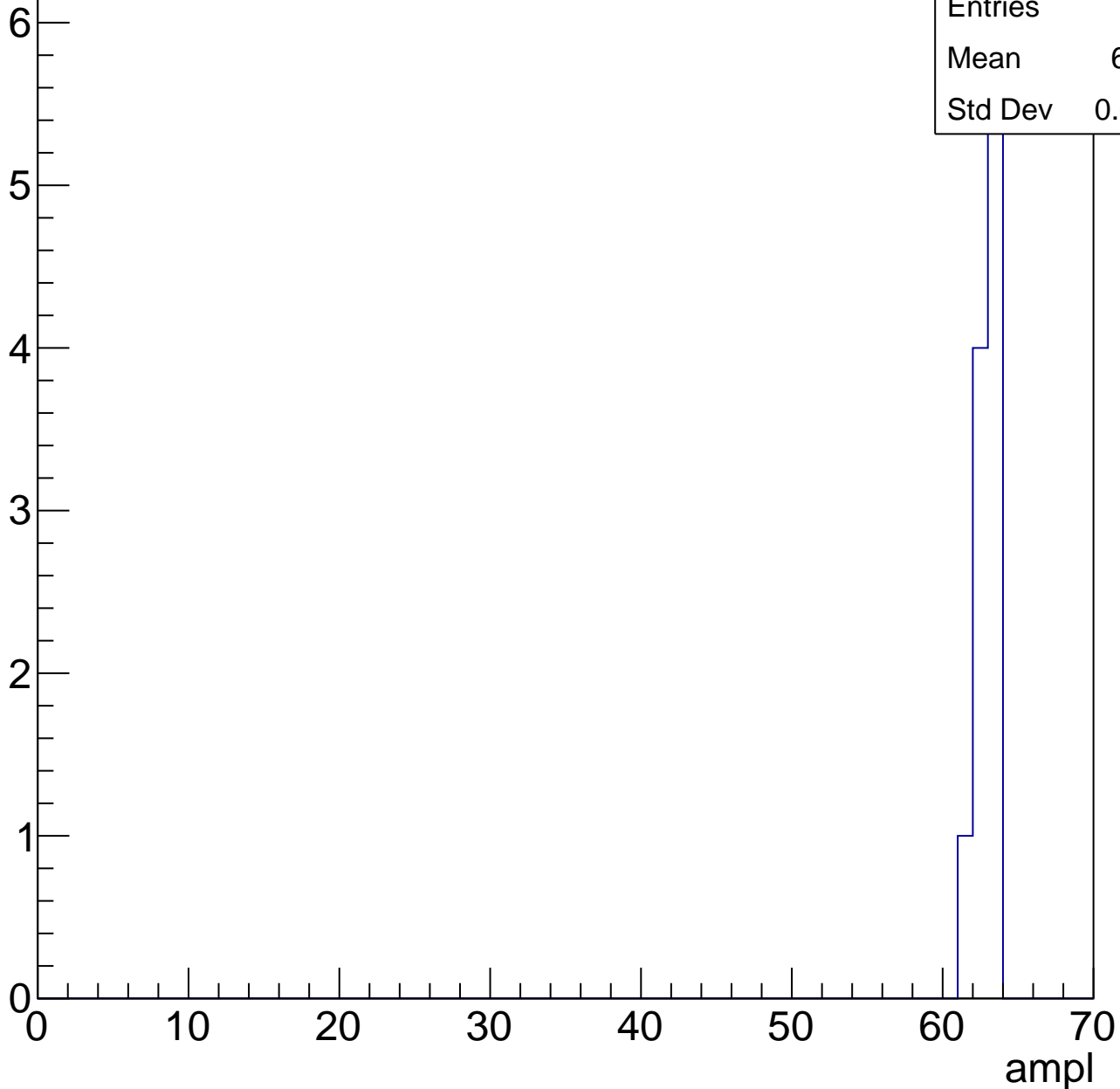


B1L001S, U21-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

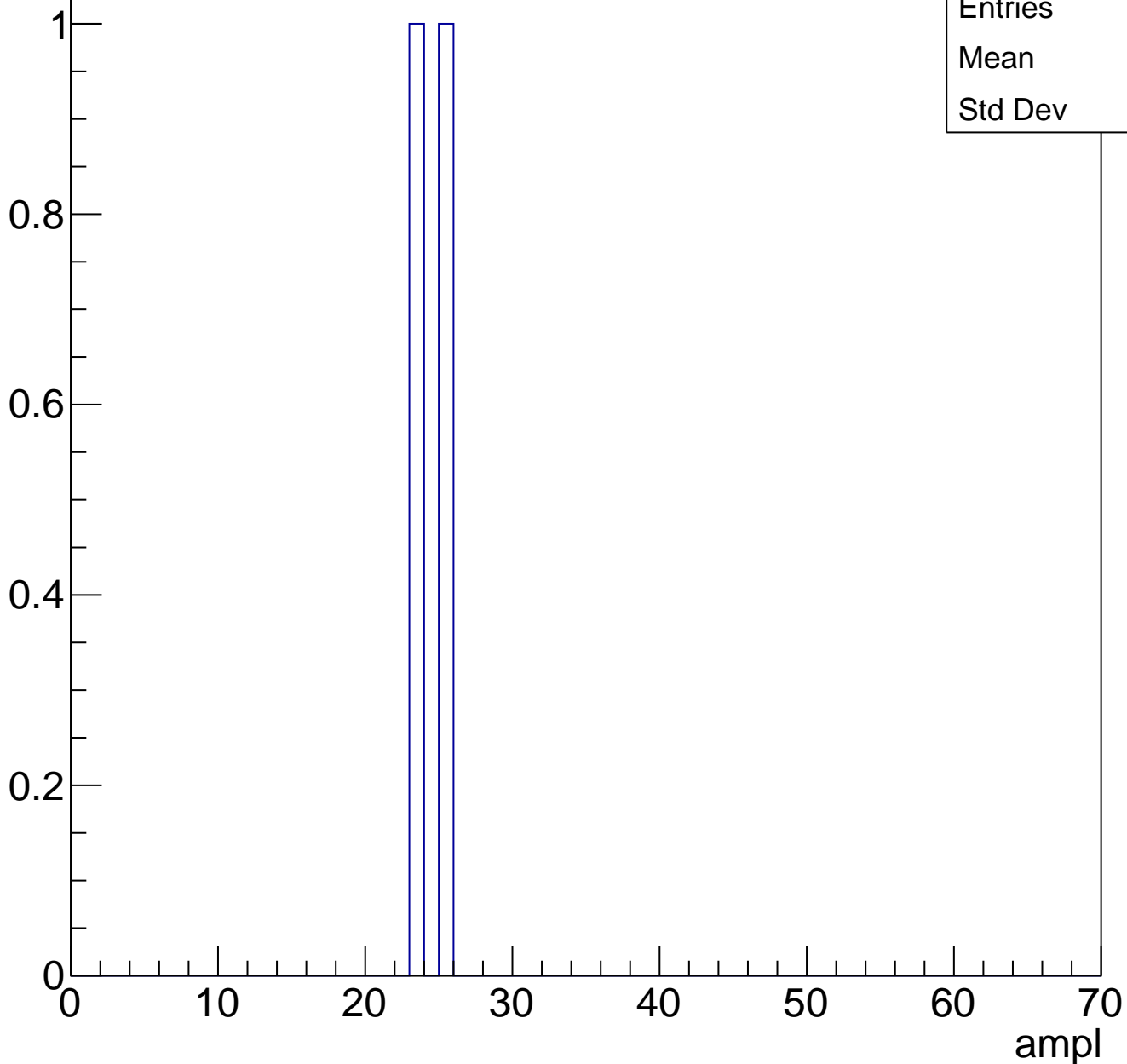
Entries	11
Mean	62.45
Std Dev	0.6556



B1L001S, U21-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch74, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.89
Std Dev	3.968

Gaus mean : 29.4790

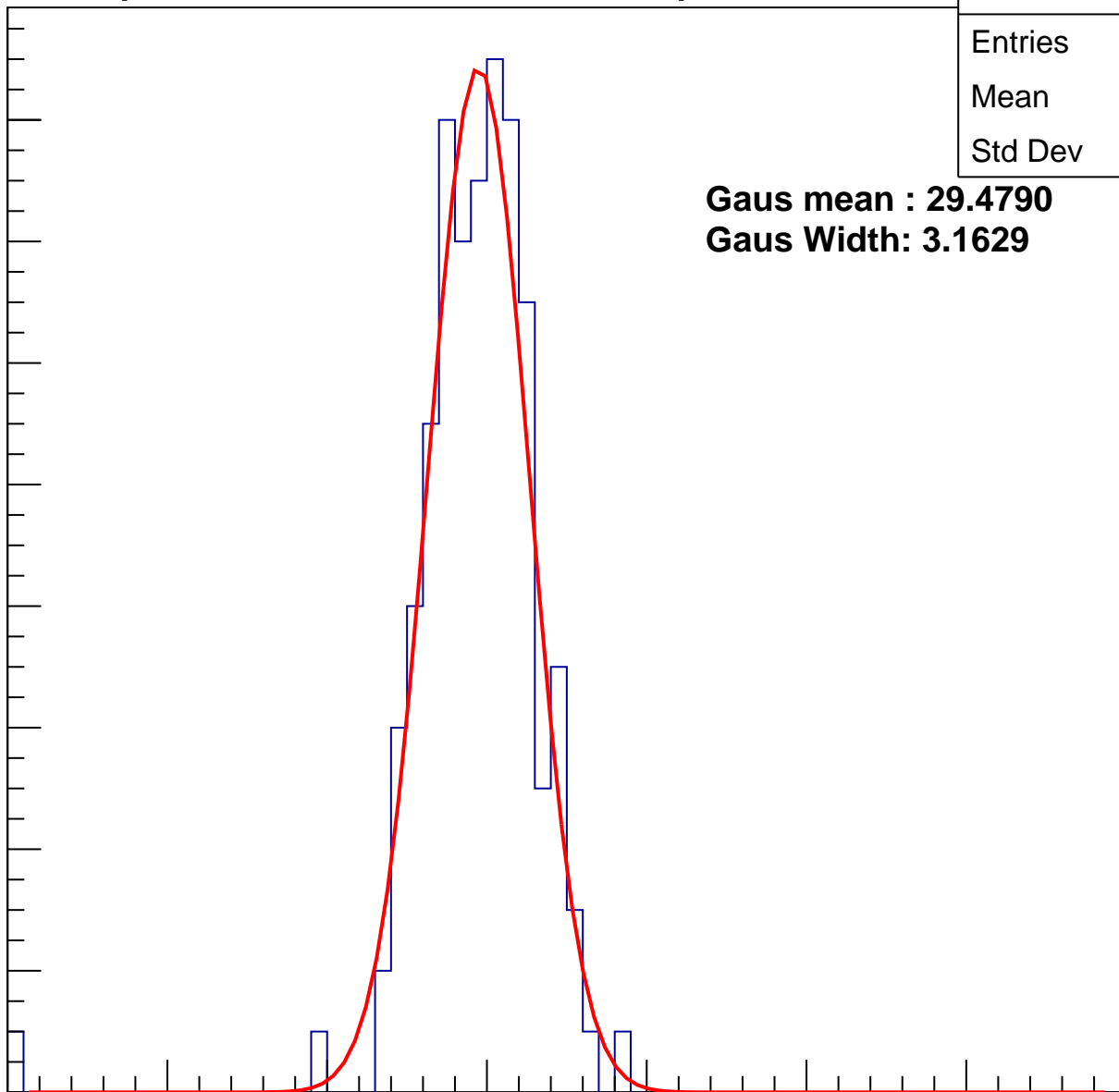
Gaus Width: 3.1629

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	35.55
Std Dev	3.243

Gaus mean : 36.1727

Gaus Width: 3.2034

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

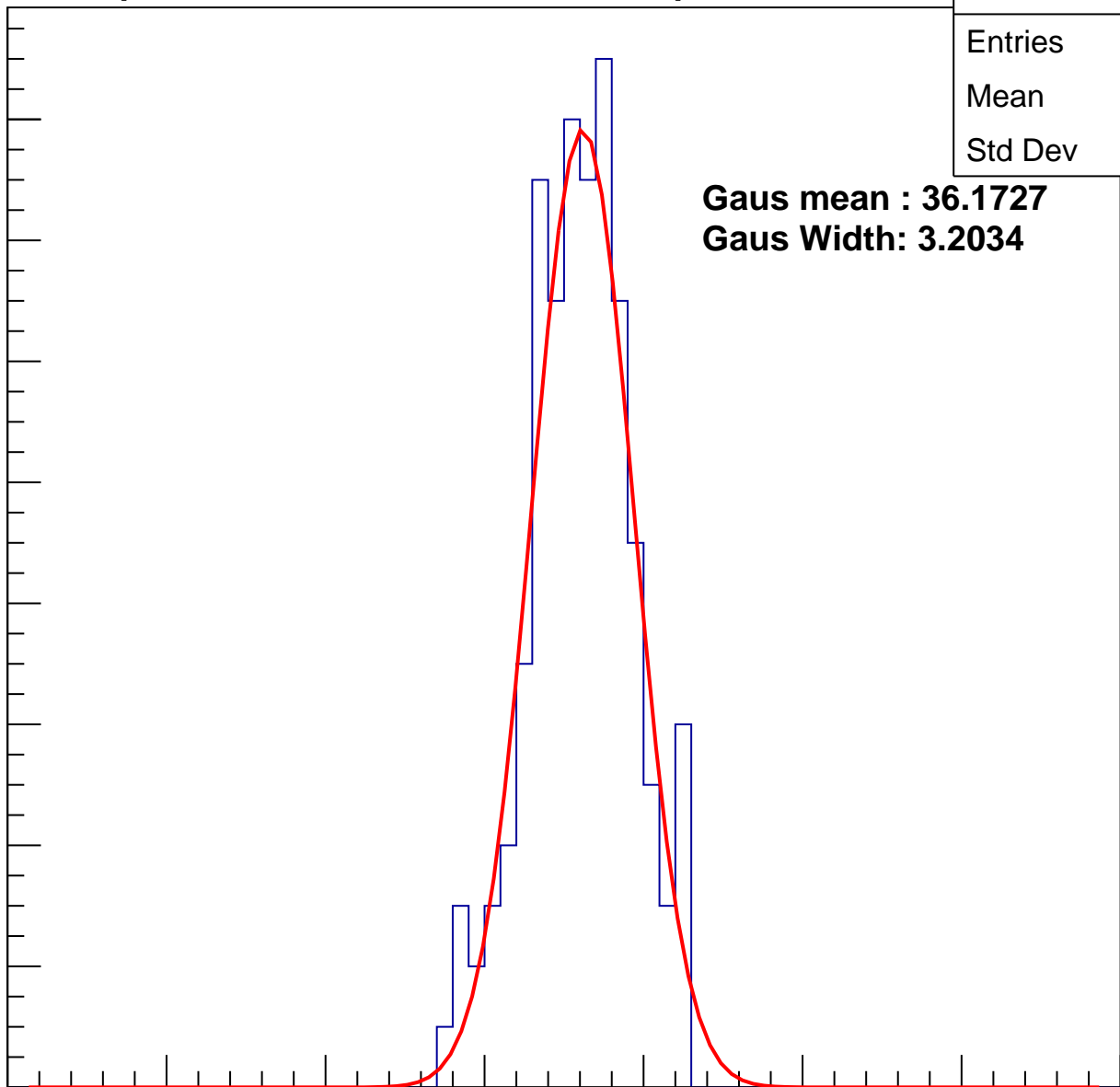
40

50

60

70

ampl



B1L001S, U21-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	42.41
Std Dev	3.245

Gaus mean : 42.8163

Gaus Width: 3.3277

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

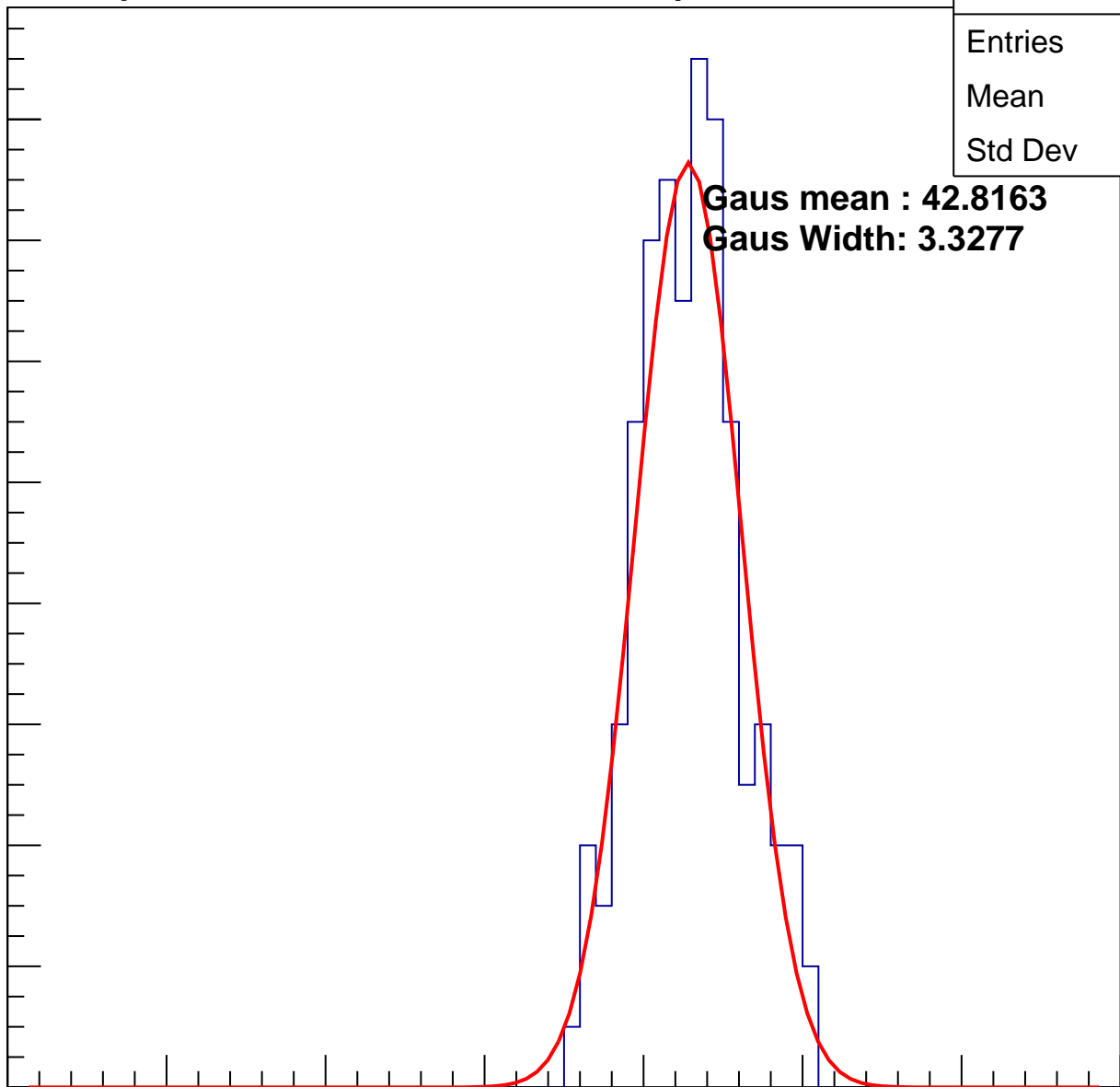
40

50

60

70

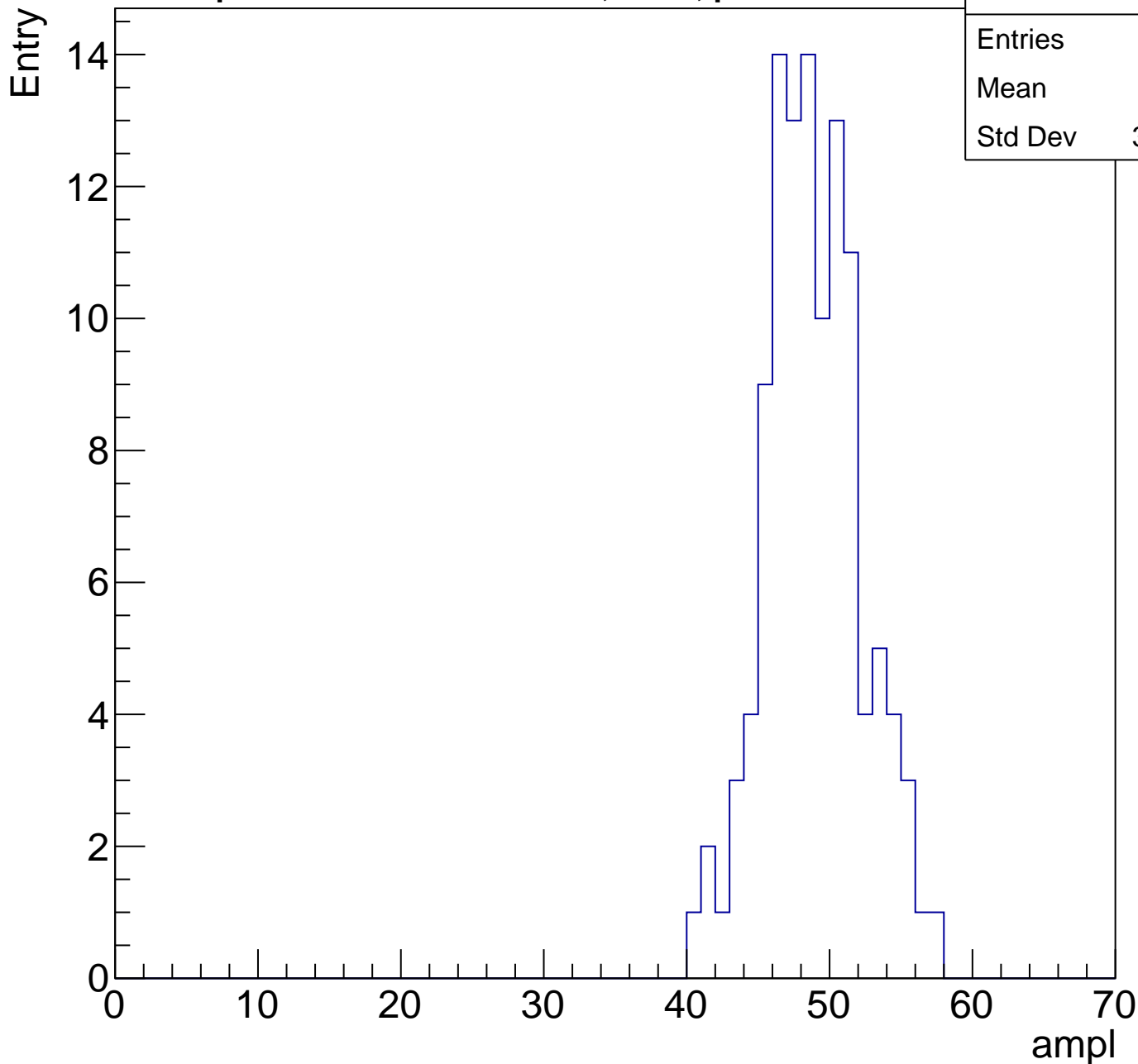
ampl



B1L001S, U21-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	48.4
Std Dev	3.333

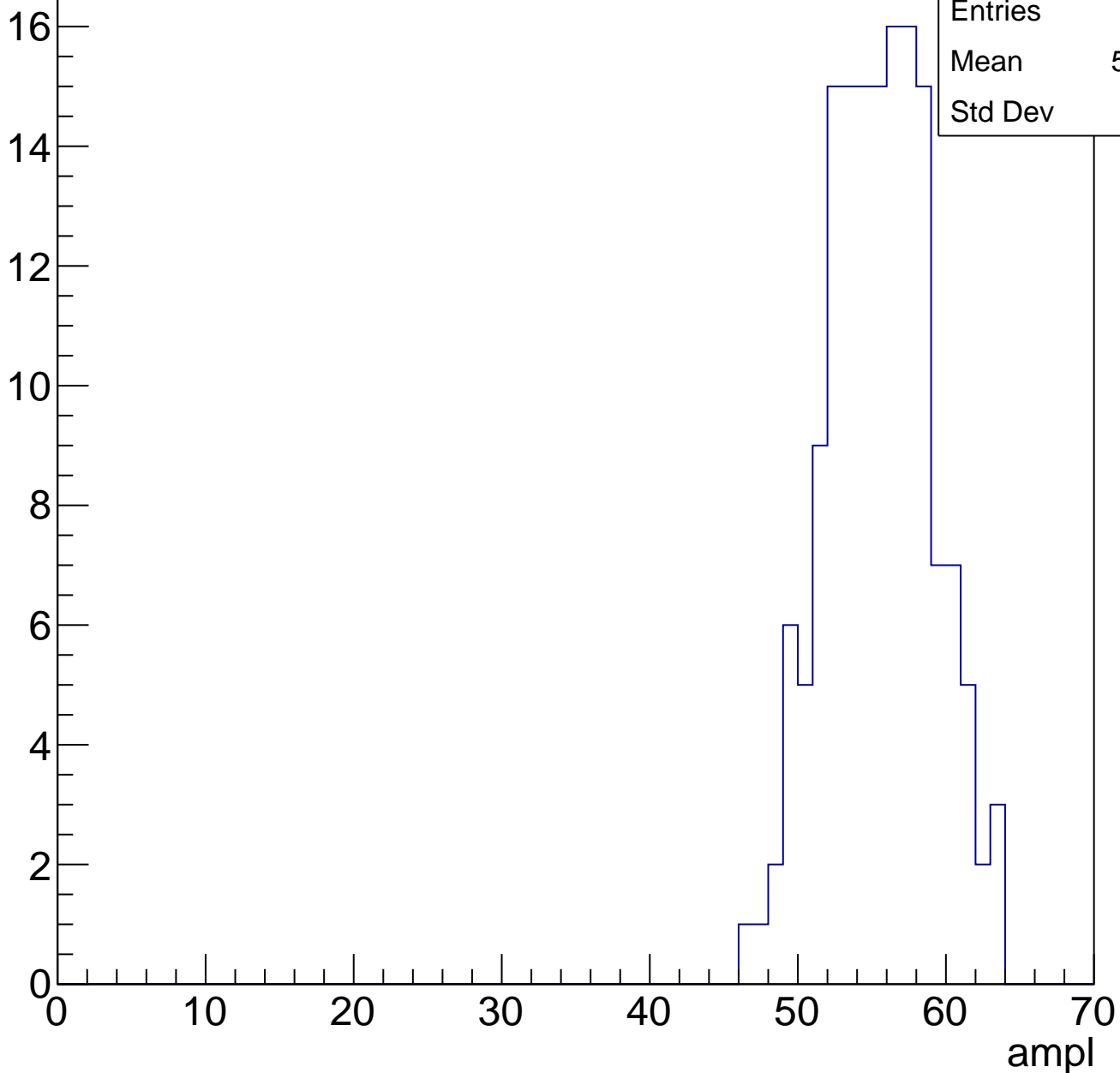


B1L001S, U21-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	55.04
Std Dev	3.51

Entry



B1L001S, U21-ch74, adc5

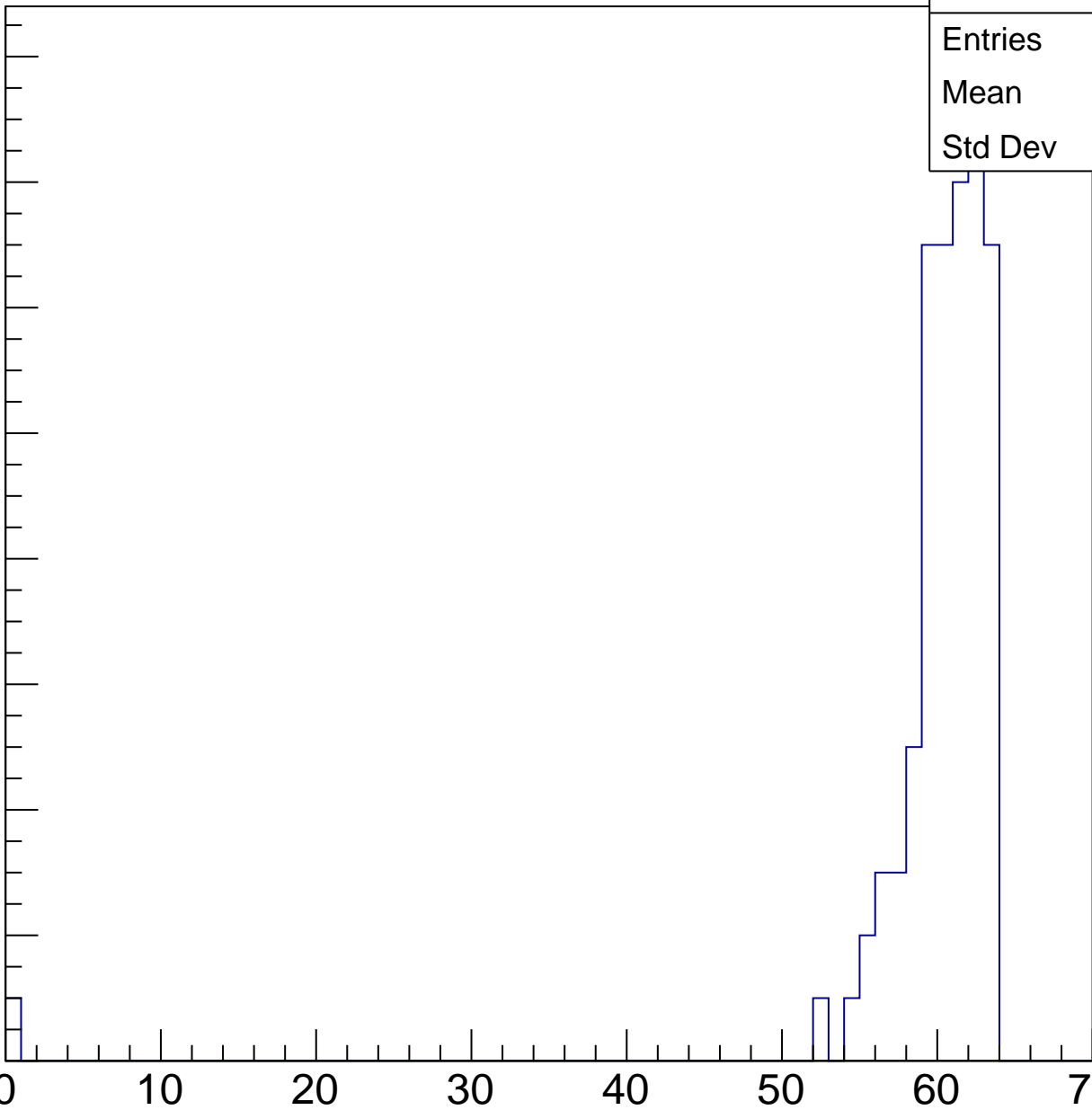
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.49
Std Dev	6.887

ampl



B1L001S, U21-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	8
Mean	54.62
Std Dev	20.66

B1L001S, U21-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch75, adc0

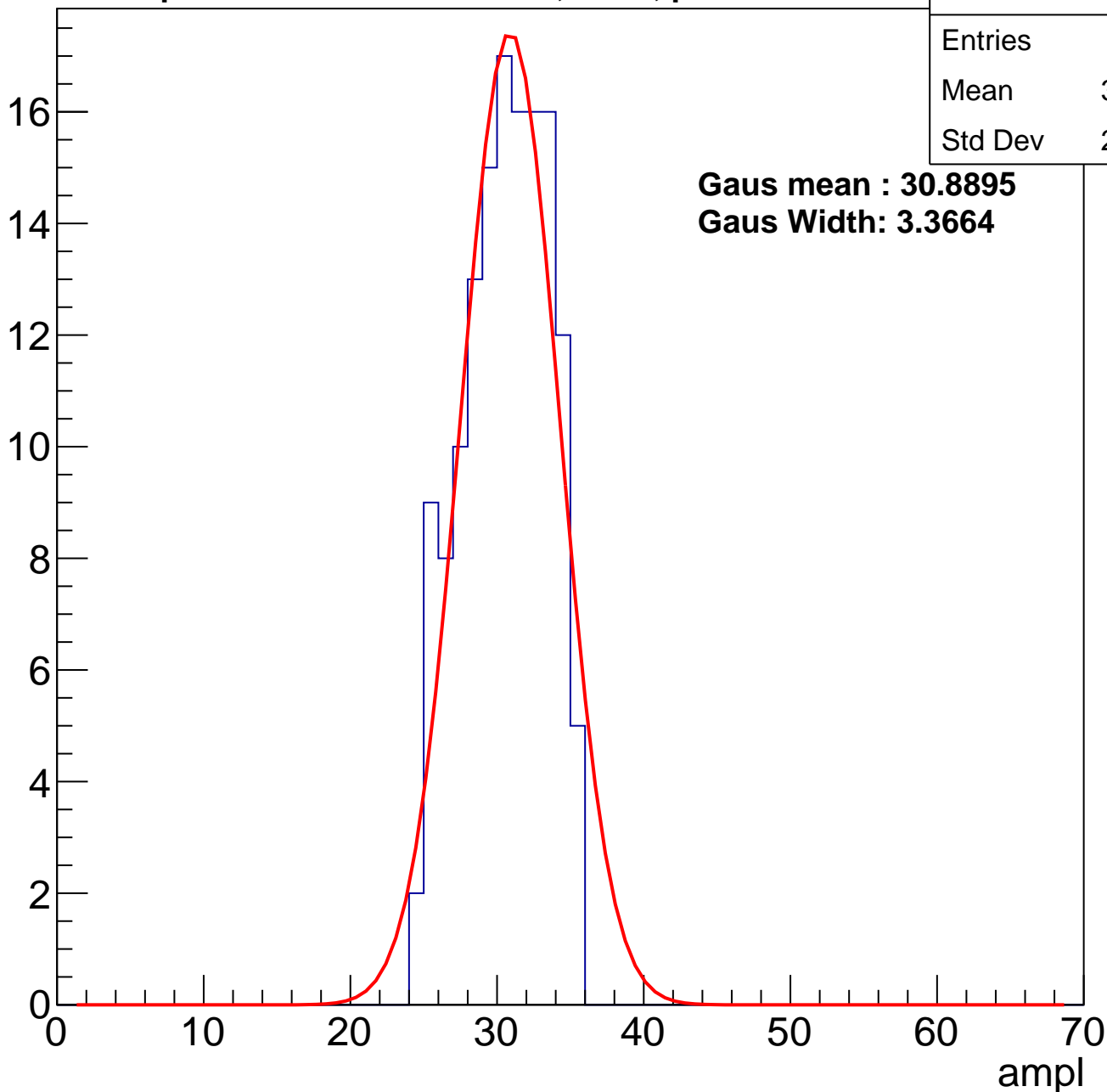
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	30.06
Std Dev	2.842

Gaus mean : 30.8895

Gaus Width: 3.3664

Entry



B1L001S, U21-ch75, adc1

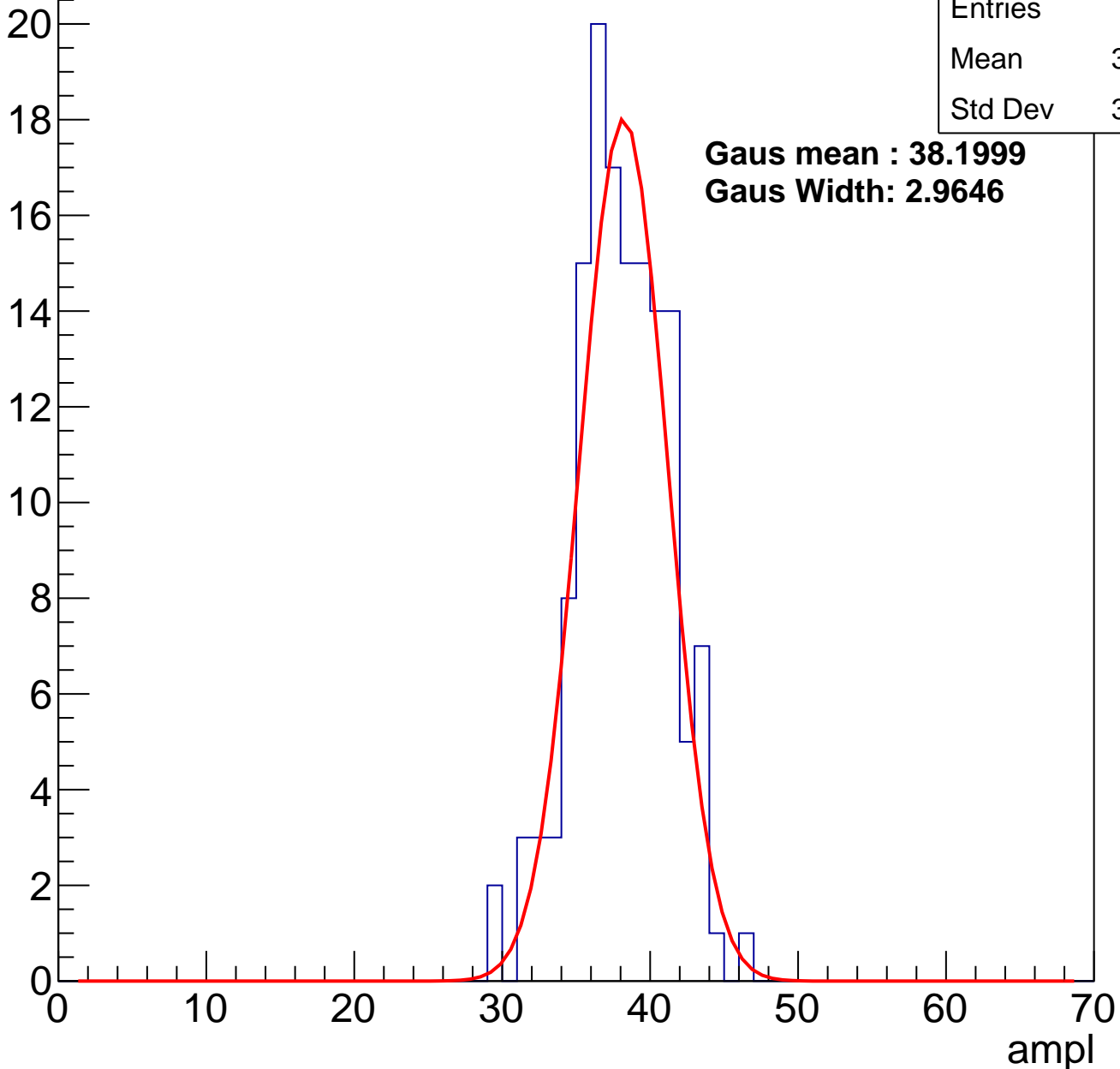
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.64
Std Dev	3.112

Gaus mean : 38.1999

Gaus Width: 2.9646

Entry



B1L001S, U21-ch75, adc2

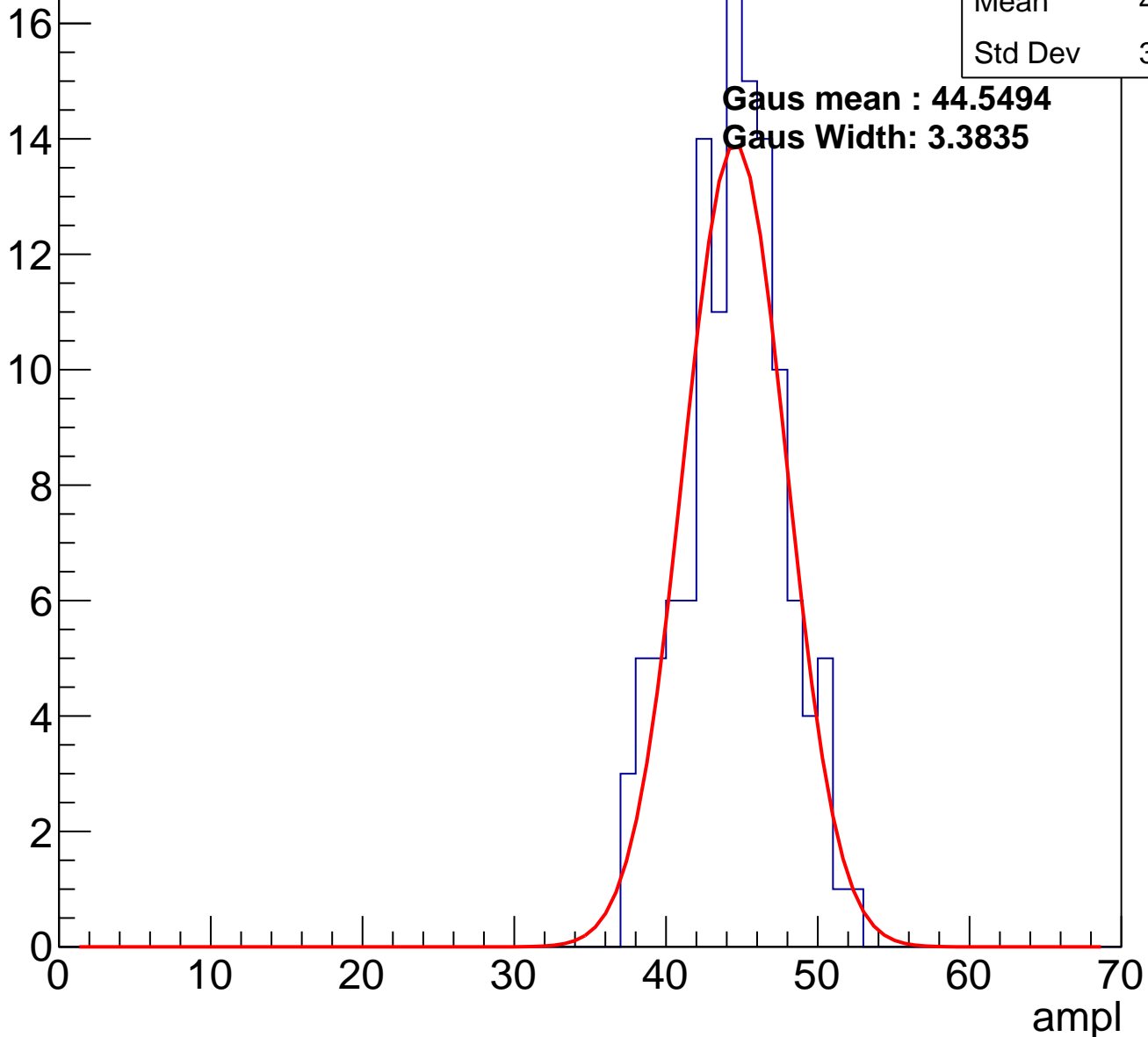
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	44.04
Std Dev	3.274

Gaus mean : 44.5494

Gaus Width: 3.3835

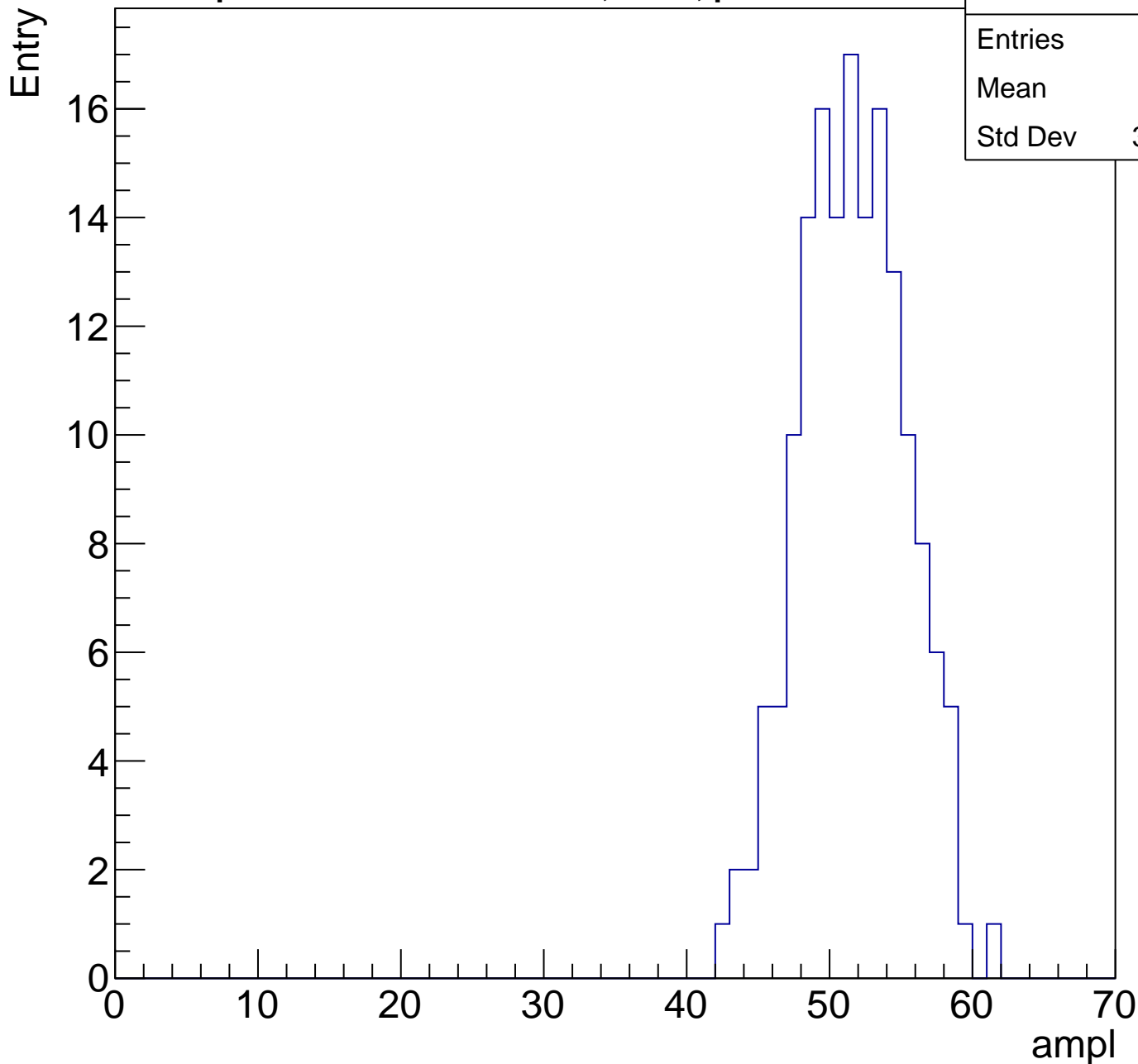
Entry



B1L001S, U21-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

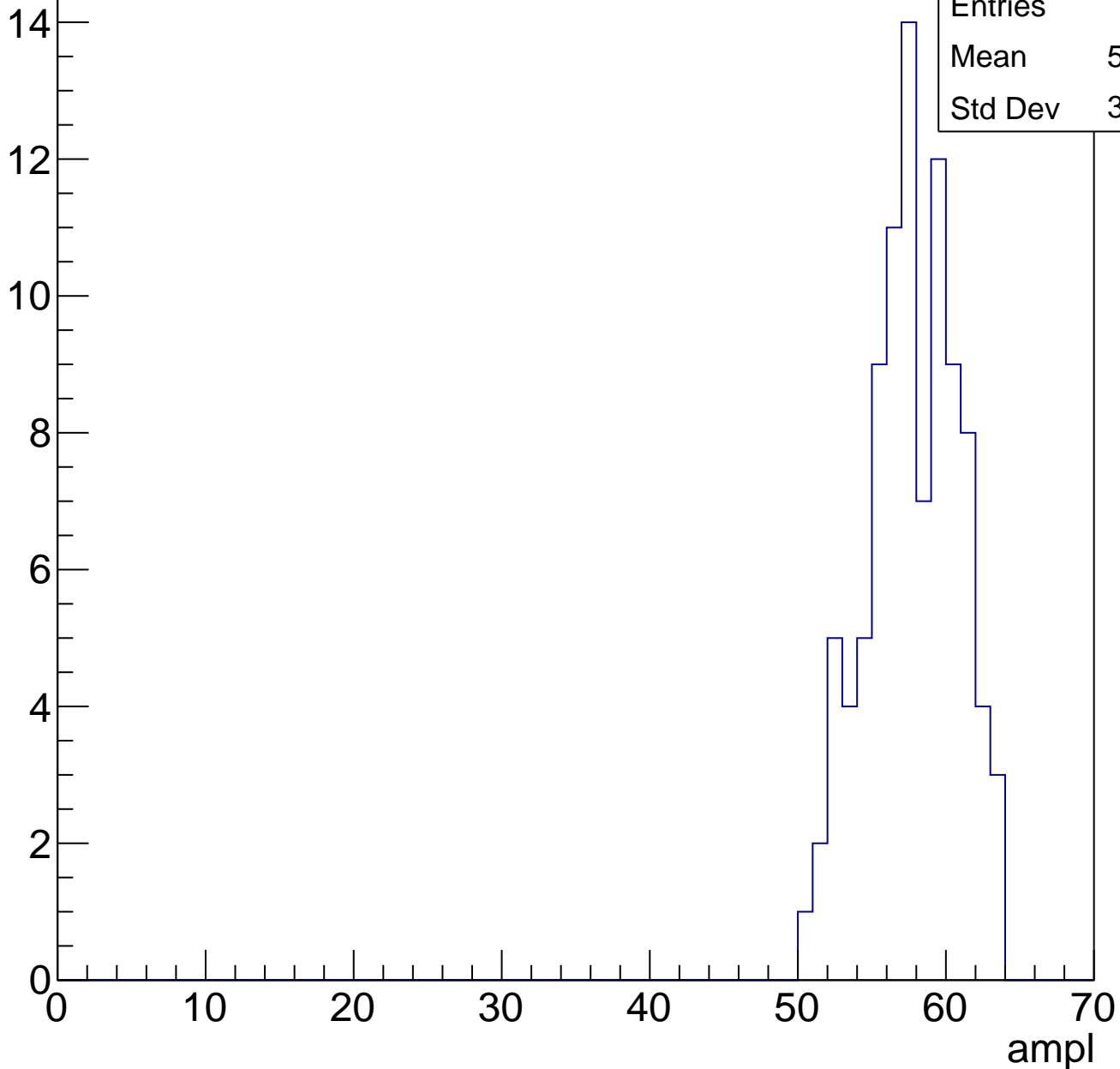
Entries	160
Mean	51.2
Std Dev	3.665



B1L001S, U21-ch75, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch75, adc5

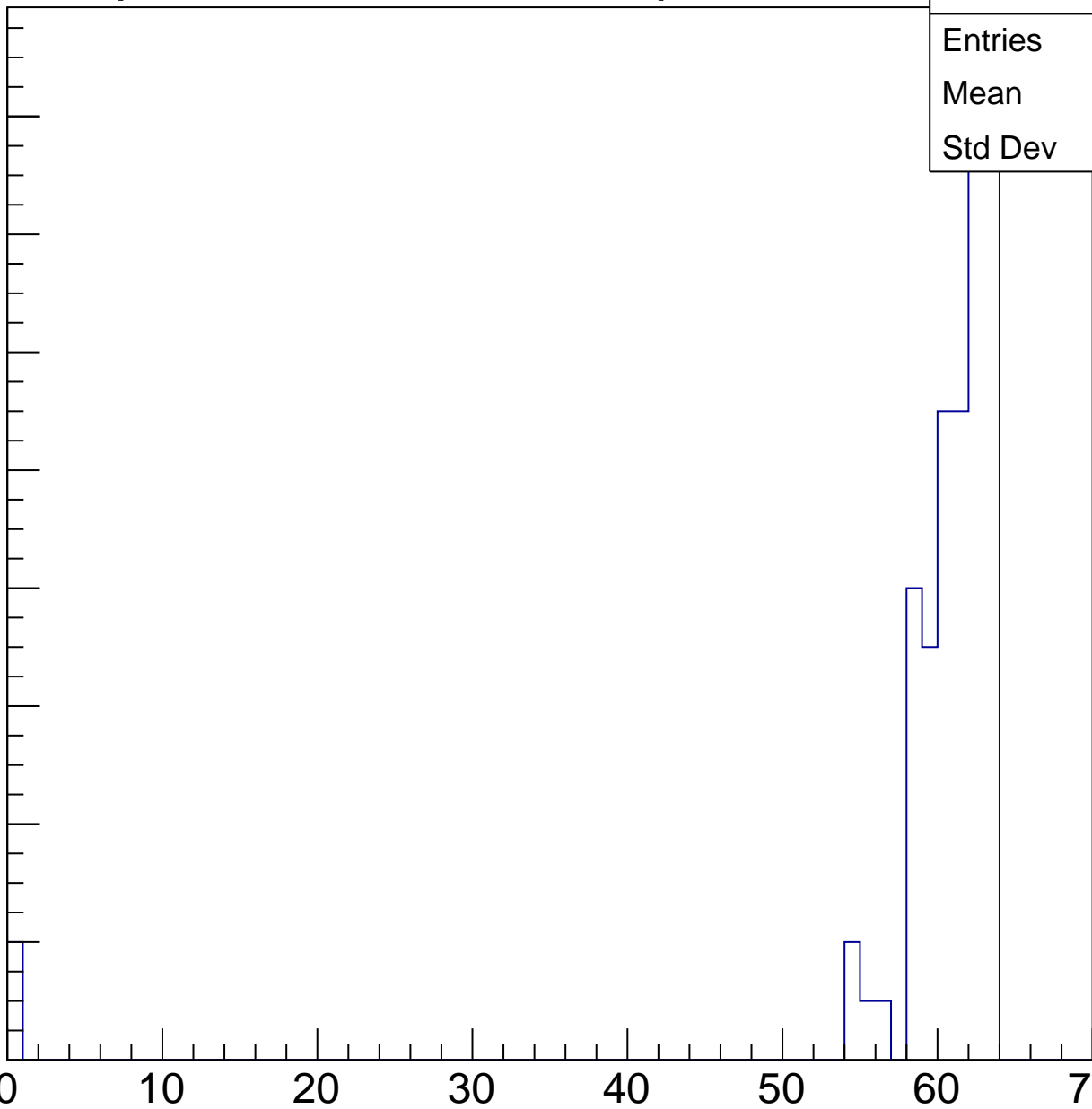
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.08
Std Dev	9.944

ampl



B1L001S, U21-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

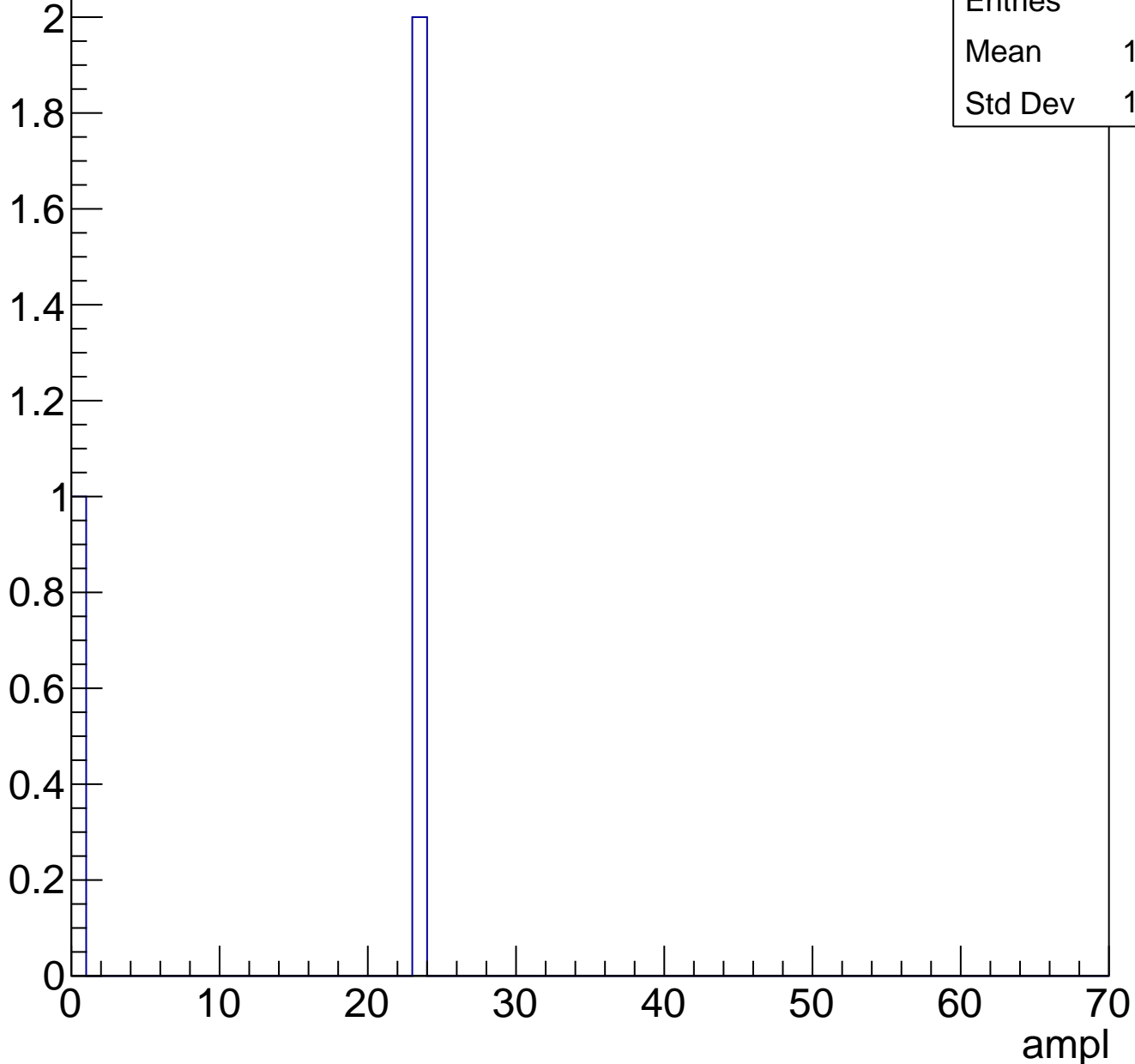


Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	15.33
Std Dev	10.84

B1L001S, U21-ch76, adc0

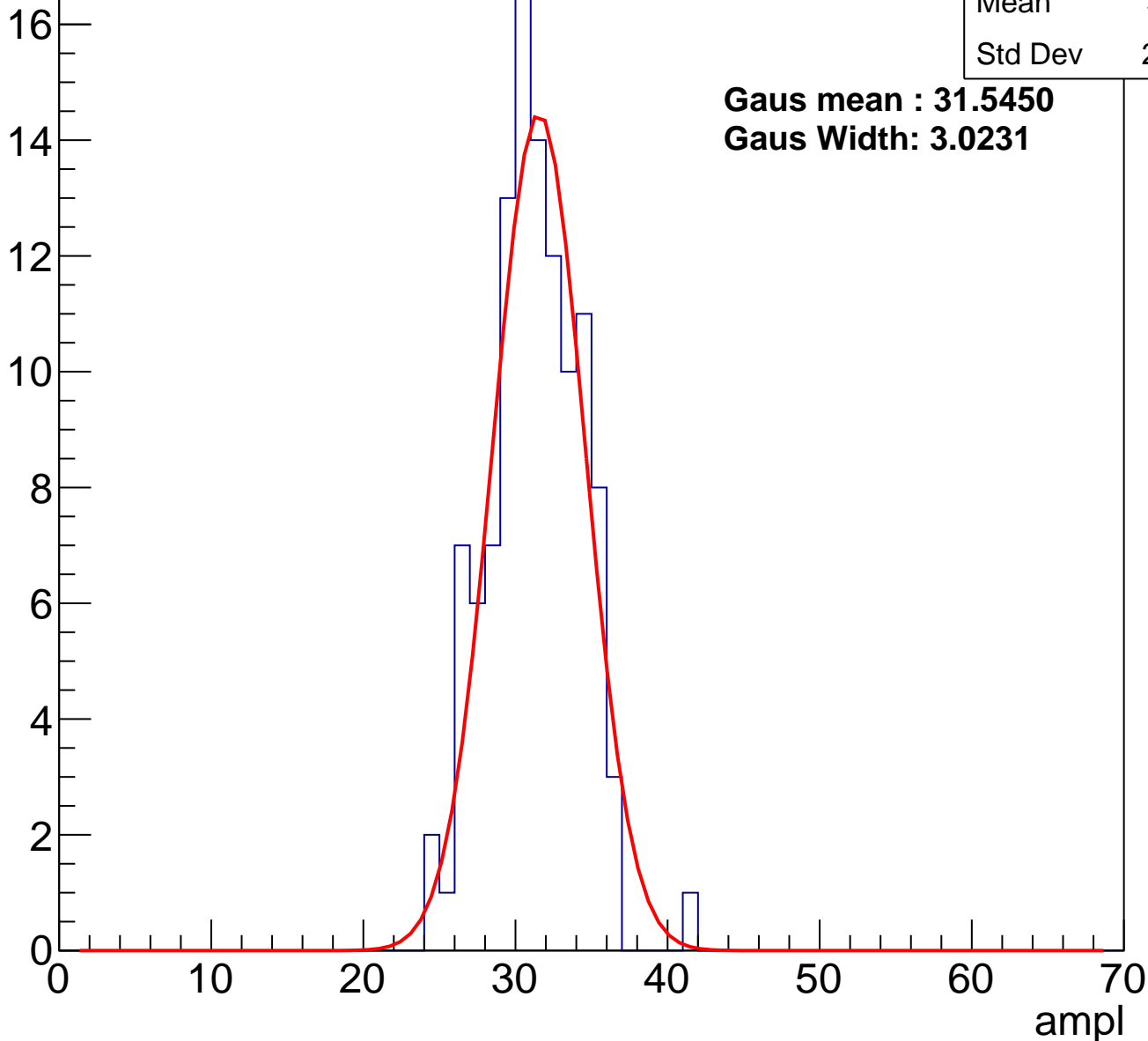
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	112
Mean	30.81
Std Dev	2.969

Gaus mean : 31.5450

Gaus Width: 3.0231



B1L001S, U21-ch76, adc1

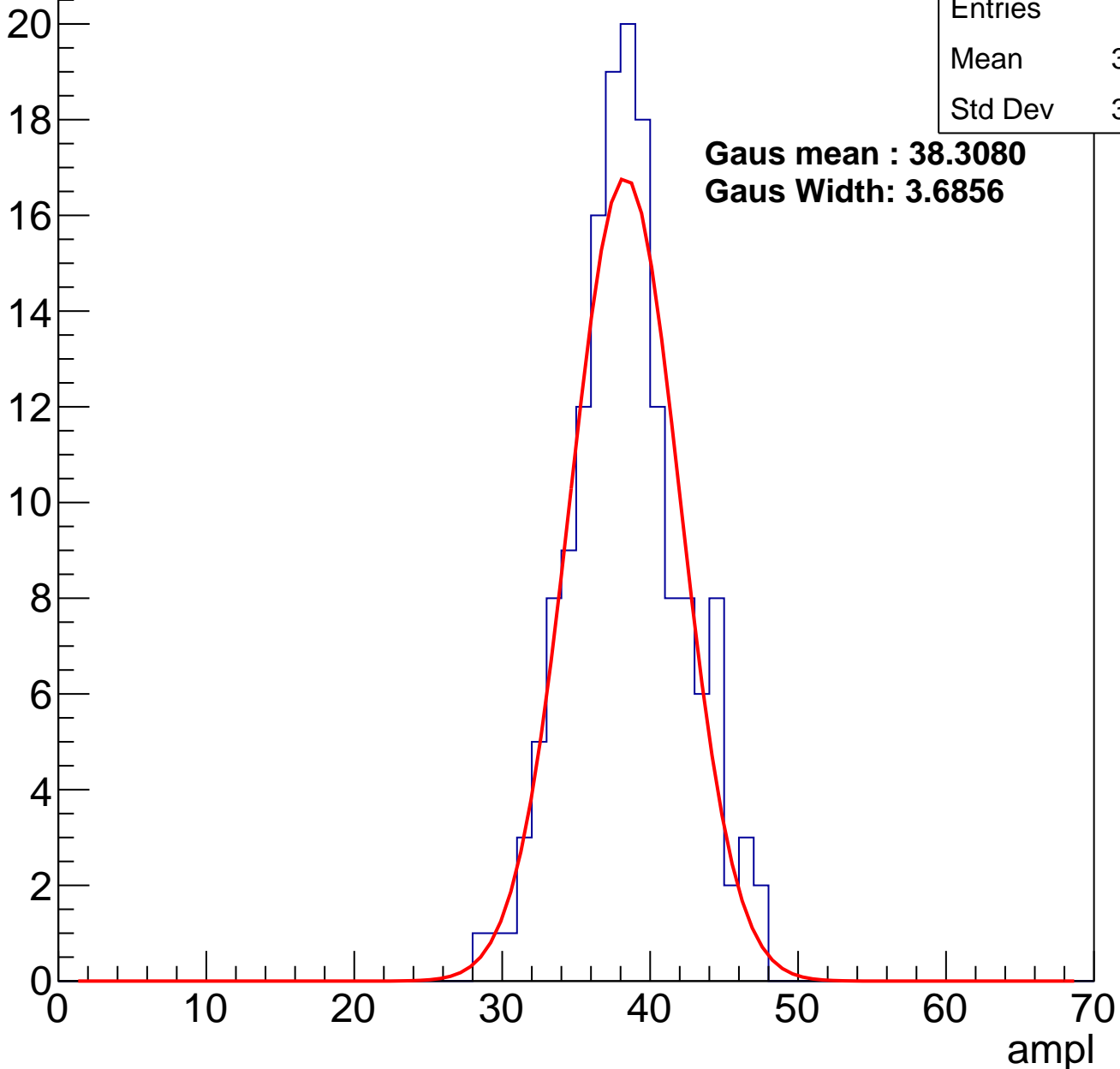
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	37.94
Std Dev	3.737

Gaus mean : 38.3080

Gaus Width: 3.6856

Entry



B1L001S, U21-ch76, adc2

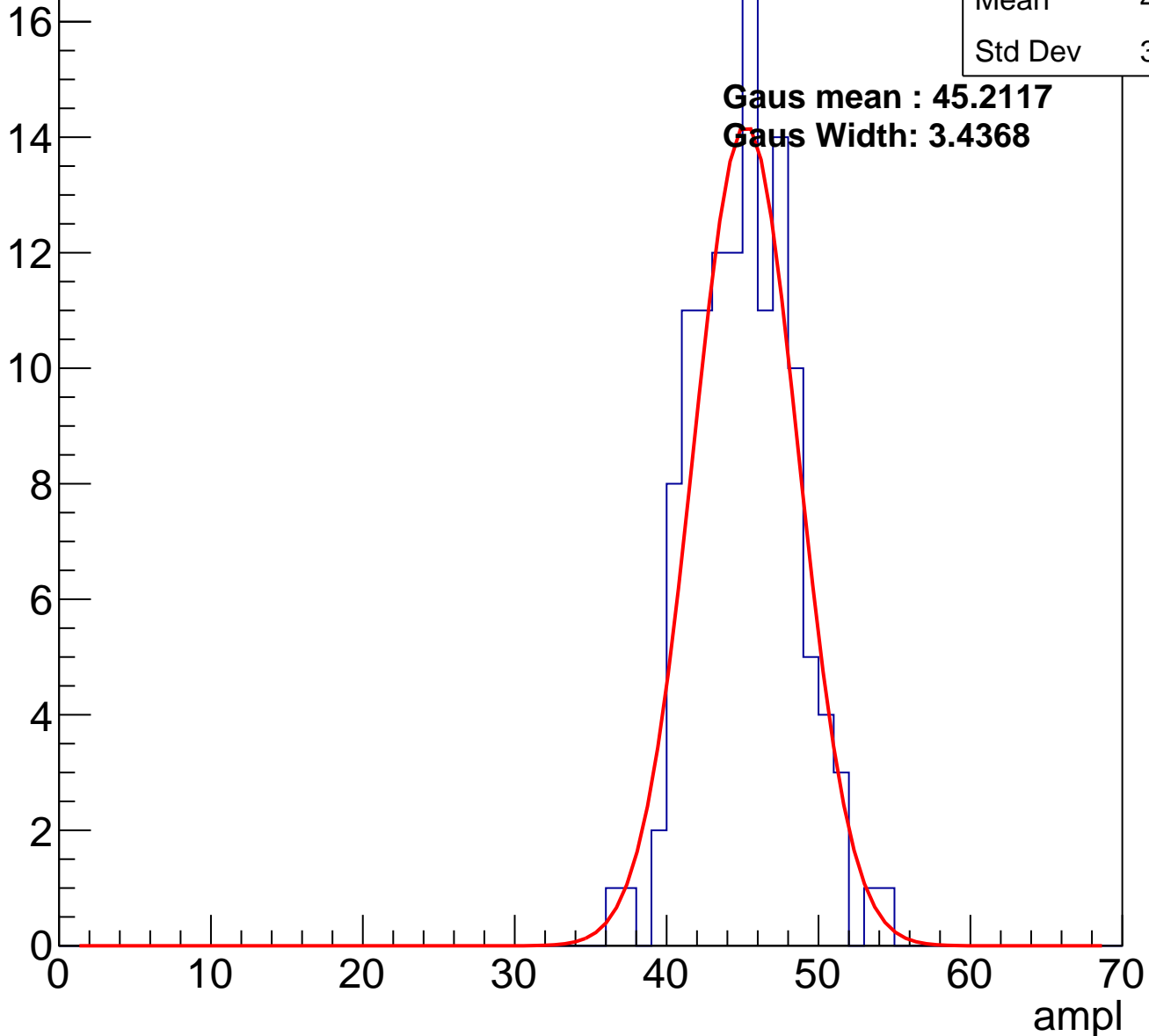
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	124
Mean	44.69
Std Dev	3.275

Gaus mean : 45.2117

Gaus Width: 3.4368

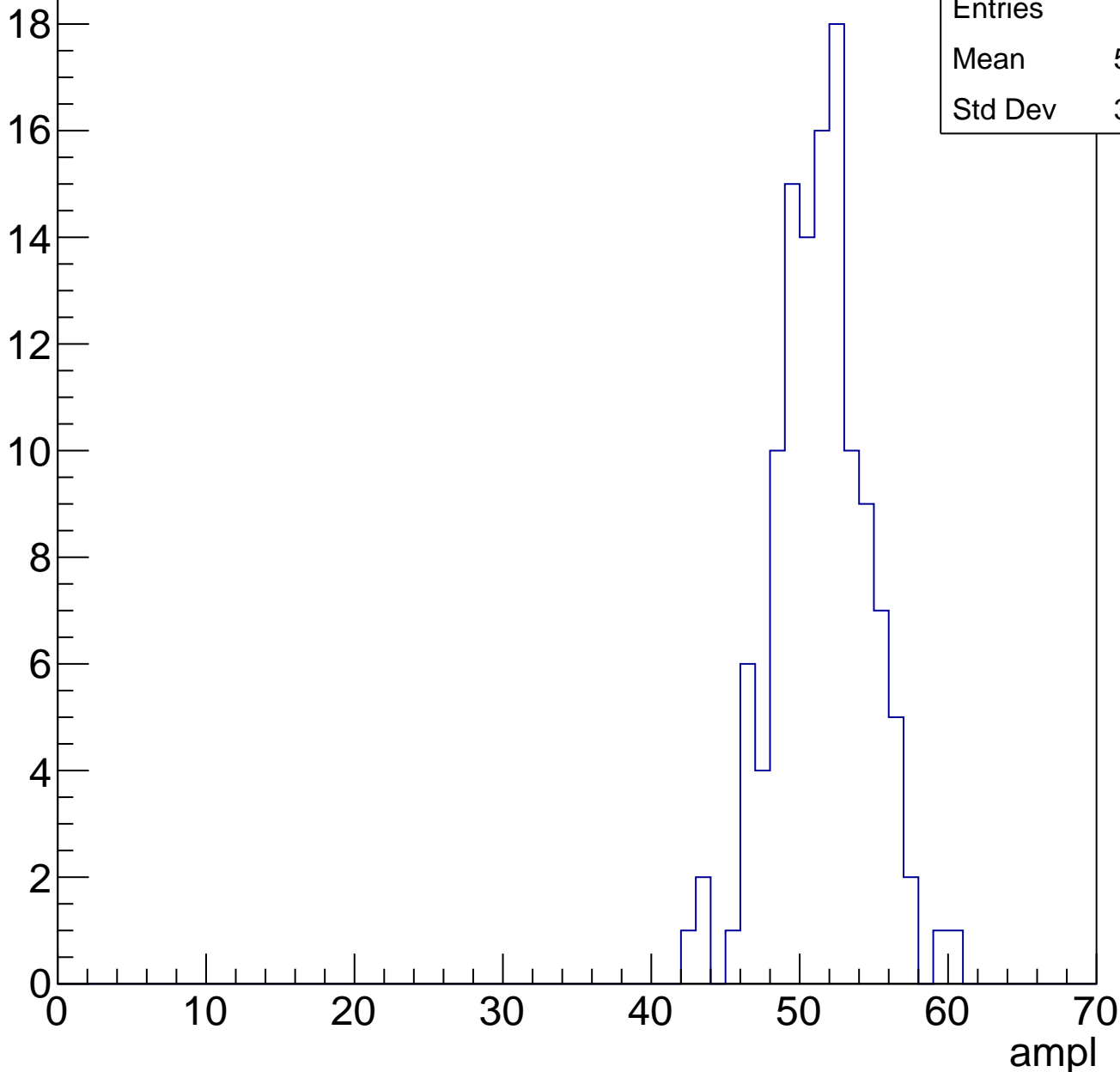


B1L001S, U21-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	50.97
Std Dev	3.157

Entry



B1L001S, U21-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

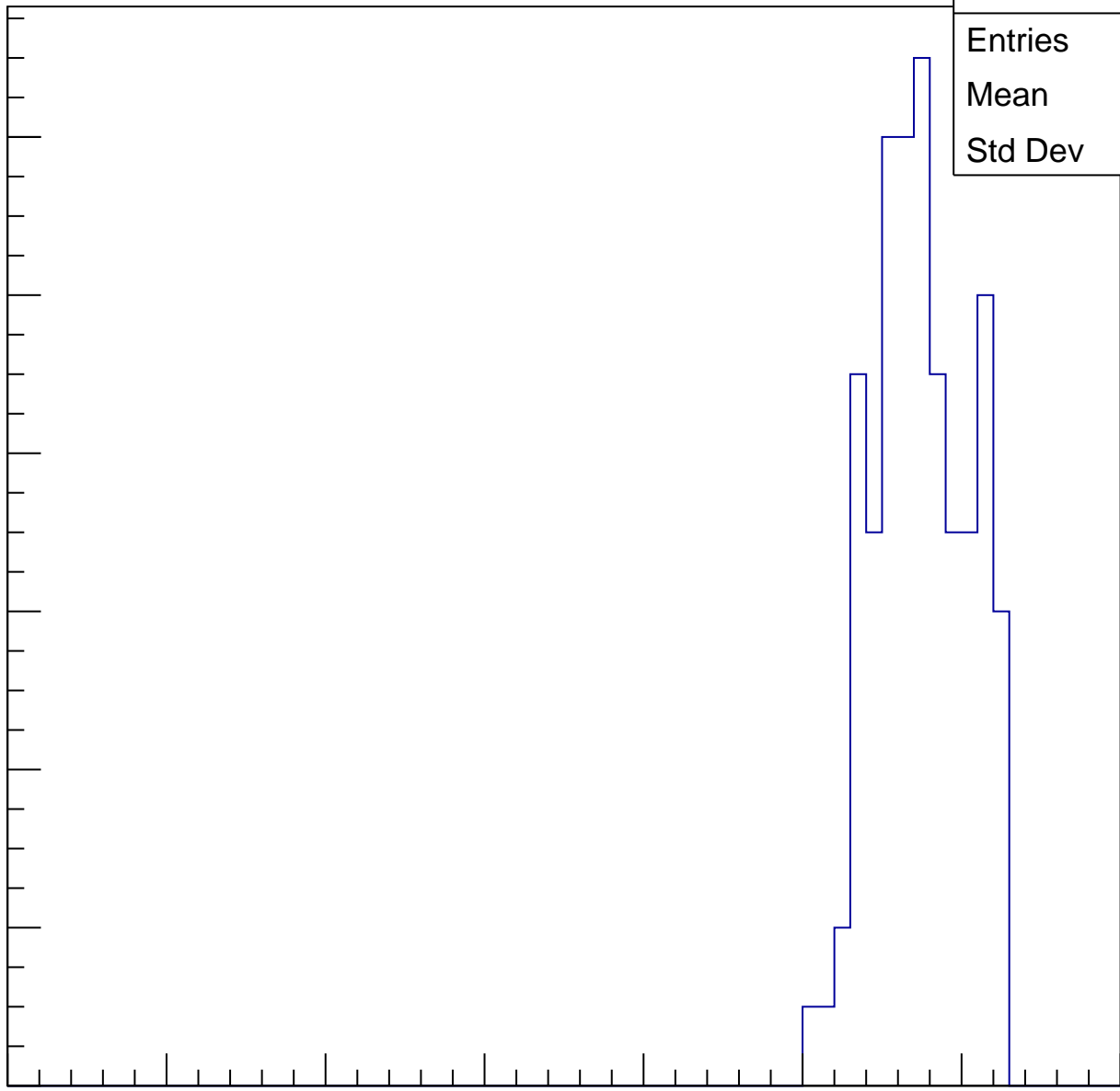
2

0

0 10 20 30 40 50 60 70

ampl

Entries	96
Mean	56.98
Std Dev	2.908



B1L001S, U21-ch76, adc5

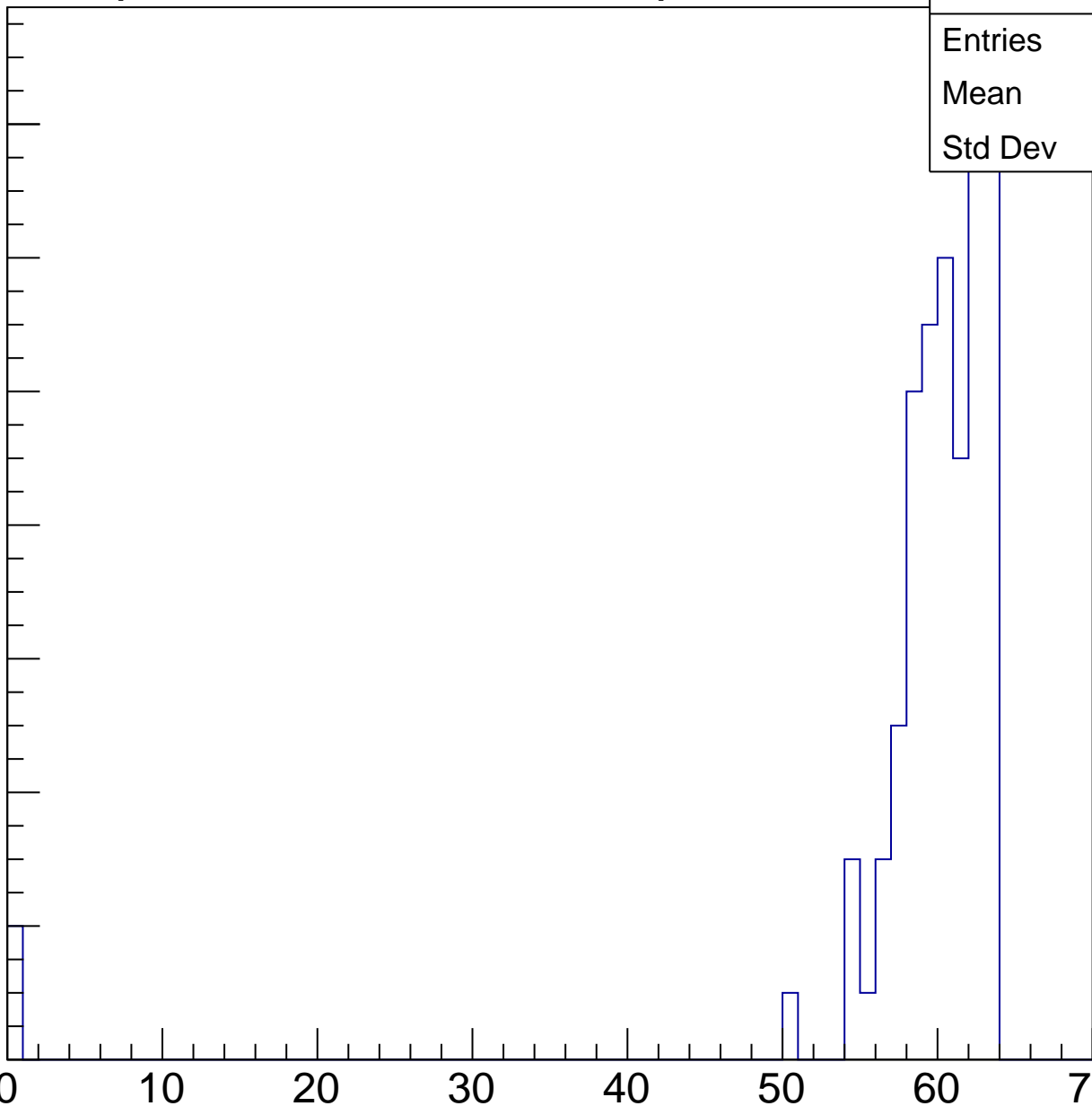
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	58.5
Std Dev	9.39

ampl

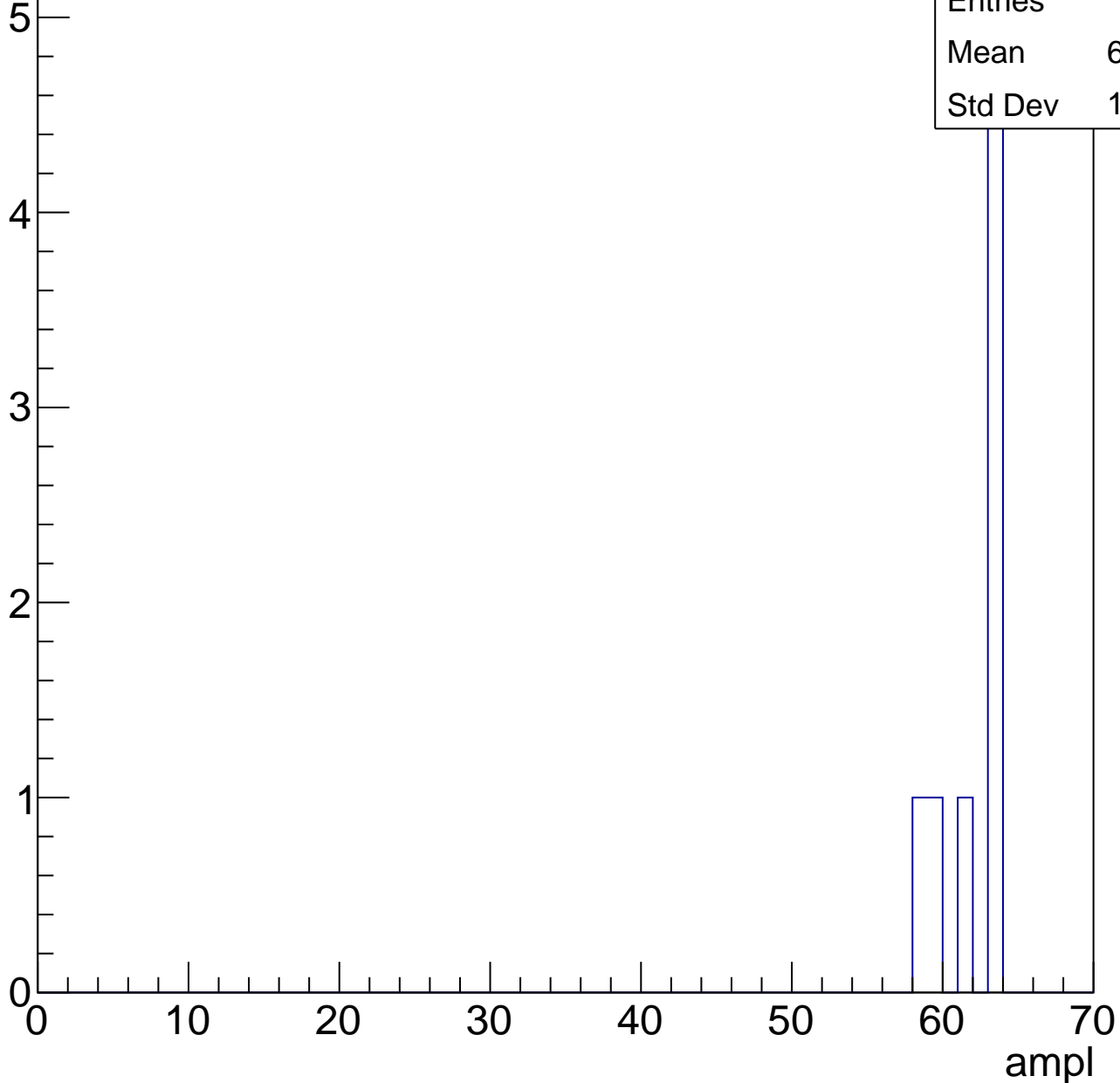


B1L001S, U21-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	61.62
Std Dev	1.932



B1L001S, U21-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L001S, U21-ch77, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	31.13
Std Dev	3.389

Gaus mean : 31.3941

Gaus Width: 3.4859

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

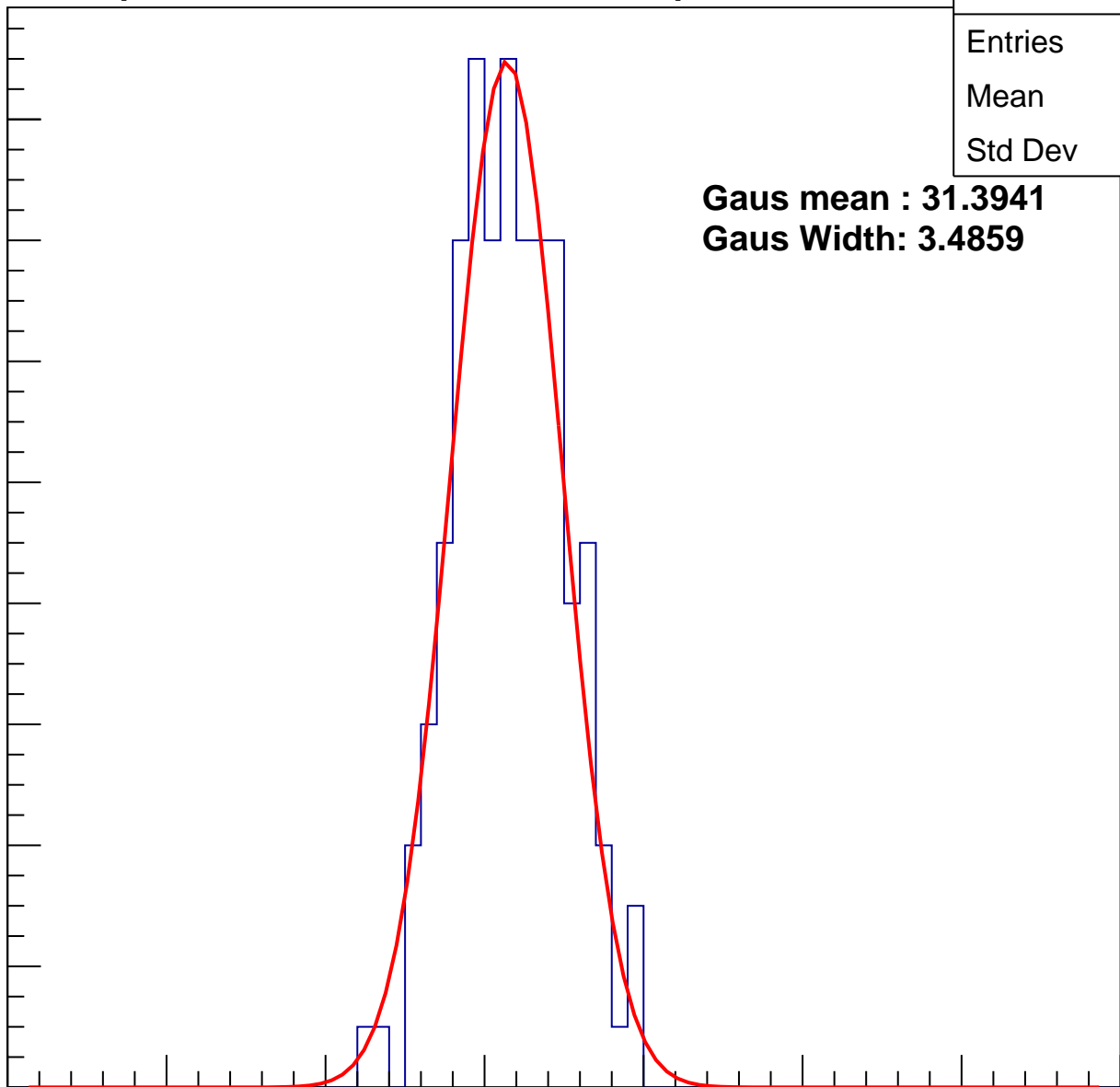
40

50

60

70

ampl



B1L001S, U21-ch77, adc1

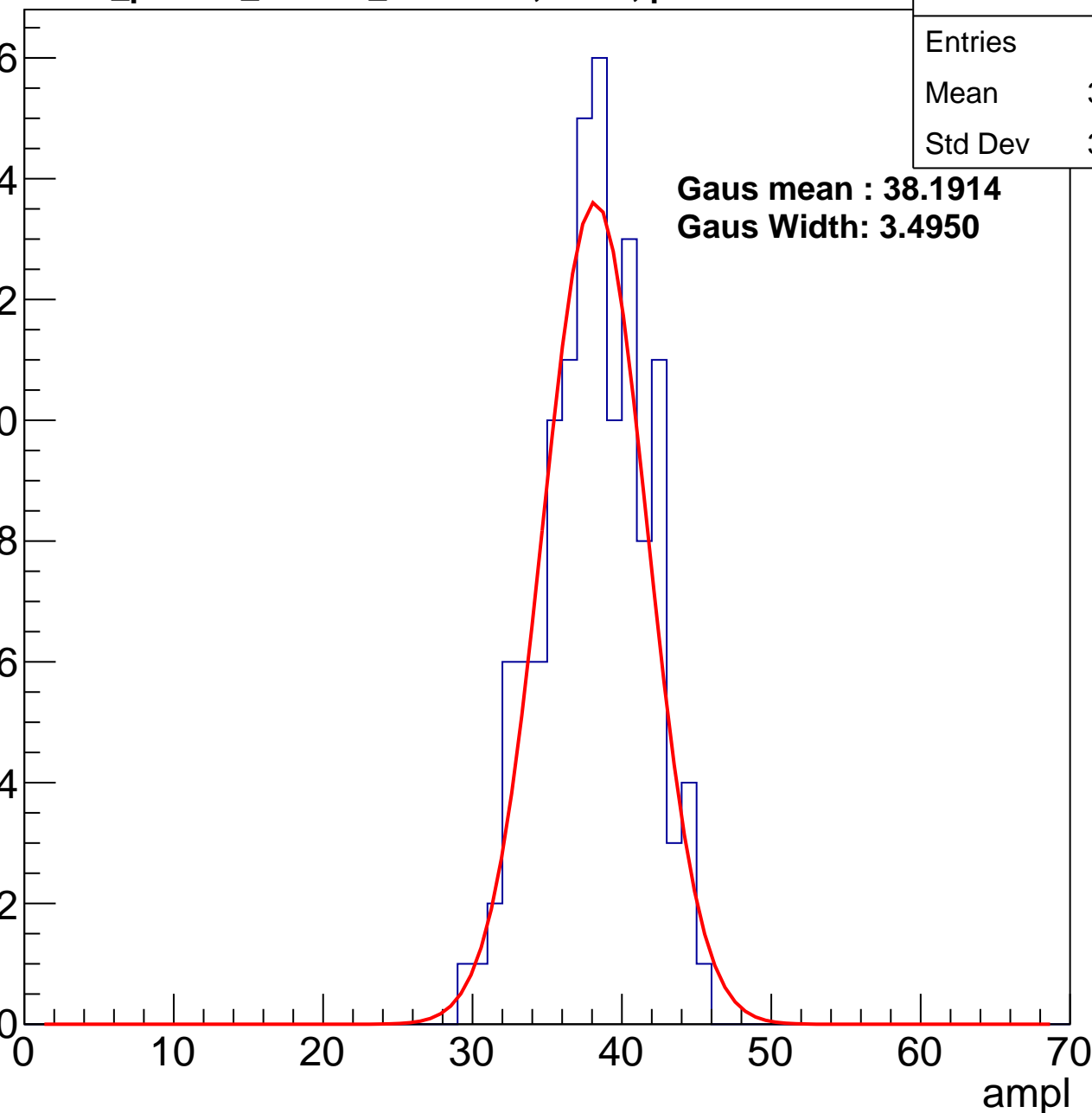
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	37.69
Std Dev	3.384

Gaus mean : 38.1914
Gaus Width: 3.4950



B1L001S, U21-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	43.33
Std Dev	3.28

Entry

12

10

8

6

4

2

0

Gaus mean : 43.5316

Gaus Width: 2.9724

ampl

0

10

20

30

40

50

60

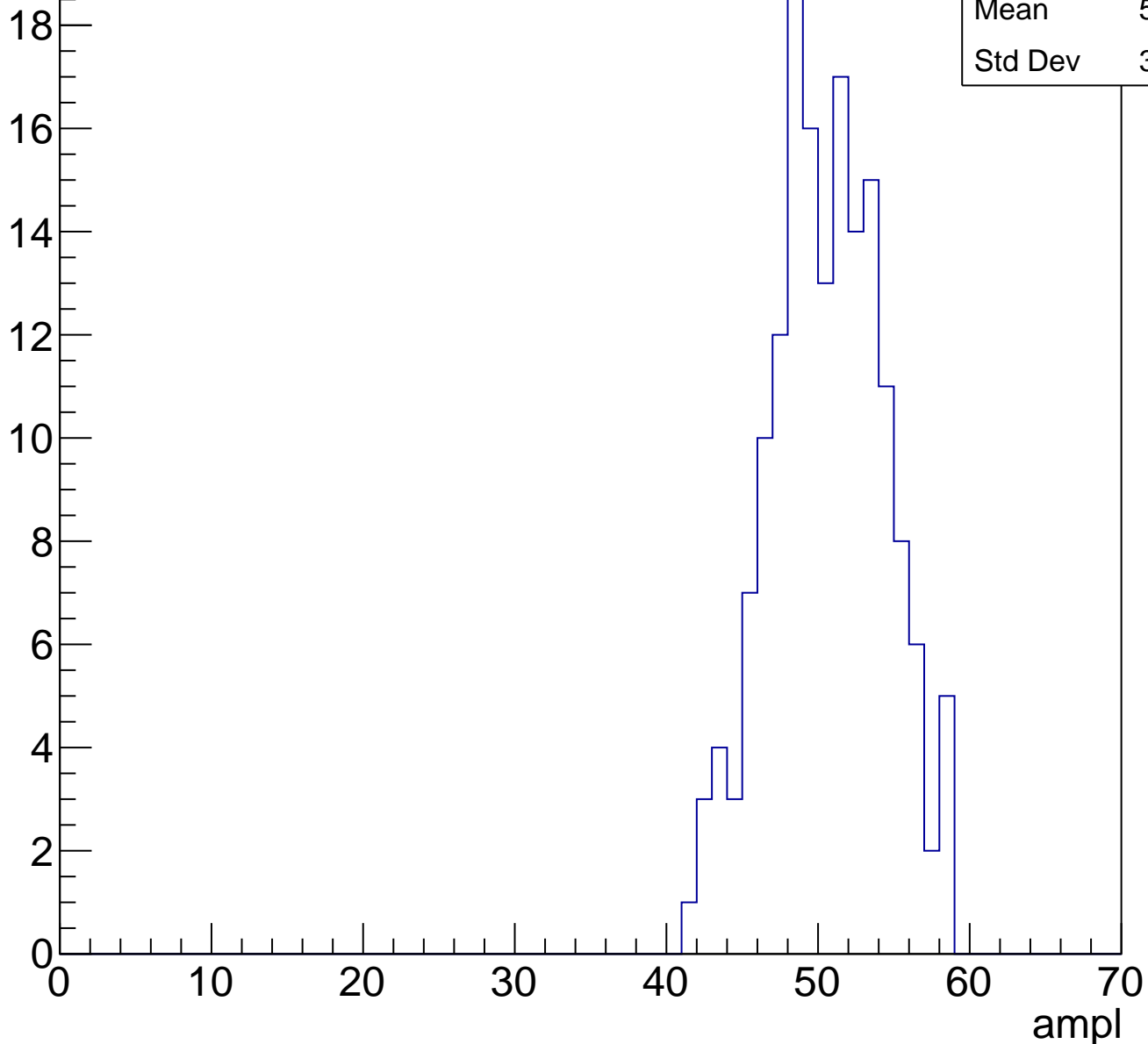
70

B1L001S, U21-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	166
Mean	50.12
Std Dev	3.745

Entry



B1L001S, U21-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

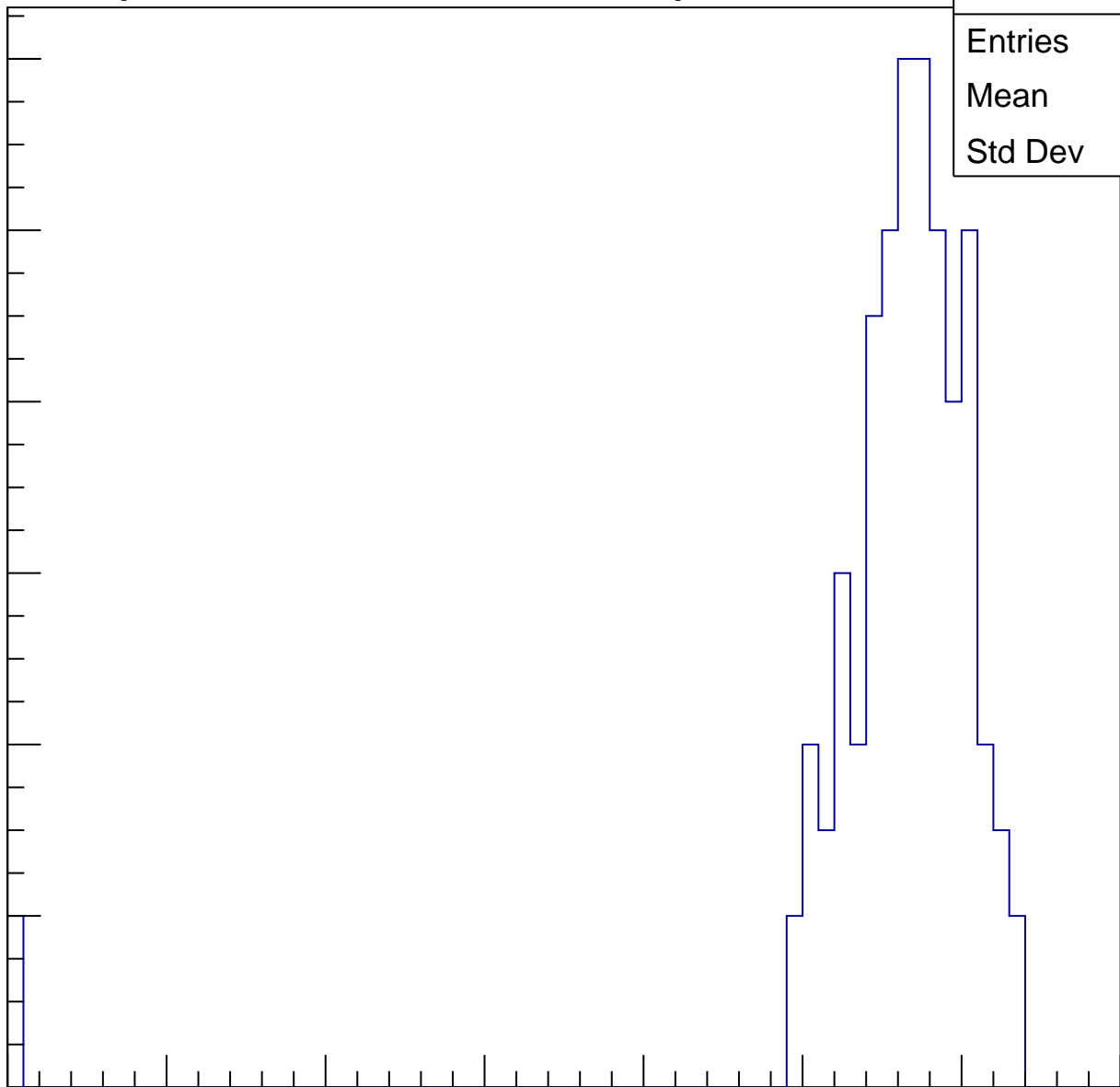
Entries	101
Mean	55.2
Std Dev	8.497

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch77, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

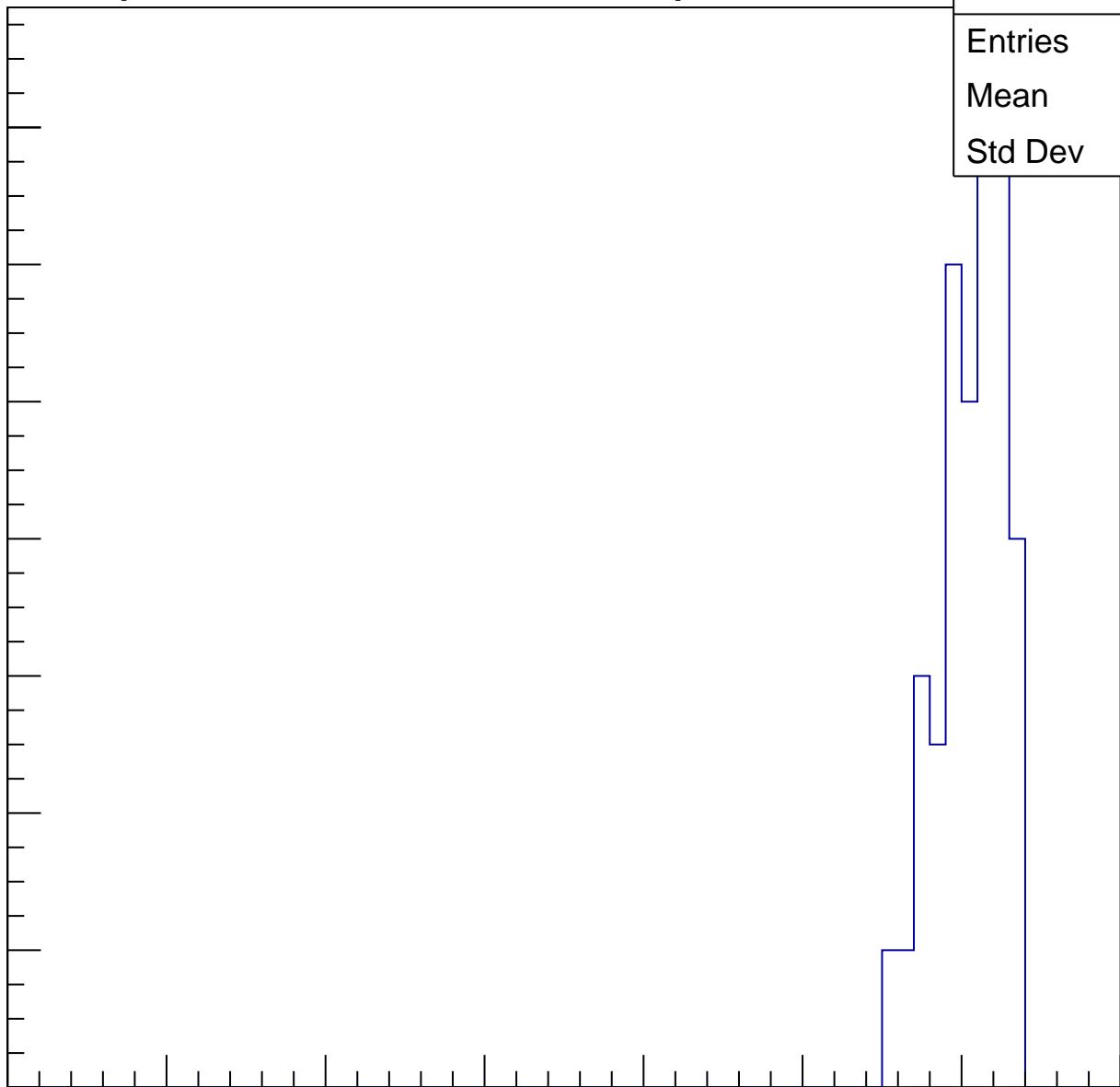
50

60

70

ampl

Entries	74
Mean	60.12
Std Dev	2.046

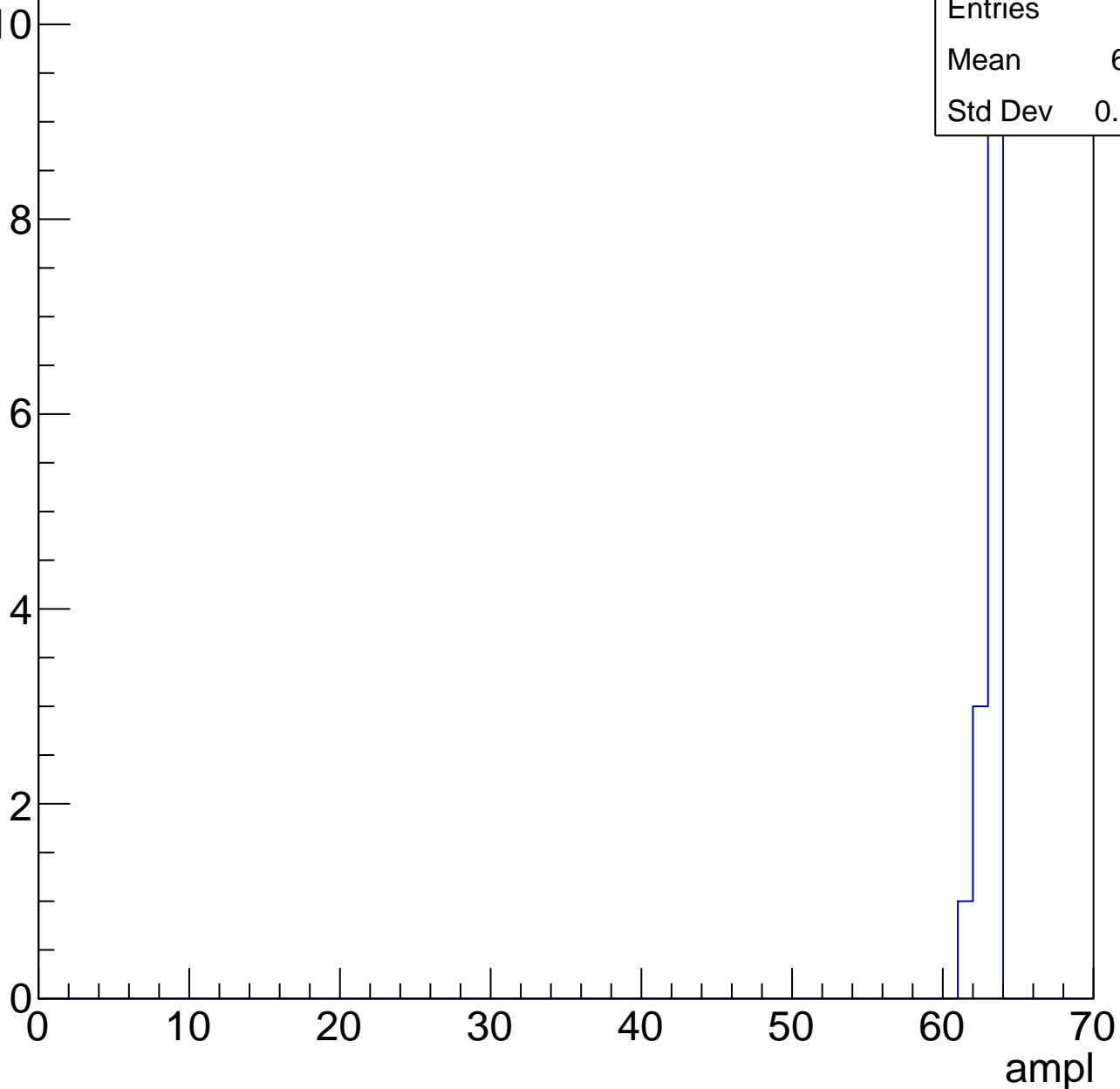


B1L001S, U21-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

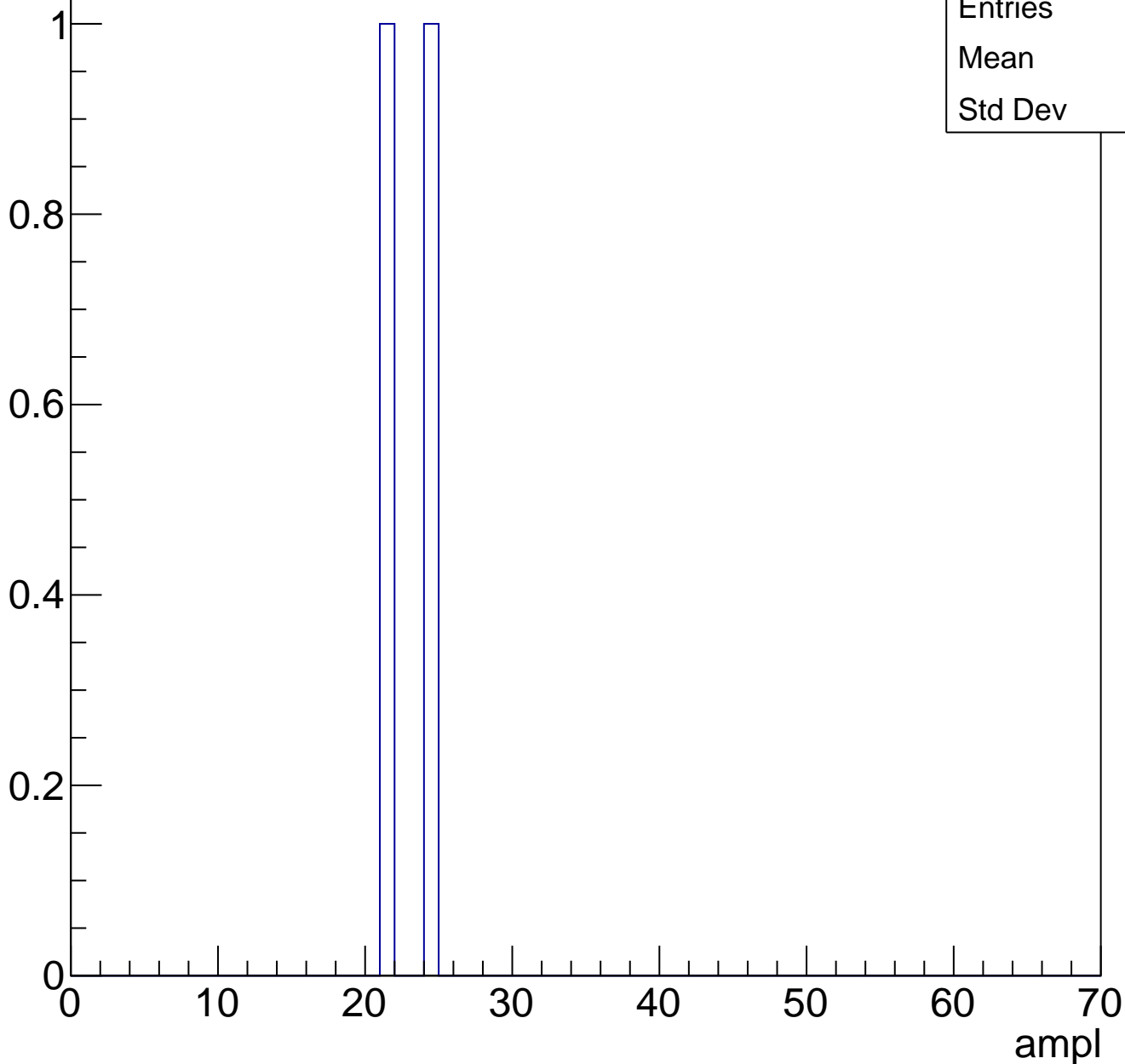
Entries	14
Mean	62.64
Std Dev	0.6103



B1L001S, U21-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



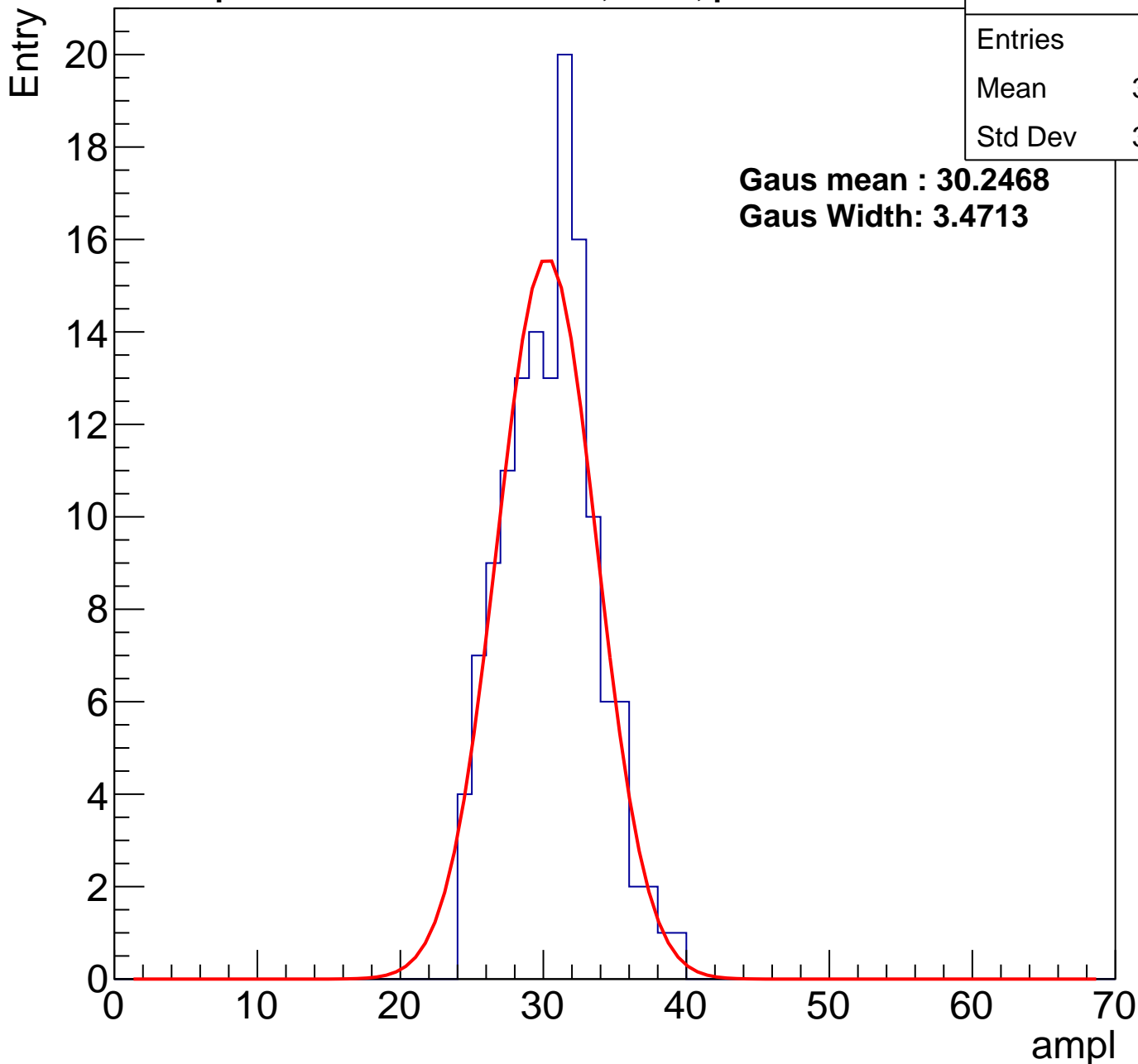
B1L001S, U21-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.08
Std Dev	3.176

Gaus mean : 30.2468

Gaus Width: 3.4713



B1L001S, U21-ch78, adc1

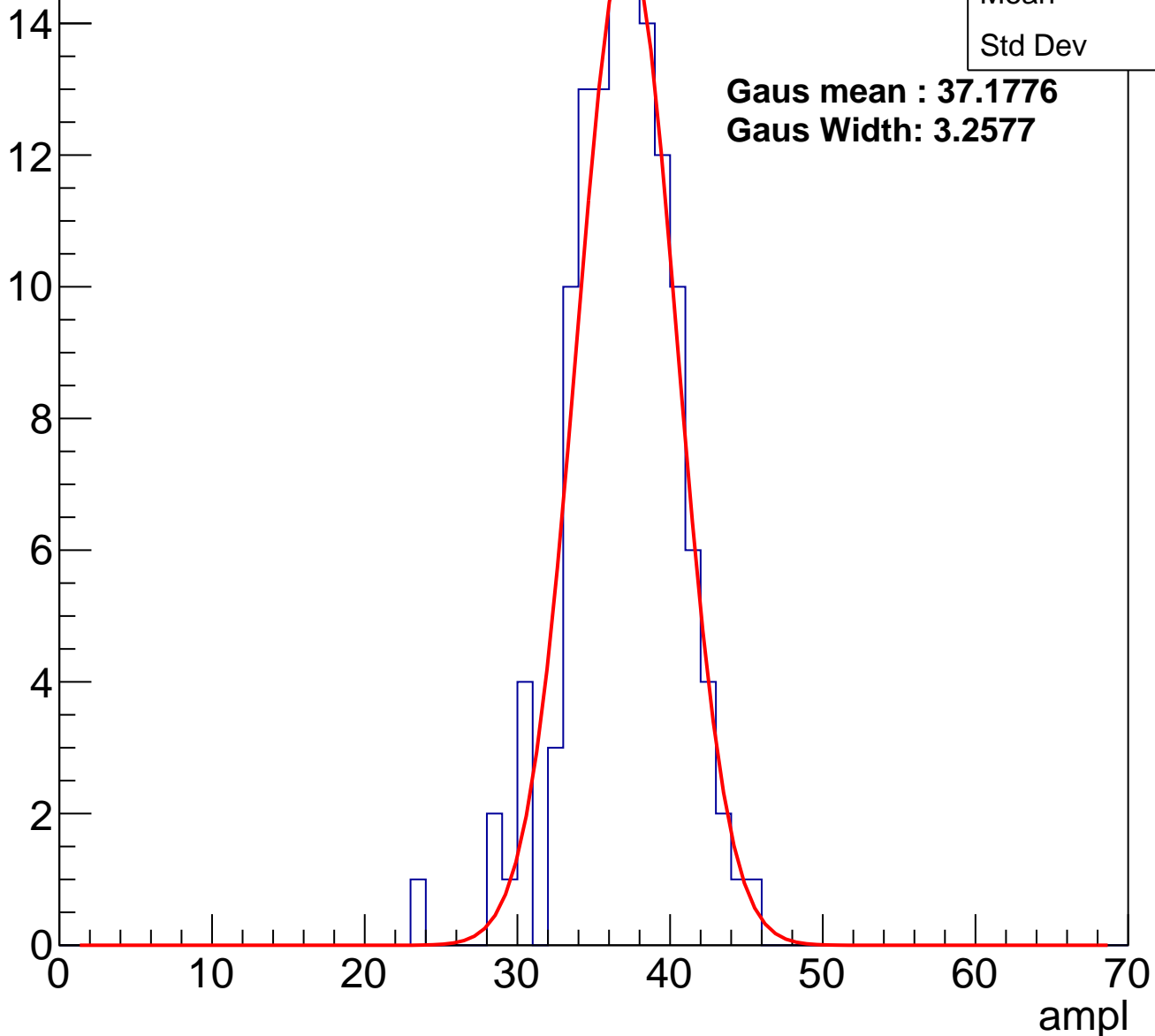
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.5
Std Dev	3.48

Gaus mean : 37.1776

Gaus Width: 3.2577

Entry



B1L001S, U21-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	42.69
Std Dev	3.211

Gaus mean : 43.1594

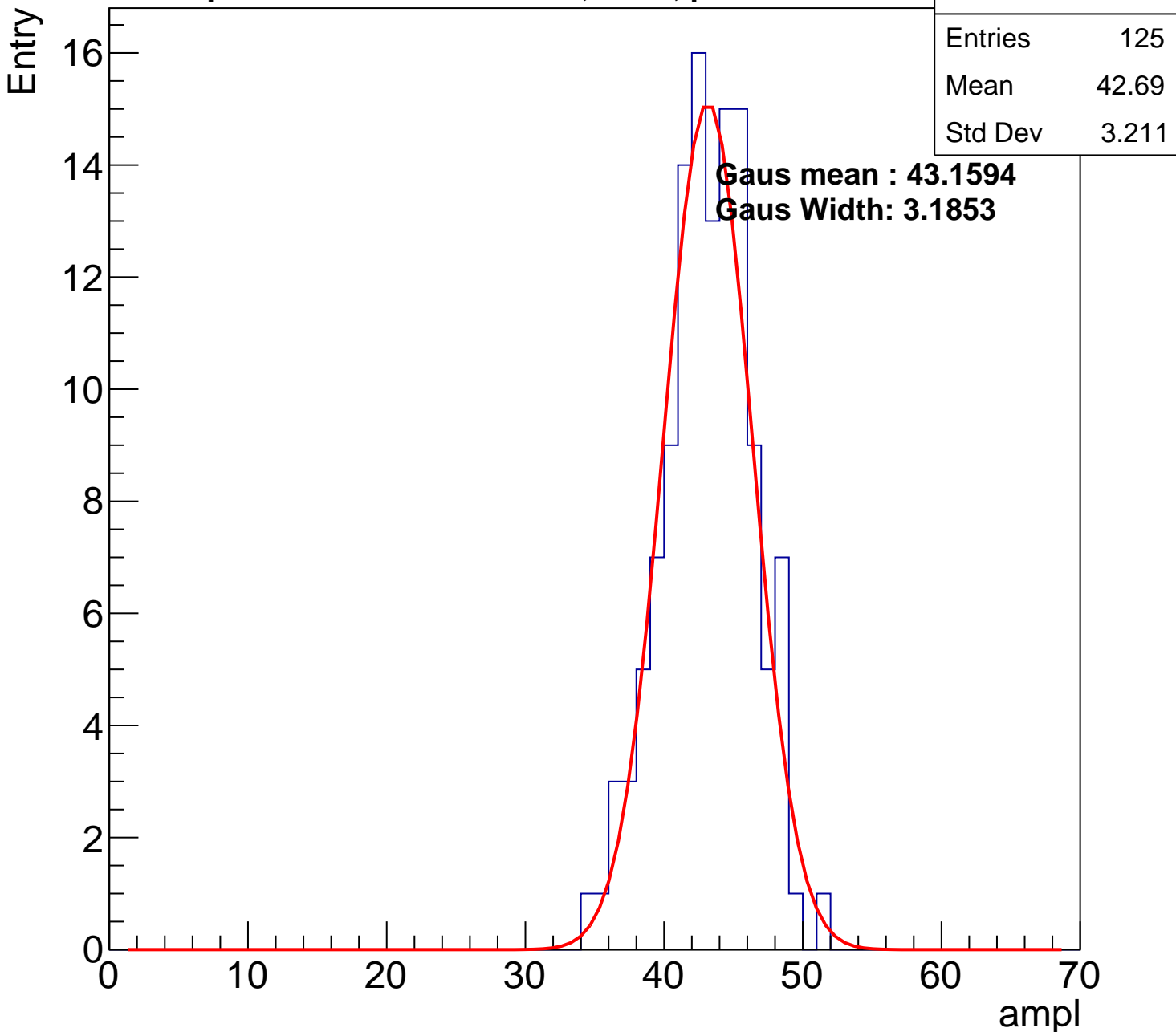
Gaus Width: 3.1853

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

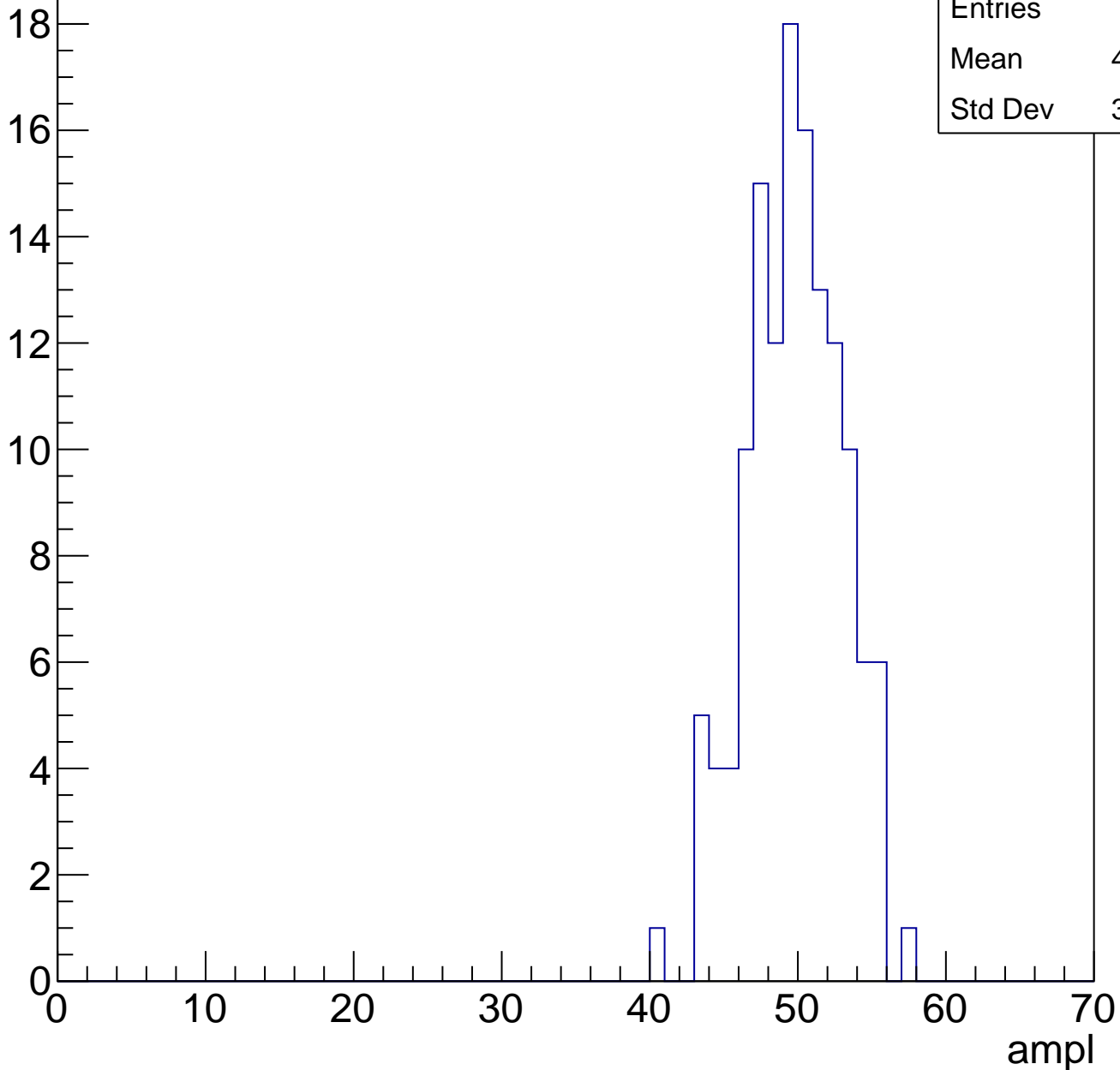


B1L001S, U21-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	49.34
Std Dev	3.172

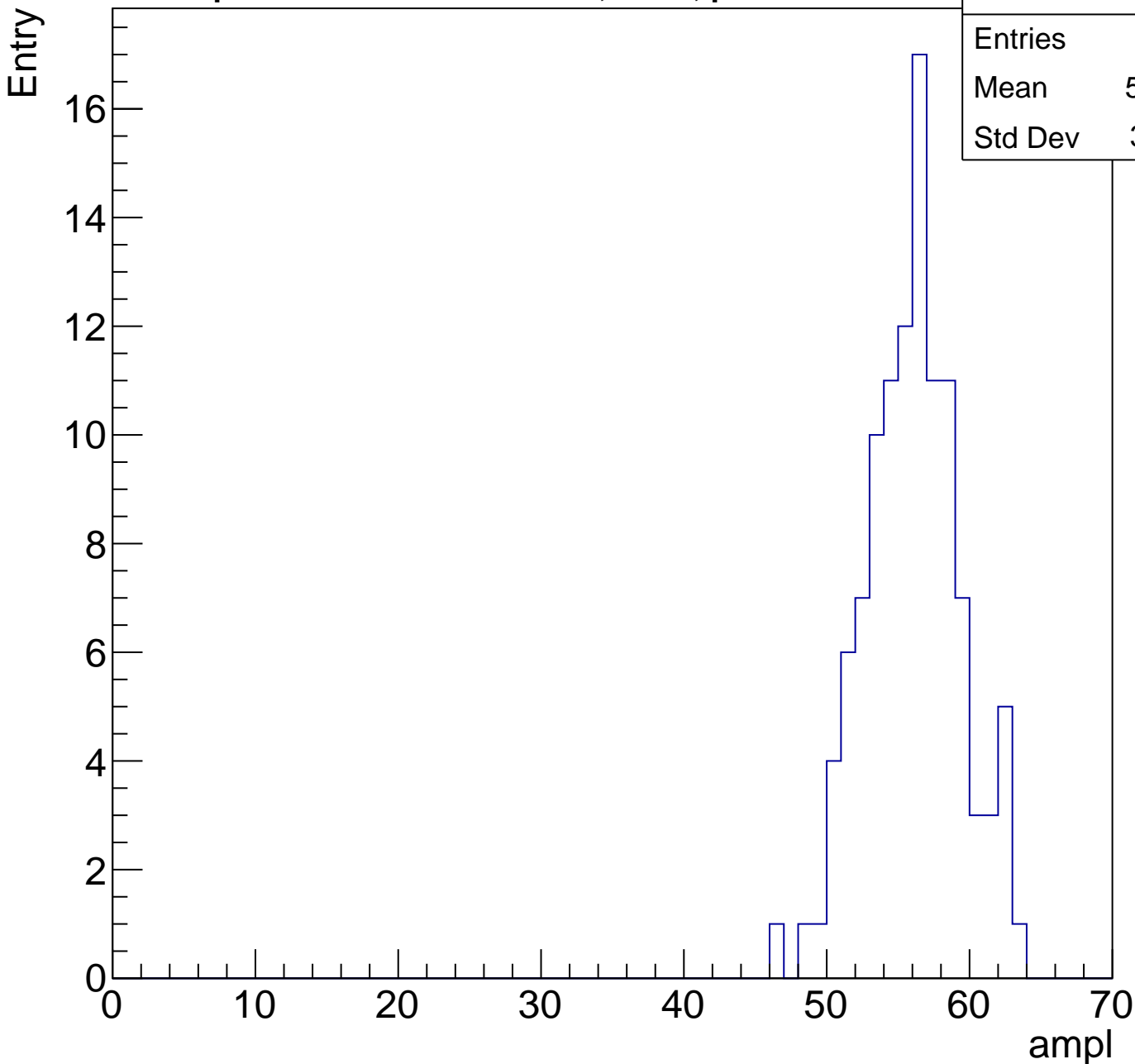
Entry



B1L001S, U21-ch78, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	55.52
Std Dev	3.301



B1L001S, U21-ch78, adc5

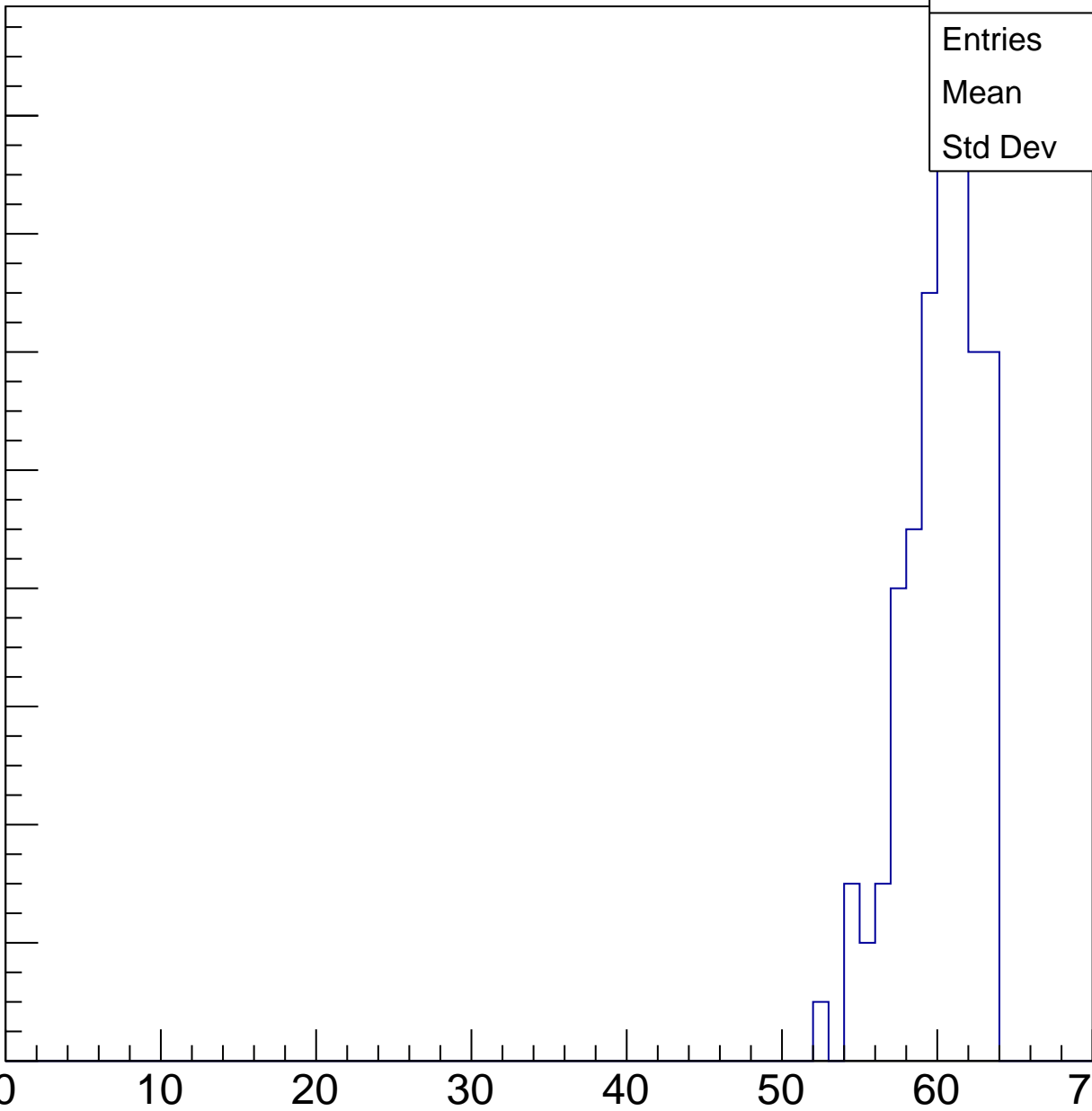
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.73
Std Dev	2.396

ampl

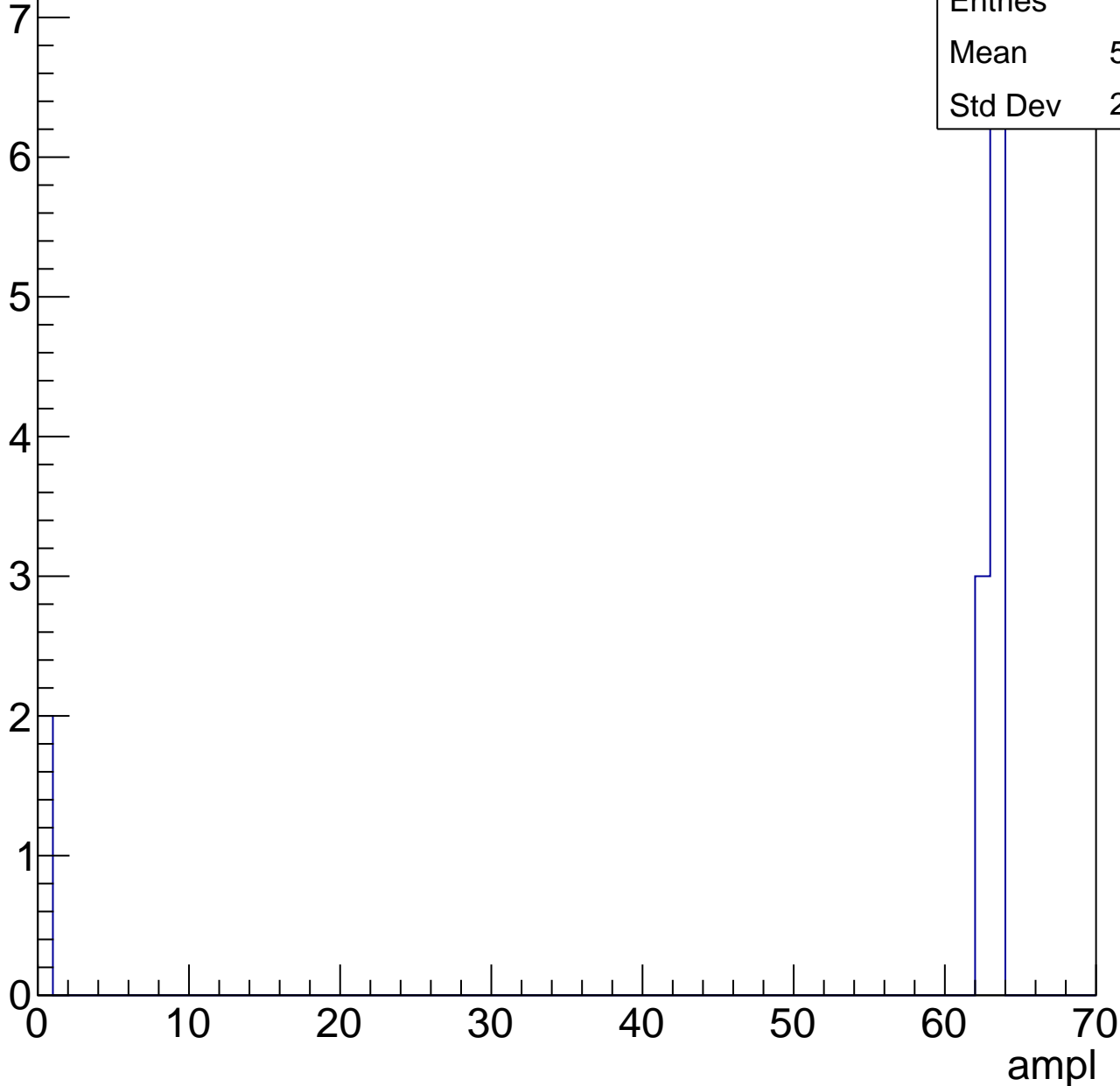


B1L001S, U21-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	52.25
Std Dev	23.37



B1L001S, U21-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch79, adc0

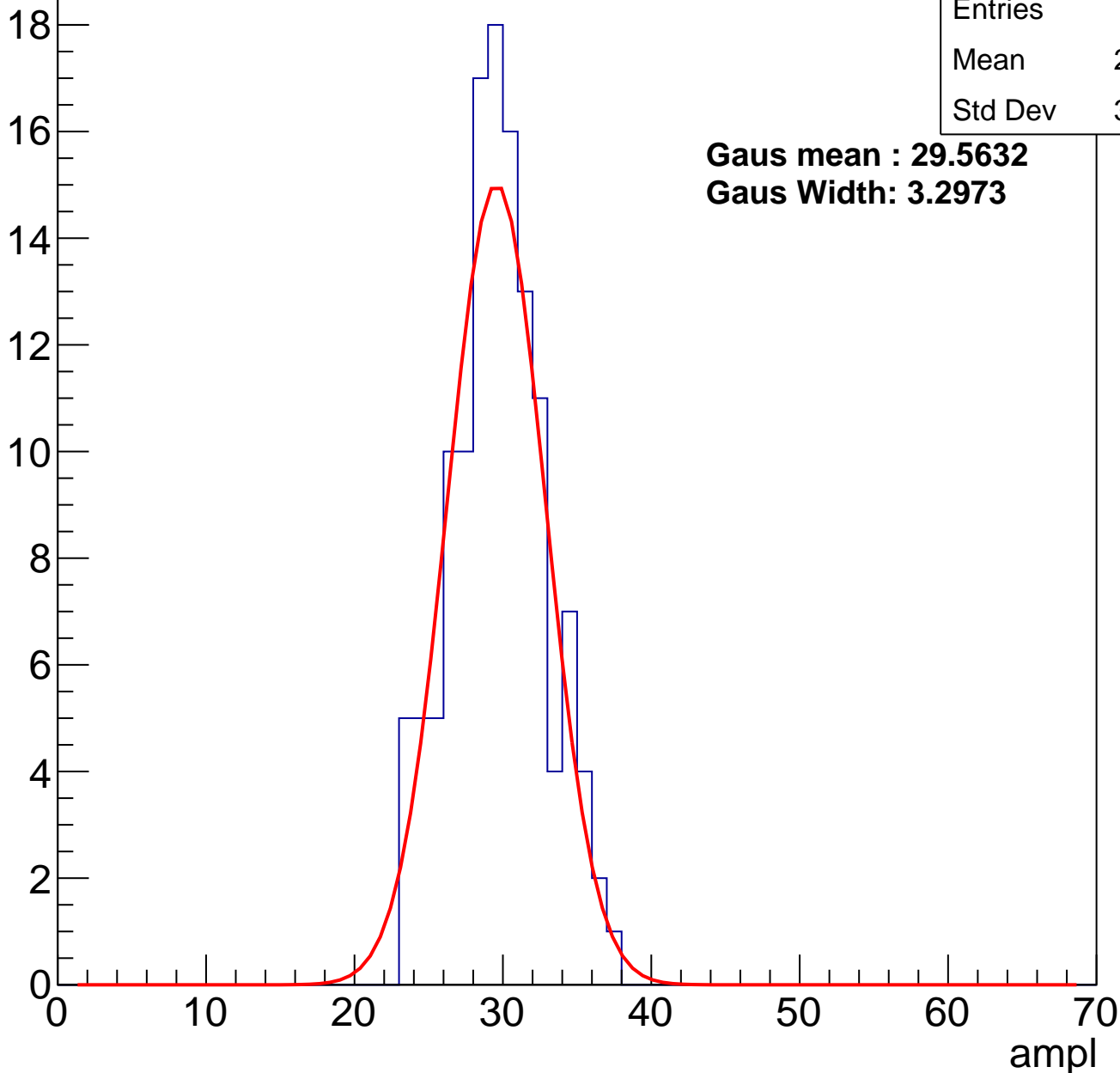
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.23
Std Dev	3.109

Gaus mean : 29.5632

Gaus Width: 3.2973

Entry



B1L001S, U21-ch79, adc1

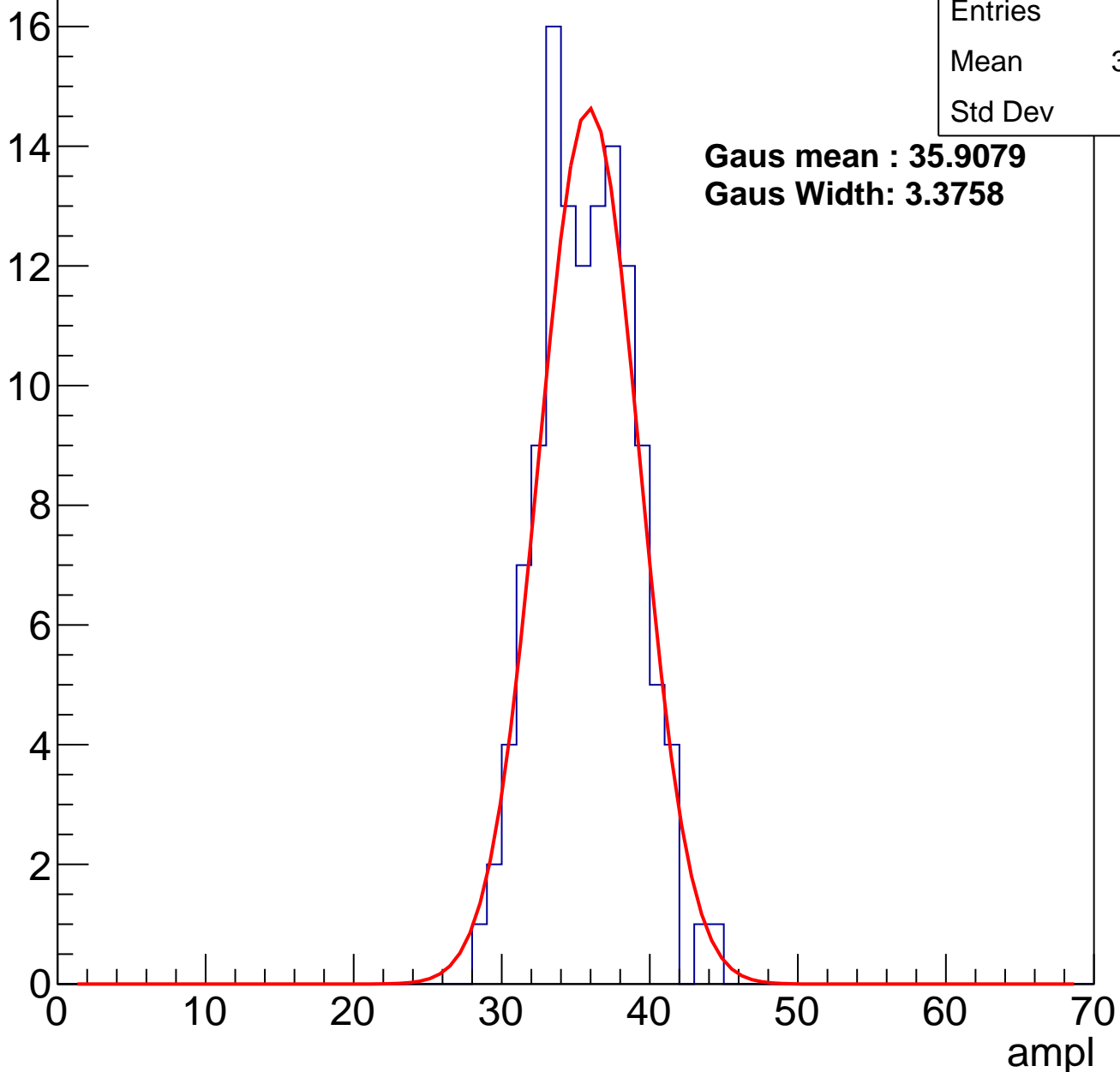
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.33
Std Dev	3.14

Gaus mean : 35.9079

Gaus Width: 3.3758

Entry



B1L001S, U21-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

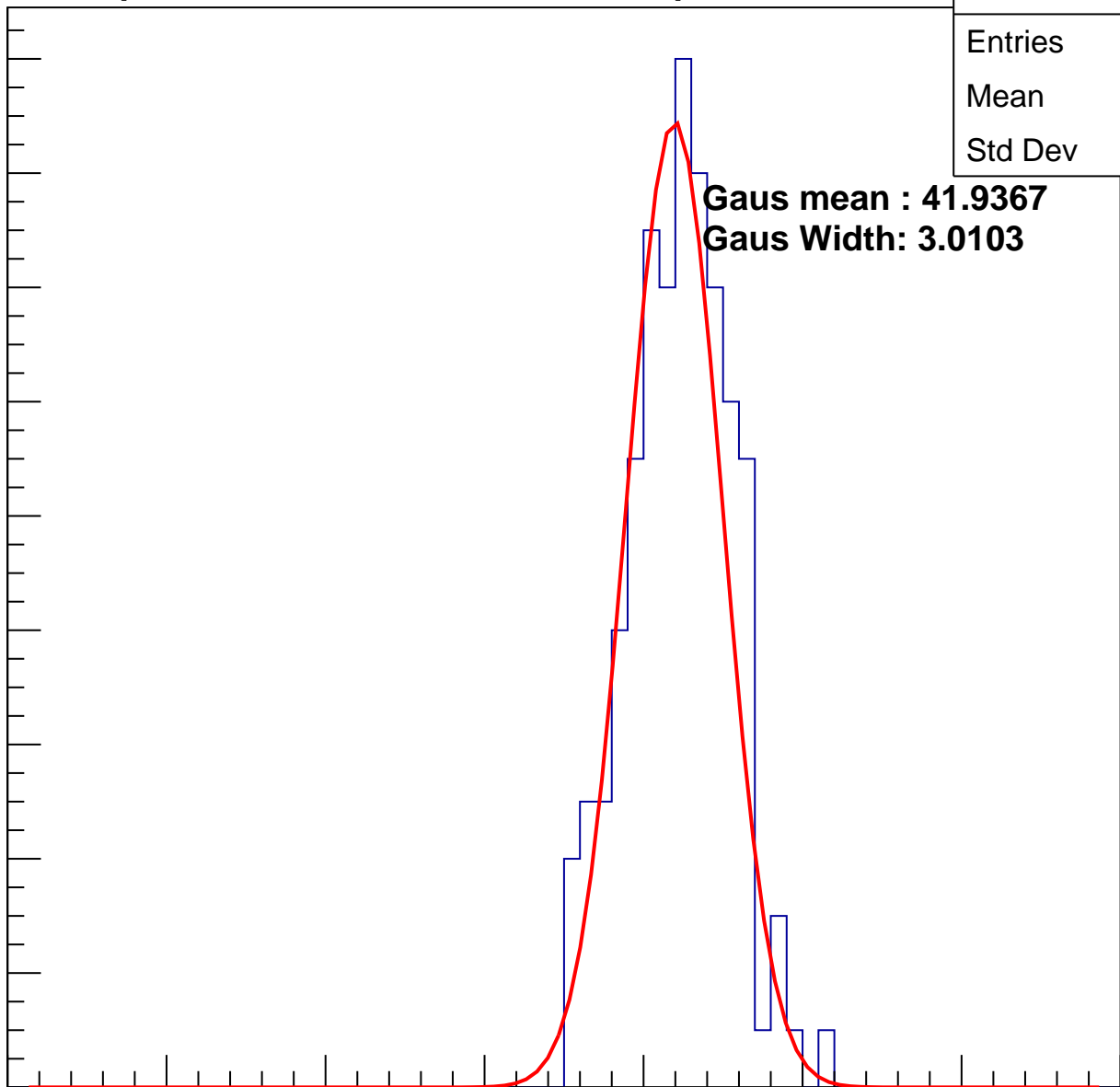
Entries	139
Mean	41.79
Std Dev	3.191

Gaus mean : 41.9367

Gaus Width: 3.0103

0 10 20 30 40 50 60 70

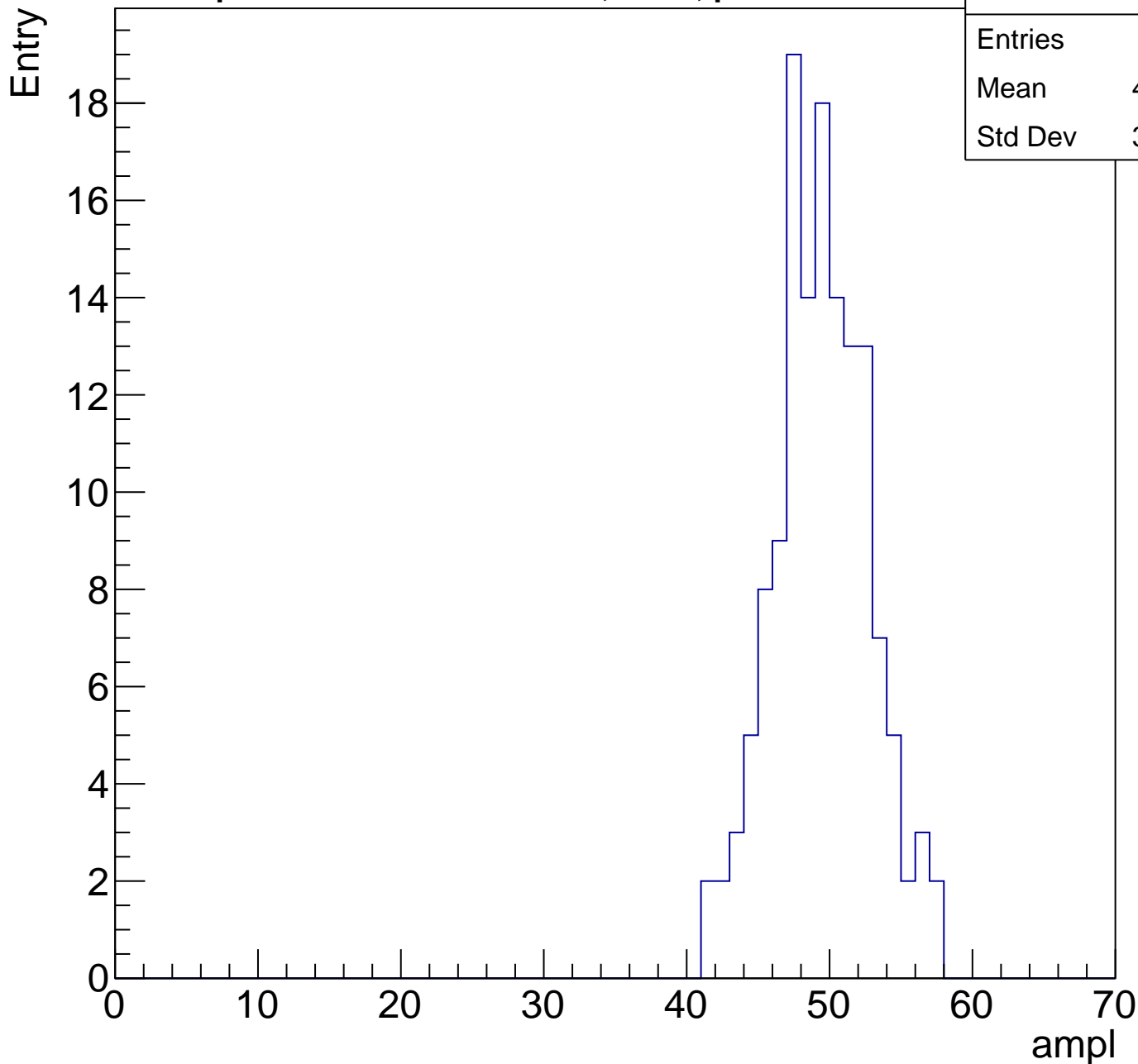
ampl



B1L001S, U21-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.98
Std Dev	3.312



B1L001S, U21-ch79, adc4

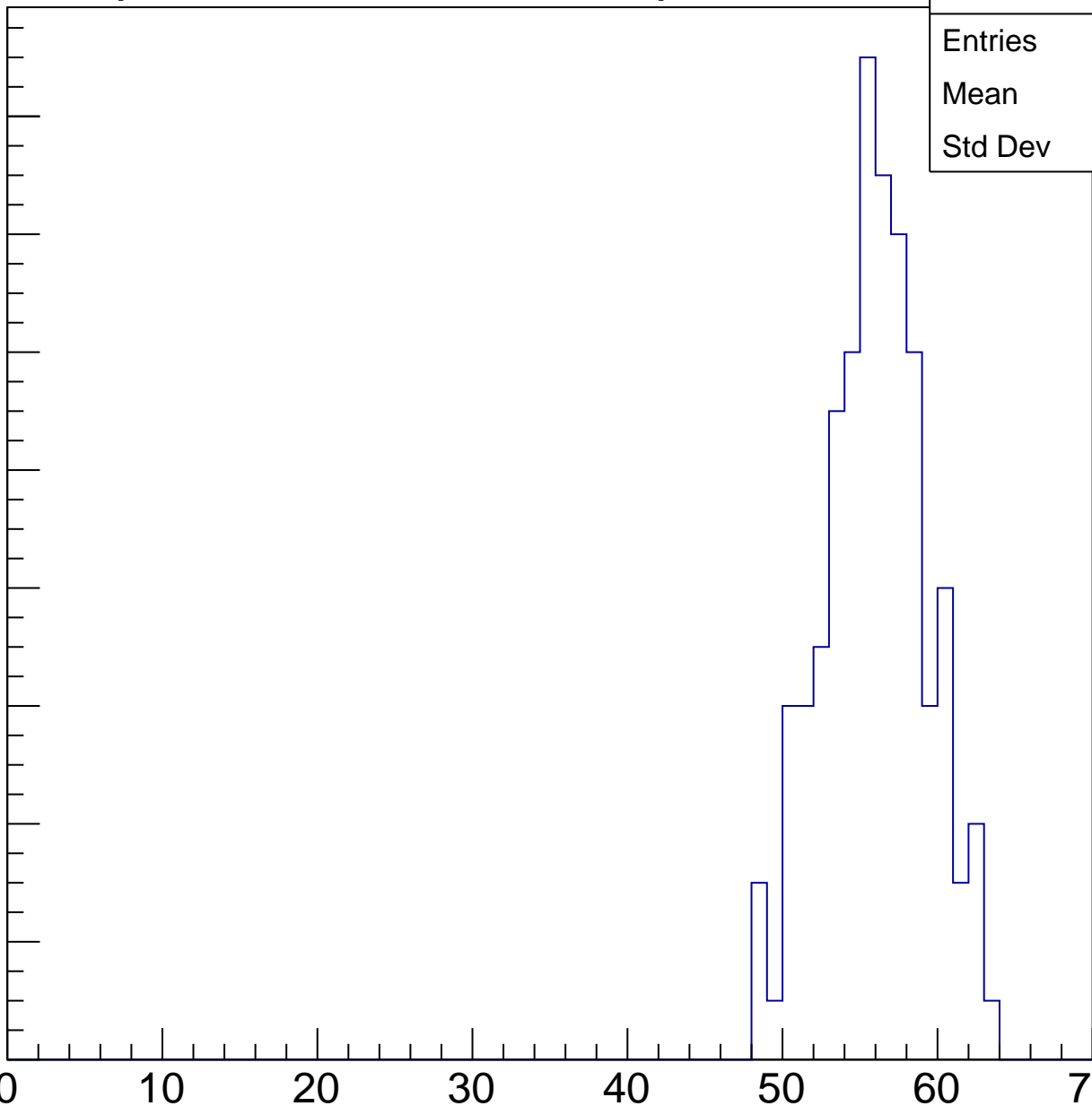
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	55.48
Std Dev	3.287

ampl



B1L001S, U21-ch79, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

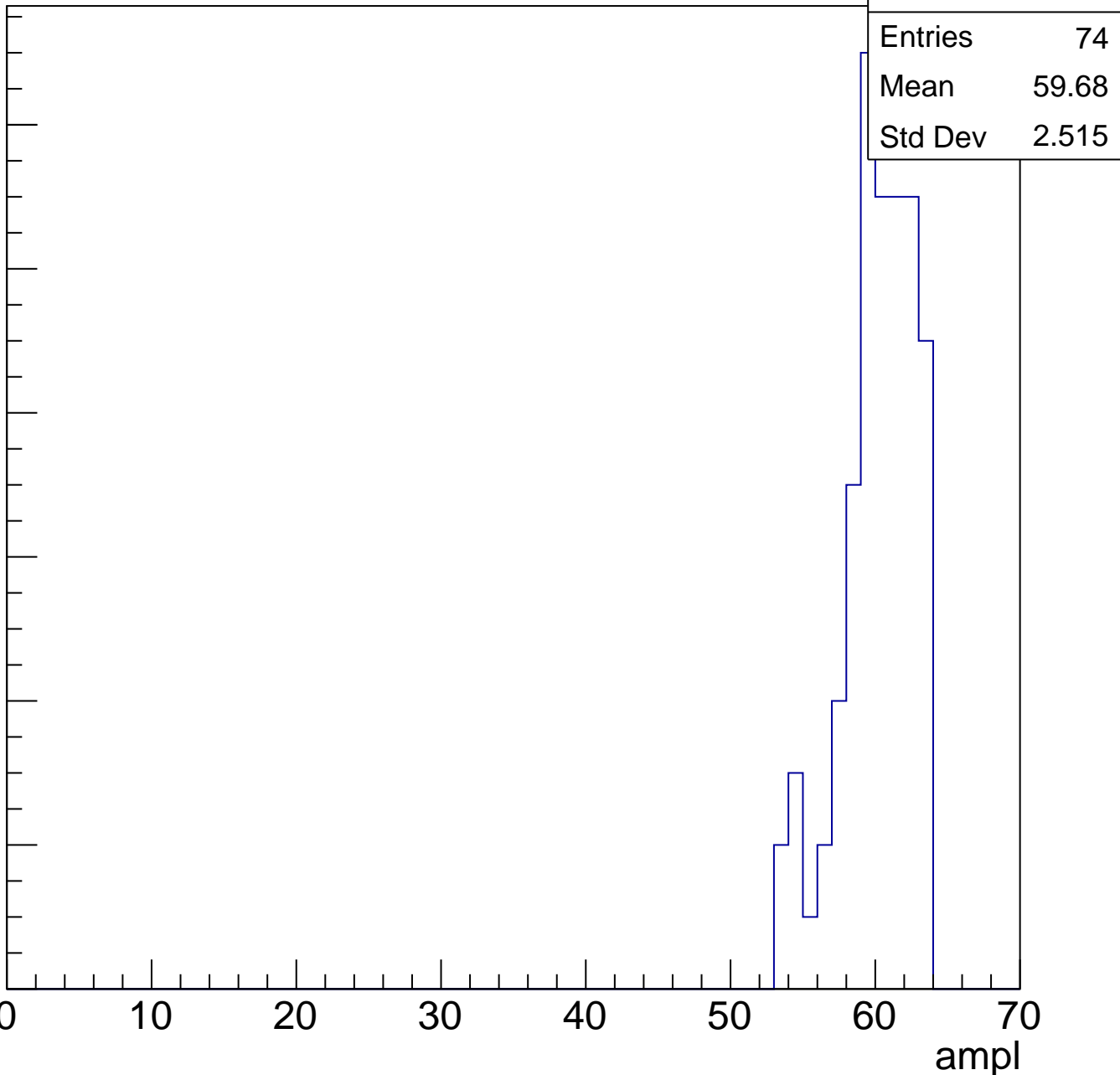
40

50

60

ampl

Entries	74
Mean	59.68
Std Dev	2.515

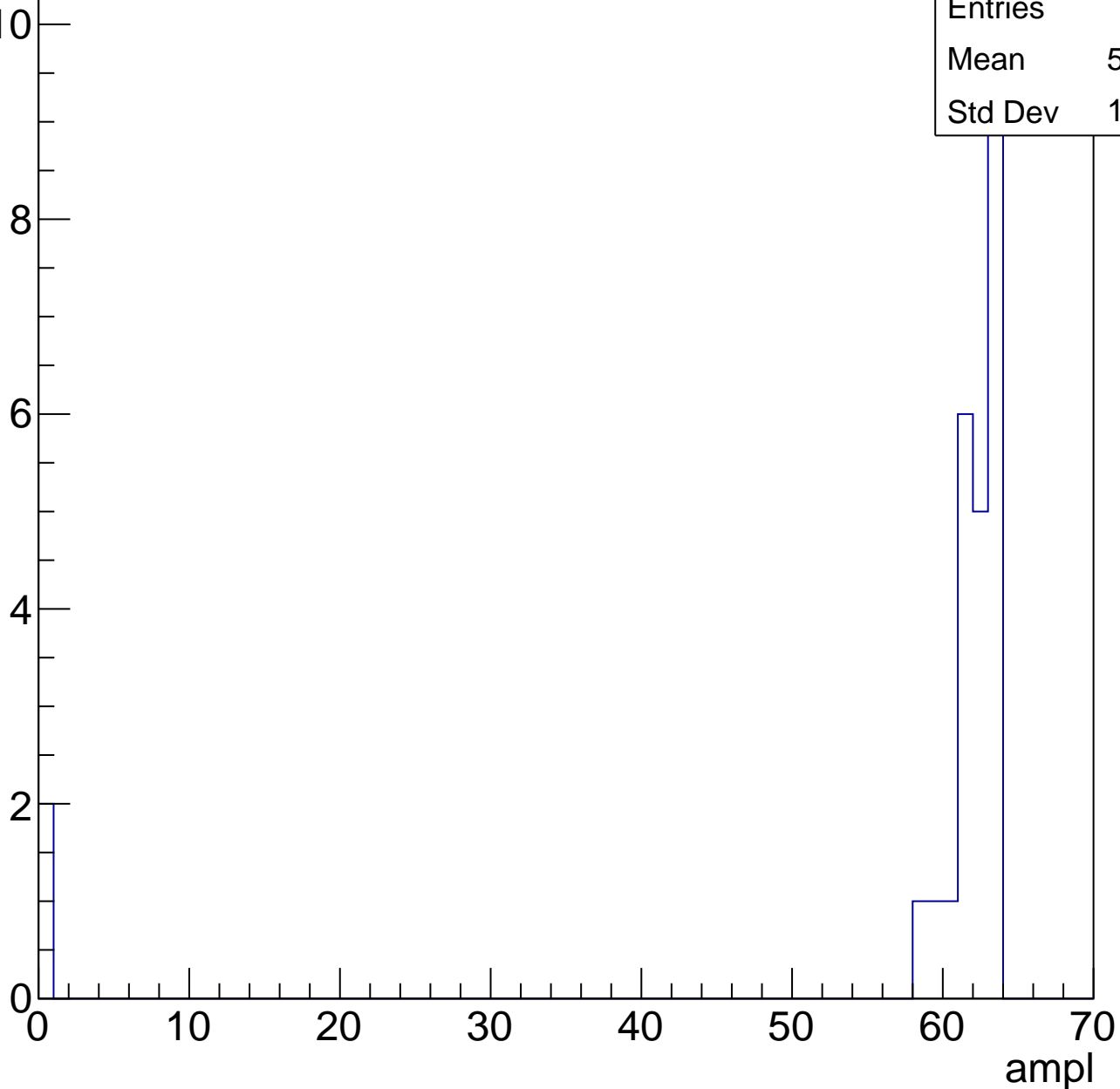


B1L001S, U21-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	57.04
Std Dev	16.52



B1L001S, U21-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U21-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

30.44

Std Dev

3.397

Gaus mean : 30.8622

Gaus Width: 3.5268

0

10

20

30

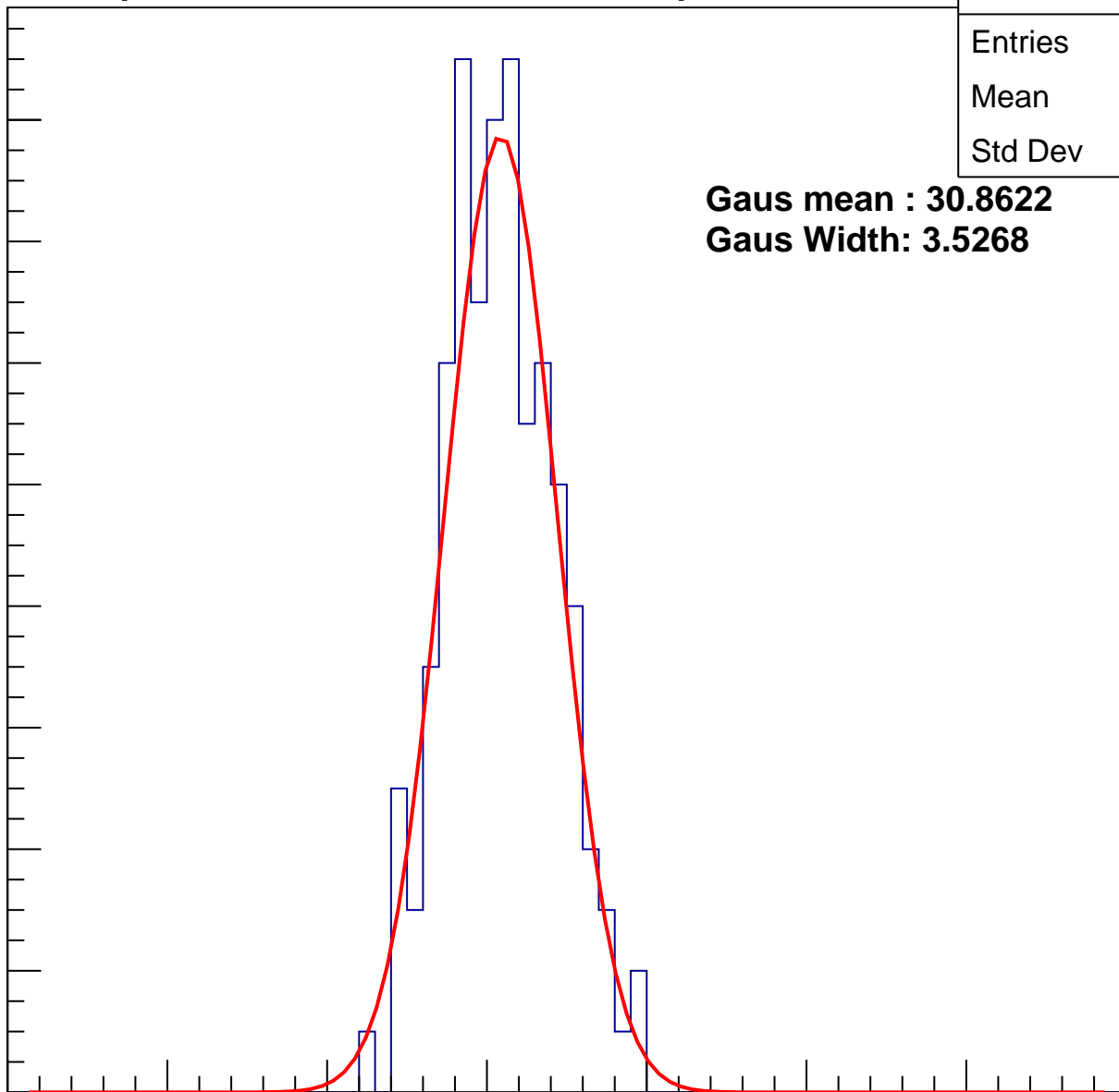
40

50

60

70

ampl



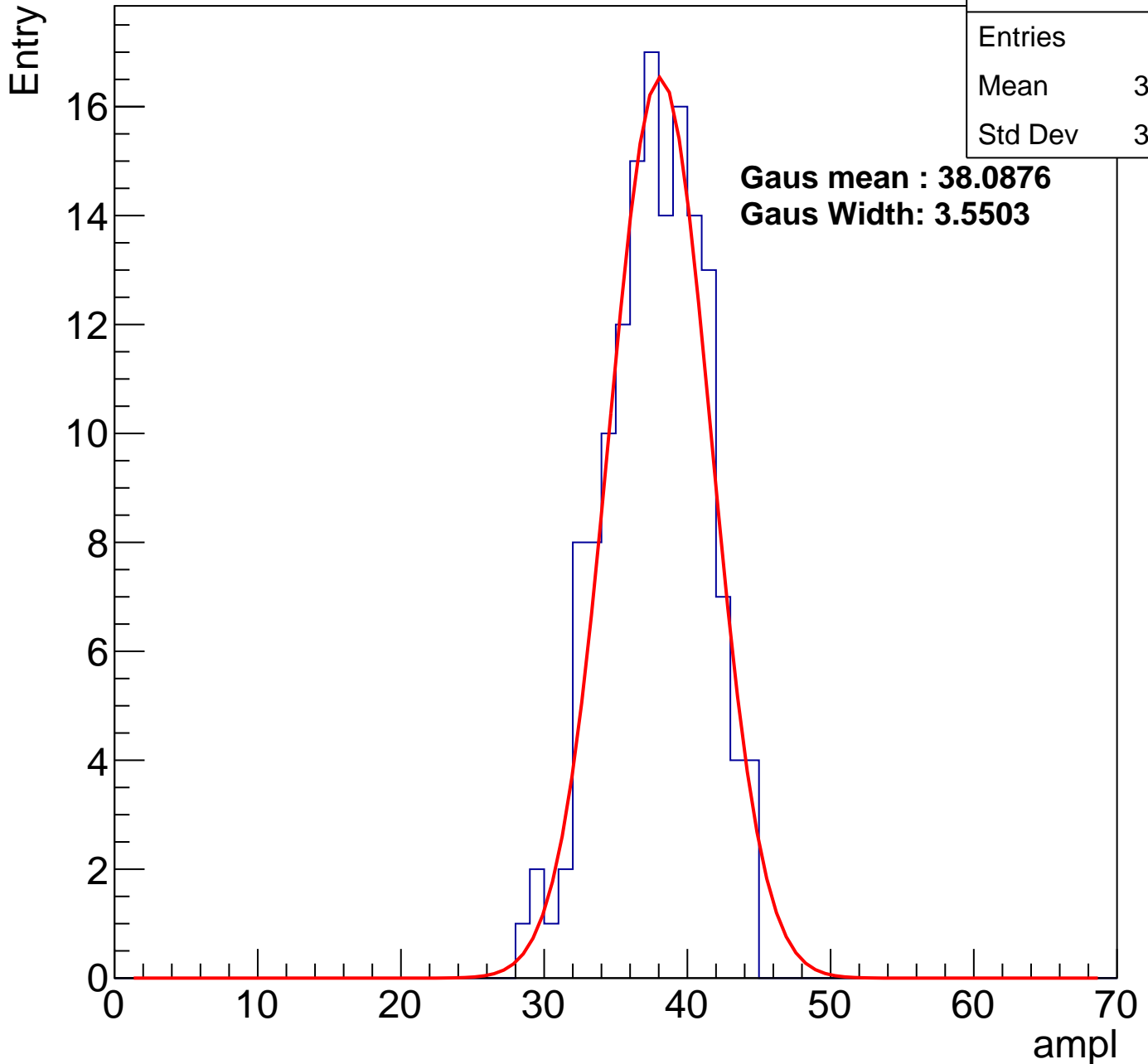
B1L001S, U21-ch80, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	37.28
Std Dev	3.415

Gaus mean : 38.0876

Gaus Width: 3.5503



B1L001S, U21-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

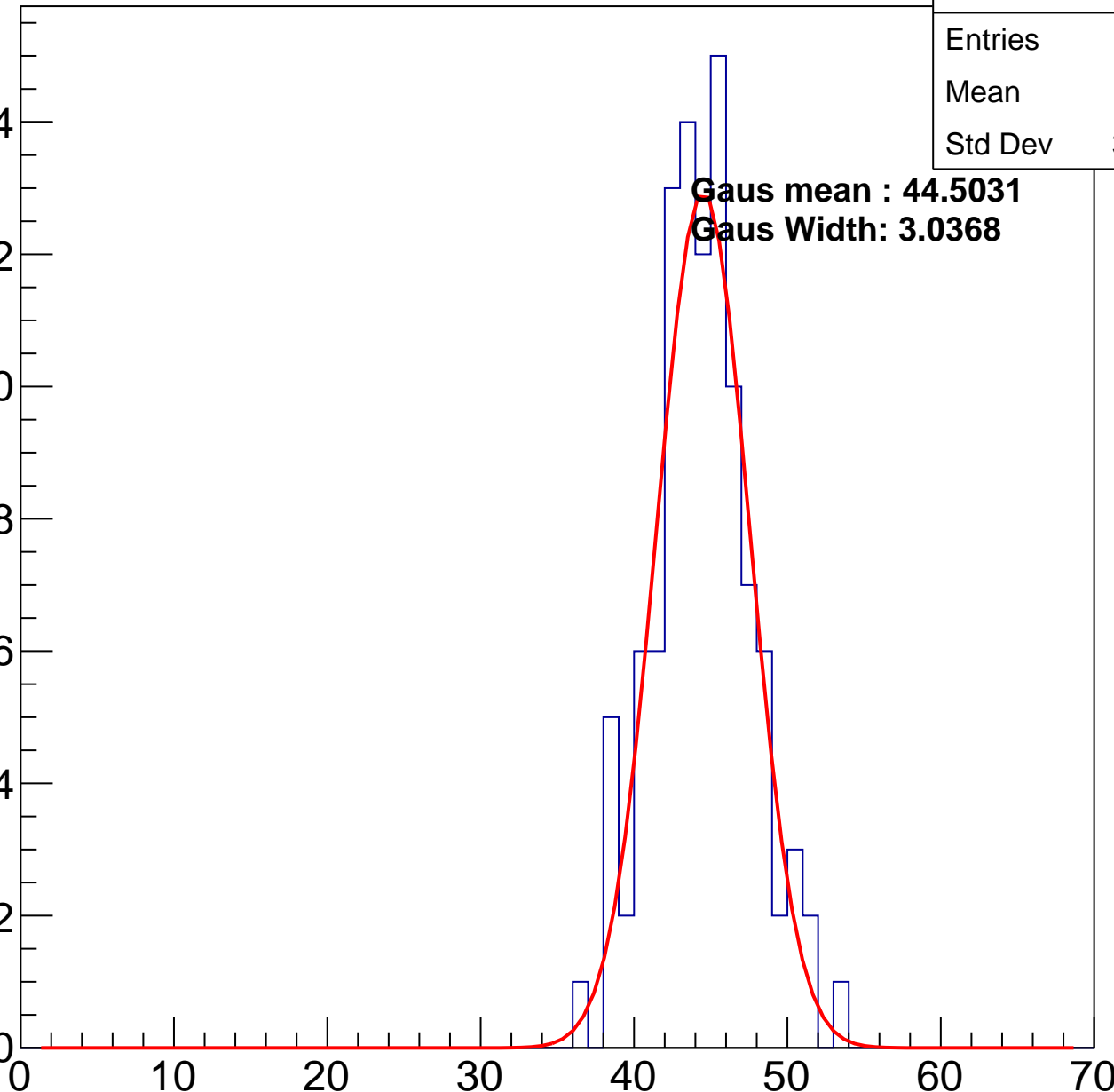
14
12
10
8
6
4
2
0

Entries	105
Mean	44.01
Std Dev	3.179

Gaus mean : 44.5031

Gaus Width: 3.0368

ampl



B1L001S, U21-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

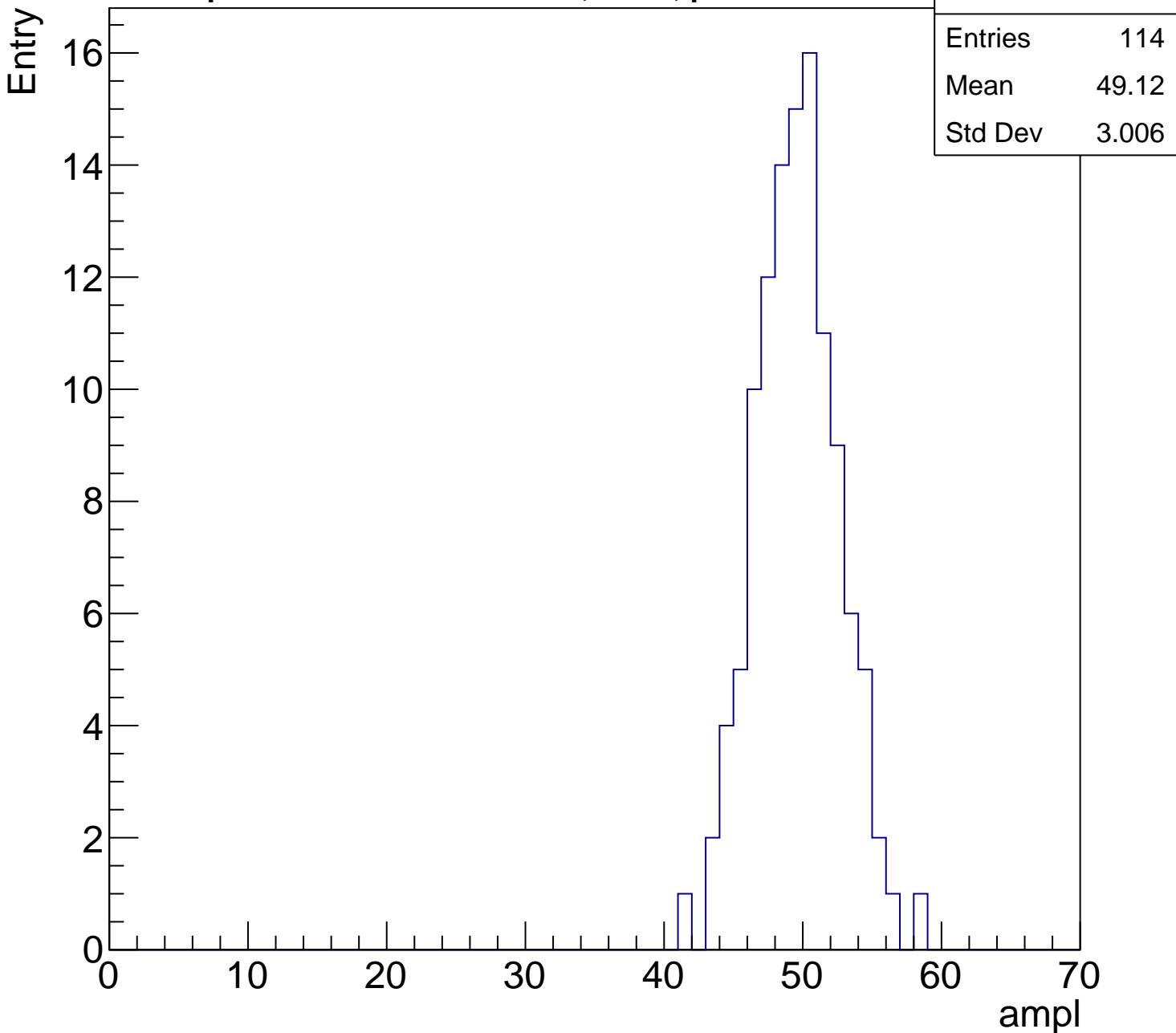
Entries	114
Mean	49.12
Std Dev	3.006

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.52
Std Dev	3.146

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

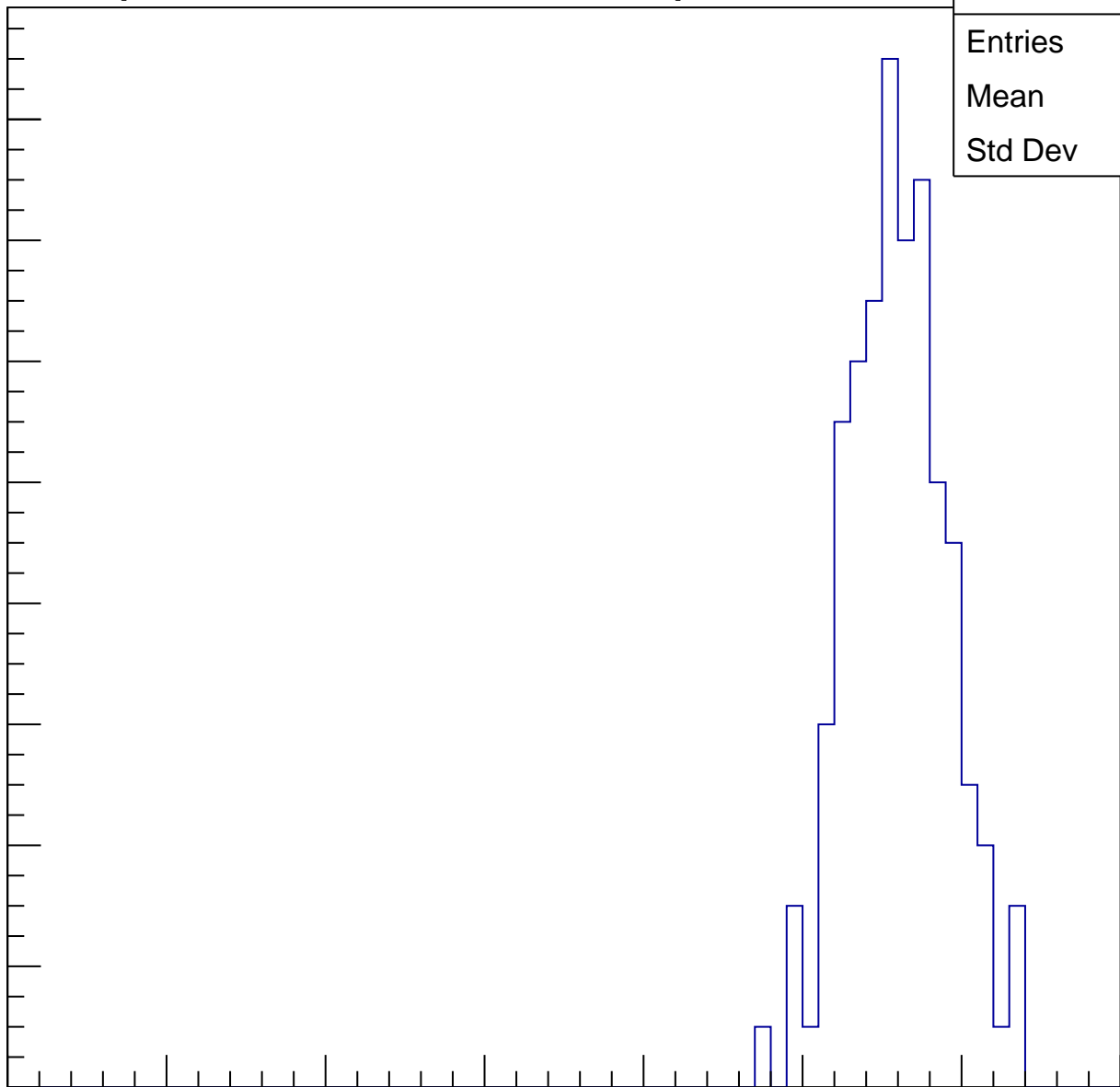
40

50

60

70

ampl



B1L001S, U21-ch80, adc5

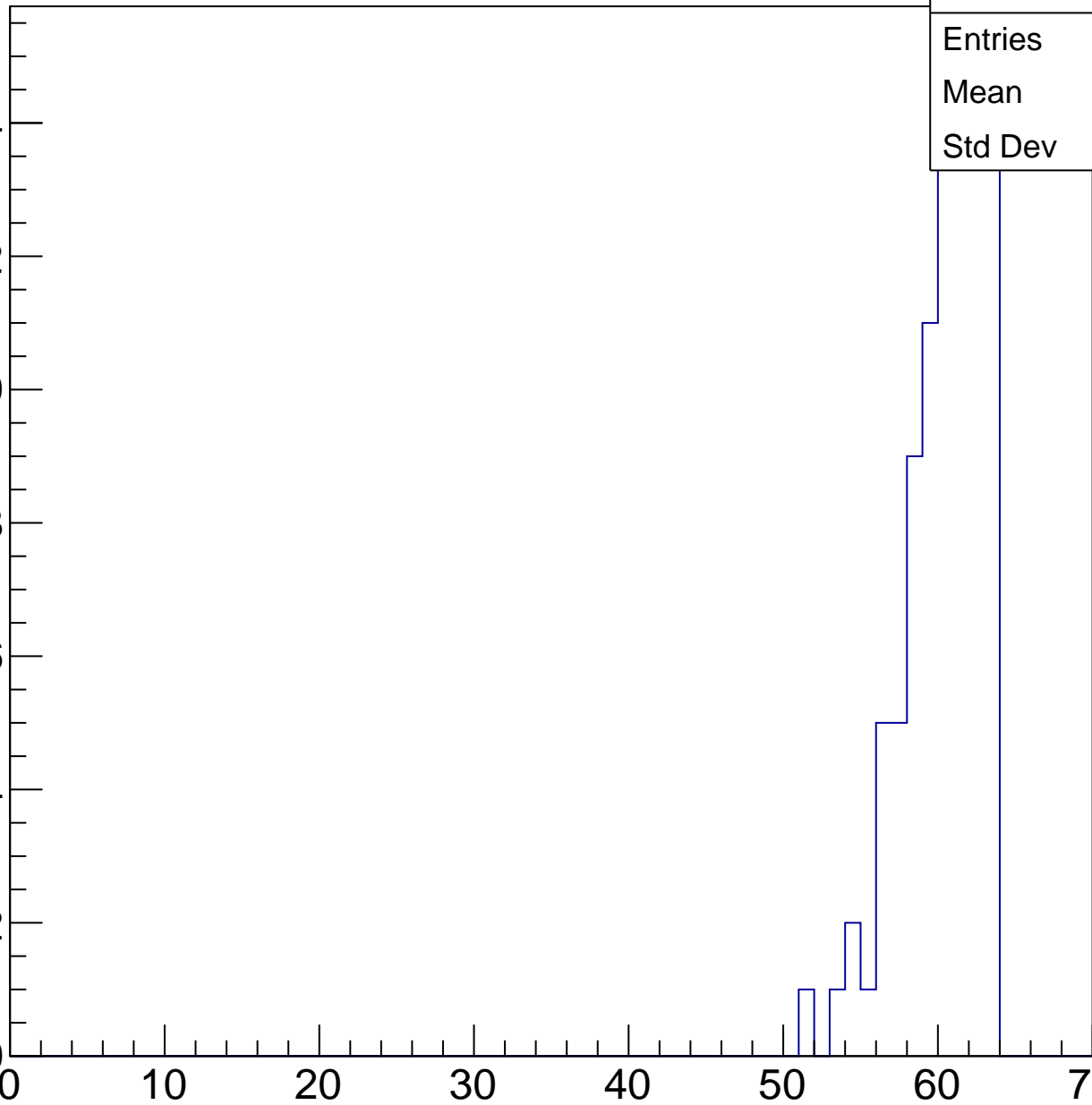
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	59.89
Std Dev	2.538

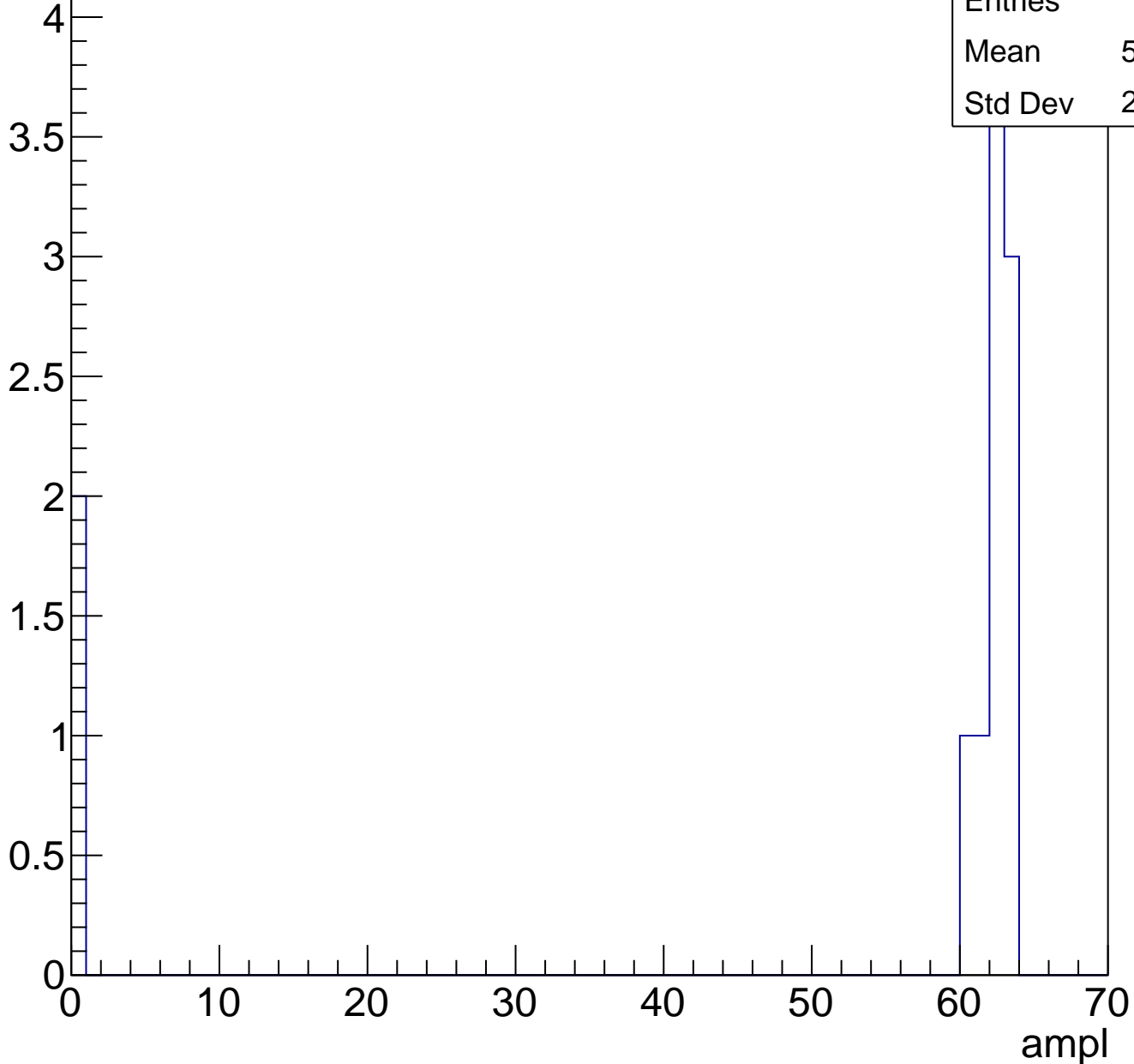
ampl



B1L001S, U21-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch81, adc0

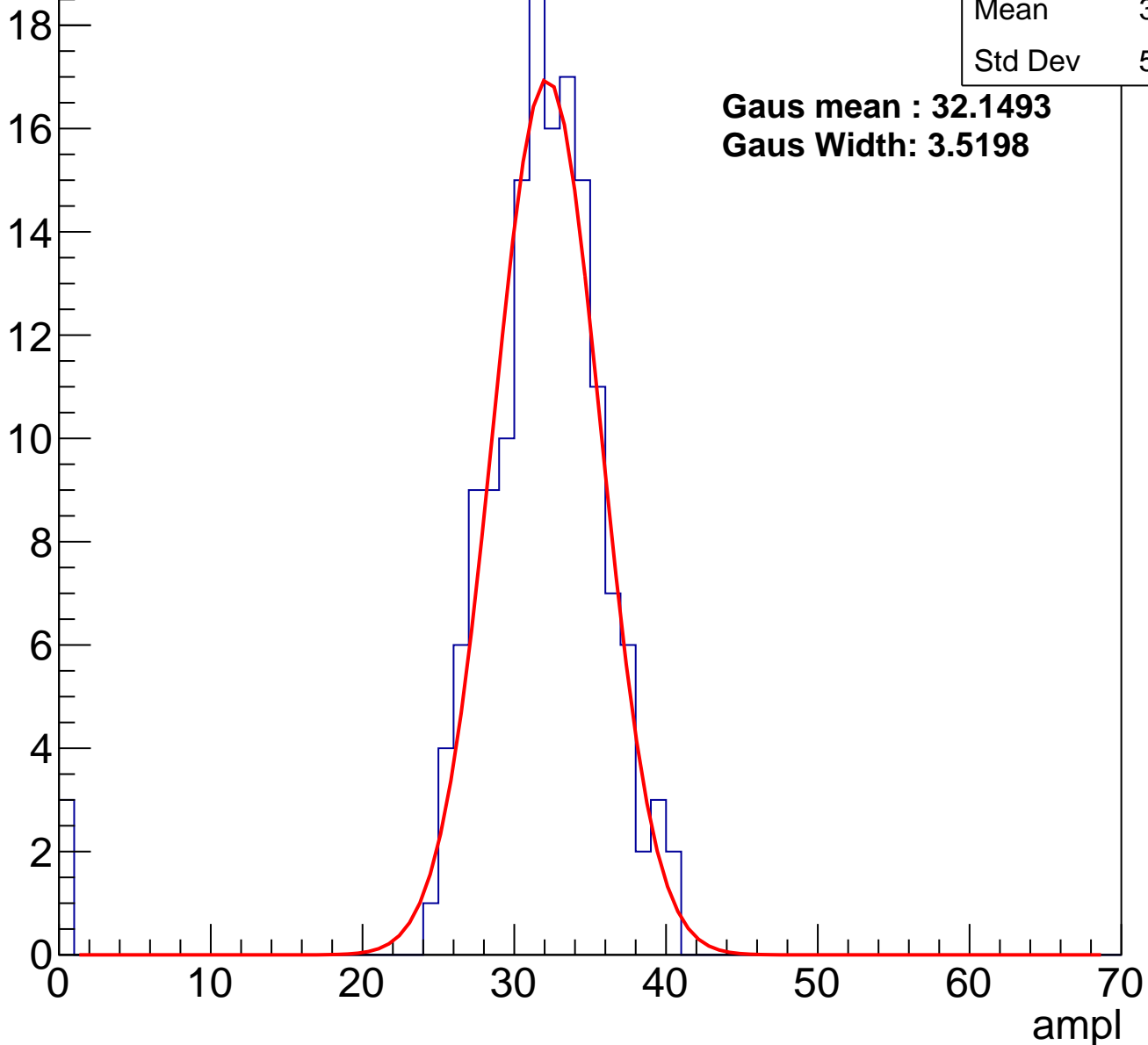
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	31.09
Std Dev	5.532

Gaus mean : 32.1493

Gaus Width: 3.5198

Entry

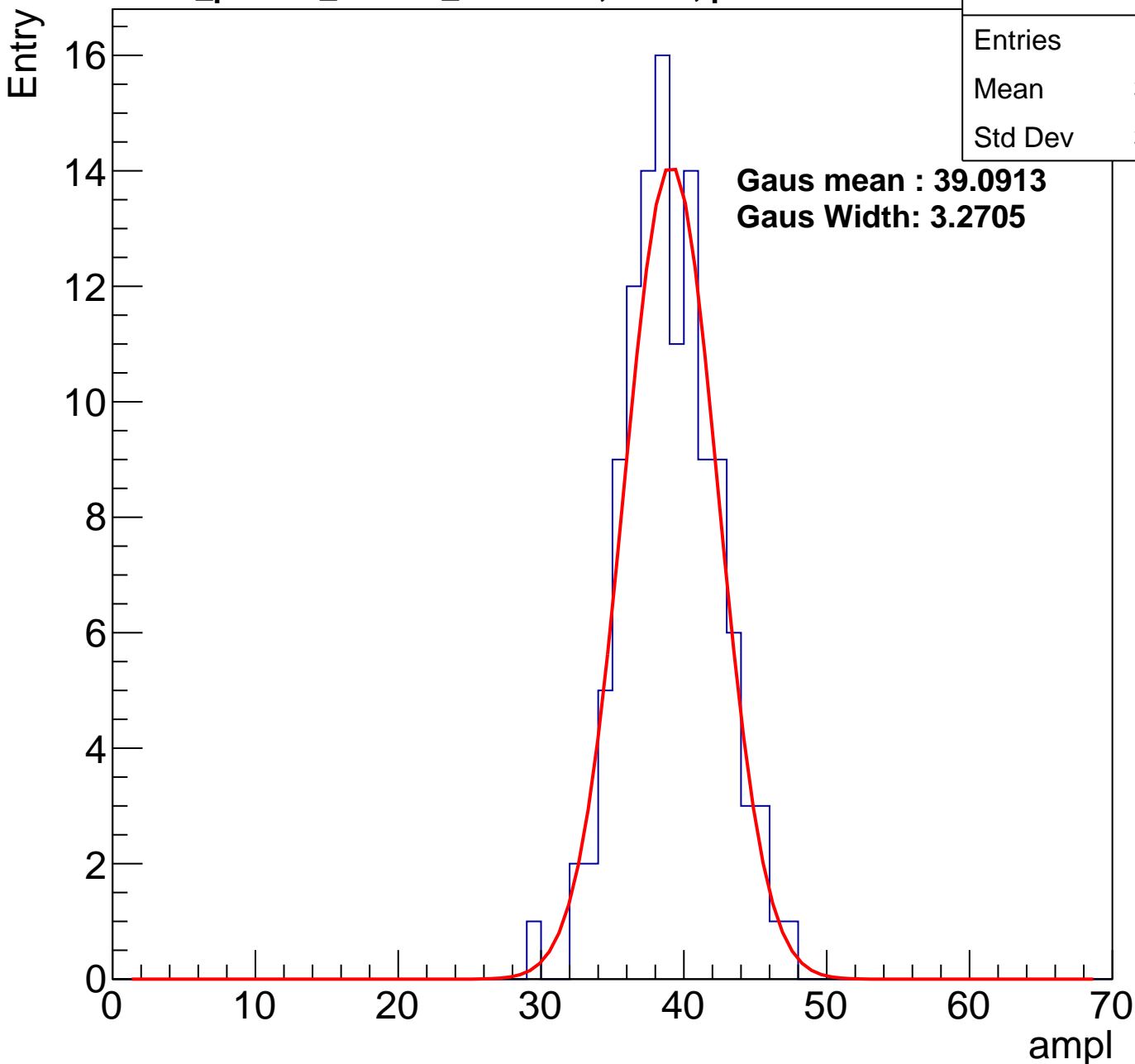


B1L001S, U21-ch81, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	38.61
Std Dev	3.221

Gaus mean : 39.0913
Gaus Width: 3.2705



B1L001S, U21-ch81, adc2

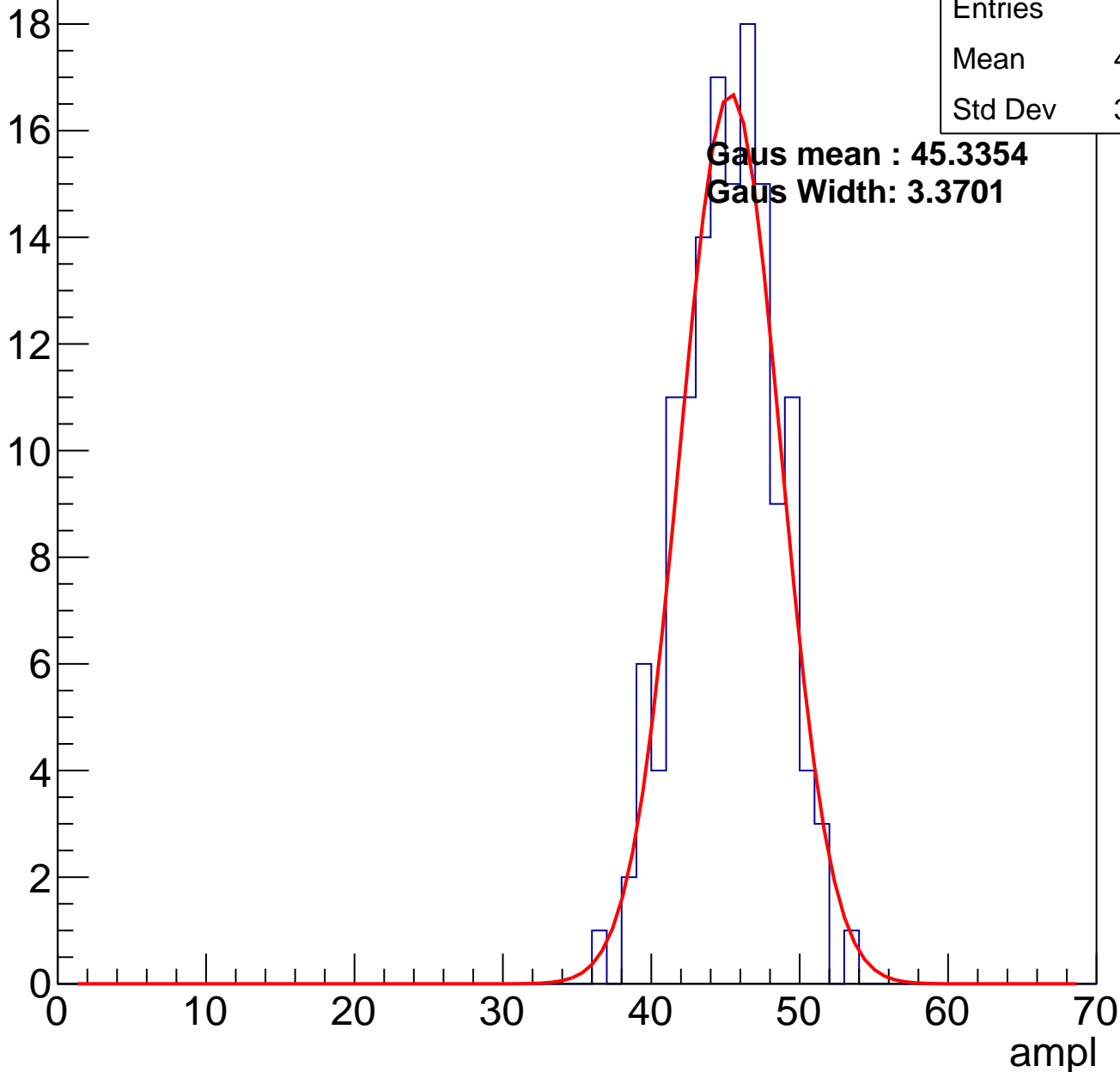
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	142
Mean	44.75
Std Dev	3.185

Gaus mean : 45.3354

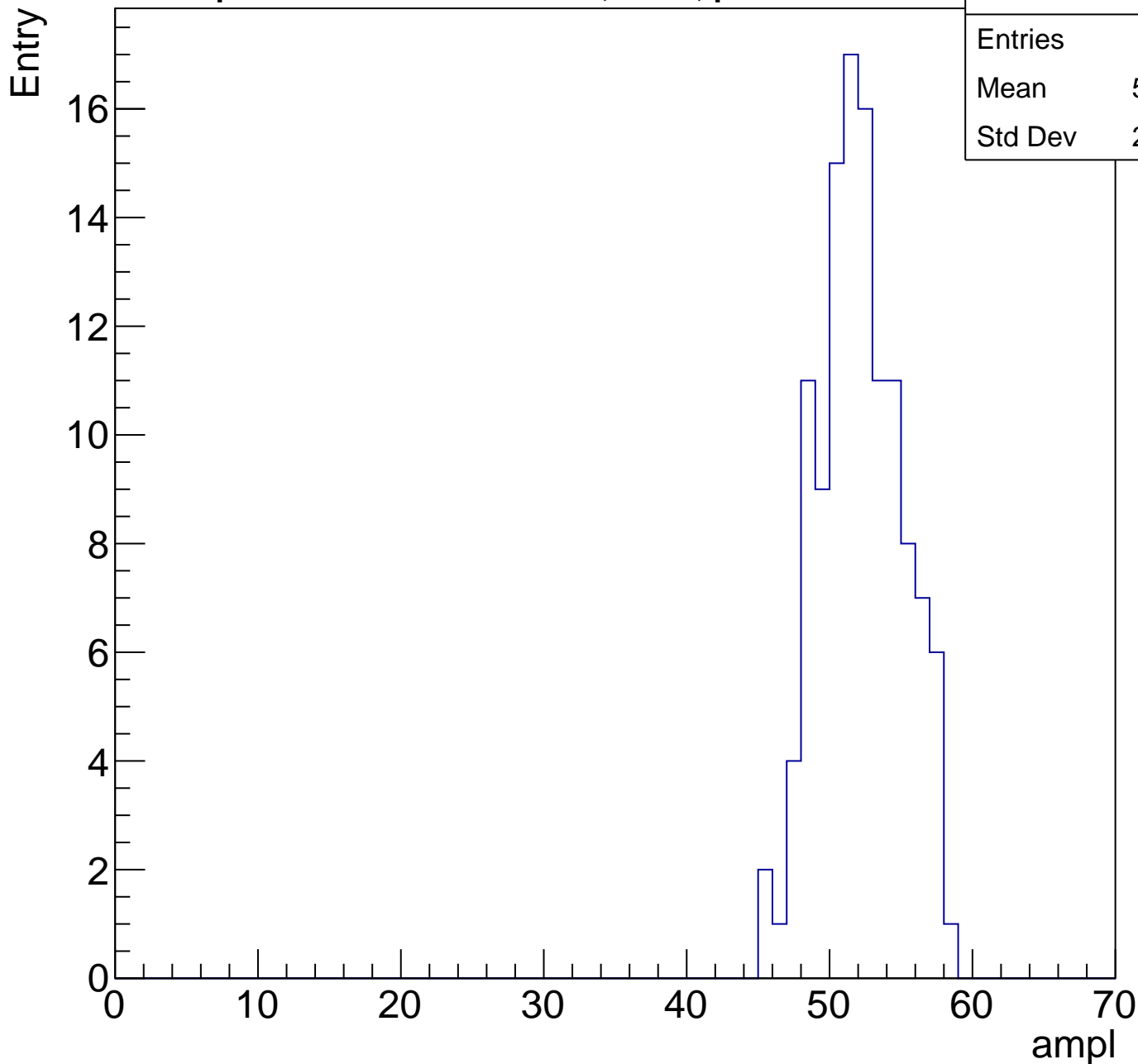
Gaus Width: 3.3701



B1L001S, U21-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	51.69
Std Dev	2.872



B1L001S, U21-ch81, adc4

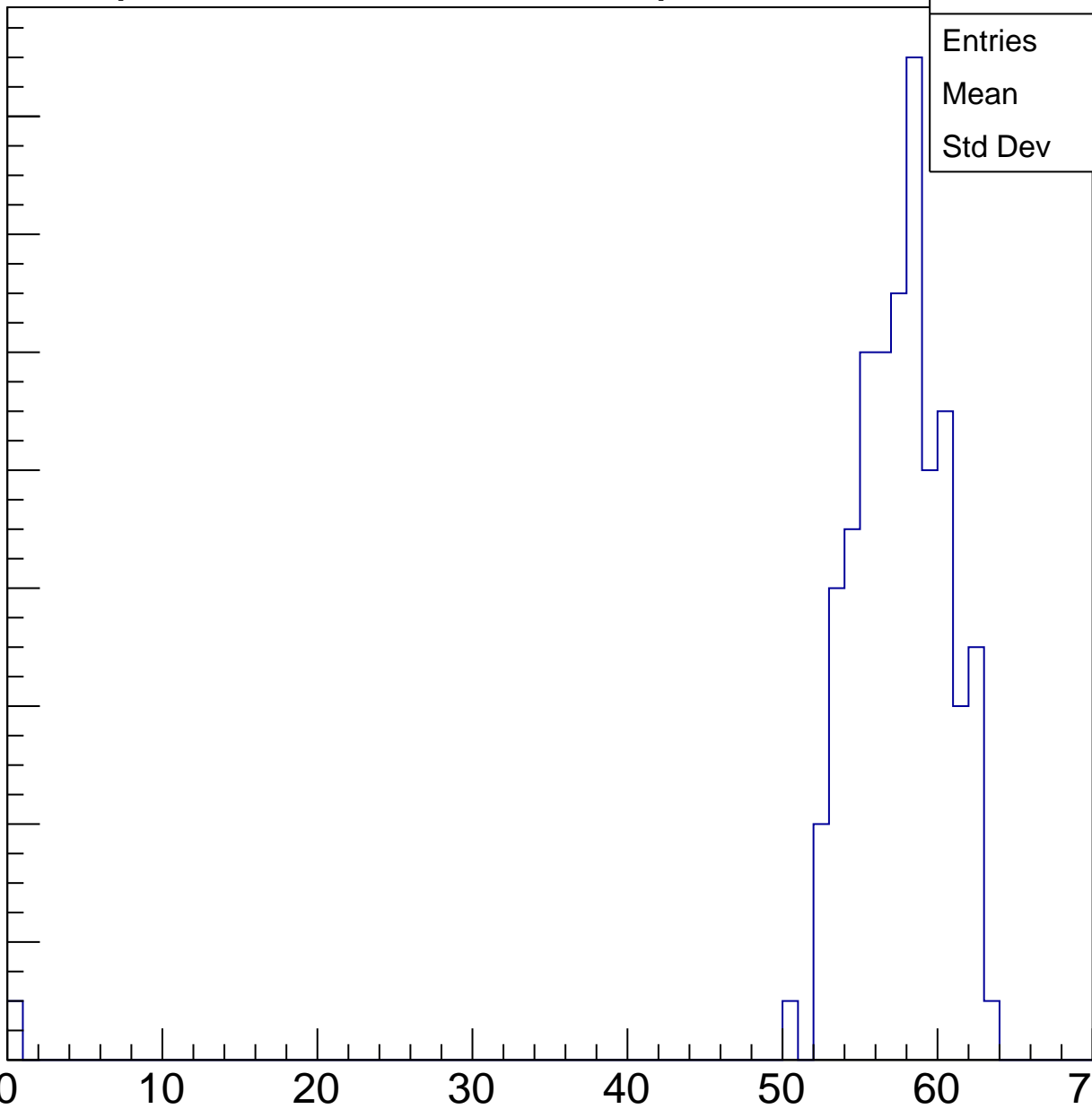
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	56.61
Std Dev	6.06

ampl



B1L001S, U21-ch81, adc5

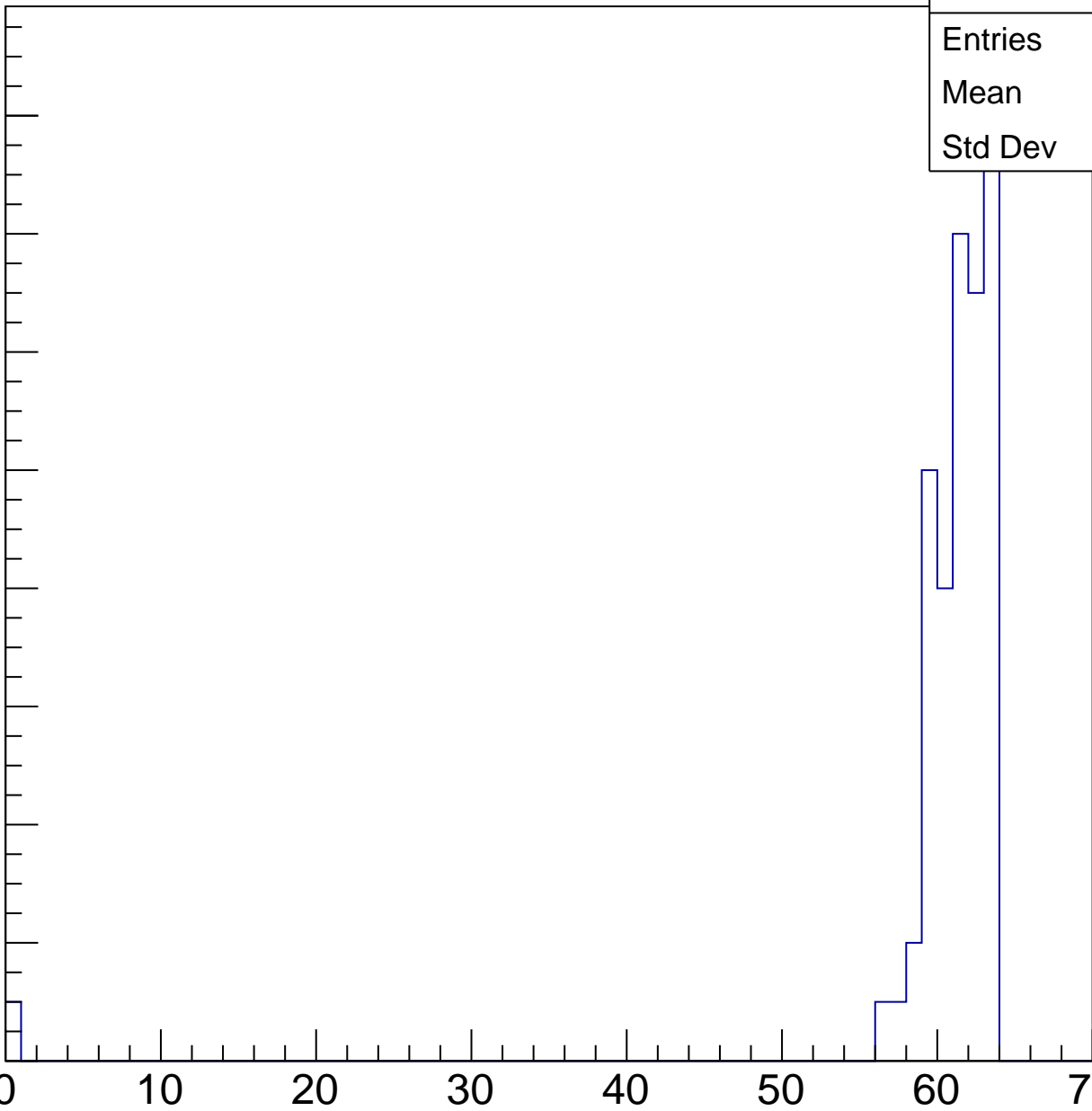
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	67
Mean	60.15
Std Dev	7.591

ampl



B1L001S, U21-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

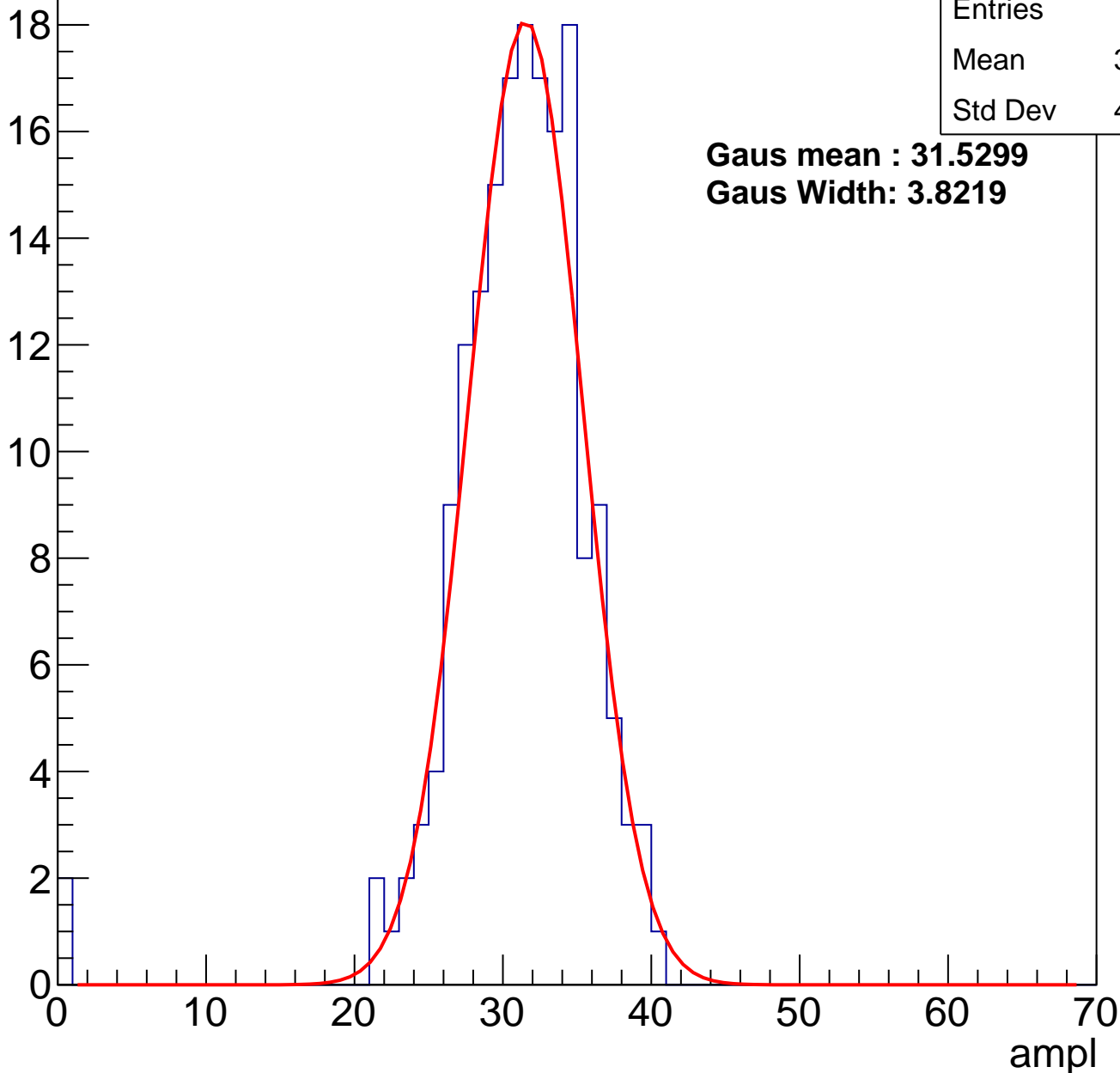


Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch82, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

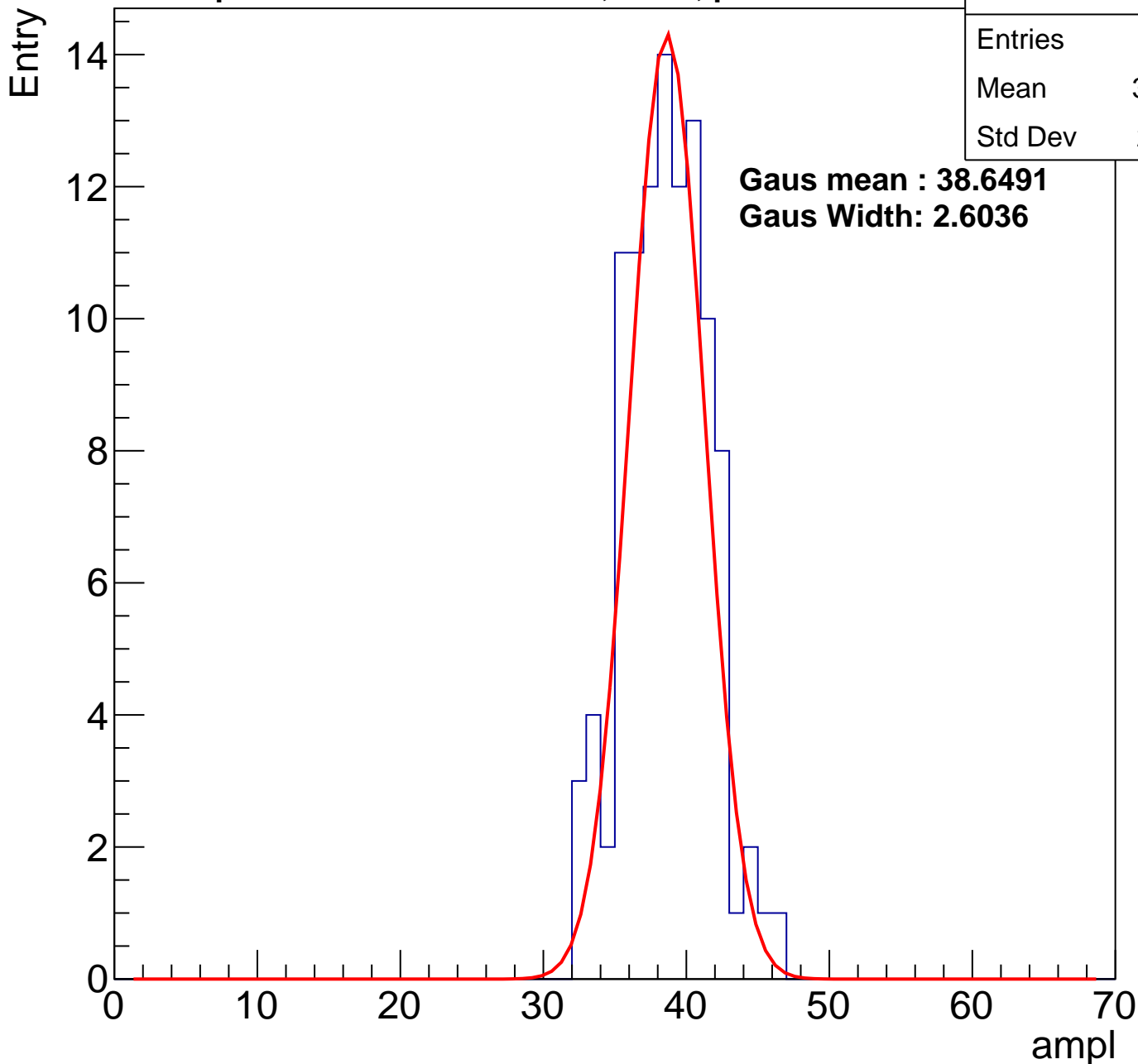


B1L001S, U21-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	38.18
Std Dev	2.901

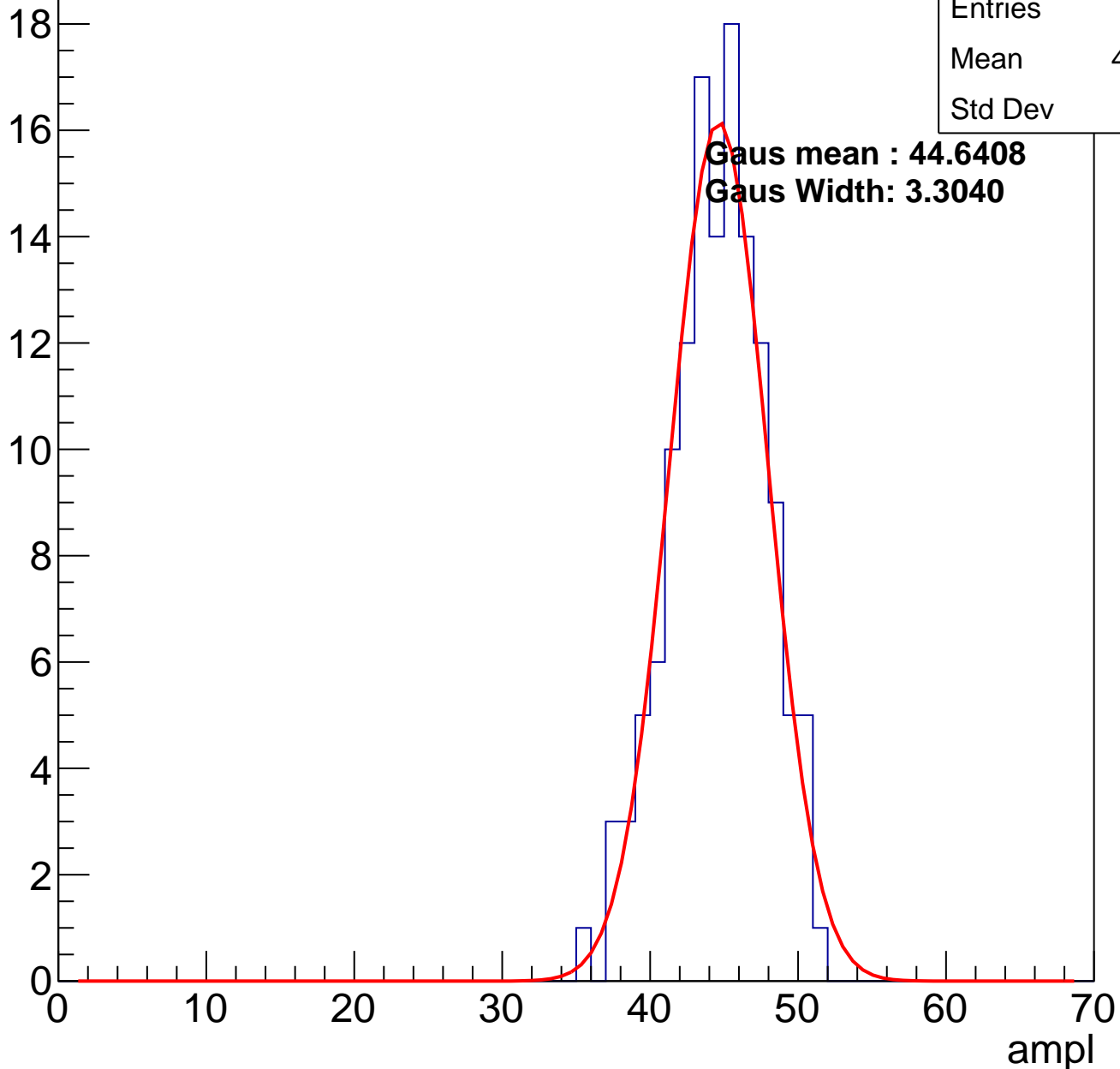
Gaus mean : 38.6491
Gaus Width: 2.6036



B1L001S, U21-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

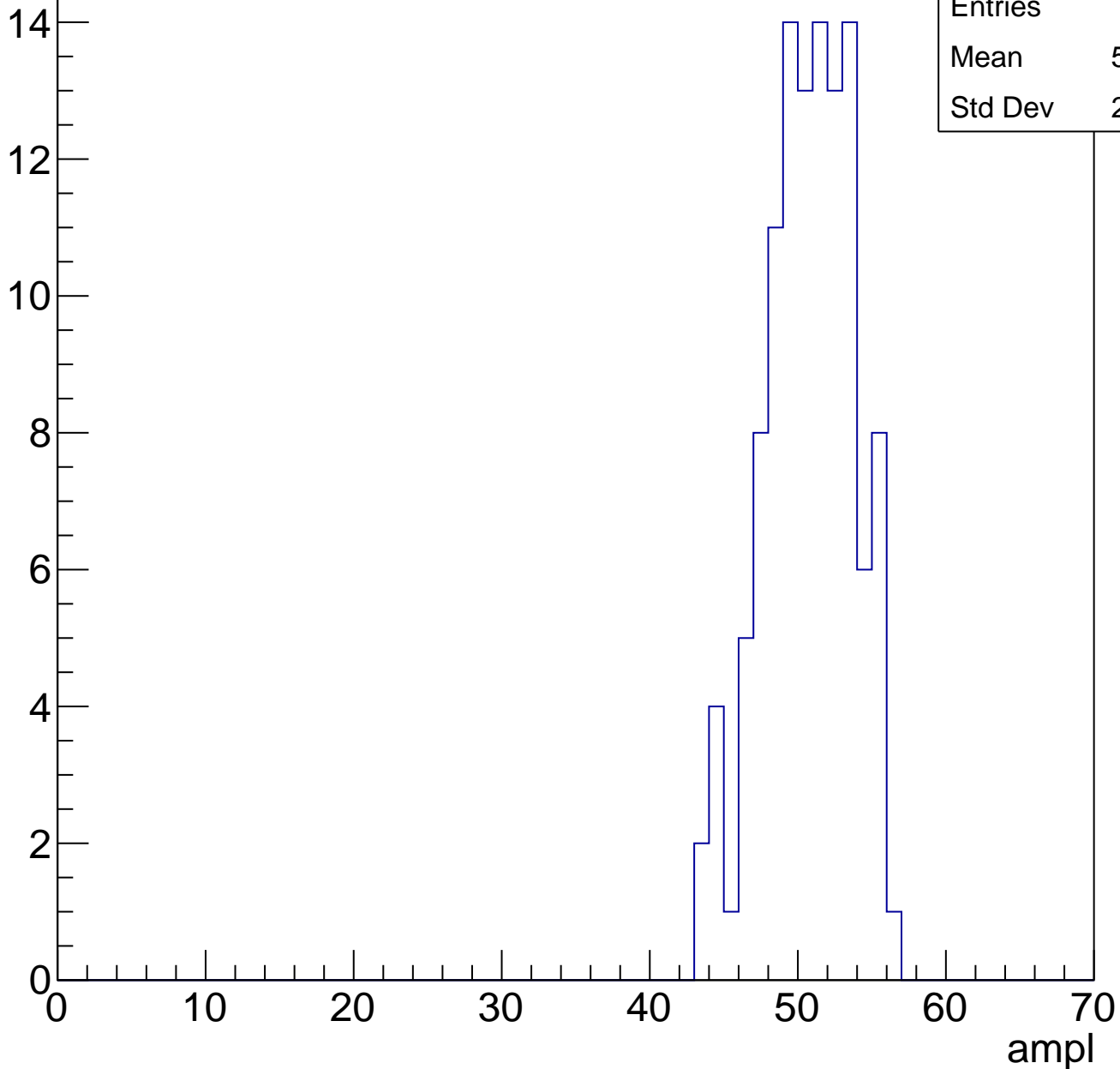
Entry



B1L001S, U21-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



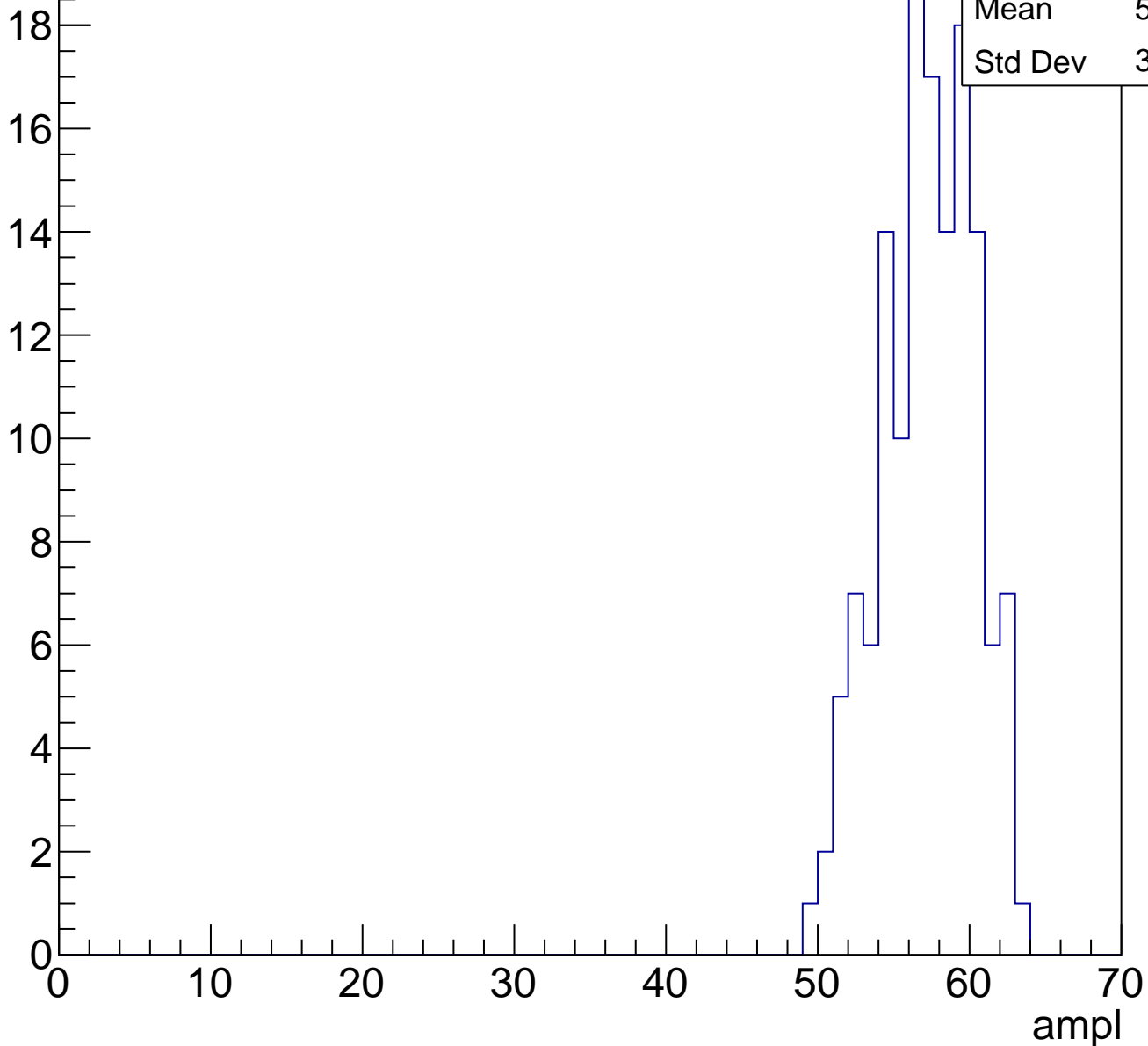
Entries	114
Mean	50.25
Std Dev	2.952

B1L001S, U21-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	56.75
Std Dev	3.046

Entry



B1L001S, U21-ch82, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

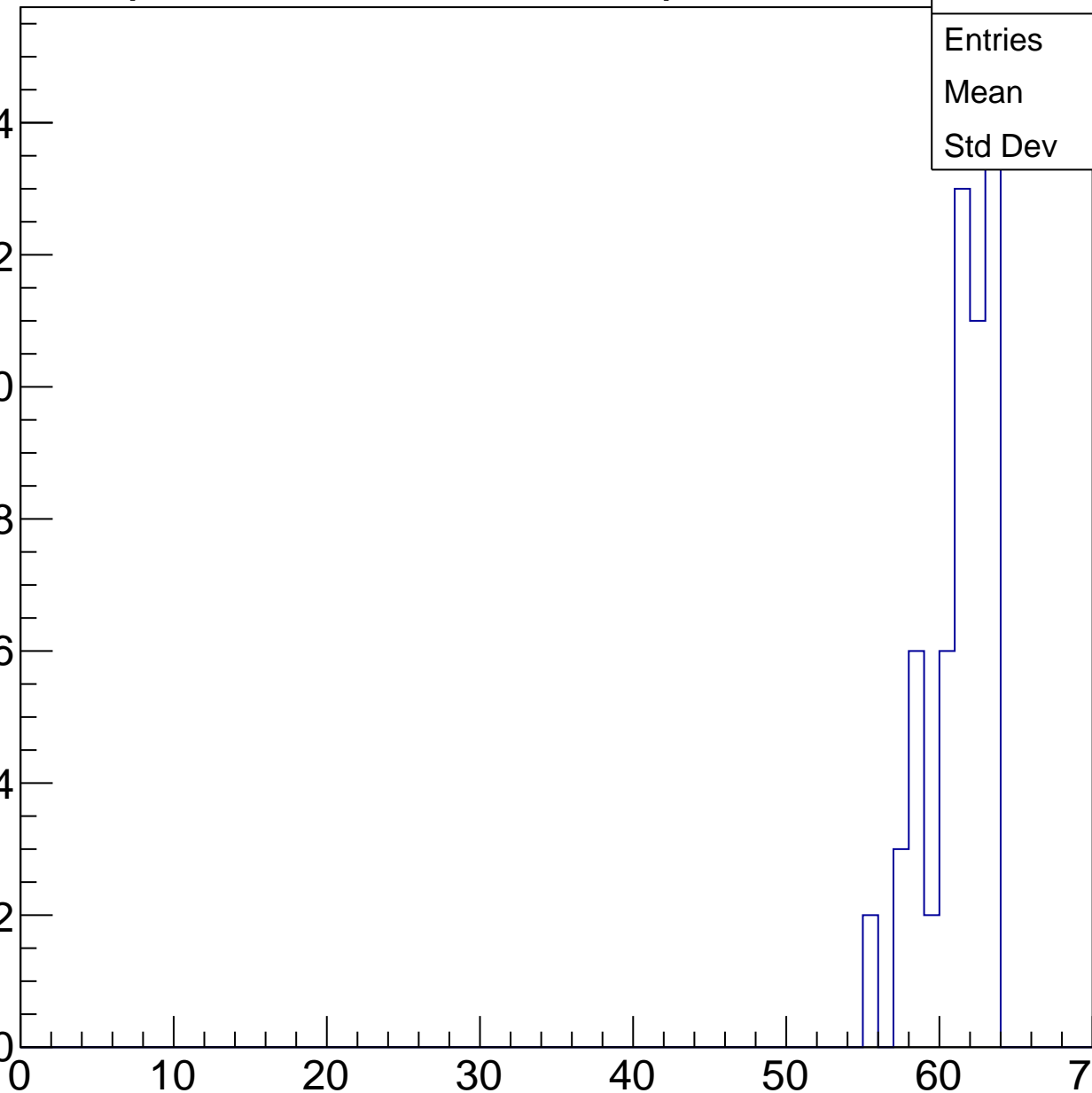
4

2

0

Entries	58
Mean	60.81
Std Dev	2.105

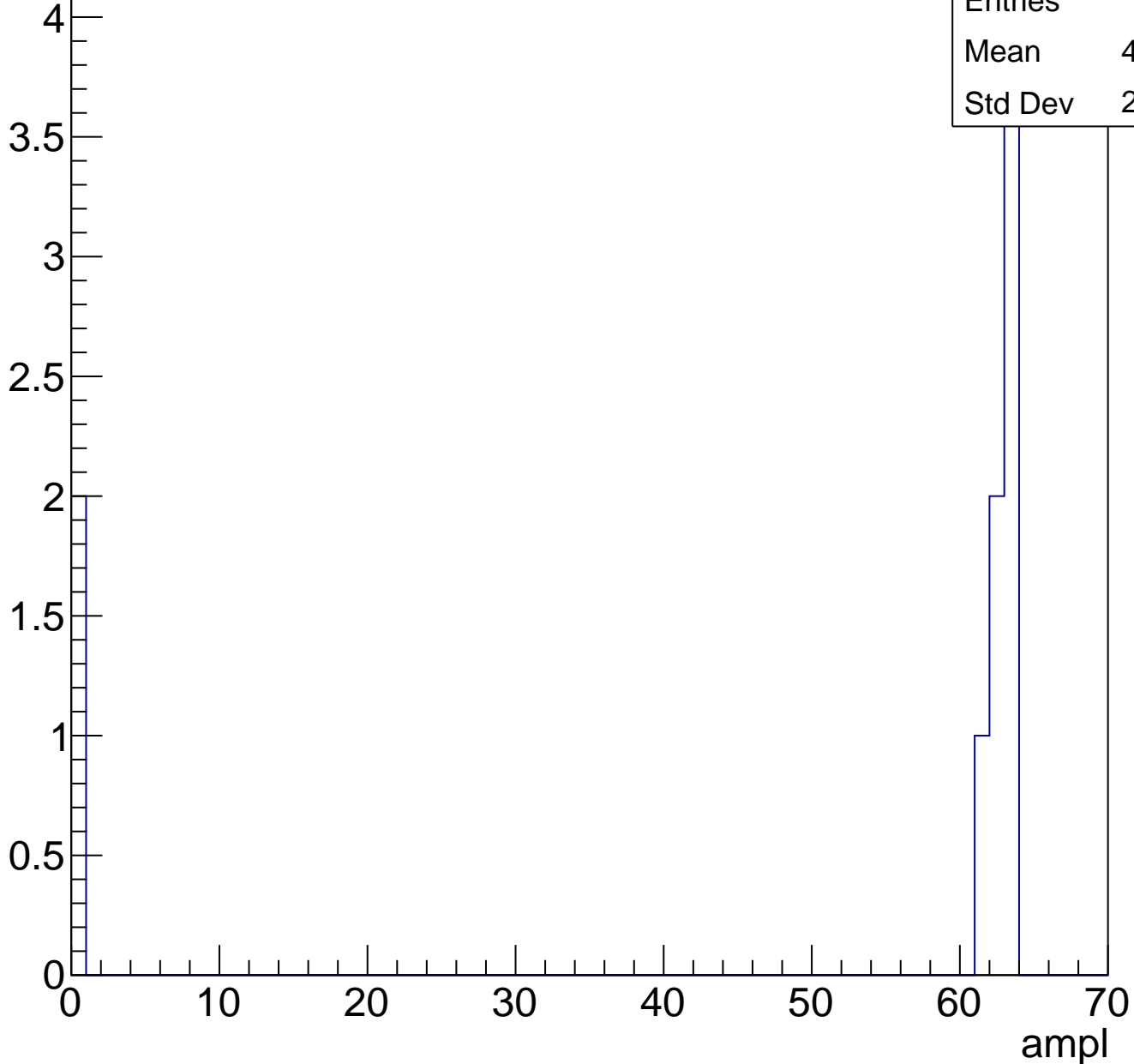
ampl



B1L001S, U21-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

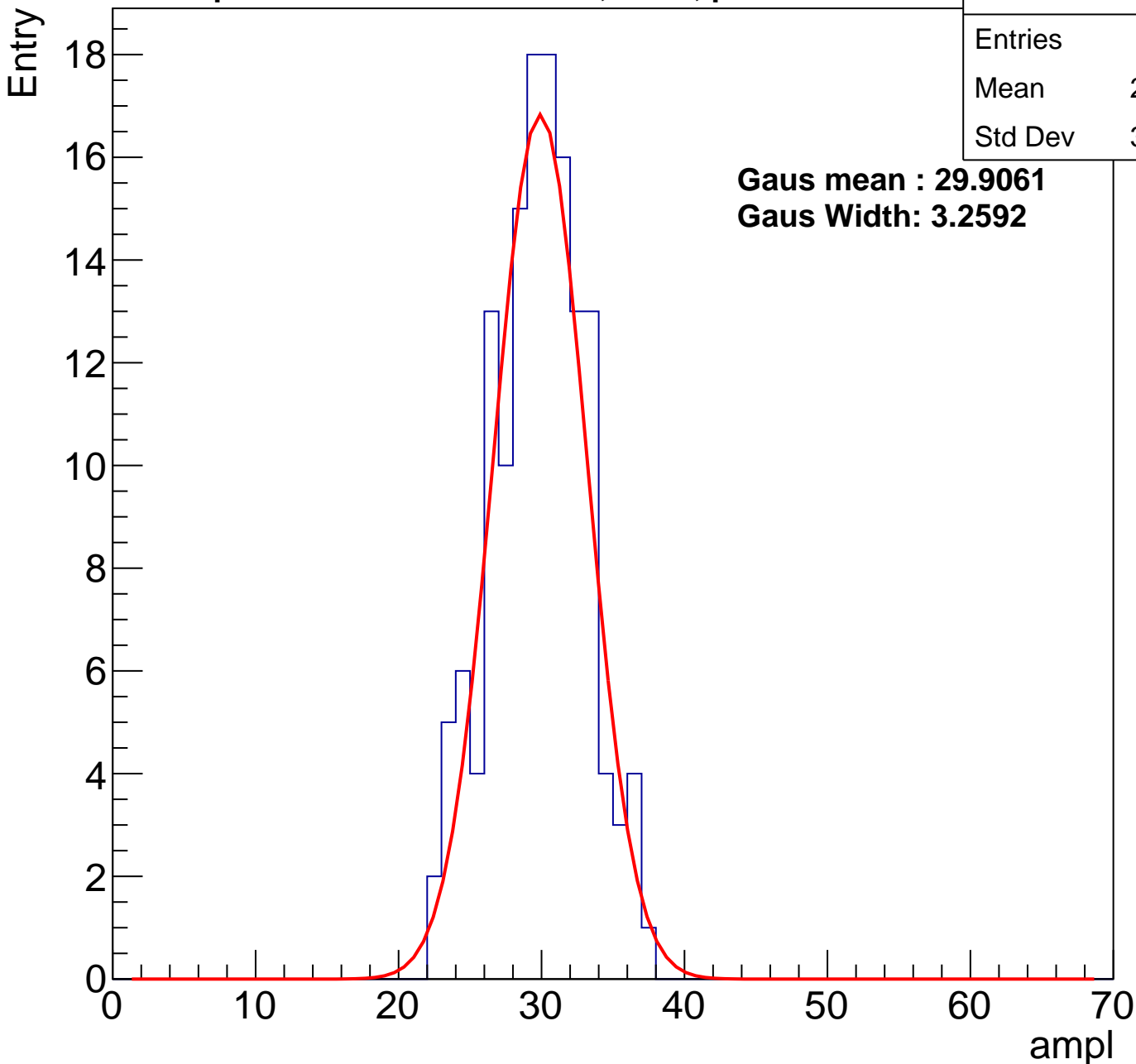
B1L001S, U21-ch83, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.35
Std Dev	3.239

Gaus mean : 29.9061

Gaus Width: 3.2592



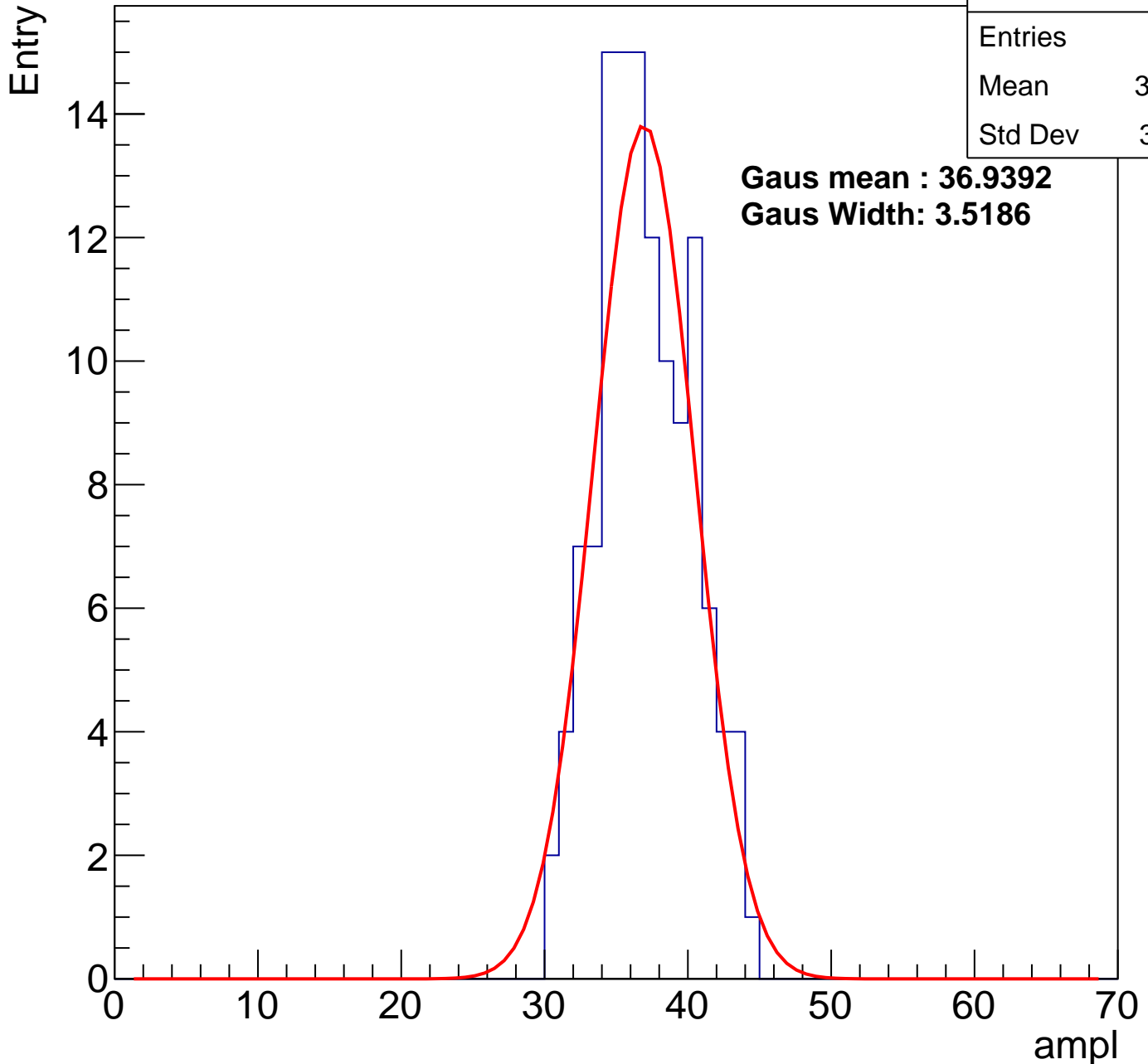
B1L001S, U21-ch83, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	36.58
Std Dev	3.201

Gaus mean : 36.9392

Gaus Width: 3.5186



B1L001S, U21-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

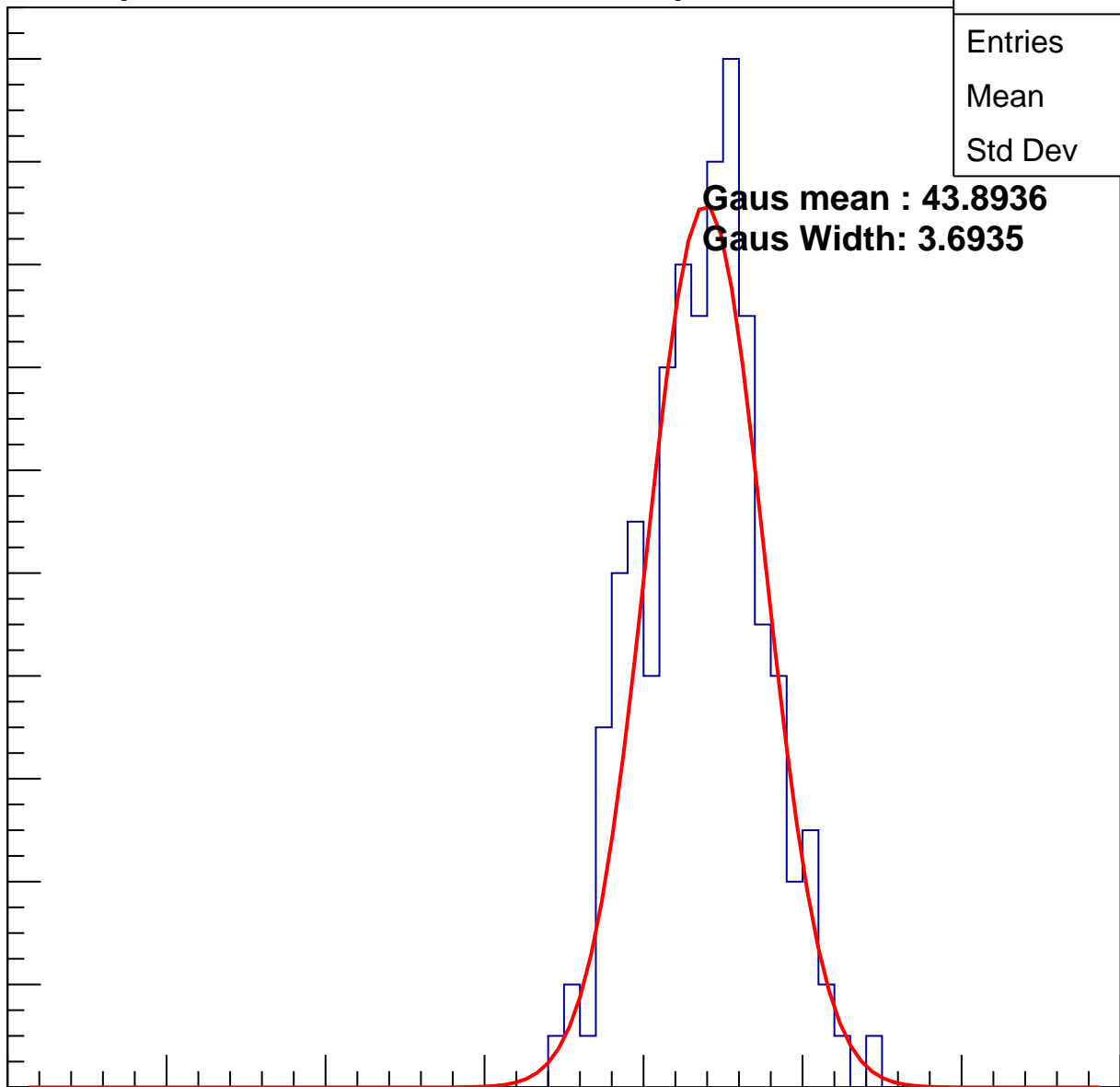
Entries	168
Mean	43.23
Std Dev	3.754

Gaus mean : 43.8936

Gaus Width: 3.6935

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

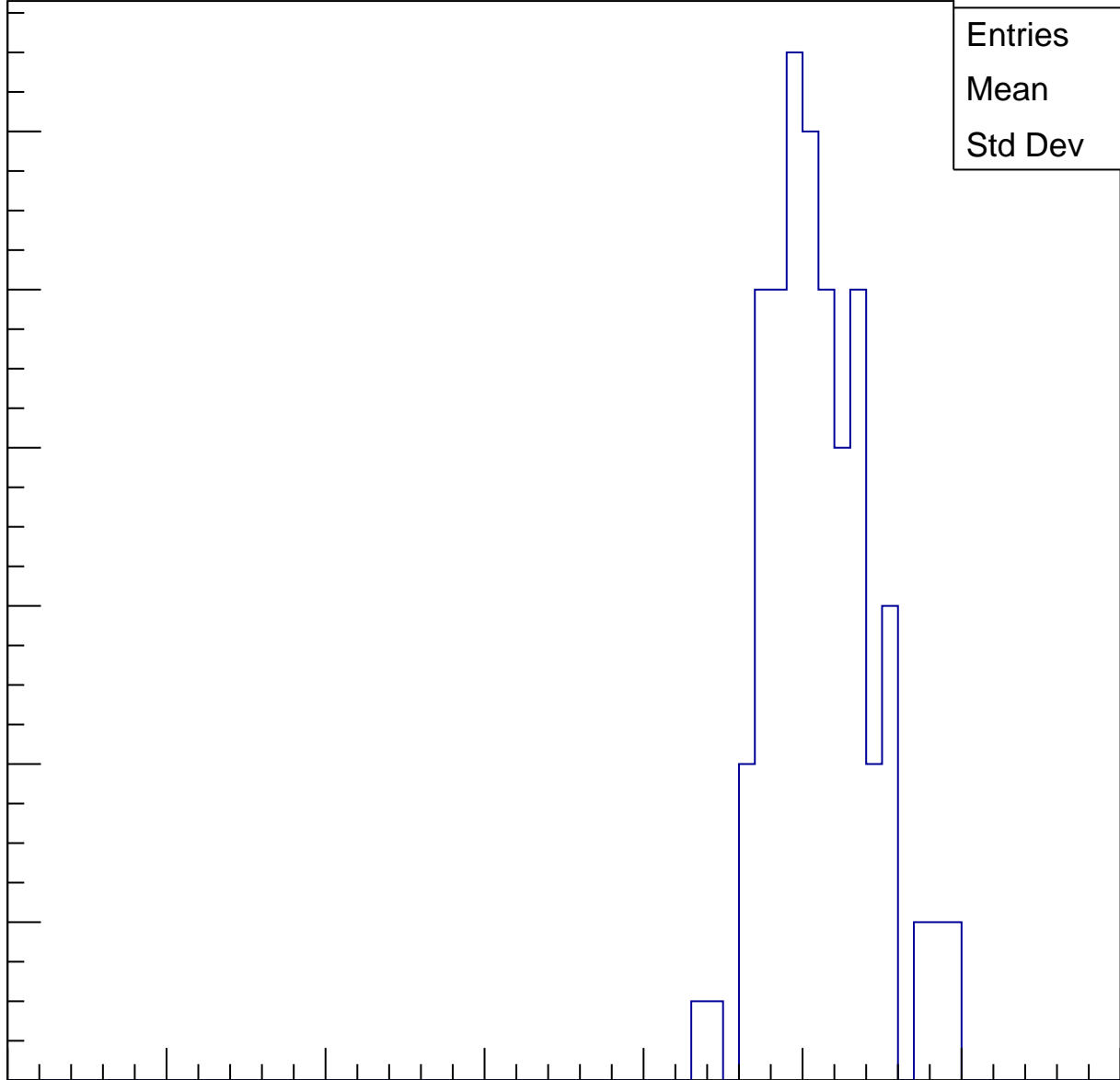
2

0

Entries	95
Mean	50.61
Std Dev	3.229

ampl

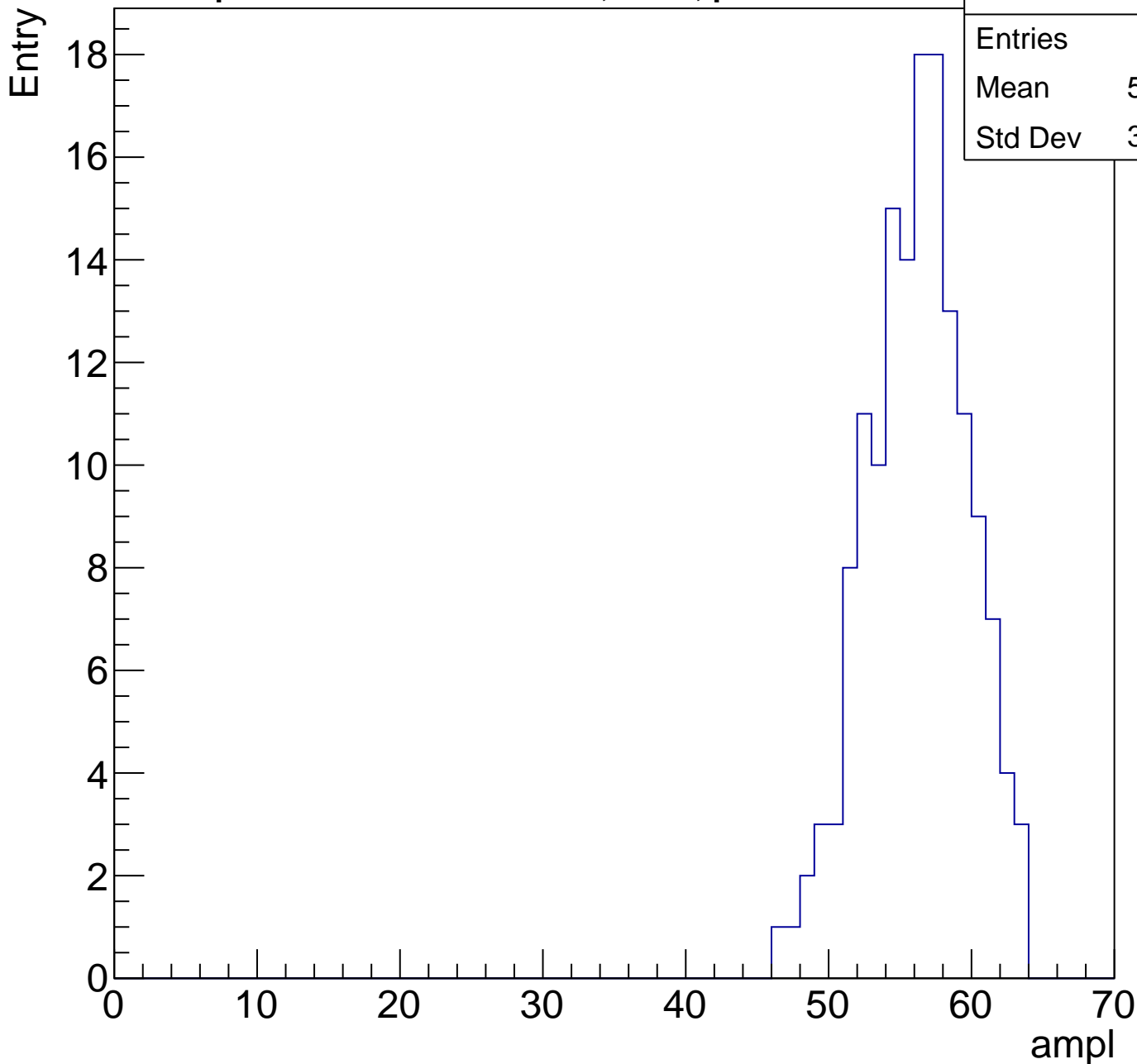
0 10 20 30 40 50 60 70



B1L001S, U21-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	55.74
Std Dev	3.522



B1L001S, U21-ch83, adc5

calib_packv5_042523_0143.root, FC#2, port C2

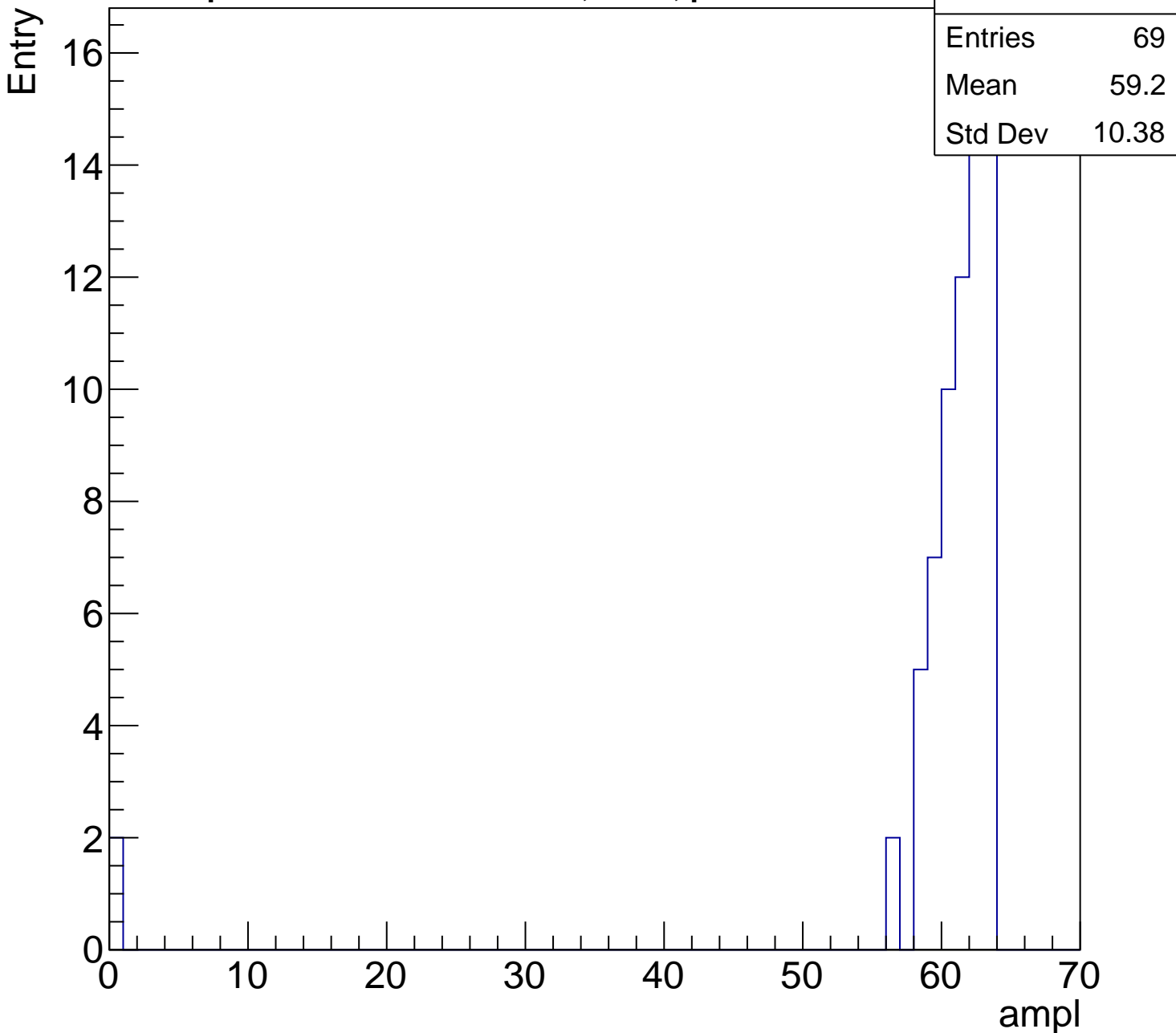
Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	59.2
Std Dev	10.38

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

61.5

Std Dev

1.118

B1L001S, U21-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U21-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	27.46
Std Dev	3.958

Gaus mean : 28.2254

Gaus Width: 3.6406

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

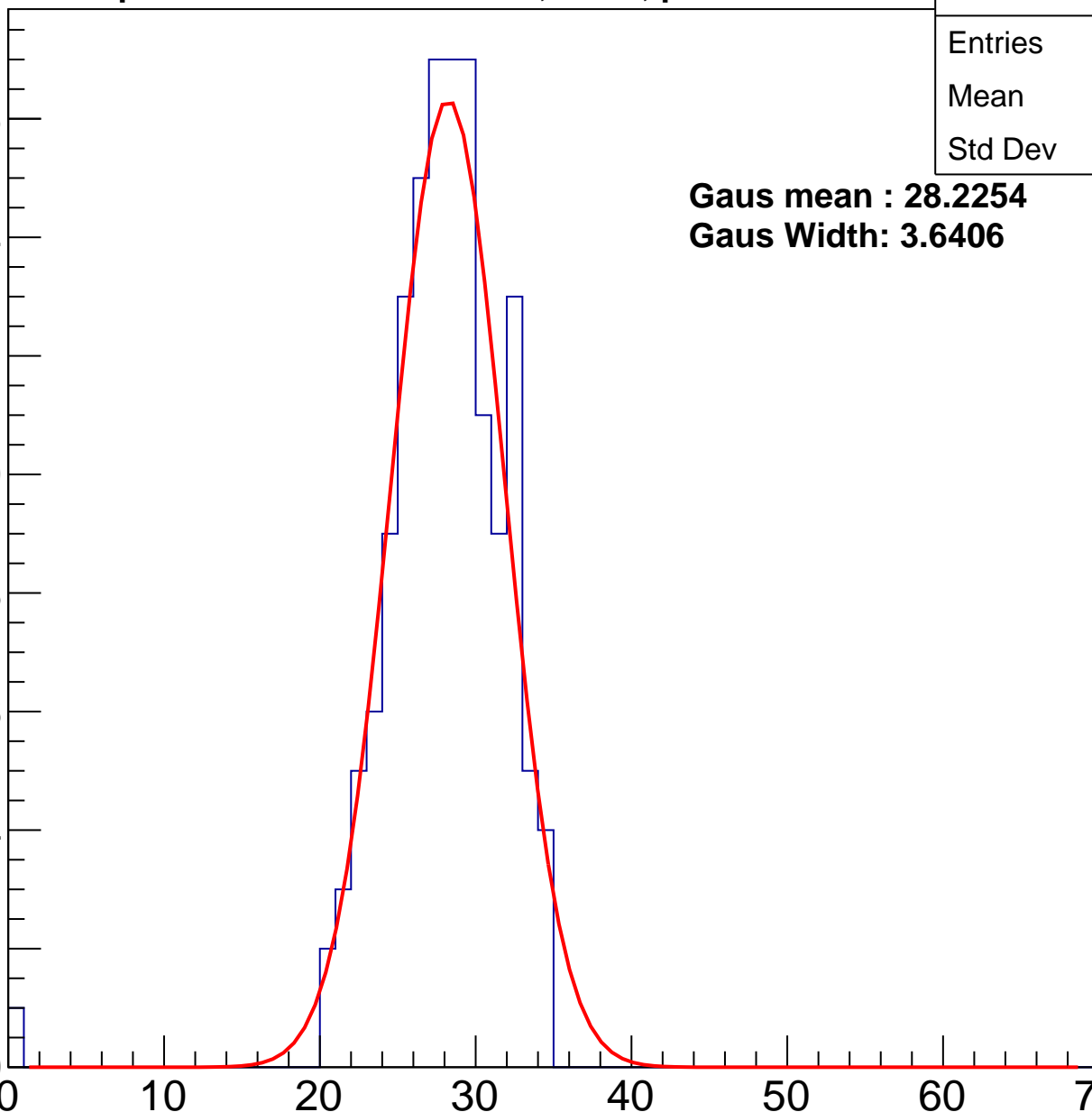
40

50

60

70

ampl



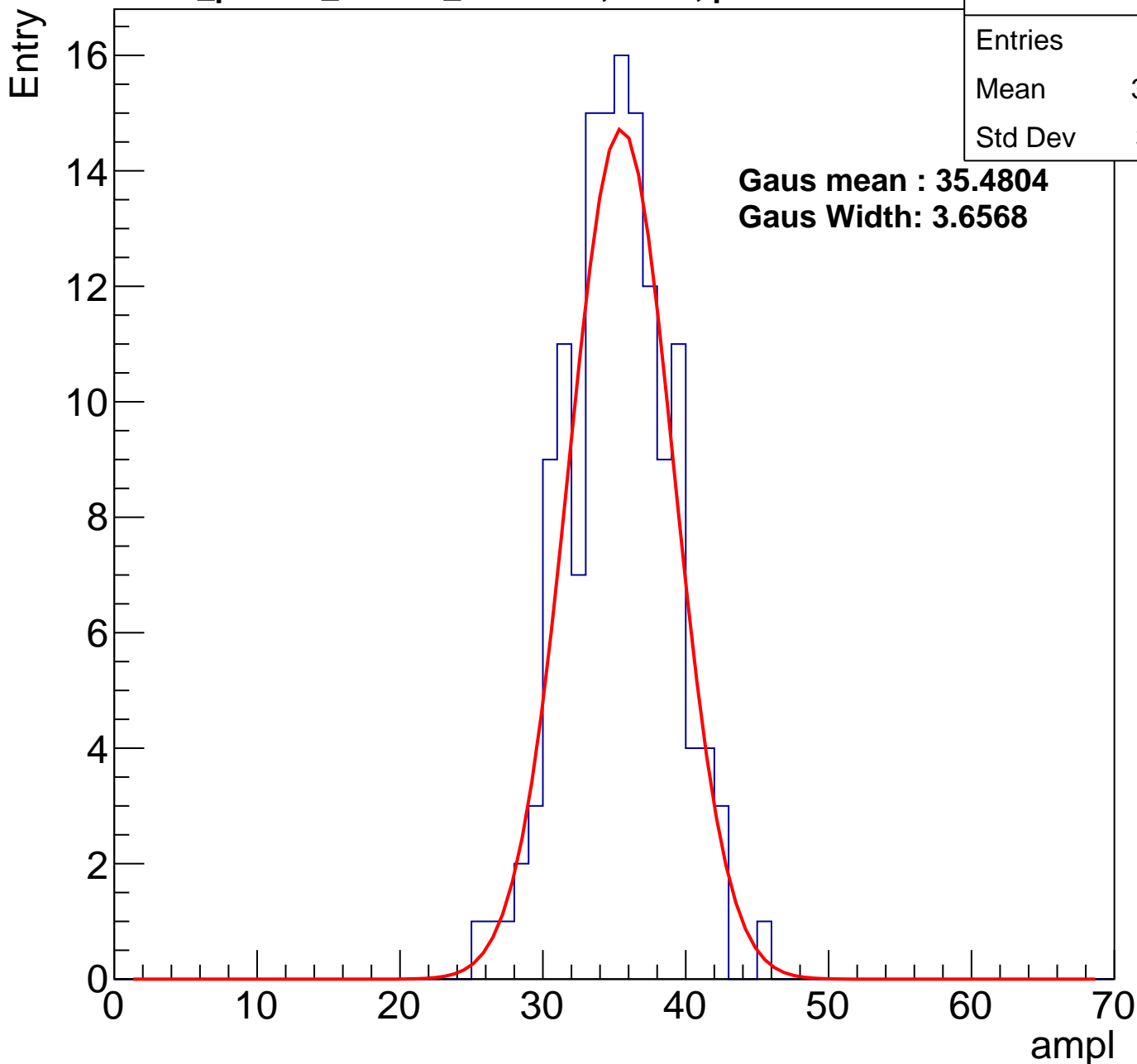
B1L001S, U21-ch84, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	34.79
Std Dev	3.581

Gaus mean : 35.4804

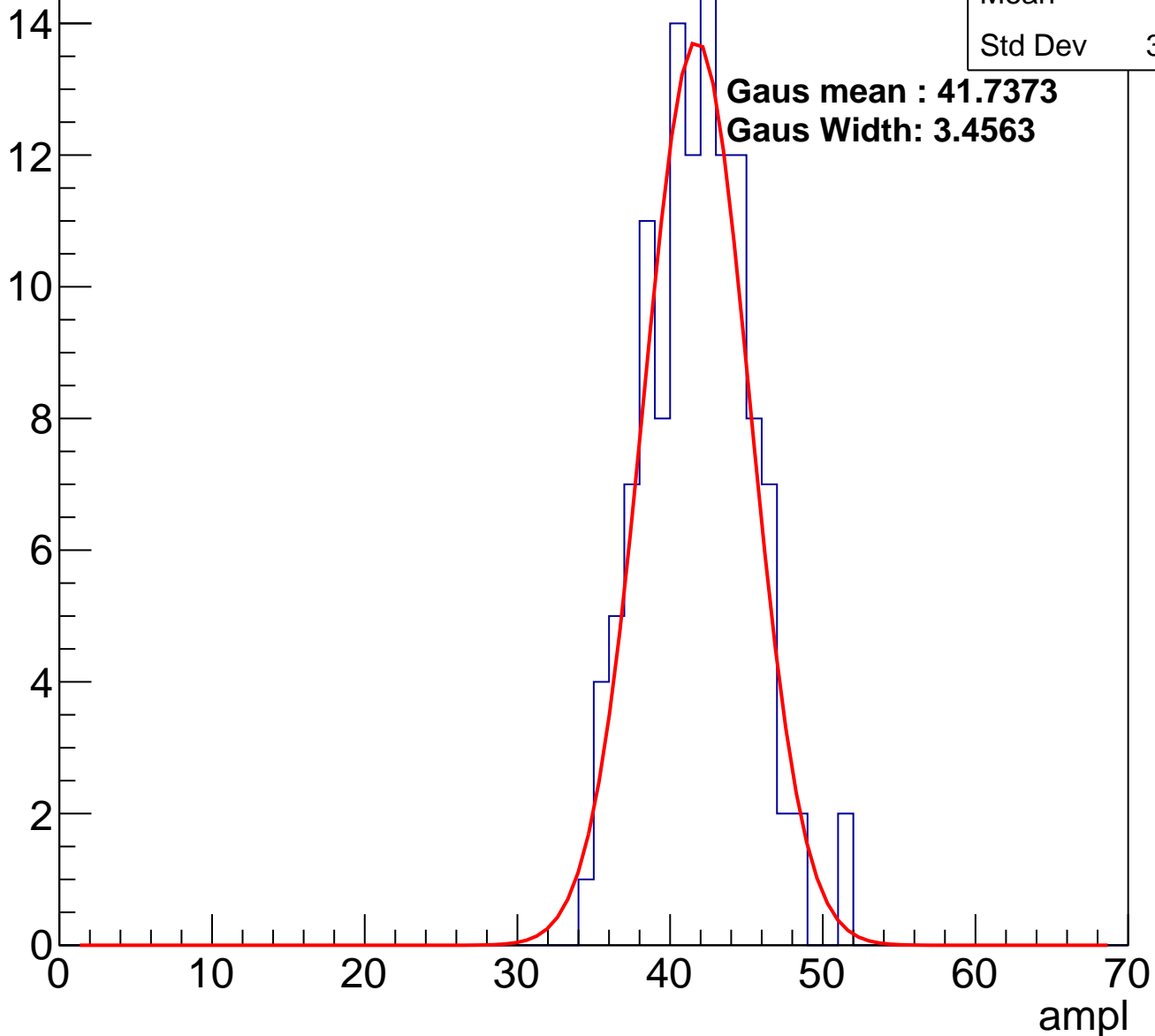
Gaus Width: 3.6568



B1L001S, U21-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

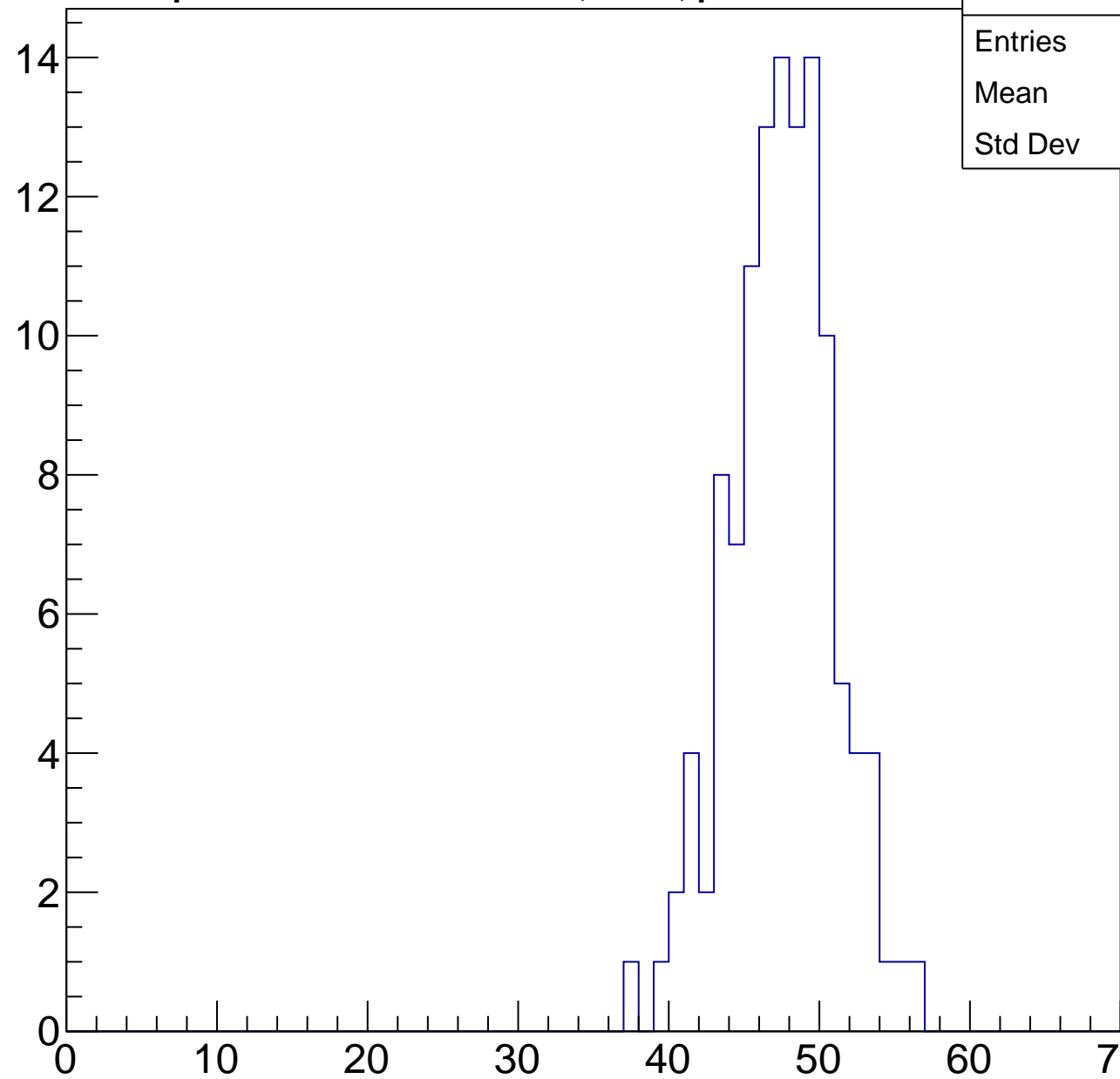
Entries	116
Mean	47.04
Std Dev	3.45

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

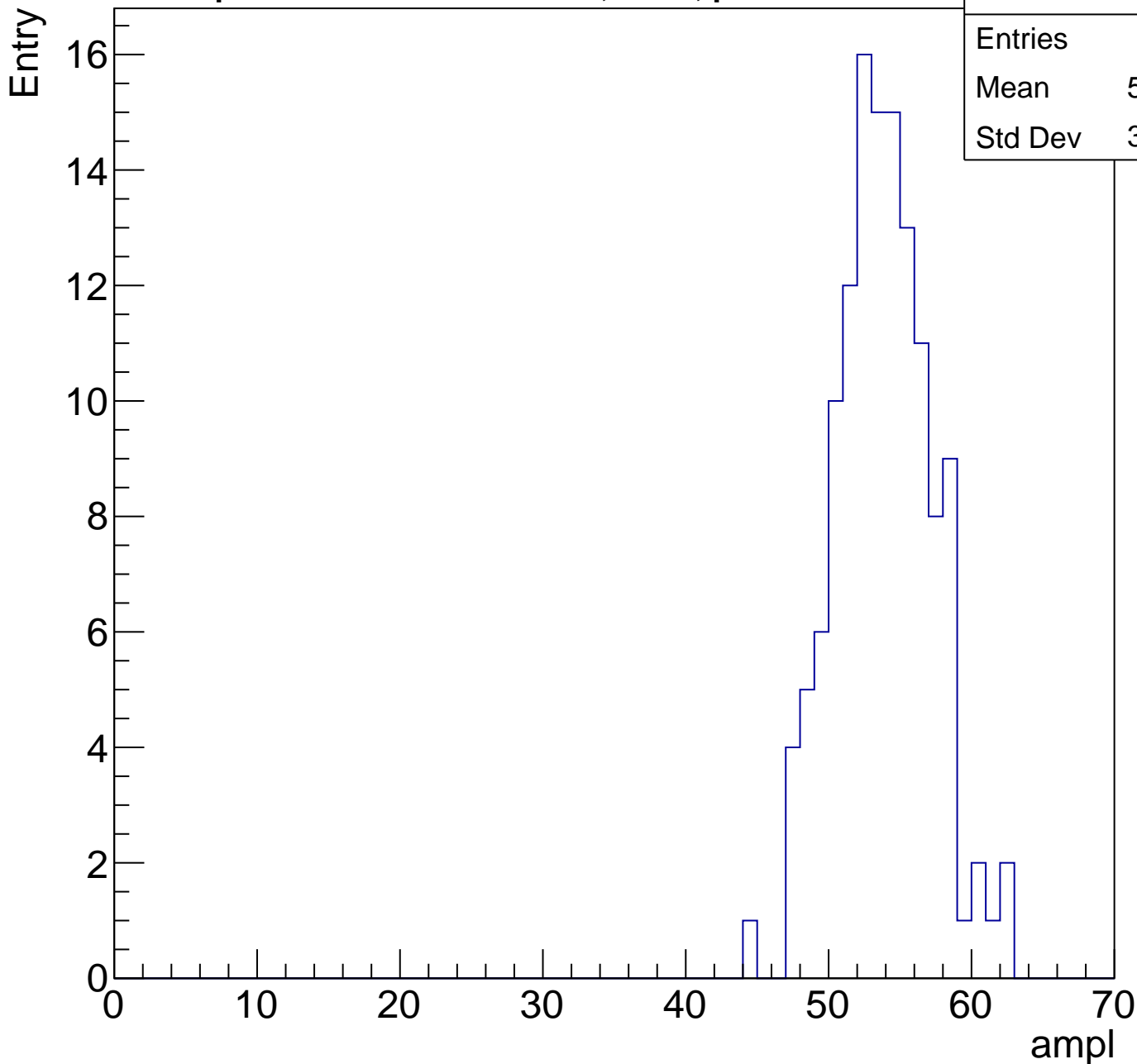
ampl



B1L001S, U21-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	53.34
Std Dev	3.342



B1L001S, U21-ch84, adc5

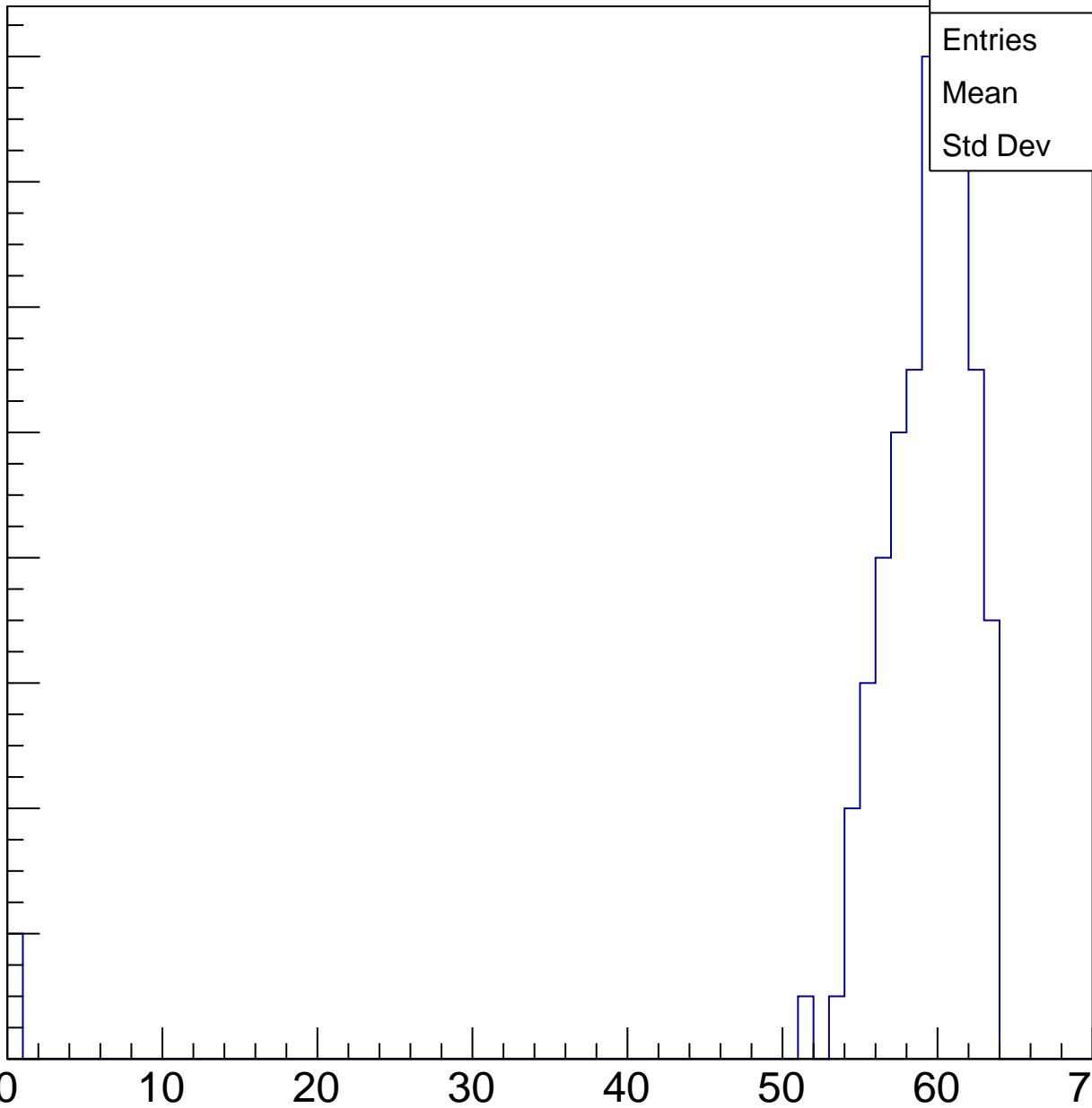
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	57.83
Std Dev	8.384

ampl



B1L001S, U21-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

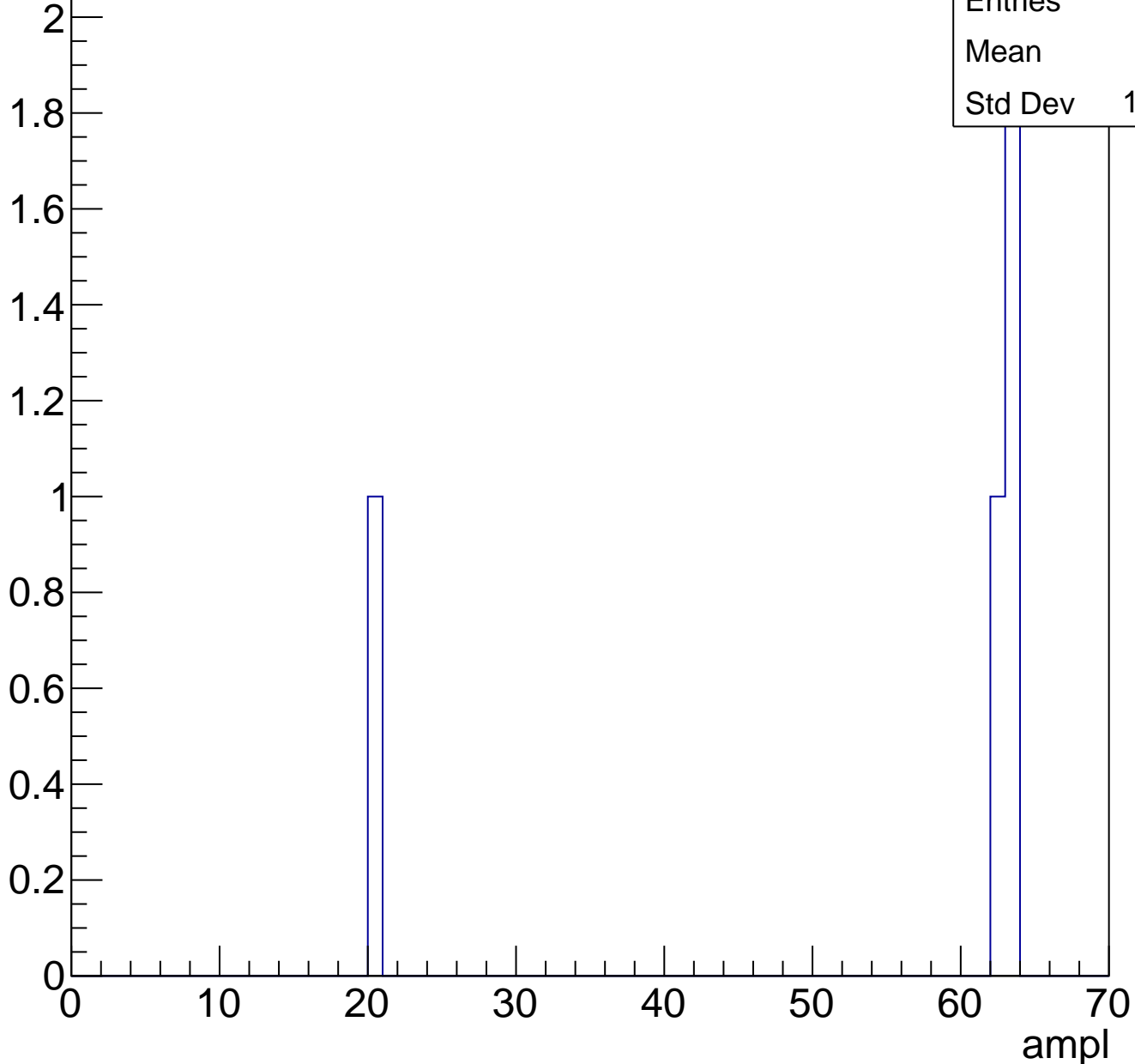
ampl

Entries	29
Mean	61.38
Std Dev	1.789

B1L001S, U21-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	52
Std Dev	18.48

B1L001S, U21-ch85, adc0

calib_packv5_042523_0143.root, FC#2, port C2

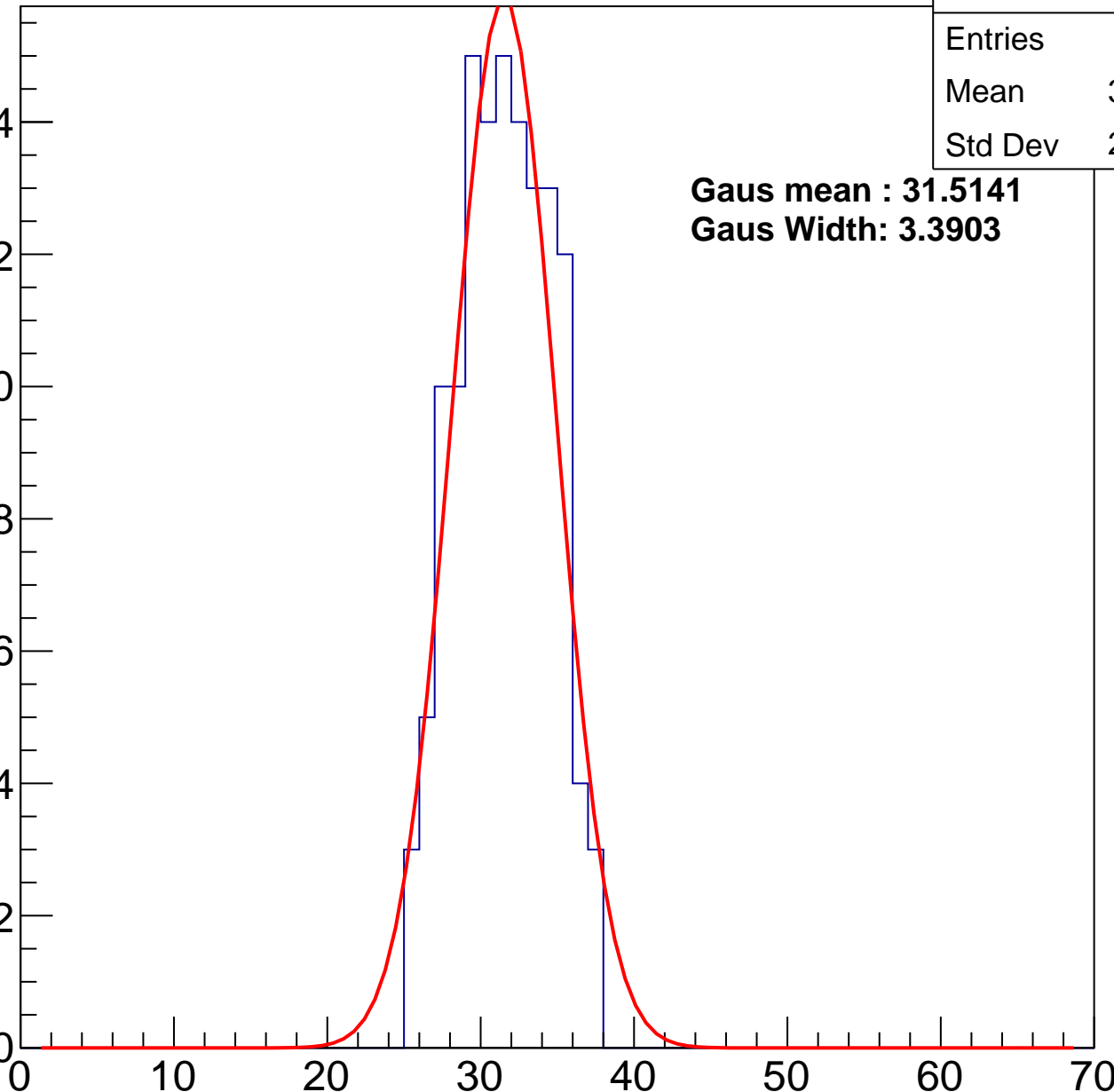
Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	31.06
Std Dev	2.949

Gaus mean : 31.5141
Gaus Width: 3.3903

ampl

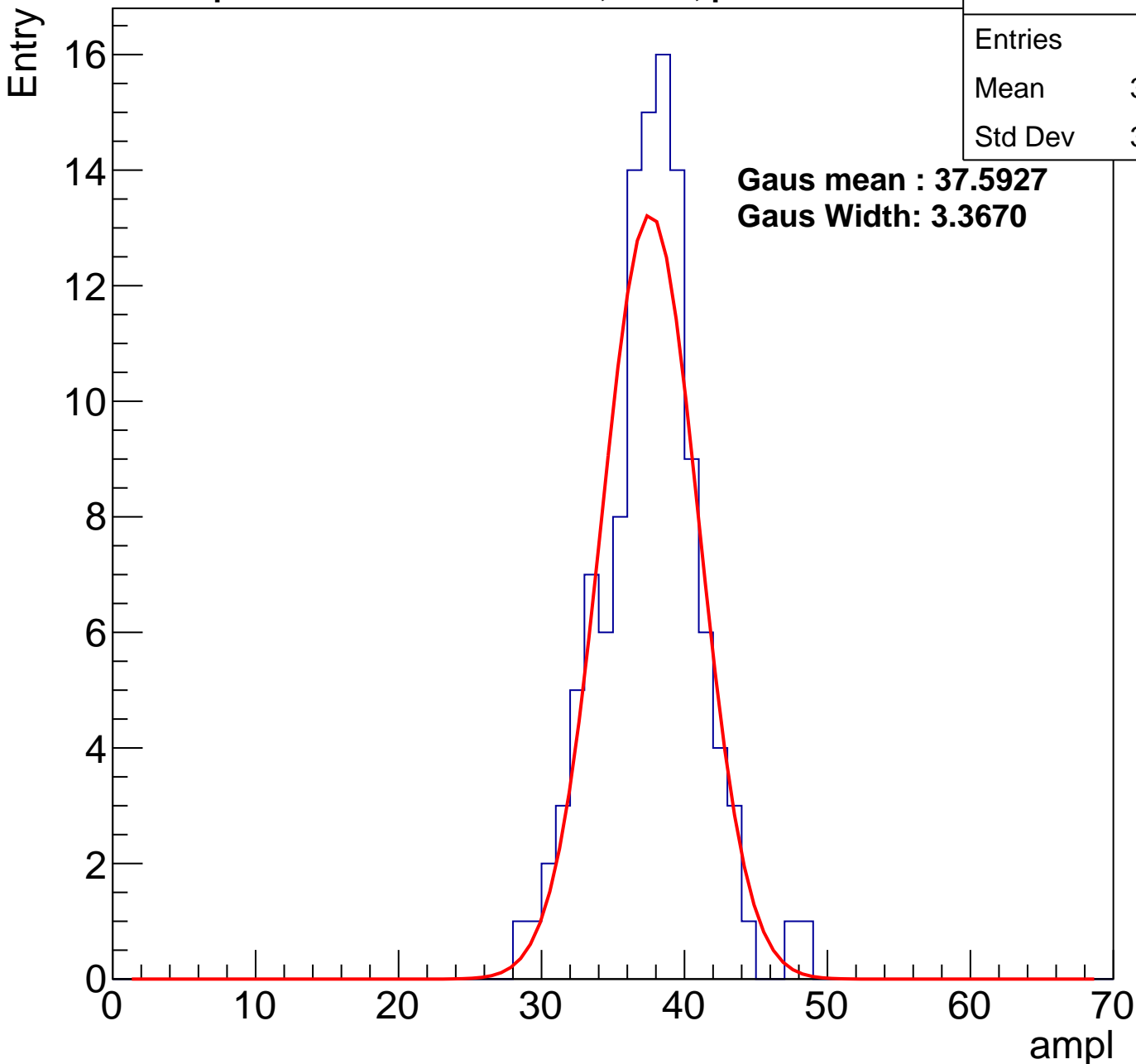


B1L001S, U21-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	37.09
Std Dev	3.476

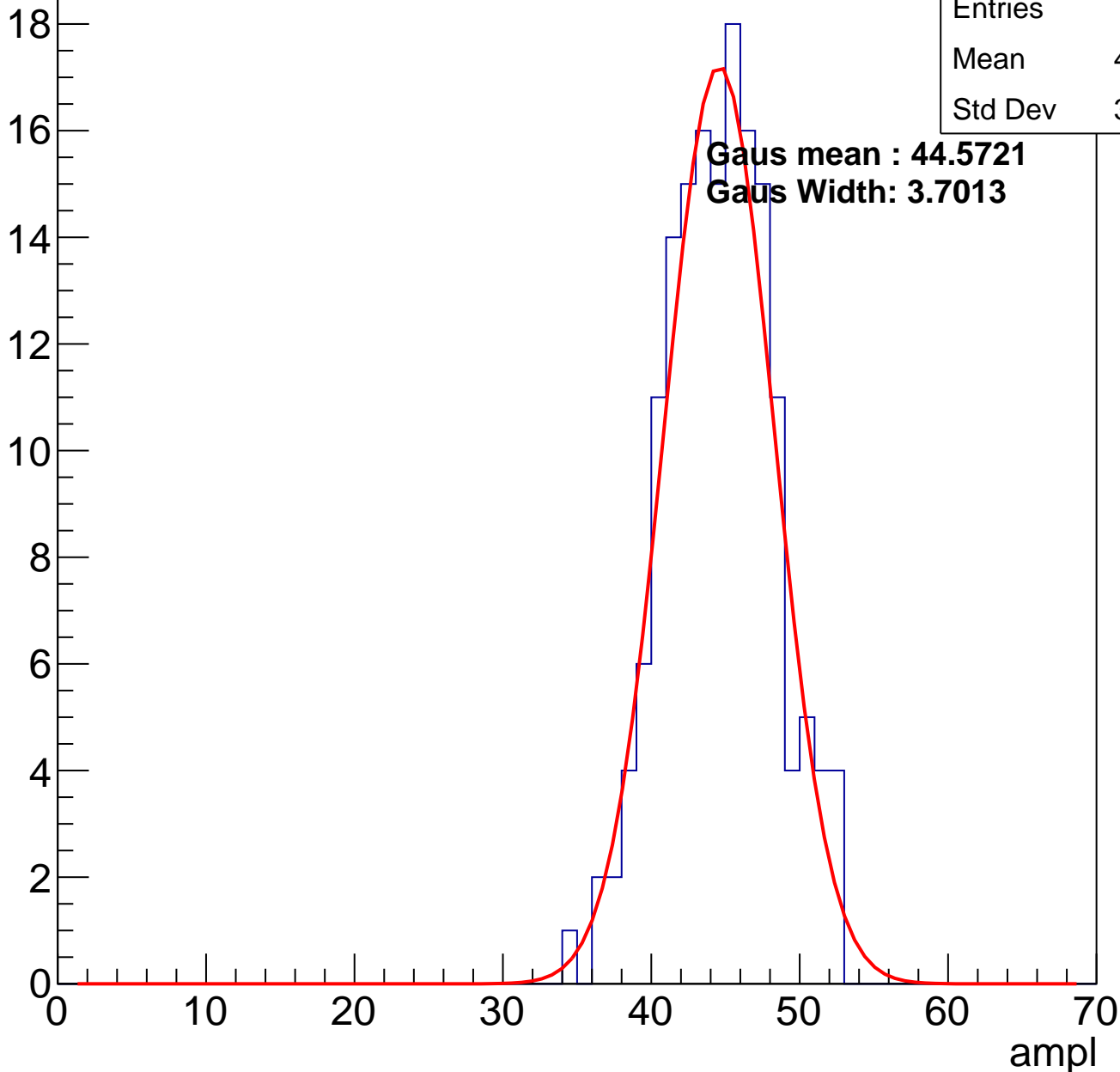
Gaus mean : 37.5927
Gaus Width: 3.3670



B1L001S, U21-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

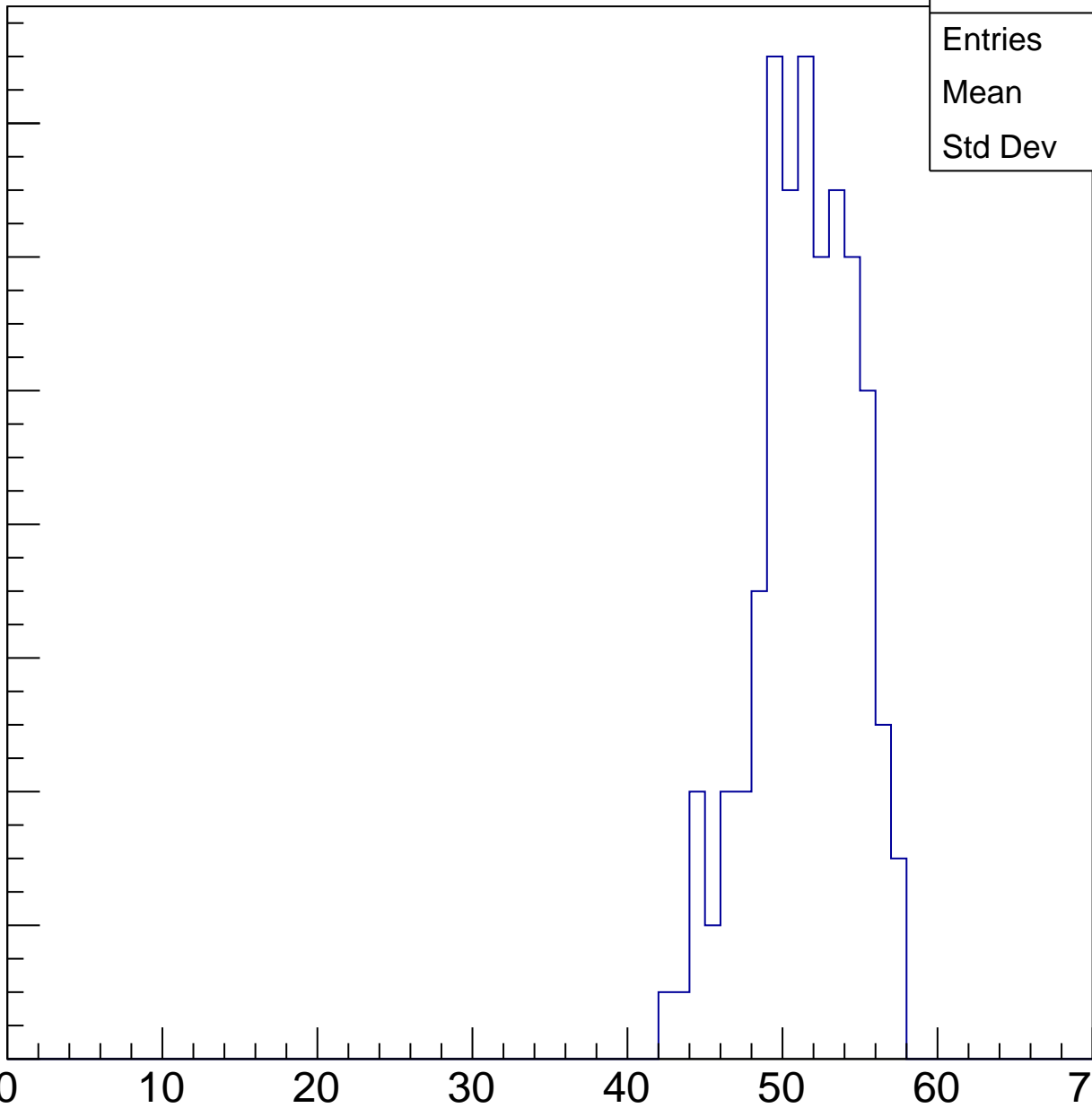
Entries	121
Mean	51
Std Dev	3.25

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

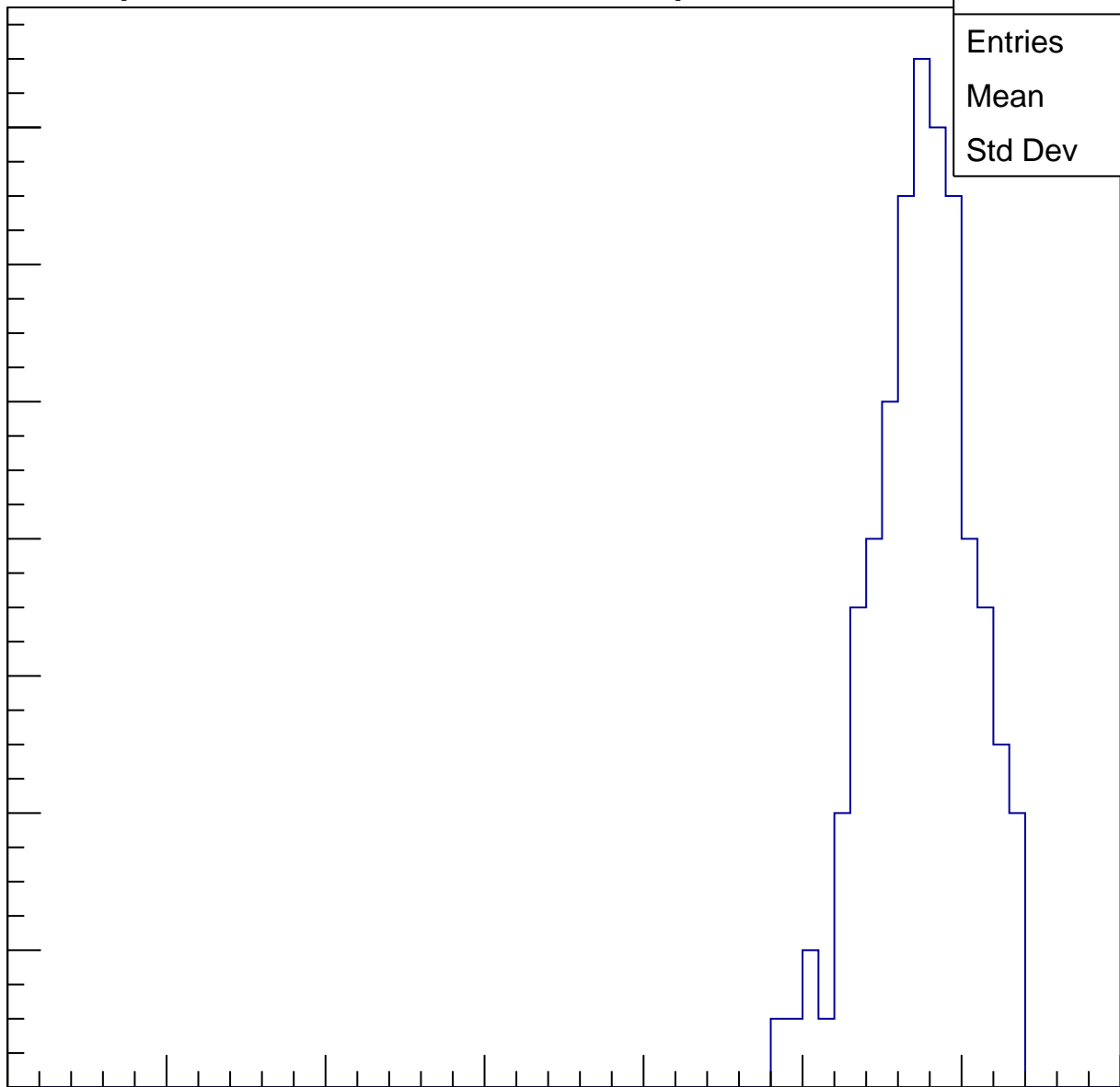
Entries	113
Mean	56.99
Std Dev	3.169

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch85, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

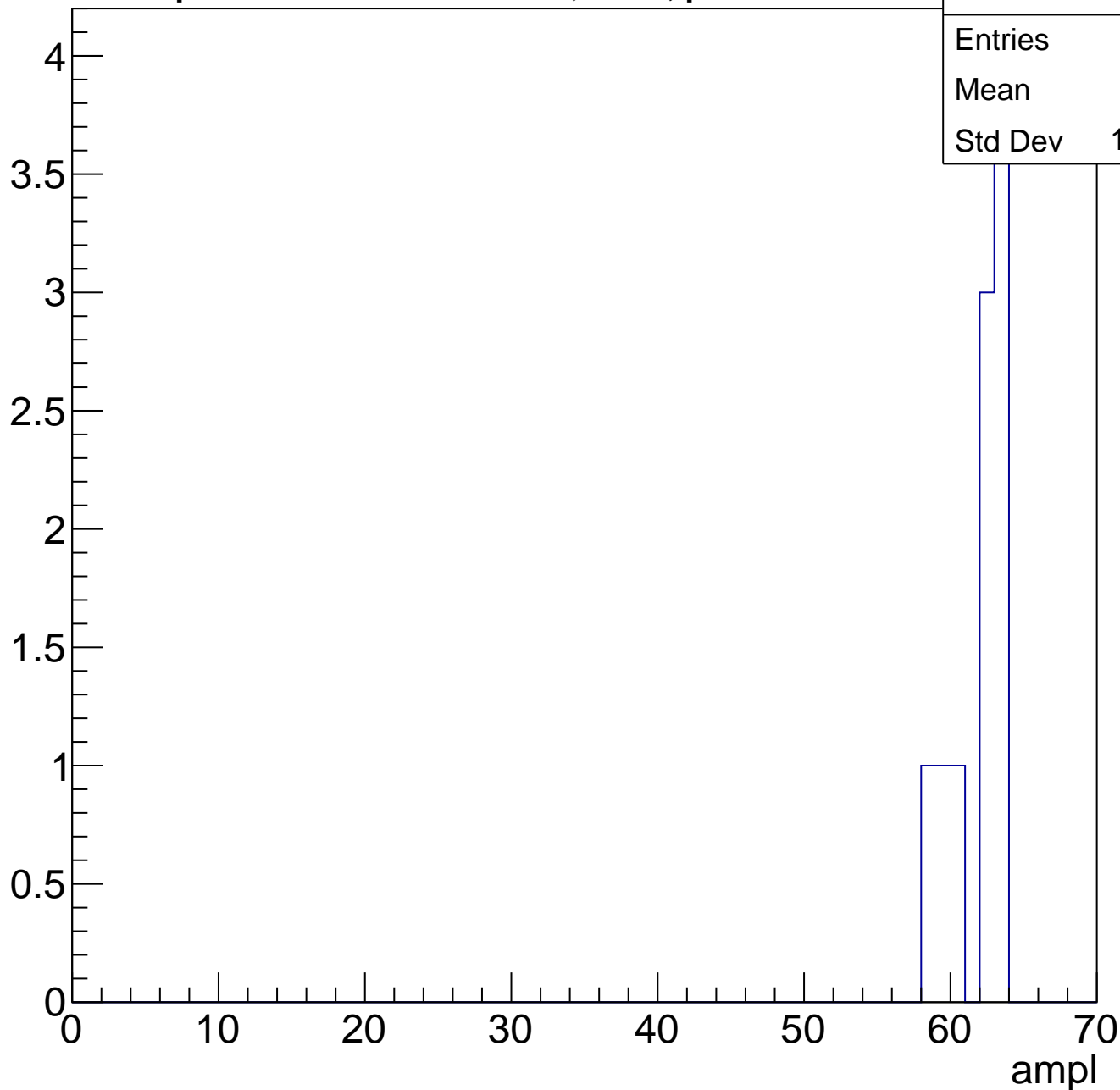
Entries	65
Mean	58.82
Std Dev	10.64

0

B1L001S, U21-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

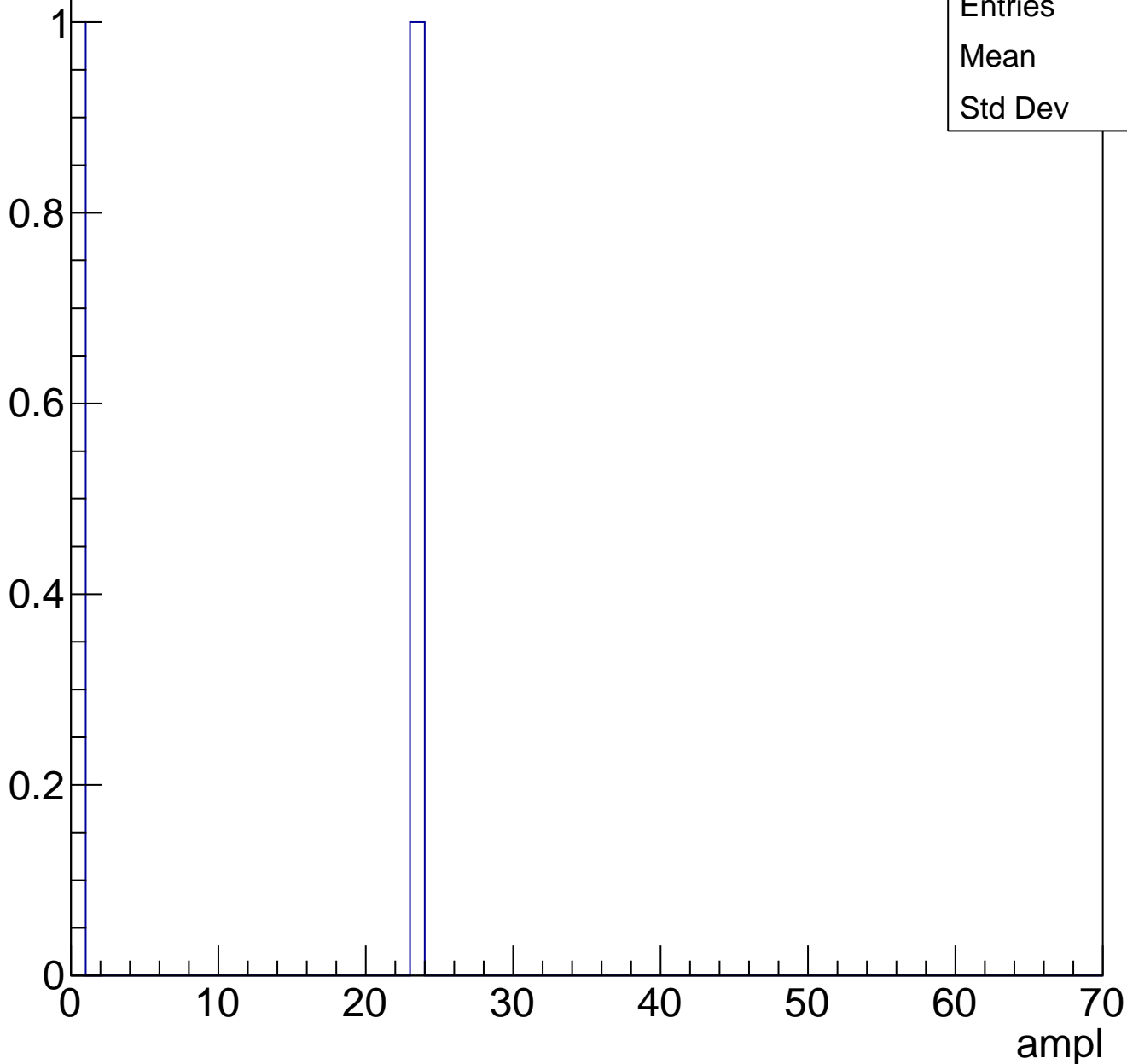
Entry



B1L001S, U21-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.3
Std Dev	3.246

Gaus mean : 29.7486

Gaus Width: 3.0489

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

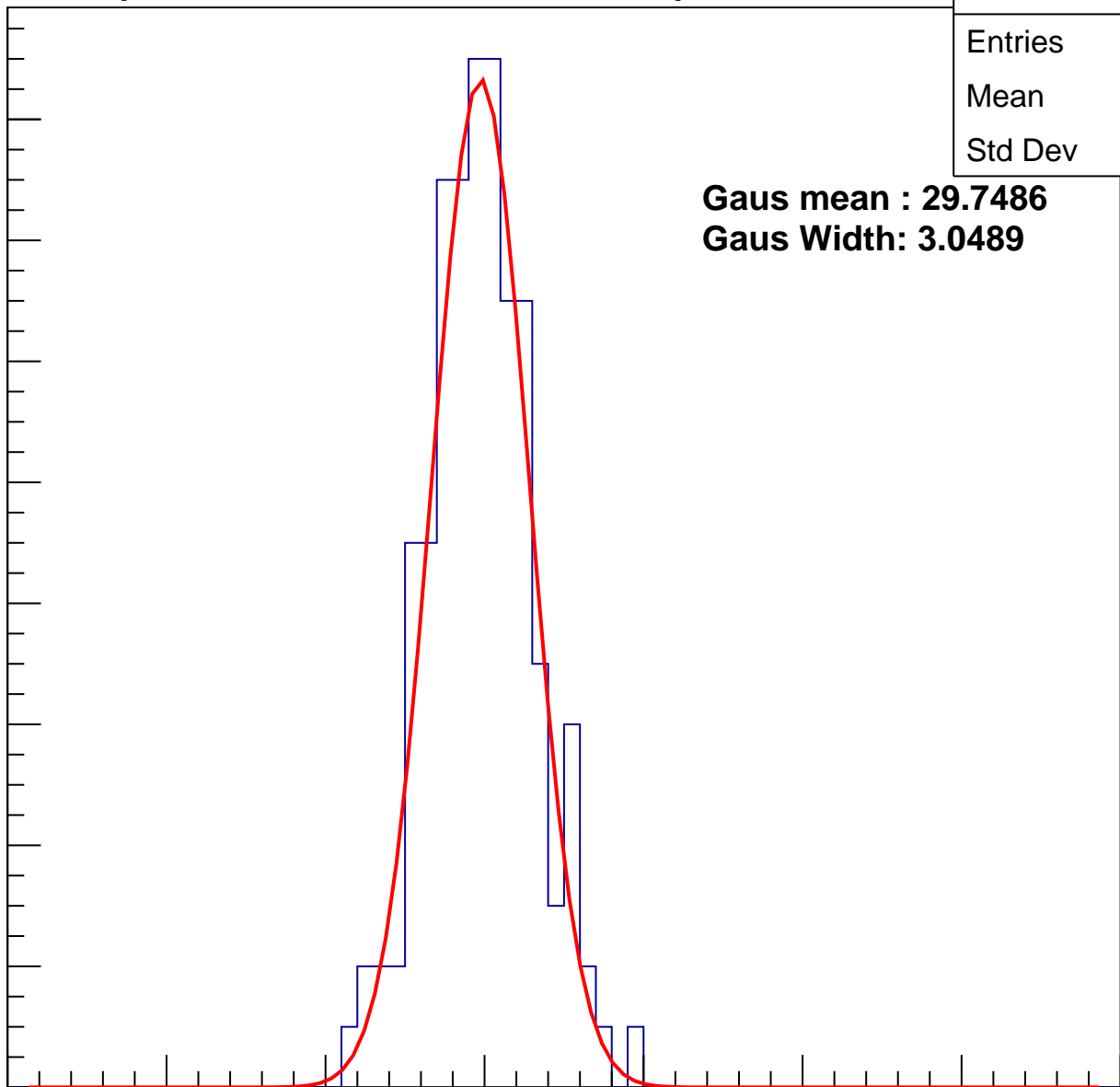
40

50

60

70

ampl



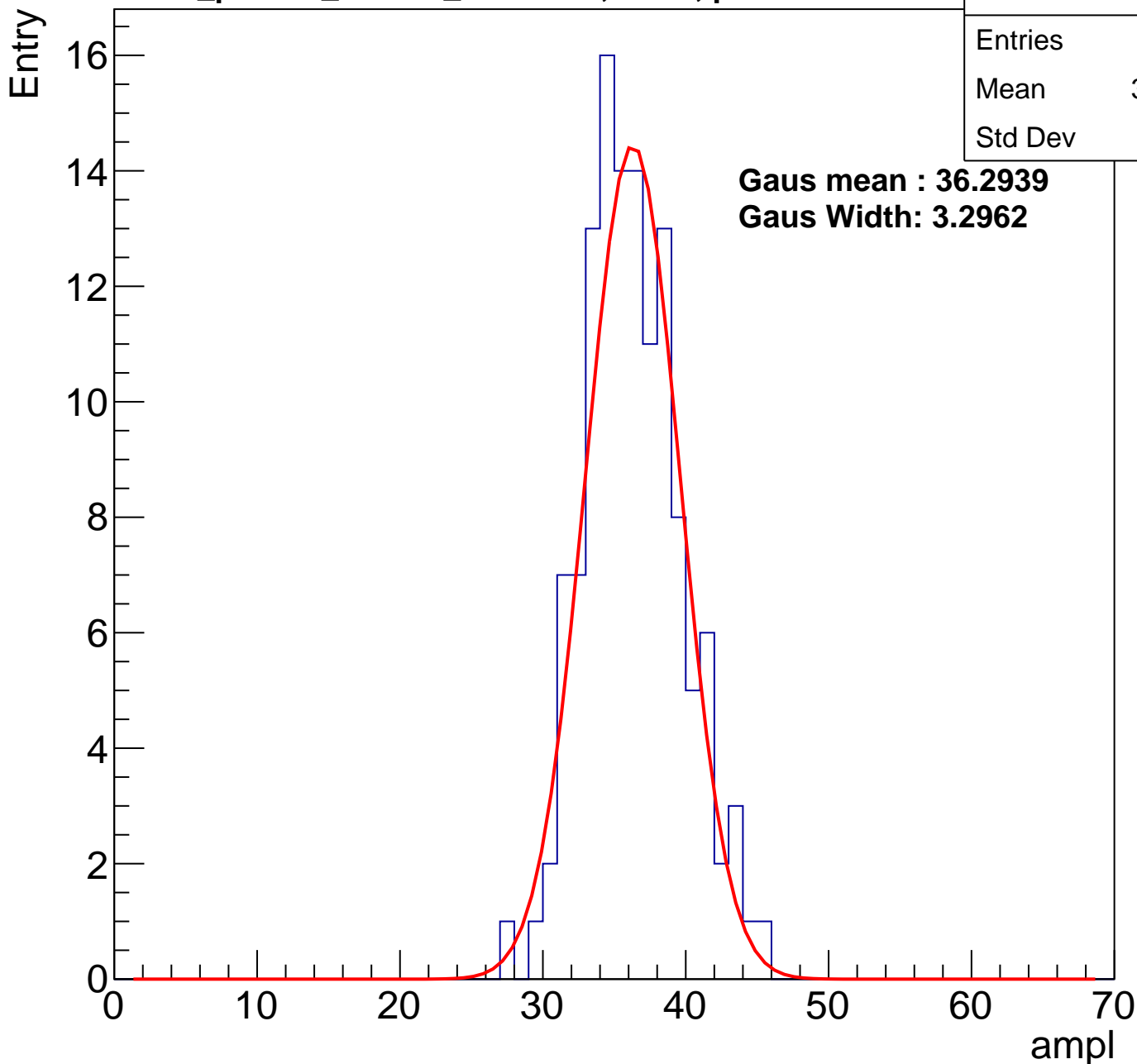
B1L001S, U21-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	35.88
Std Dev	3.36

Gaus mean : 36.2939

Gaus Width: 3.2962



B1L001S, U21-ch86, adc2

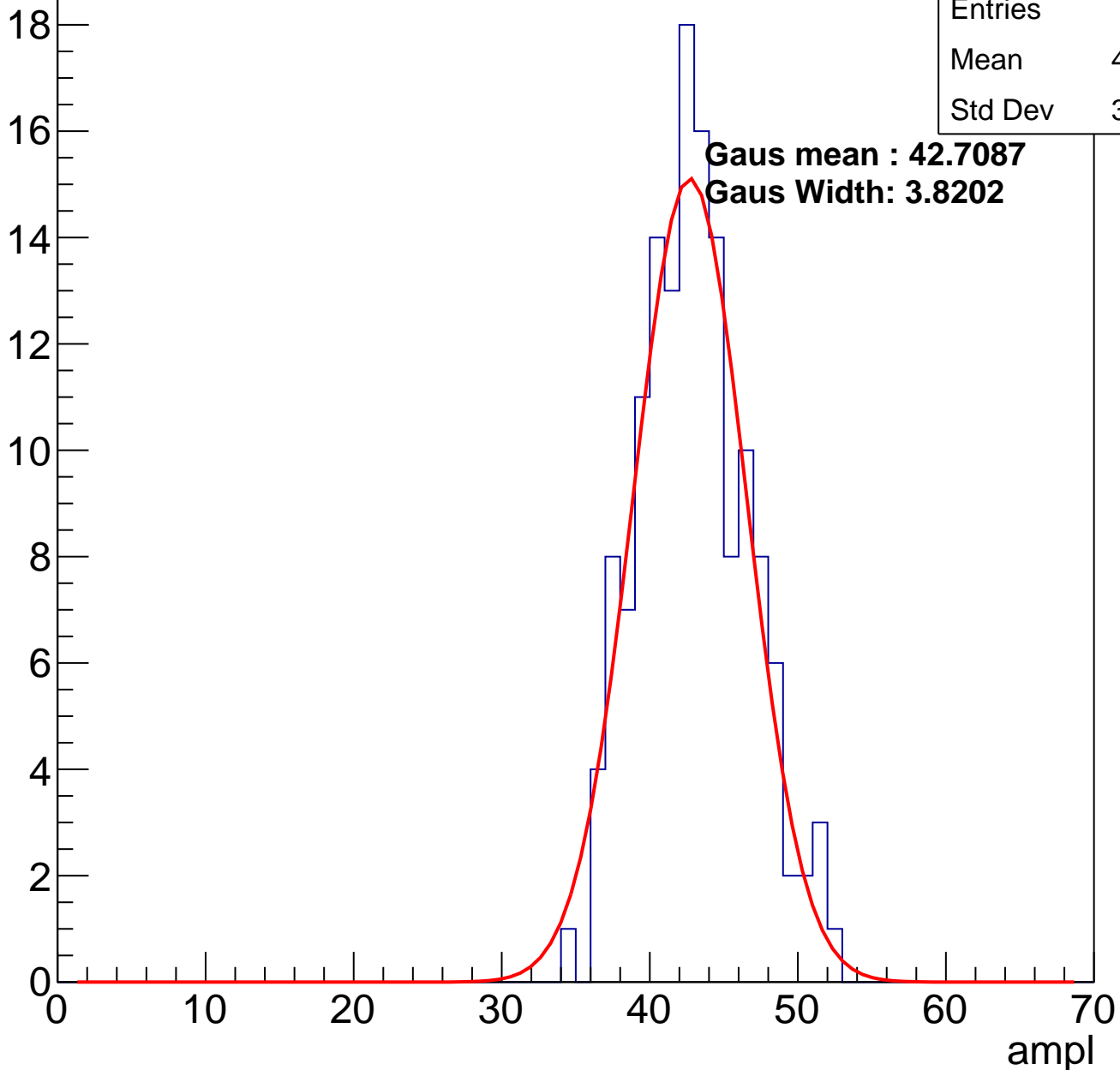
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	42.53
Std Dev	3.642

Gaus mean : 42.7087

Gaus Width: 3.8202

Entry

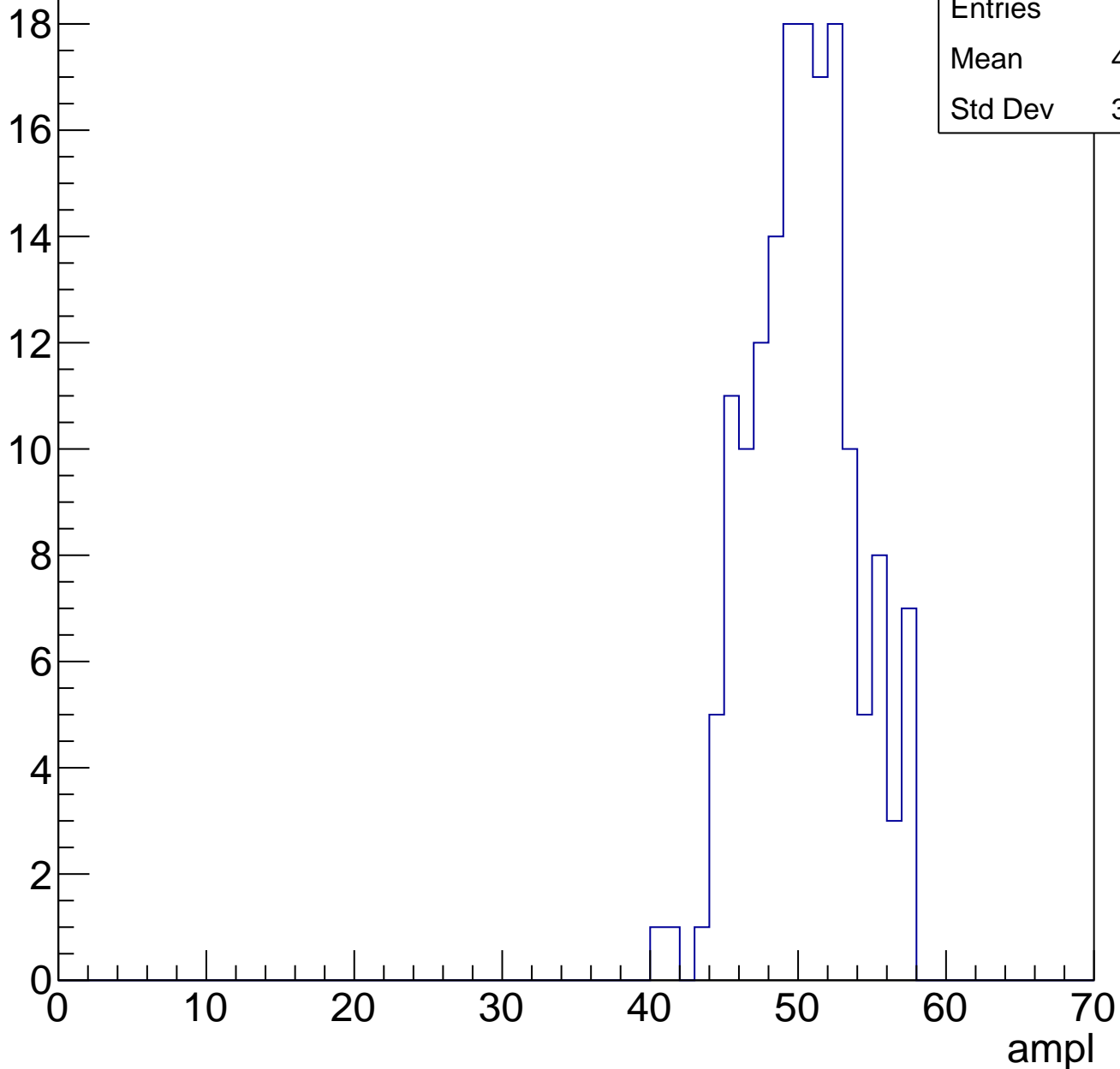


B1L001S, U21-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	49.86
Std Dev	3.487

Entry

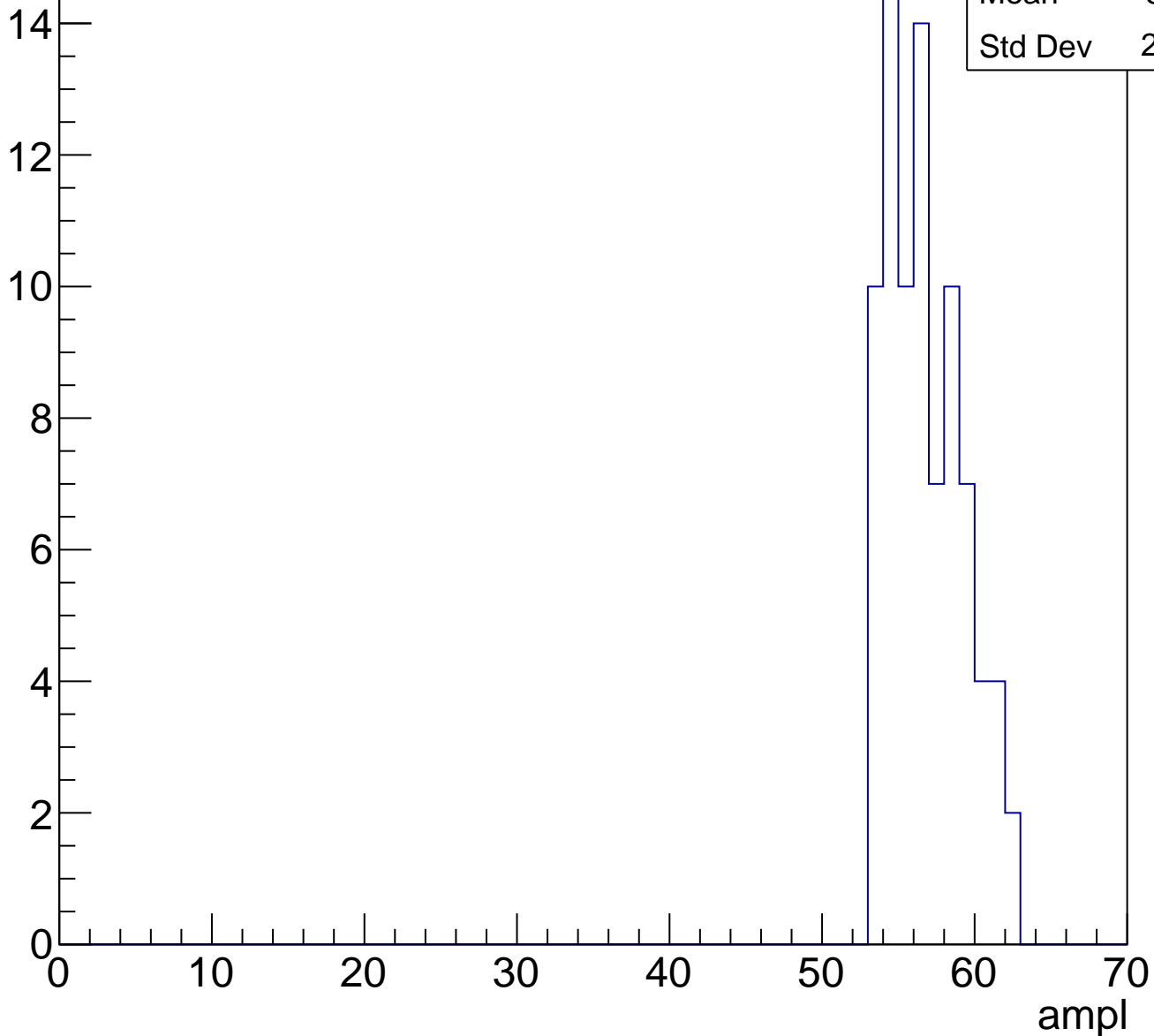


B1L001S, U21-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	83
Mean	56.31
Std Dev	2.449

Entry



B1L001S, U21-ch86, adc5

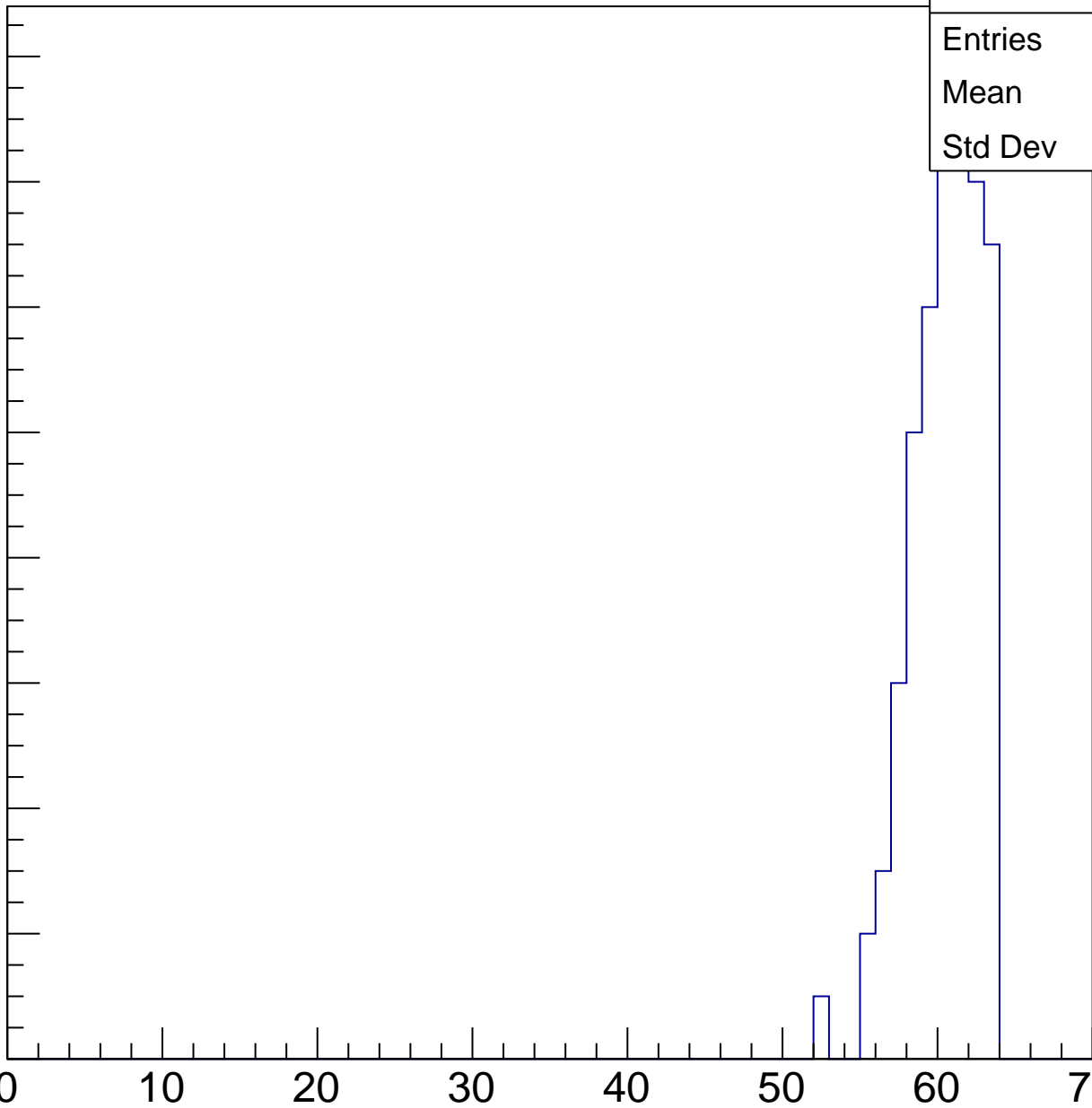
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	60.02
Std Dev	2.226

ampl

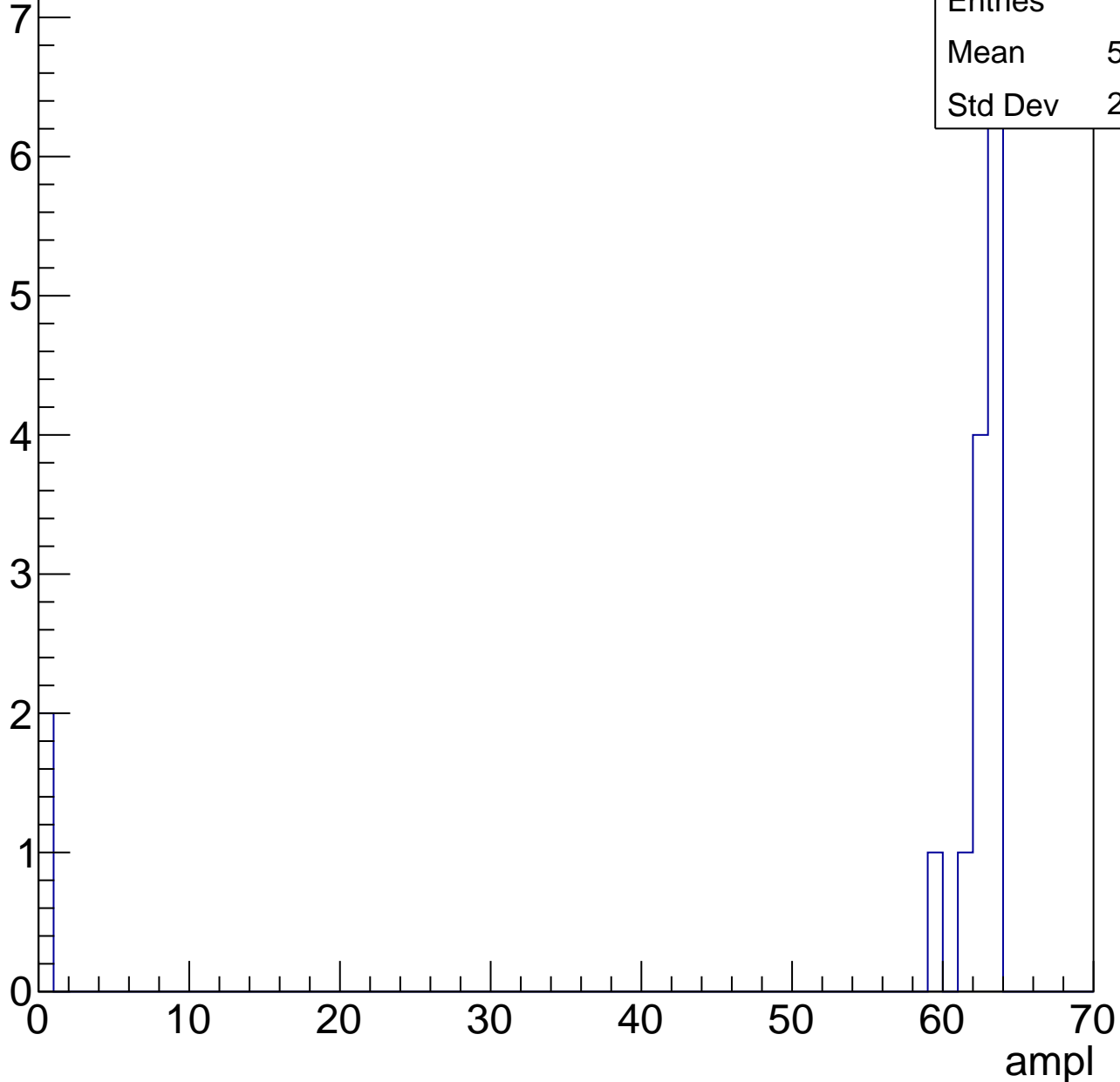


B1L001S, U21-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	53.93
Std Dev	21.18



B1L001S, U21-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L001S, U21-ch87, adc0

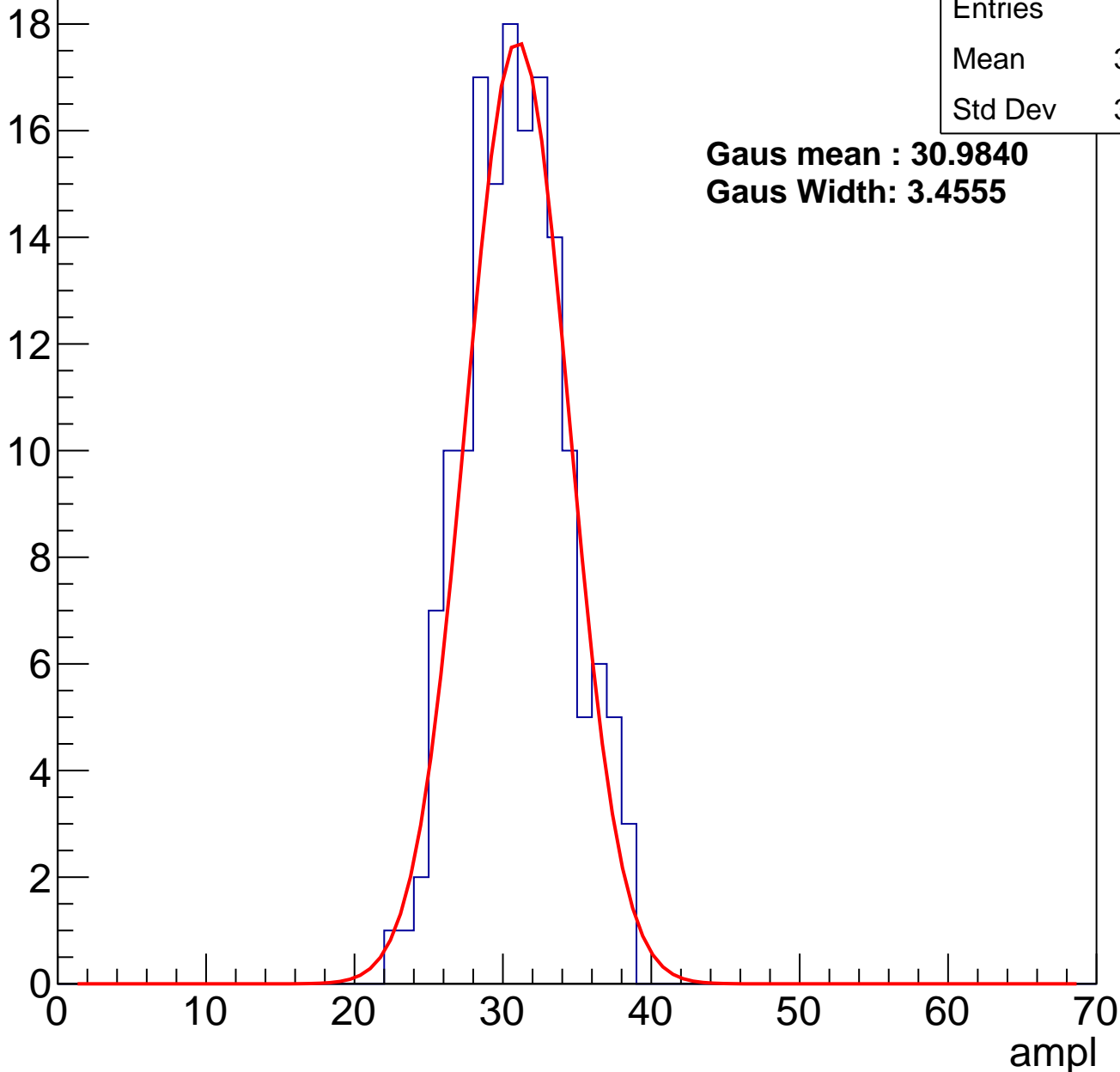
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	30.45
Std Dev	3.394

Gaus mean : 30.9840

Gaus Width: 3.4555

Entry

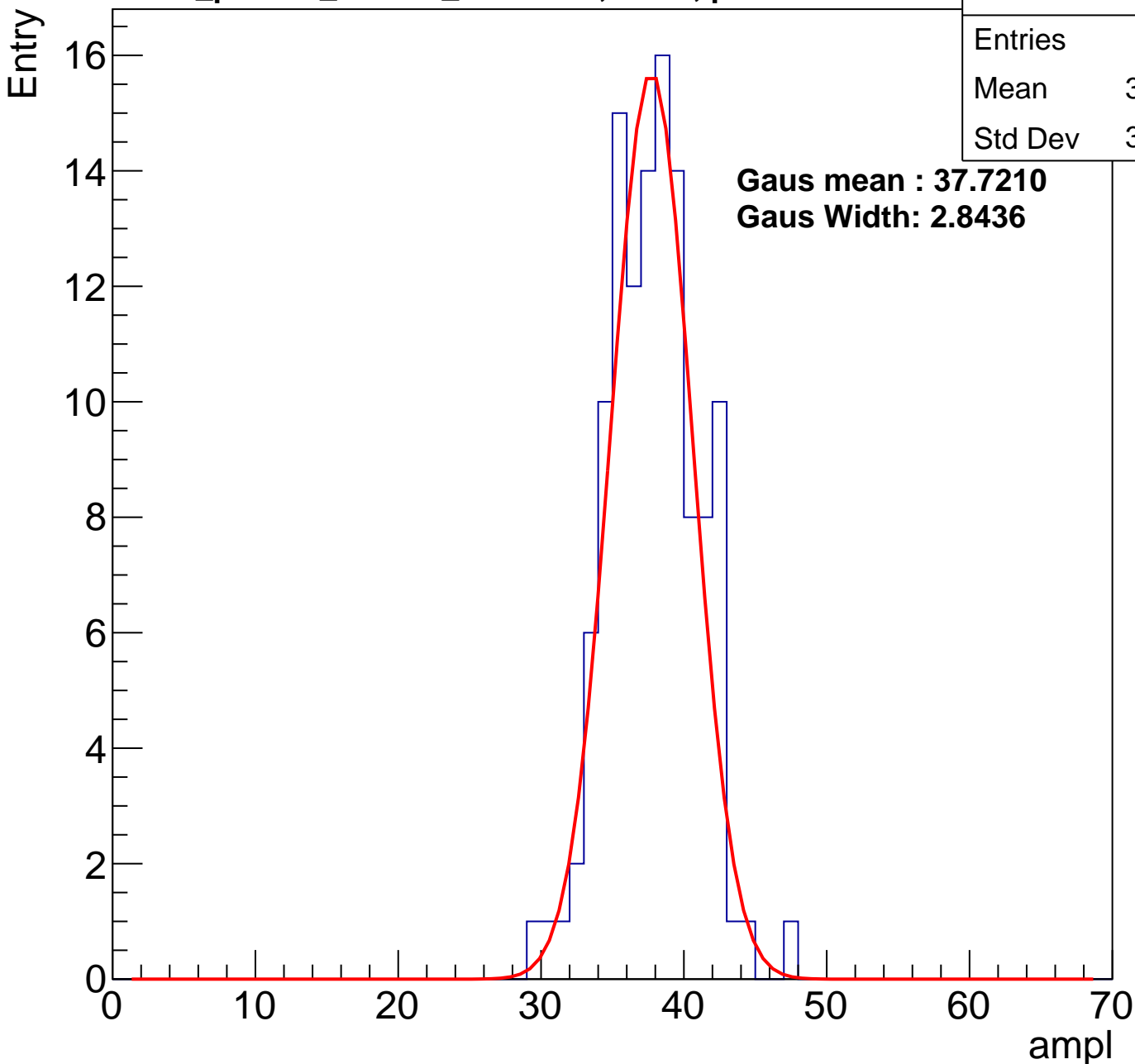


B1L001S, U21-ch87, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	37.38
Std Dev	3.078

Gaus mean : 37.7210
Gaus Width: 2.8436



B1L001S, U21-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

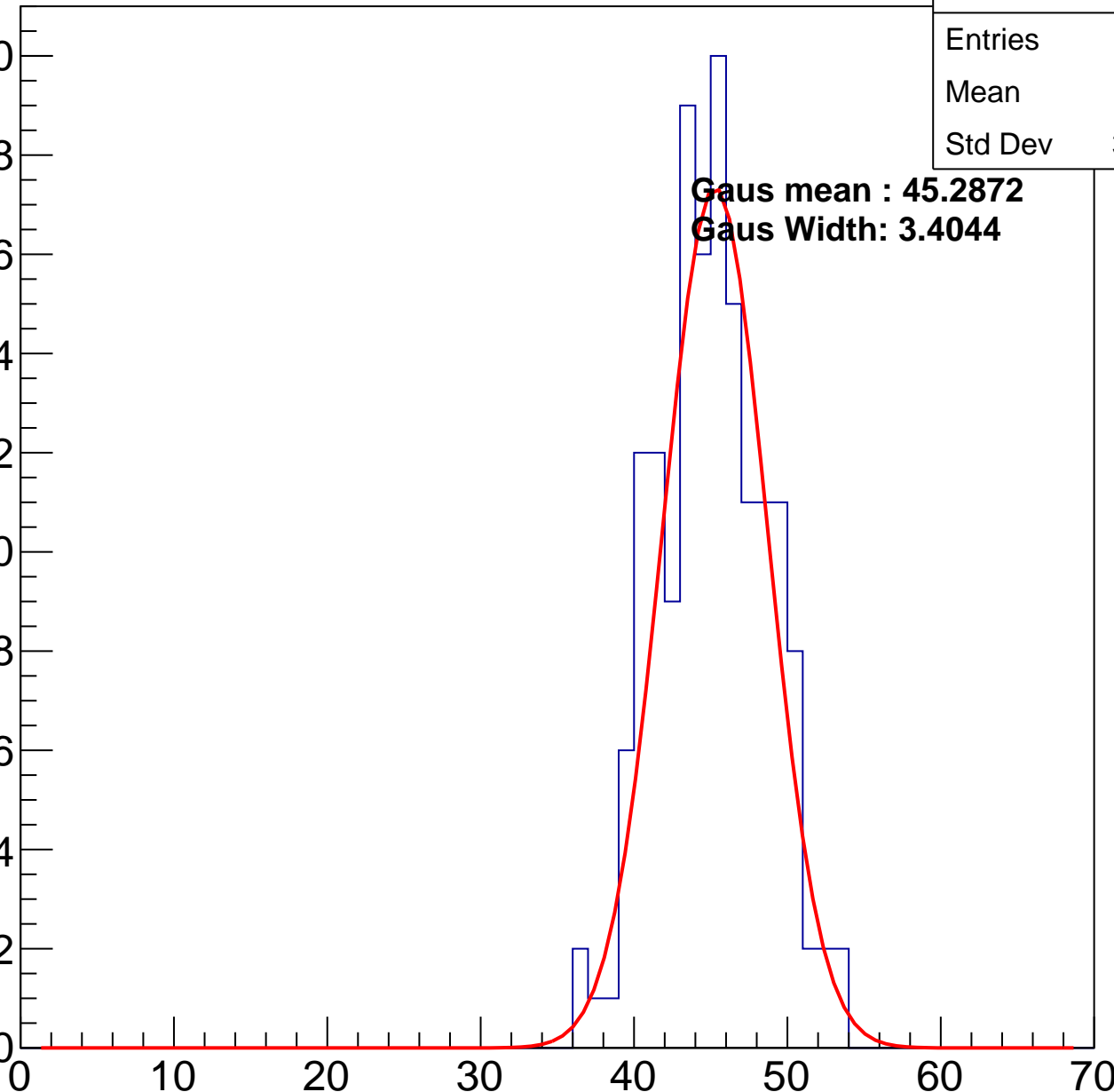
20
18
16
14
12
10
8
6
4
2
0

Entries	160
Mean	44.61
Std Dev	3.504

Gaus mean : 45.2872

Gaus Width: 3.4044

ampl



B1L001S, U21-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	50.41
Std Dev	3.045

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

ampl

B1L001S, U21-ch87, adc4

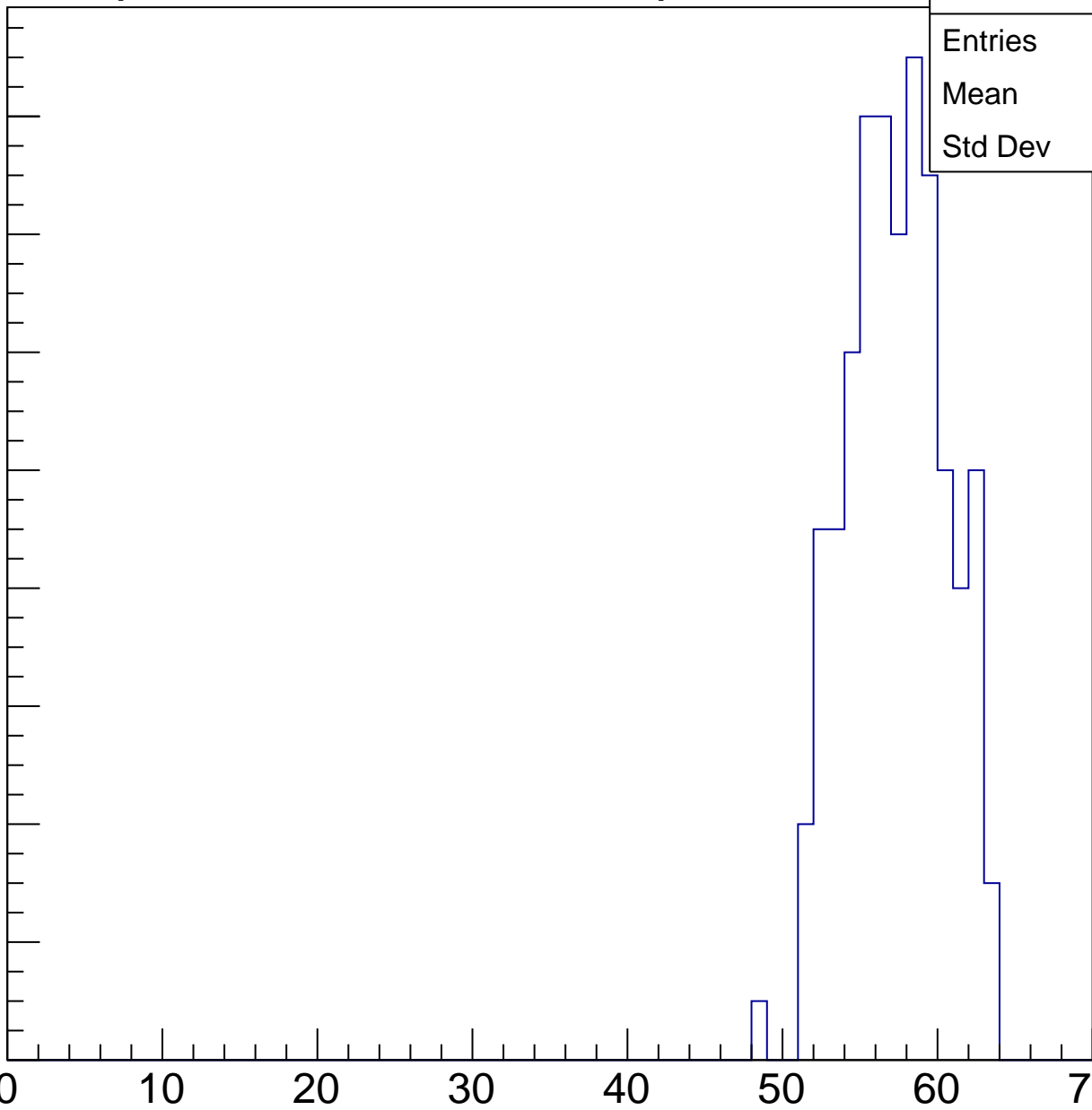
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	56.85
Std Dev	3.153

Entry

16
14
12
10
8
6
4
2
0

ampl

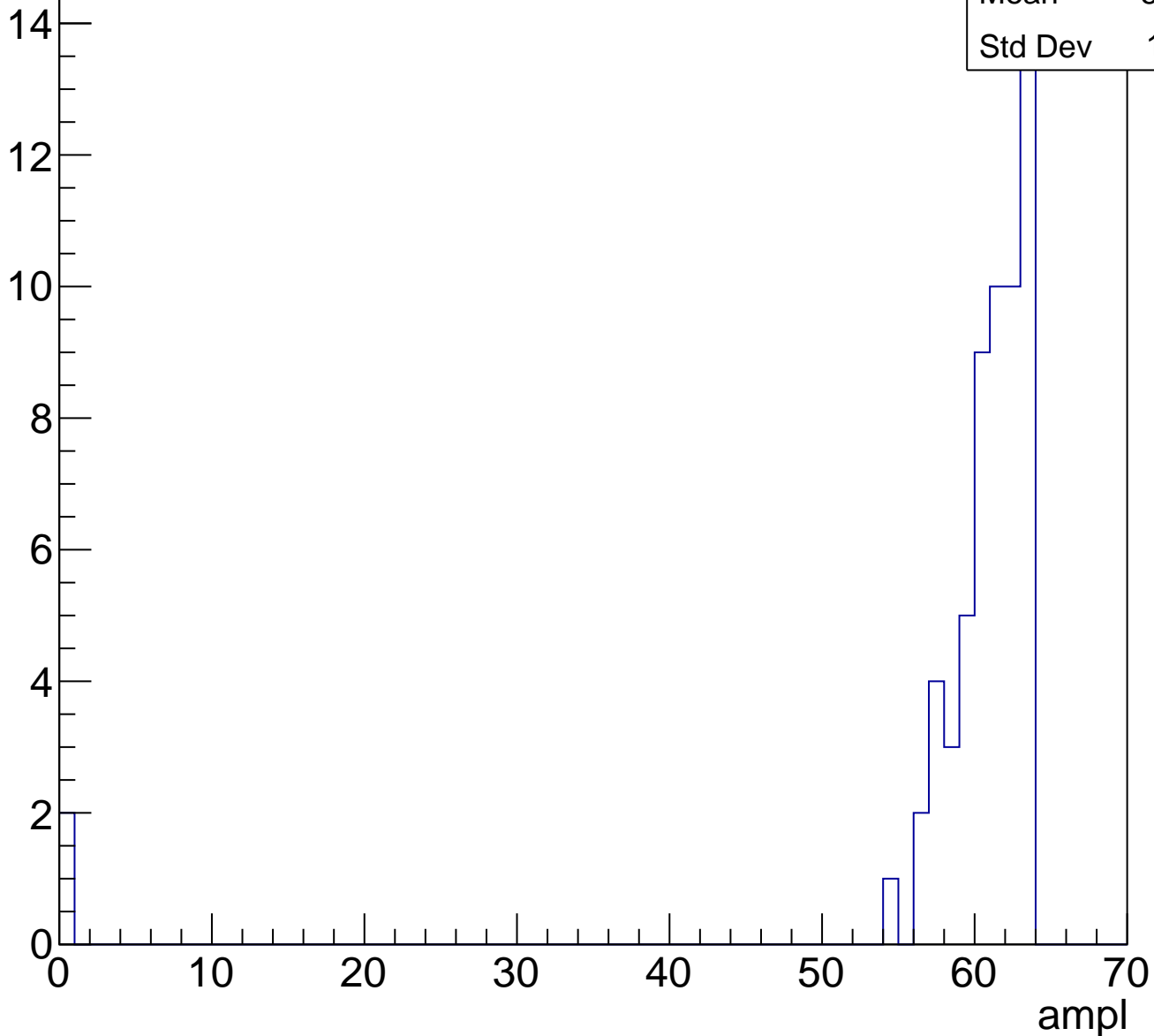


B1L001S, U21-ch87, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	61
Mean	58.66
Std Dev	11.01

Entry



B1L001S, U21-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.2

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U21-ch88, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	28.94
Std Dev	3.004

Gaus mean : 29.5134

Gaus Width: 2.9073

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U21-ch88, adc1

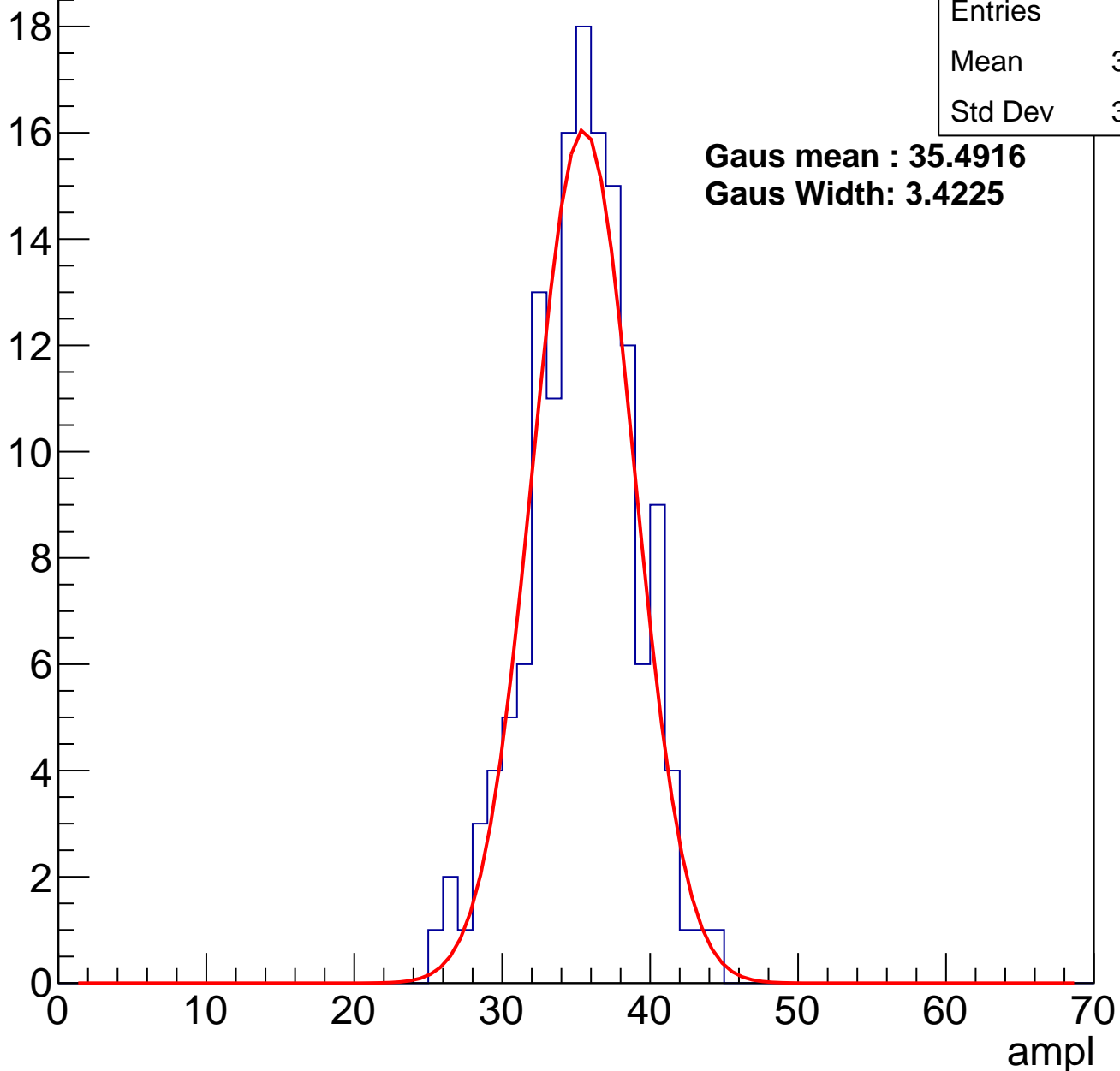
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	34.94
Std Dev	3.579

Gaus mean : 35.4916

Gaus Width: 3.4225

Entry



B1L001S, U21-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	41.5
Std Dev	2.995

Gaus mean : 41.8560

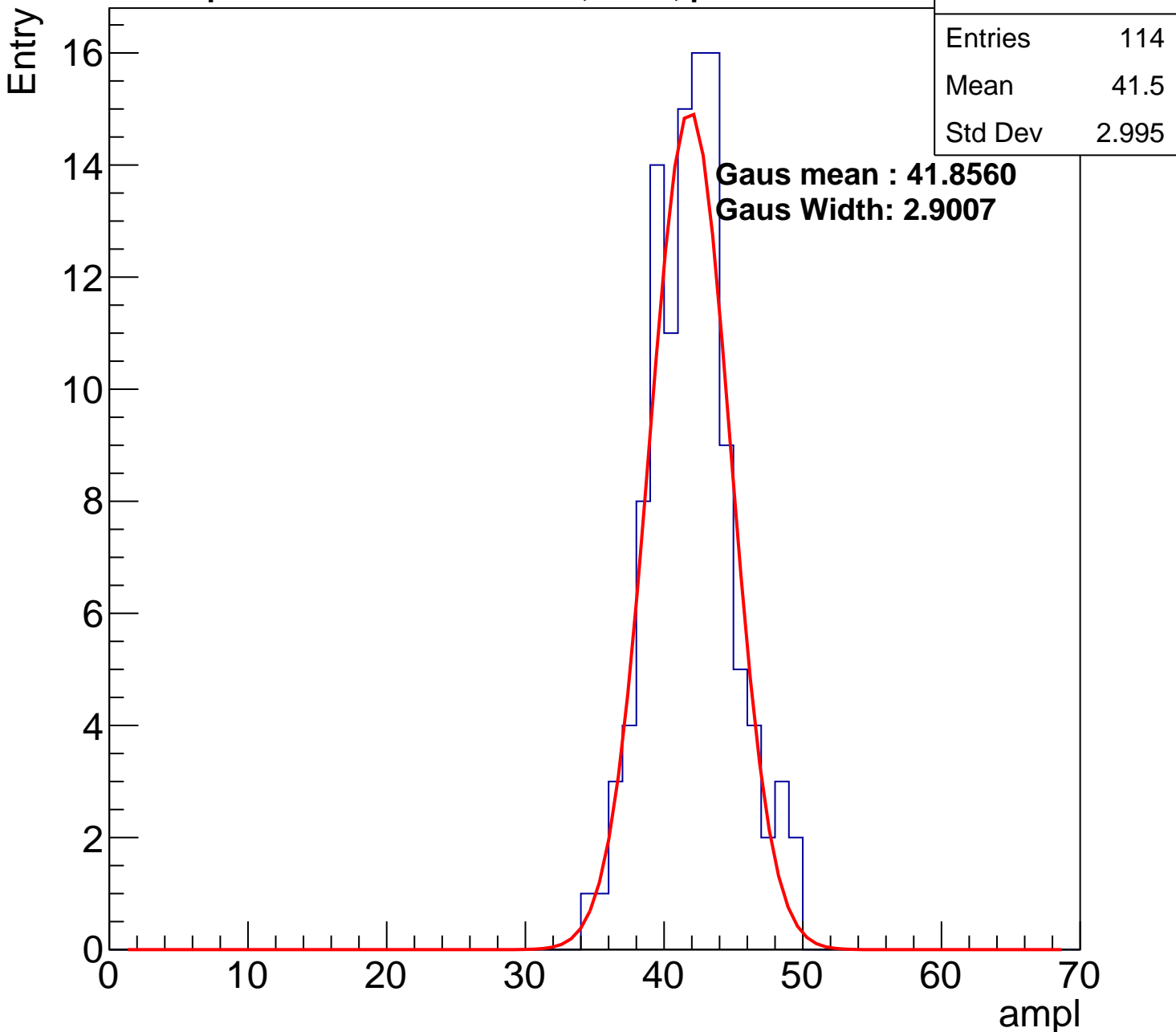
Gaus Width: 2.9007

Entry

16
14
12
10
8
6
4
2
0

ampl

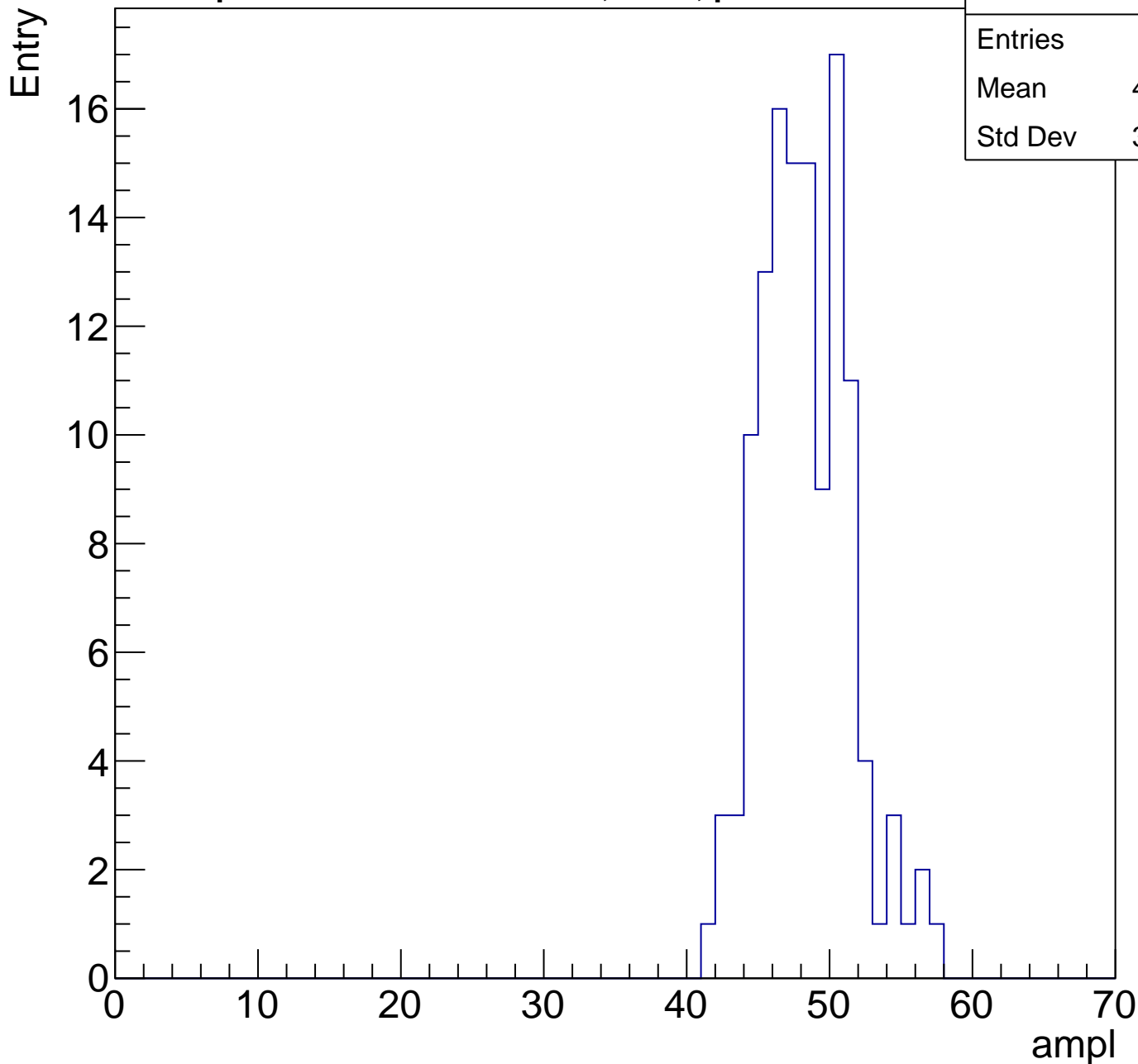
0 10 20 30 40 50 60 70



B1L001S, U21-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	47.85
Std Dev	3.119

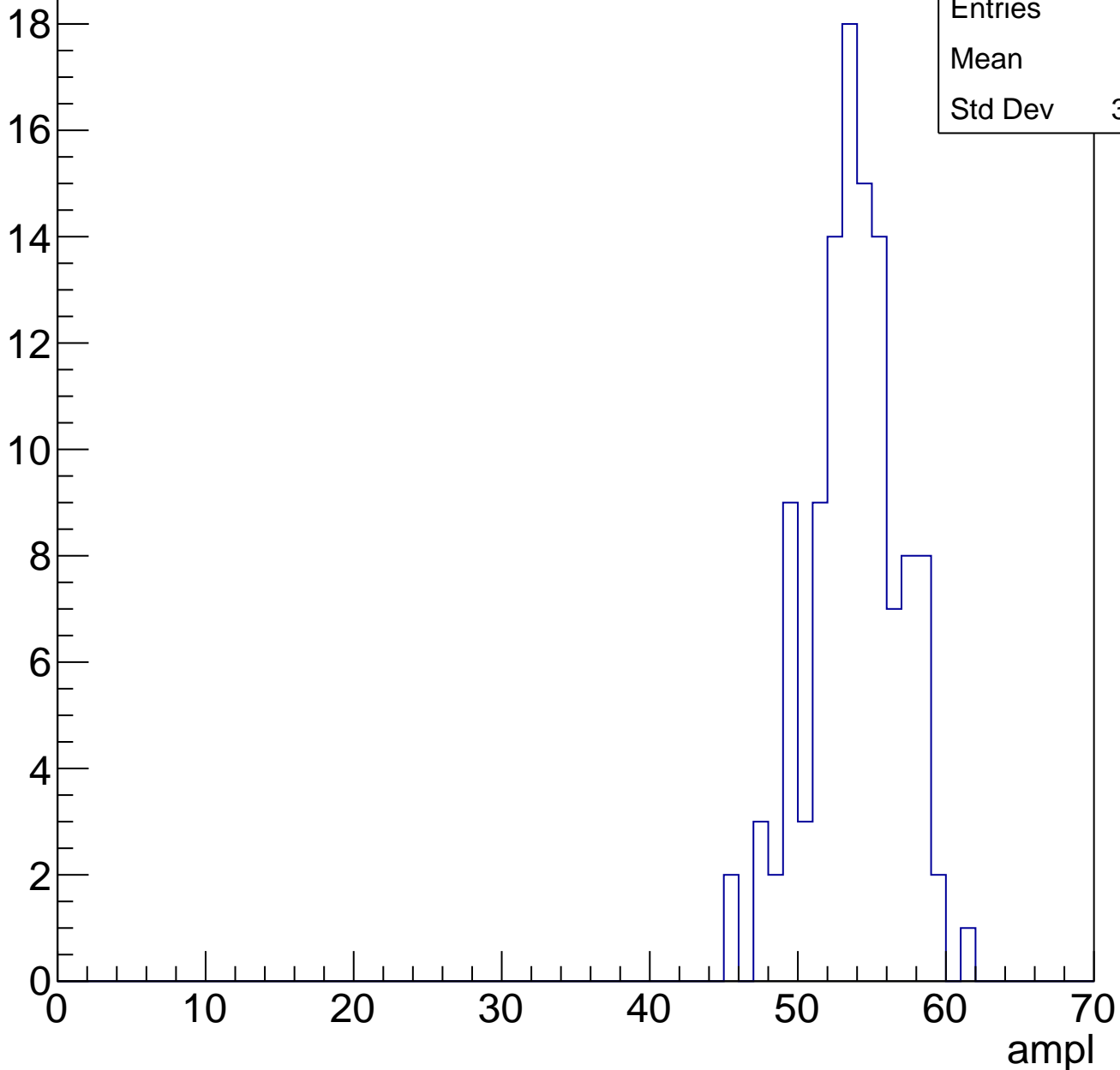


B1L001S, U21-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	53.3
Std Dev	3.085

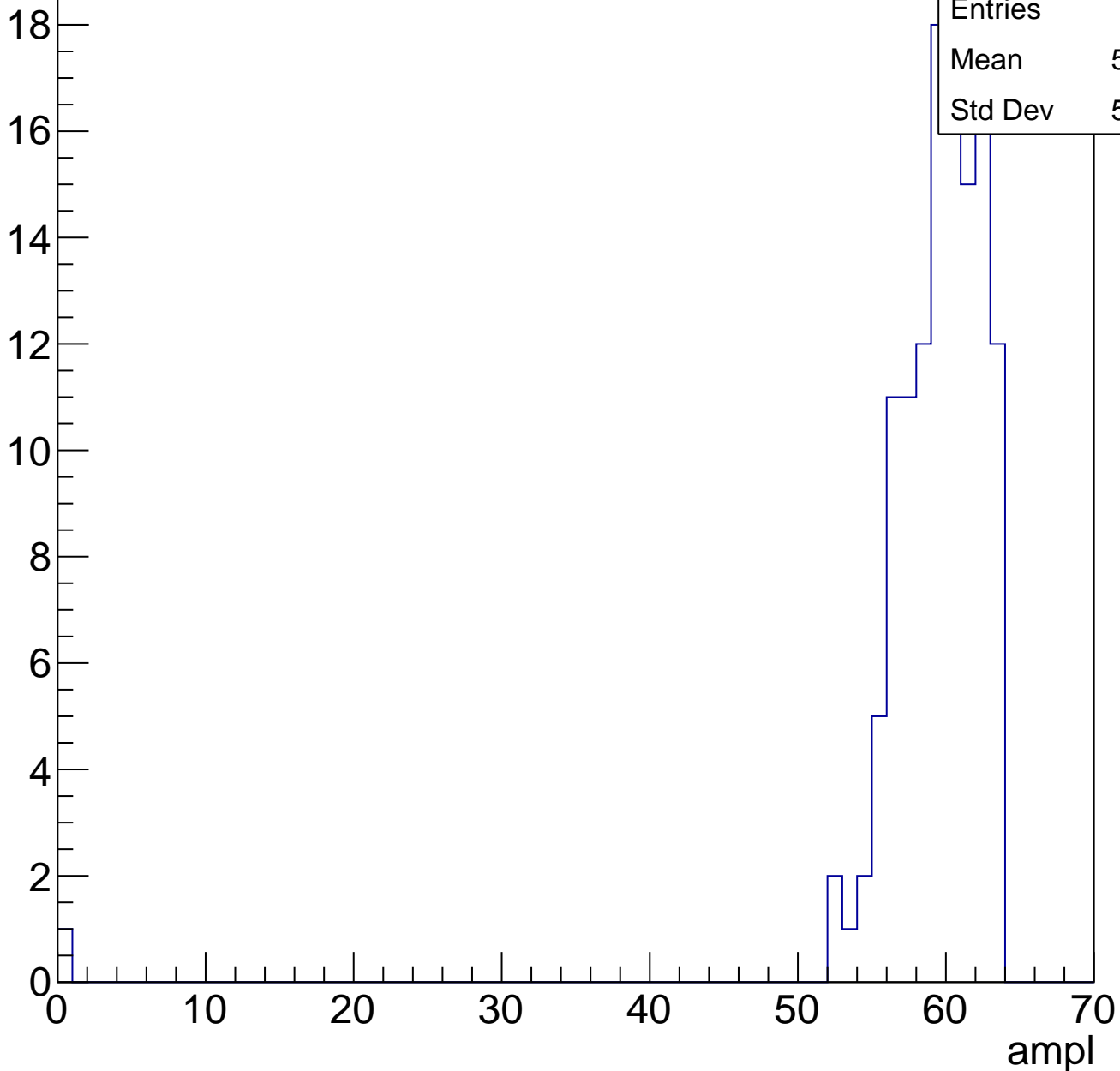
Entry



B1L001S, U21-ch88, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

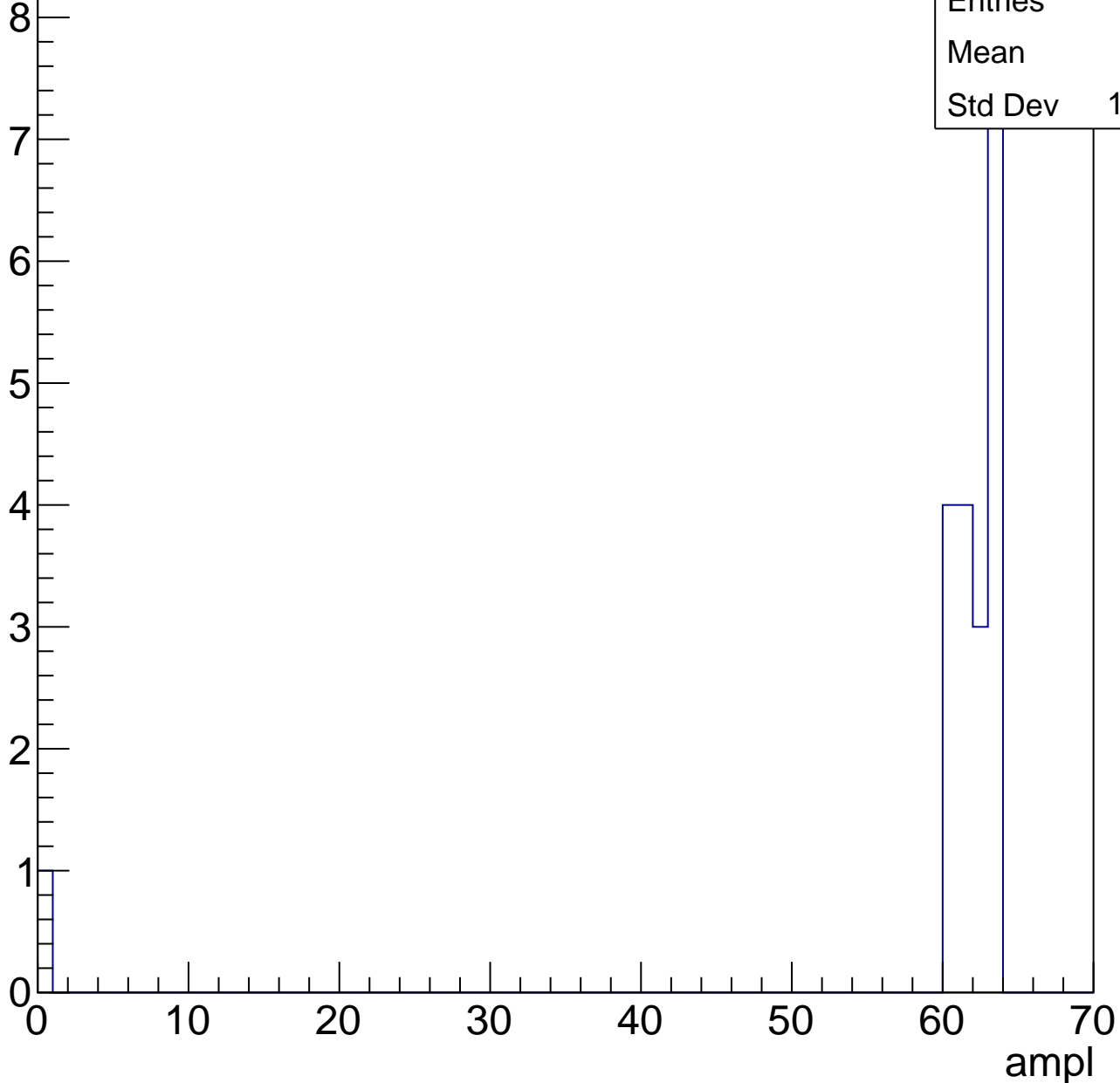


B1L001S, U21-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

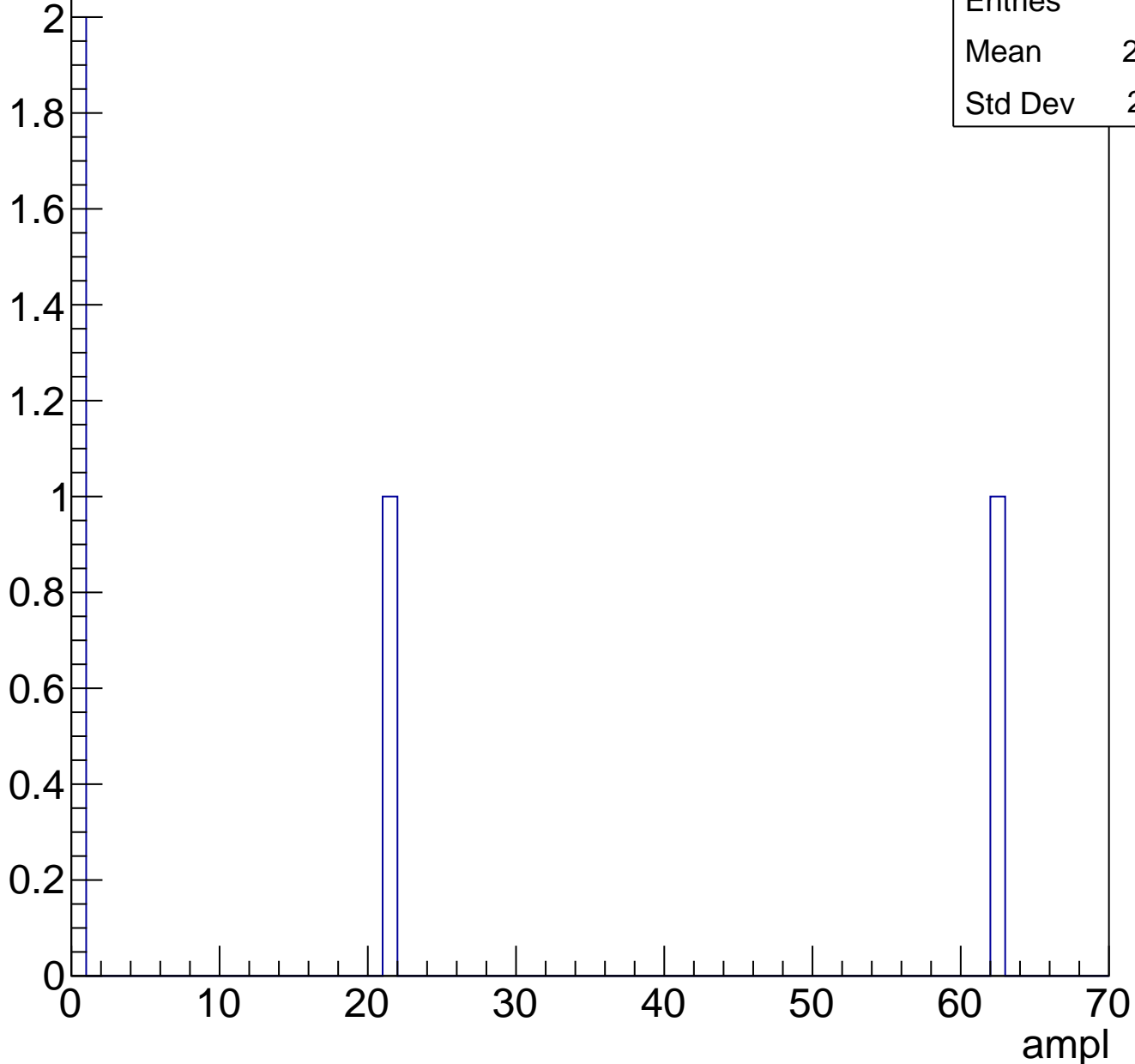
Entries	20
Mean	58.7
Std Dev	13.52



B1L001S, U21-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	20.75
Std Dev	25.31

B1L001S, U21-ch89, adc0

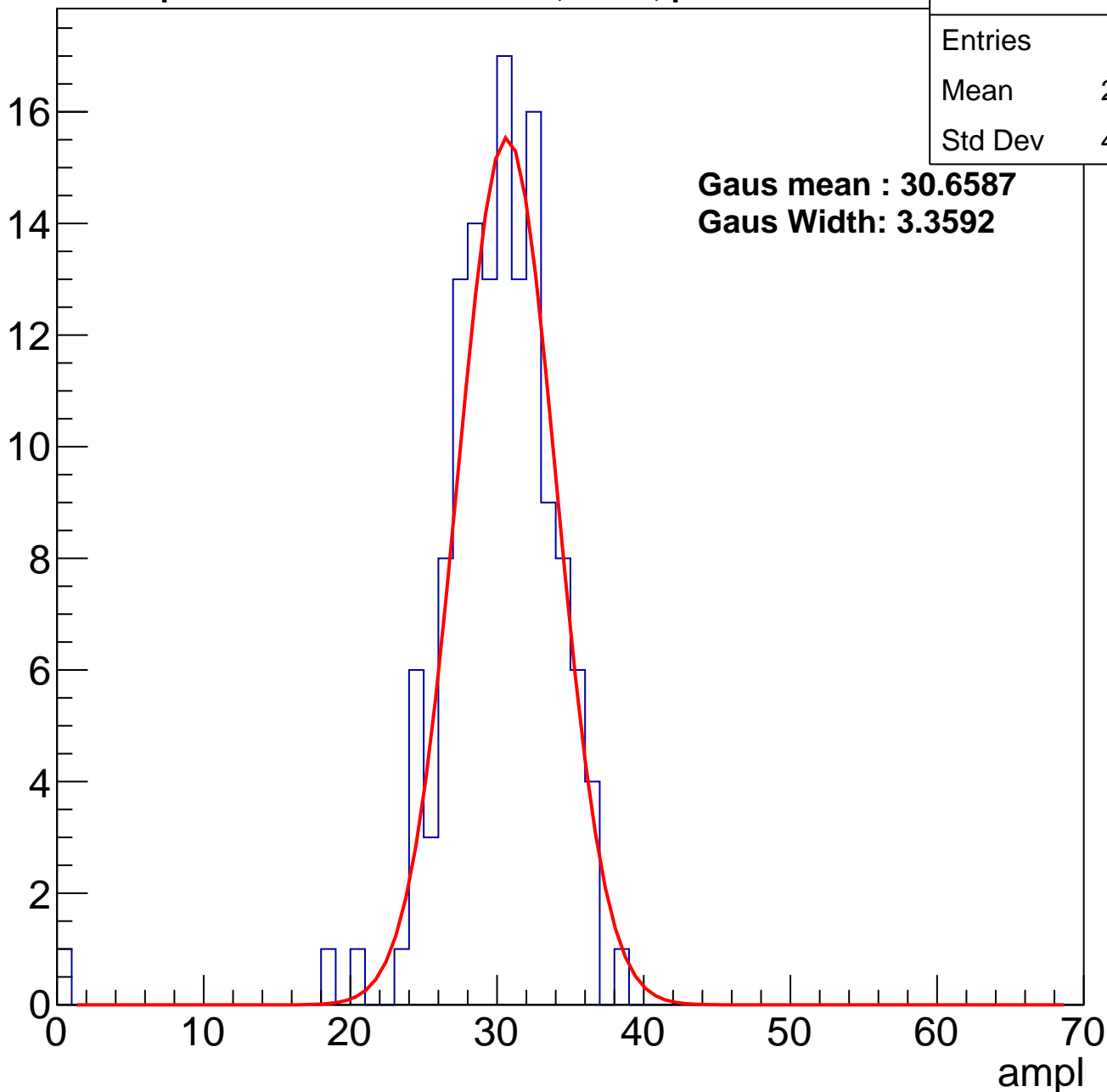
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.59
Std Dev	4.233

Gaus mean : 30.6587

Gaus Width: 3.3592

Entry



B1L001S, U21-ch89, adc1

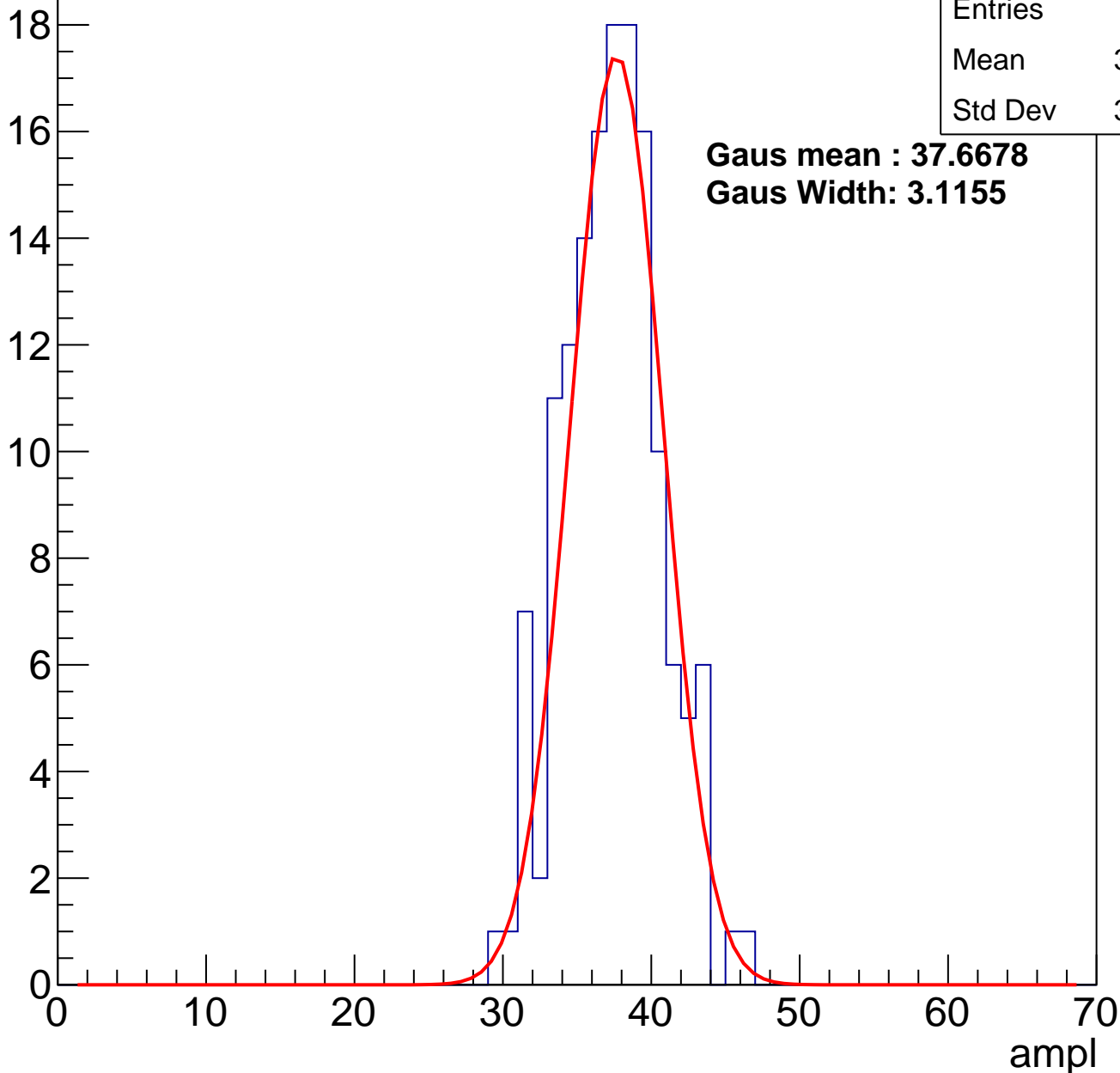
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.94
Std Dev	3.242

Gaus mean : 37.6678

Gaus Width: 3.1155

Entry



B1L001S, U21-ch89, adc2

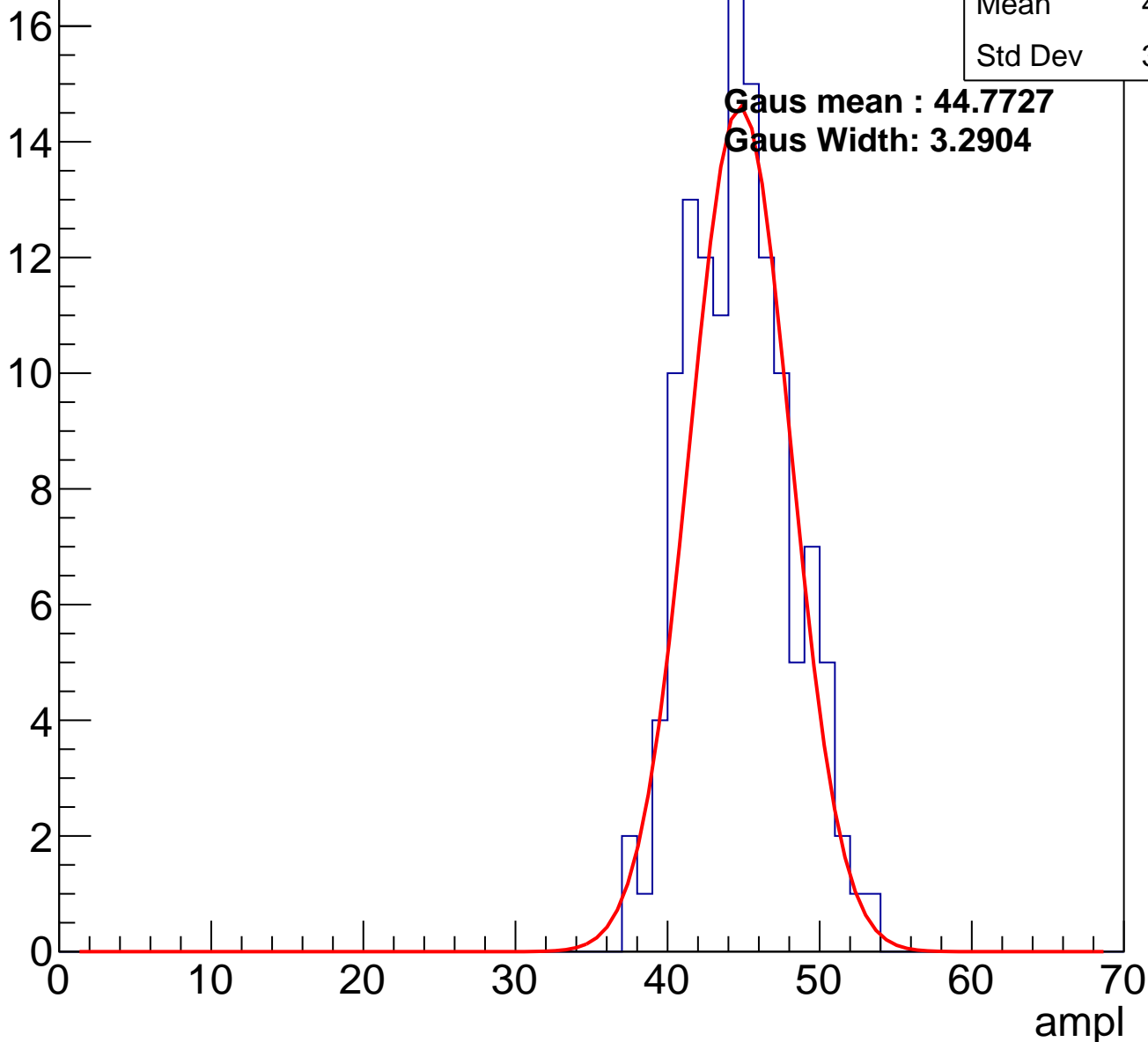
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	128
Mean	44.24
Std Dev	3.299

Gaus mean : 44.7727

Gaus Width: 3.2904



B1L001S, U21-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	50.17
Std Dev	3.533

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

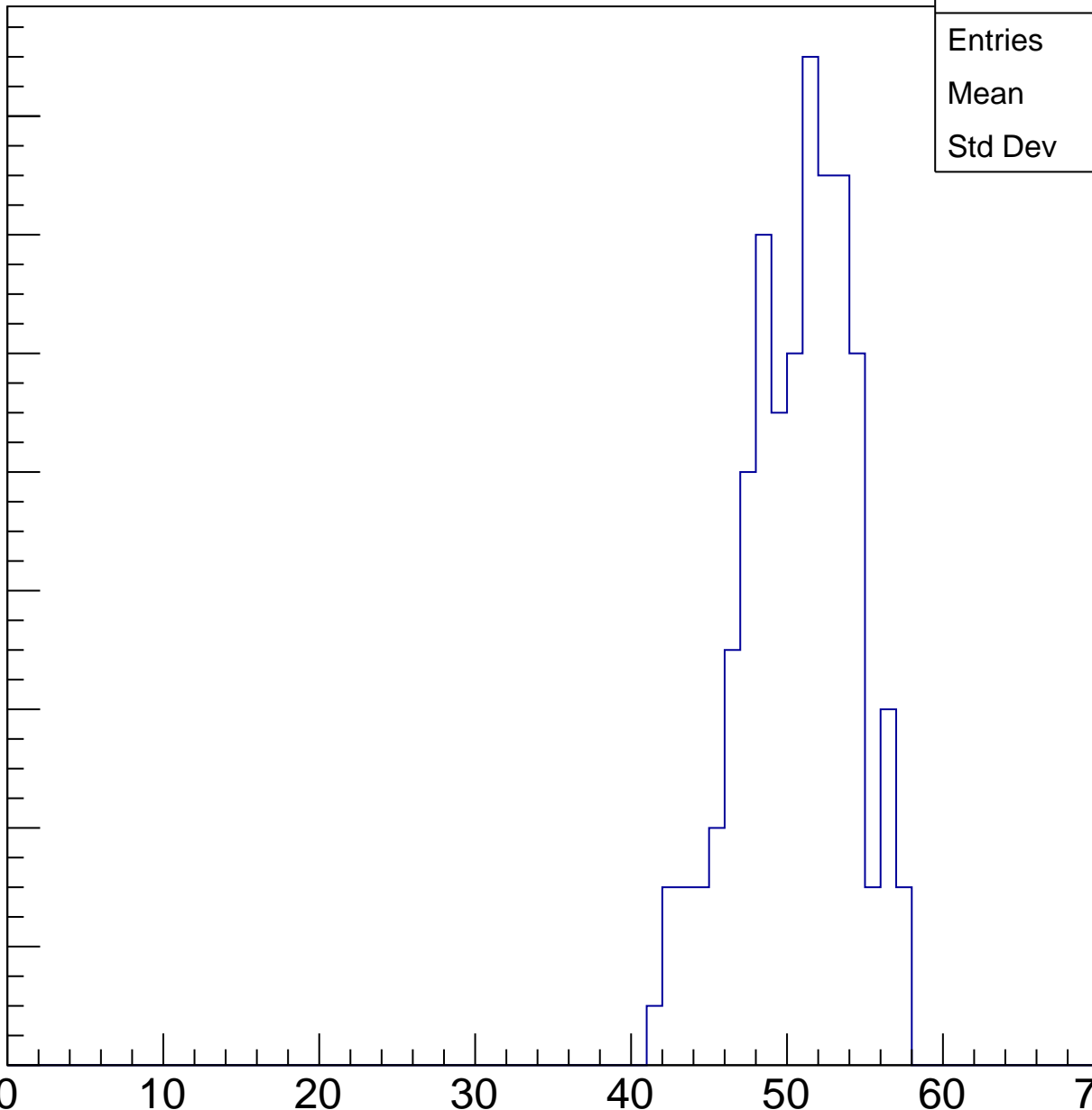
40

50

60

70

ampl



B1L001S, U21-ch89, adc4

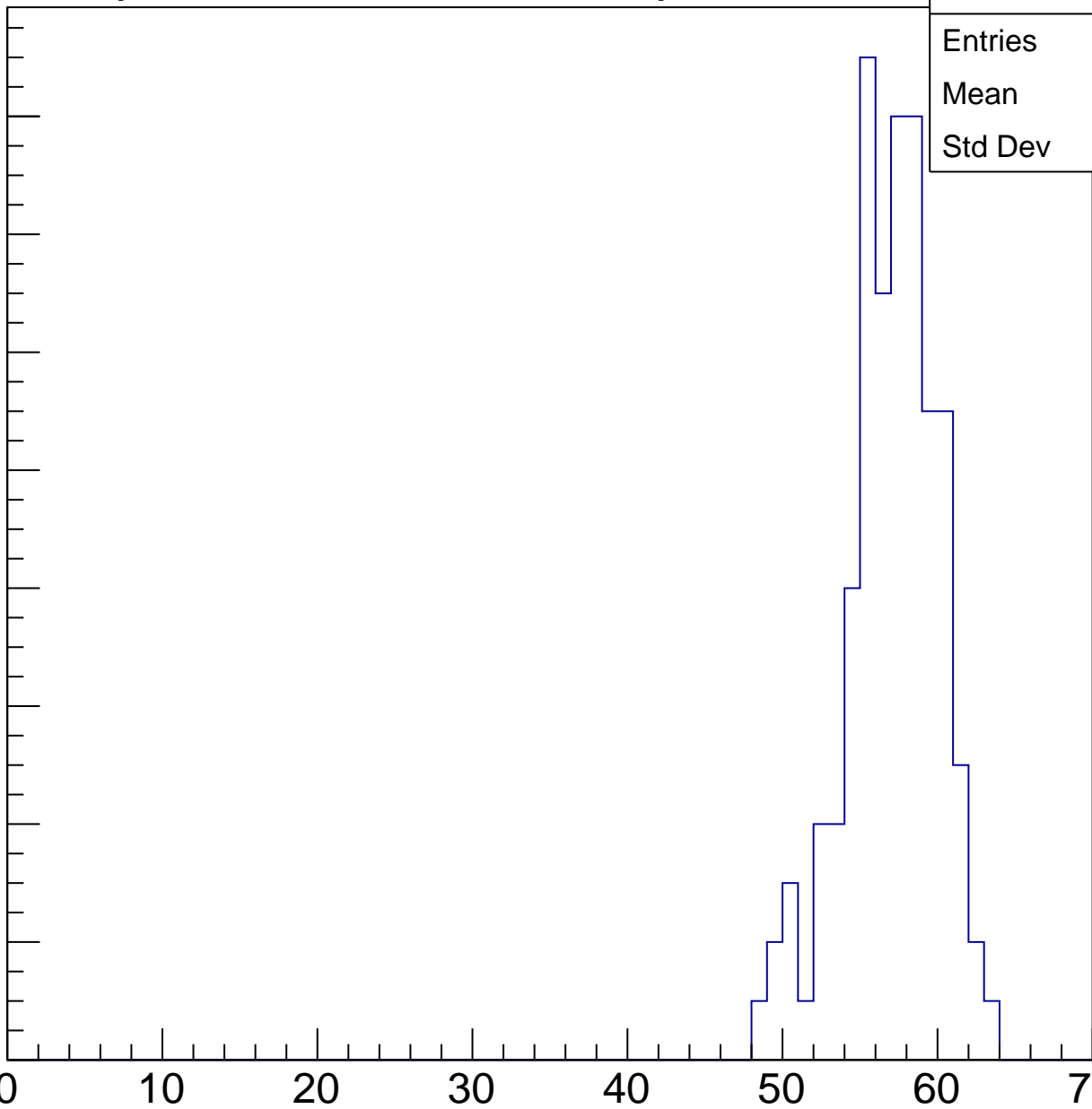
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	56.55
Std Dev	2.964

ampl



B1L001S, U21-ch89, adc5

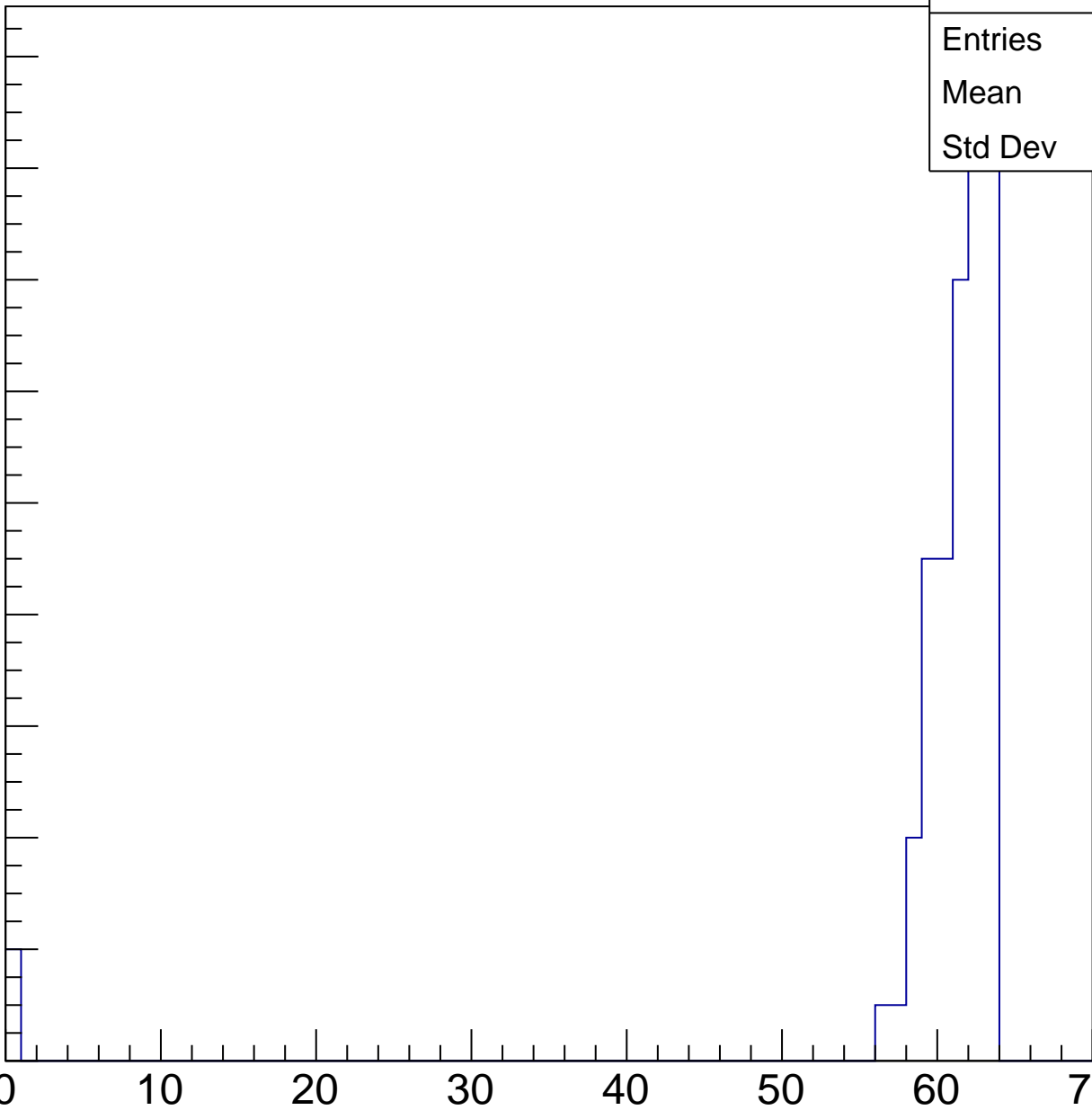
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.47
Std Dev	9.919

ampl



B1L001S, U21-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

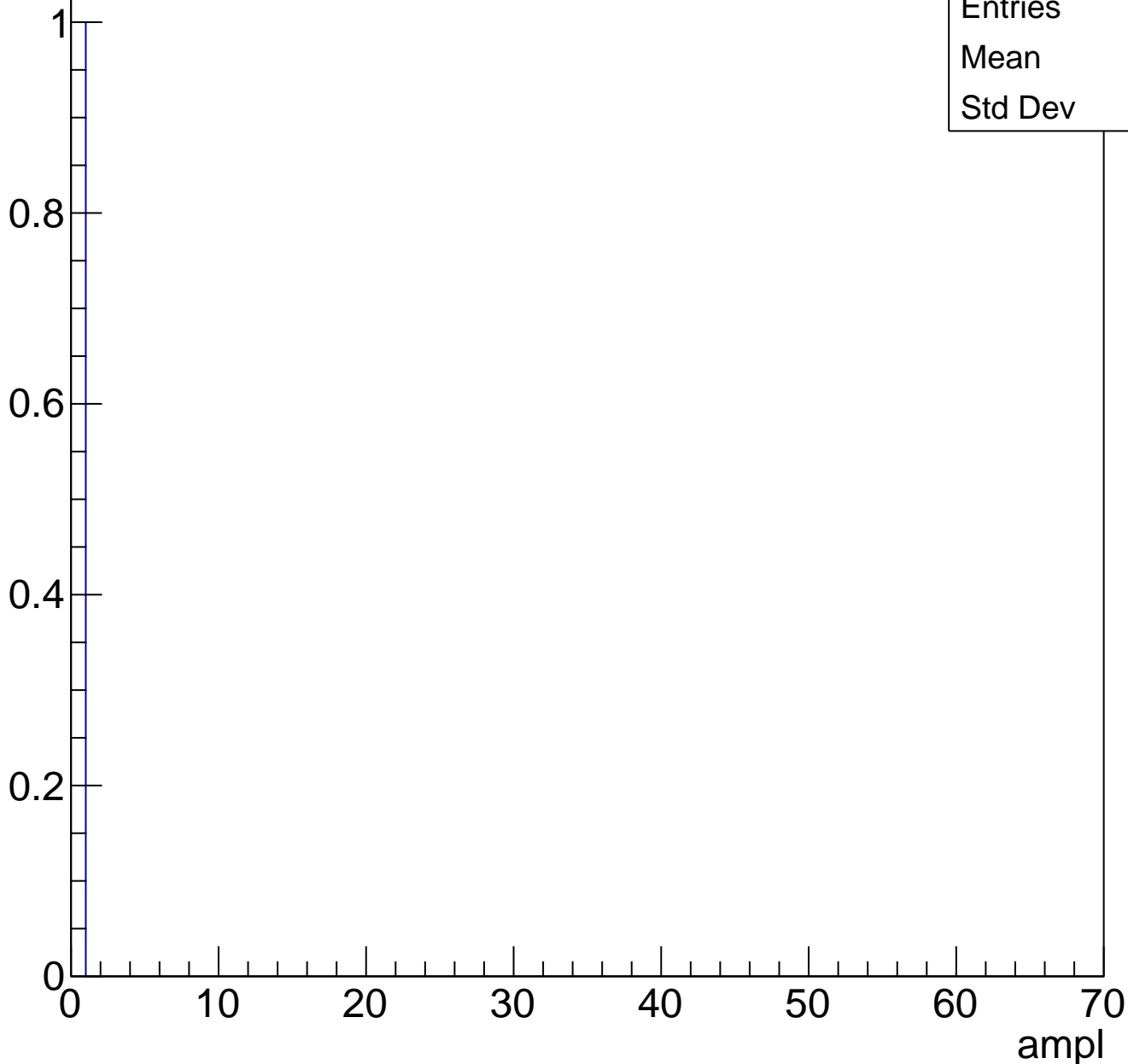
Entry



B1L001S, U21-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch90, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	29.34
Std Dev	3.195

Gaus mean : 29.4028

Gaus Width: 2.9836

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

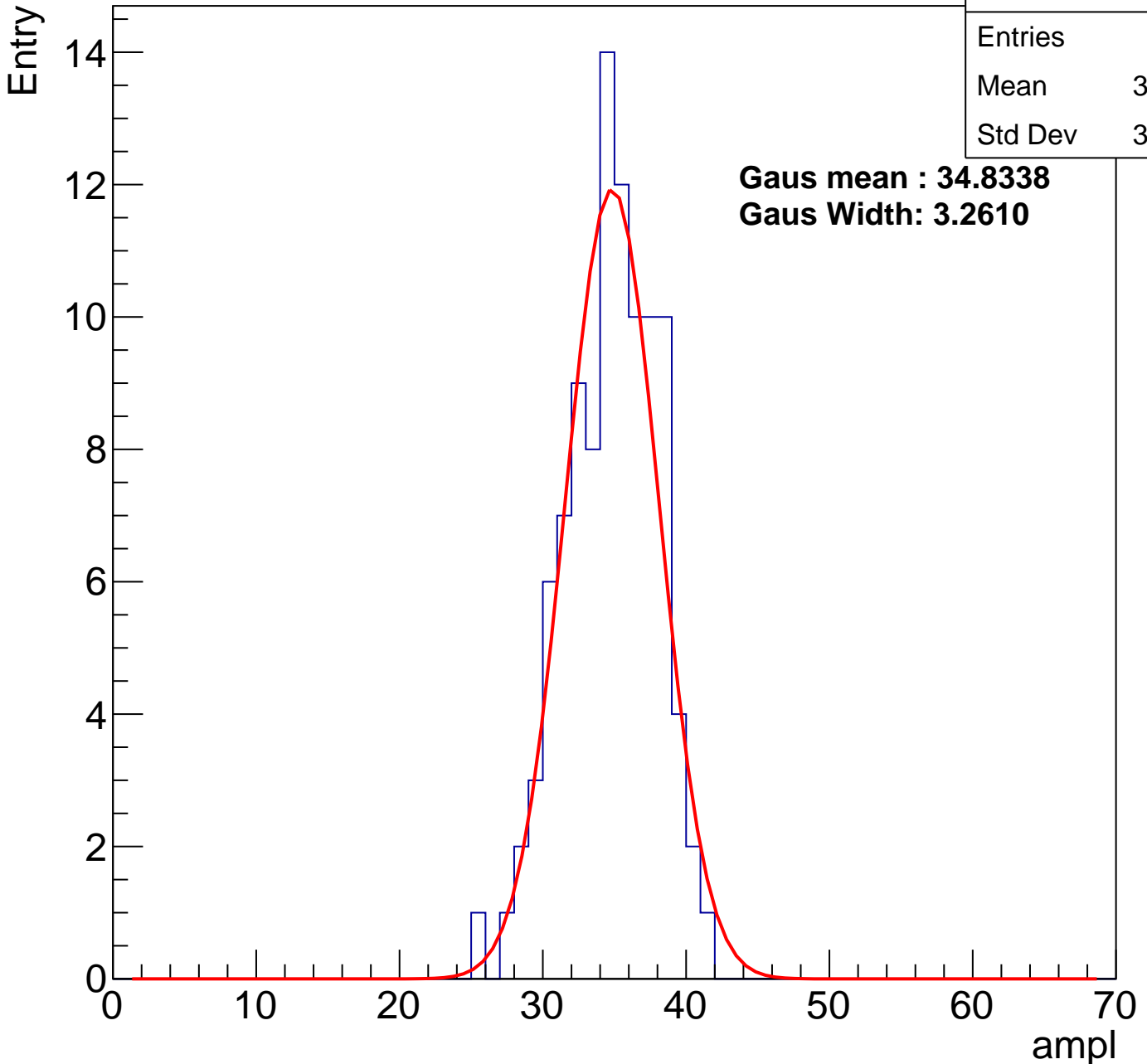
B1L001S, U21-ch90, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	34.27
Std Dev	3.155

Gaus mean : 34.8338

Gaus Width: 3.2610



B1L001S, U21-ch90, adc2

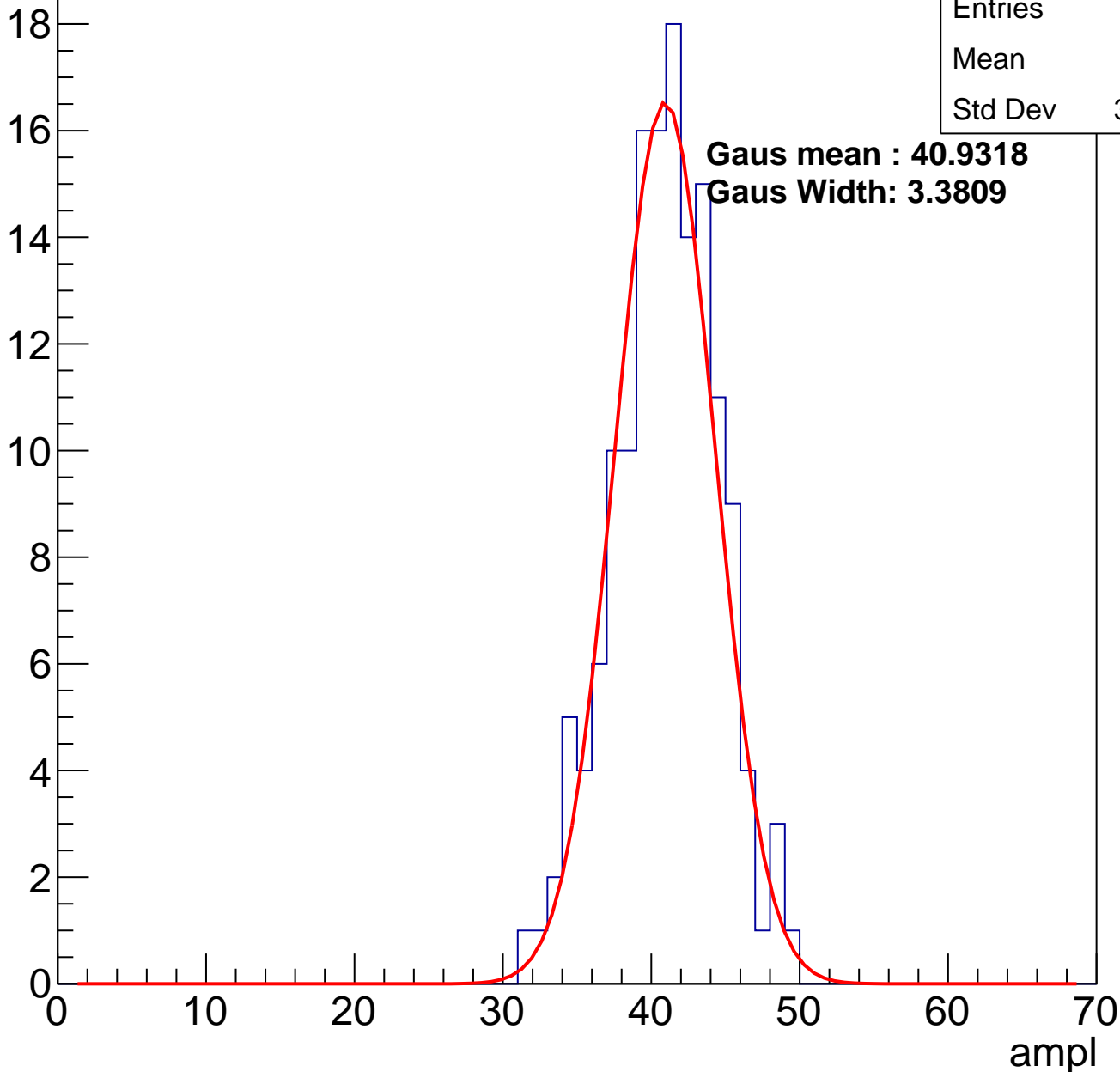
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	40.5
Std Dev	3.504

Gaus mean : 40.9318

Gaus Width: 3.3809

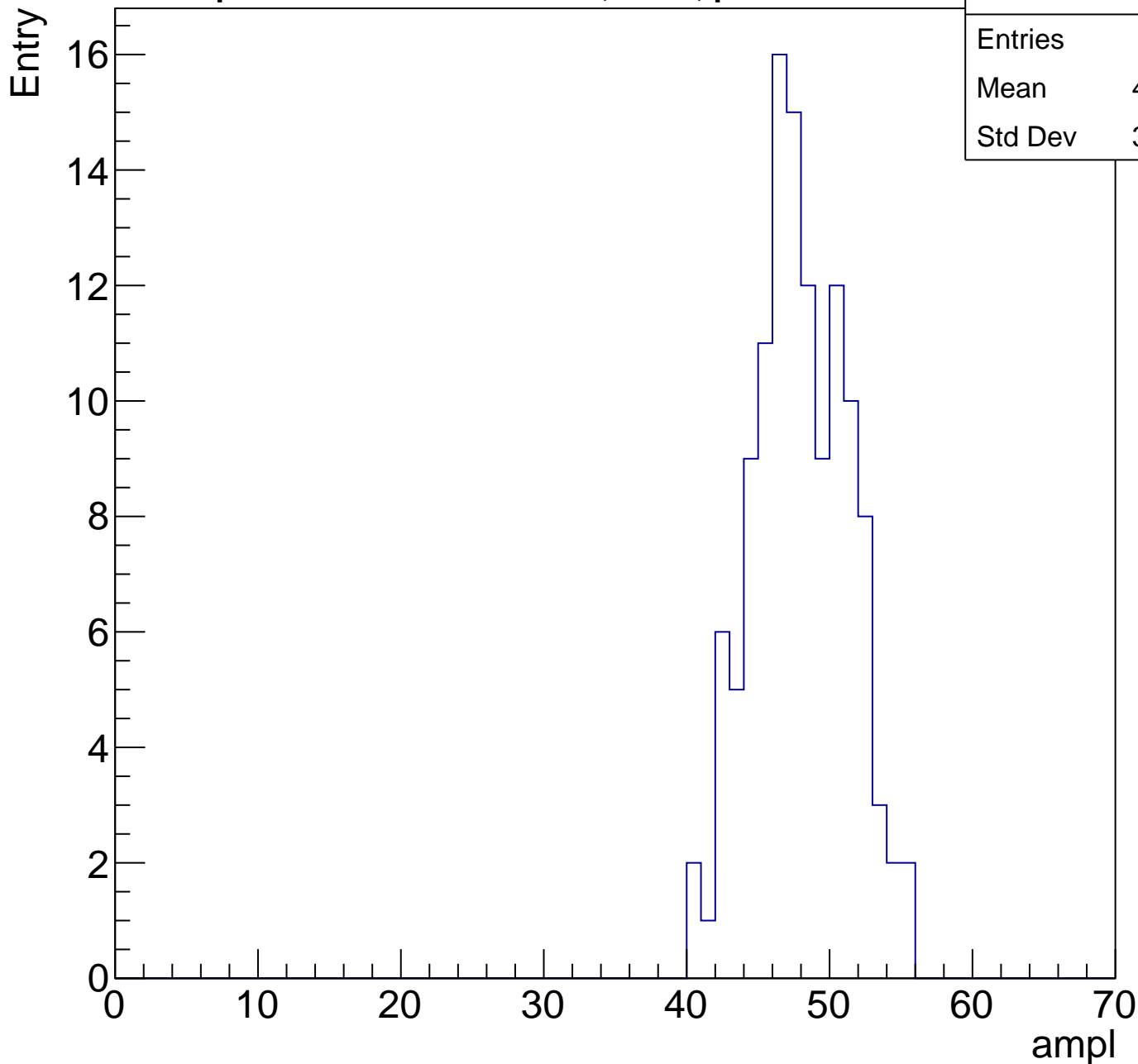
Entry



B1L001S, U21-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

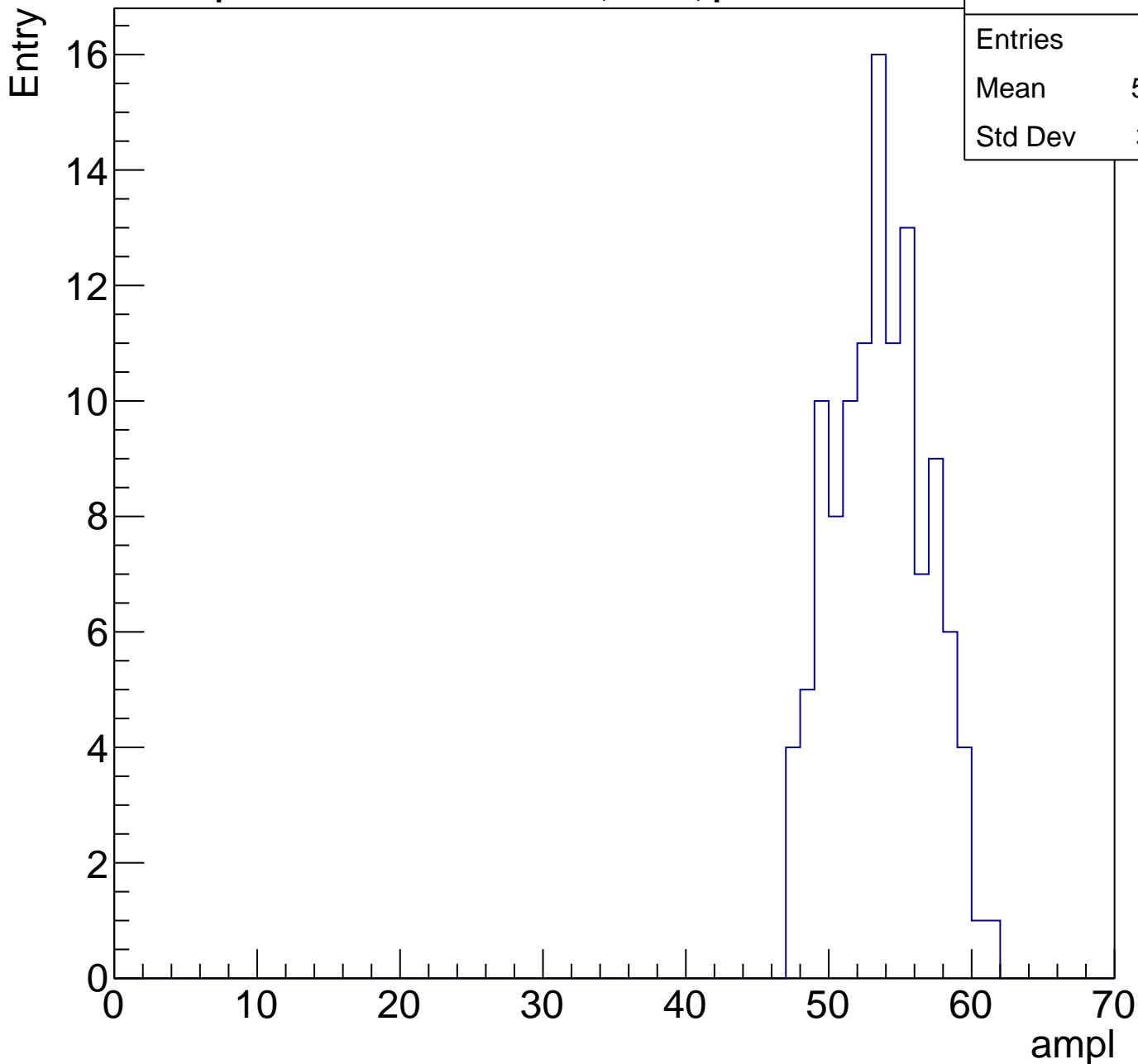
Entries	123
Mean	47.48
Std Dev	3.284



B1L001S, U21-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	53.16
Std Dev	3.251



B1L001S, U21-ch90, adc5

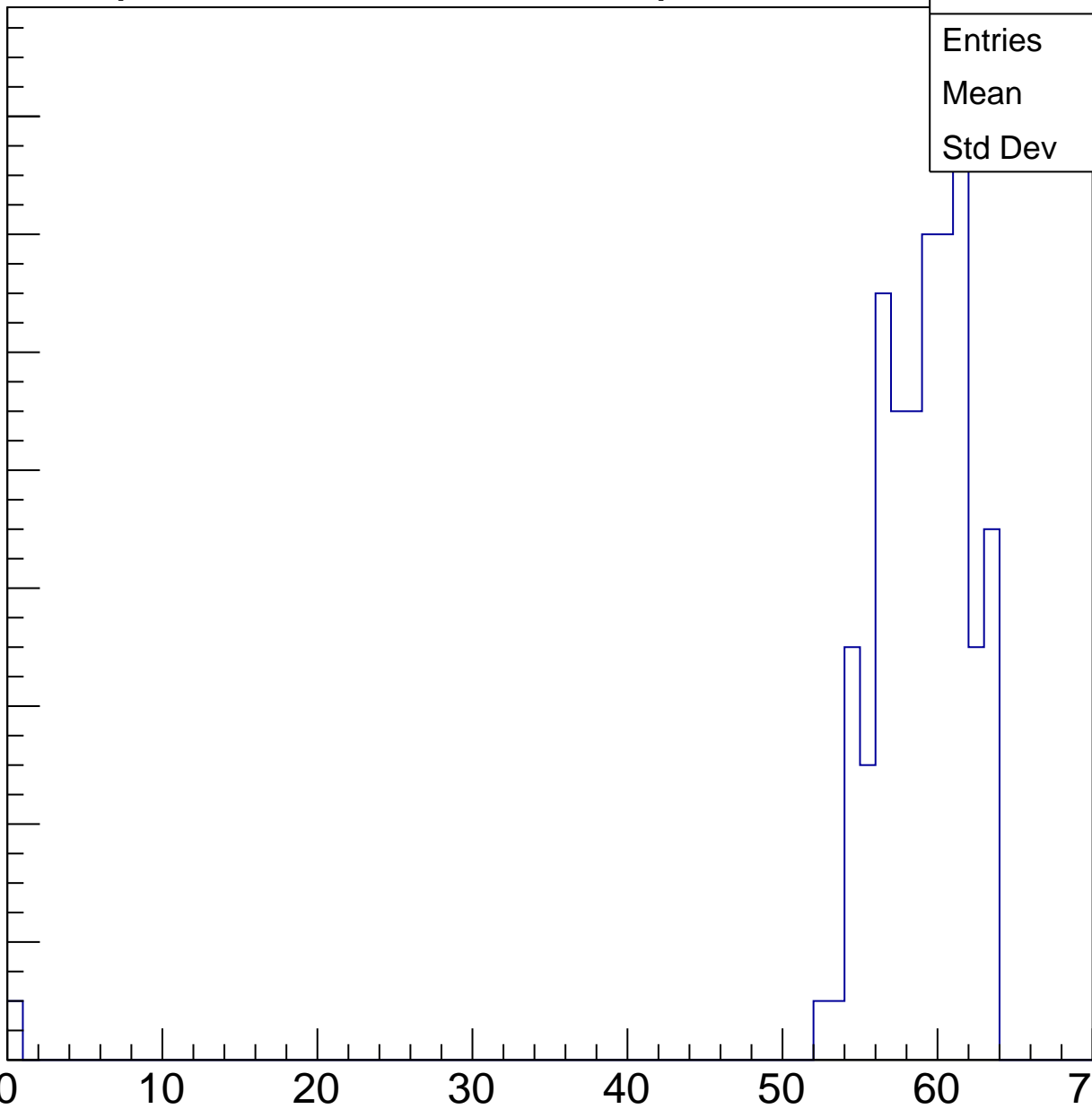
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.15
Std Dev	6.153

ampl



B1L001S, U21-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	36
Mean	59.75
Std Dev	10.22

ampl

0

10

20

30

40

50

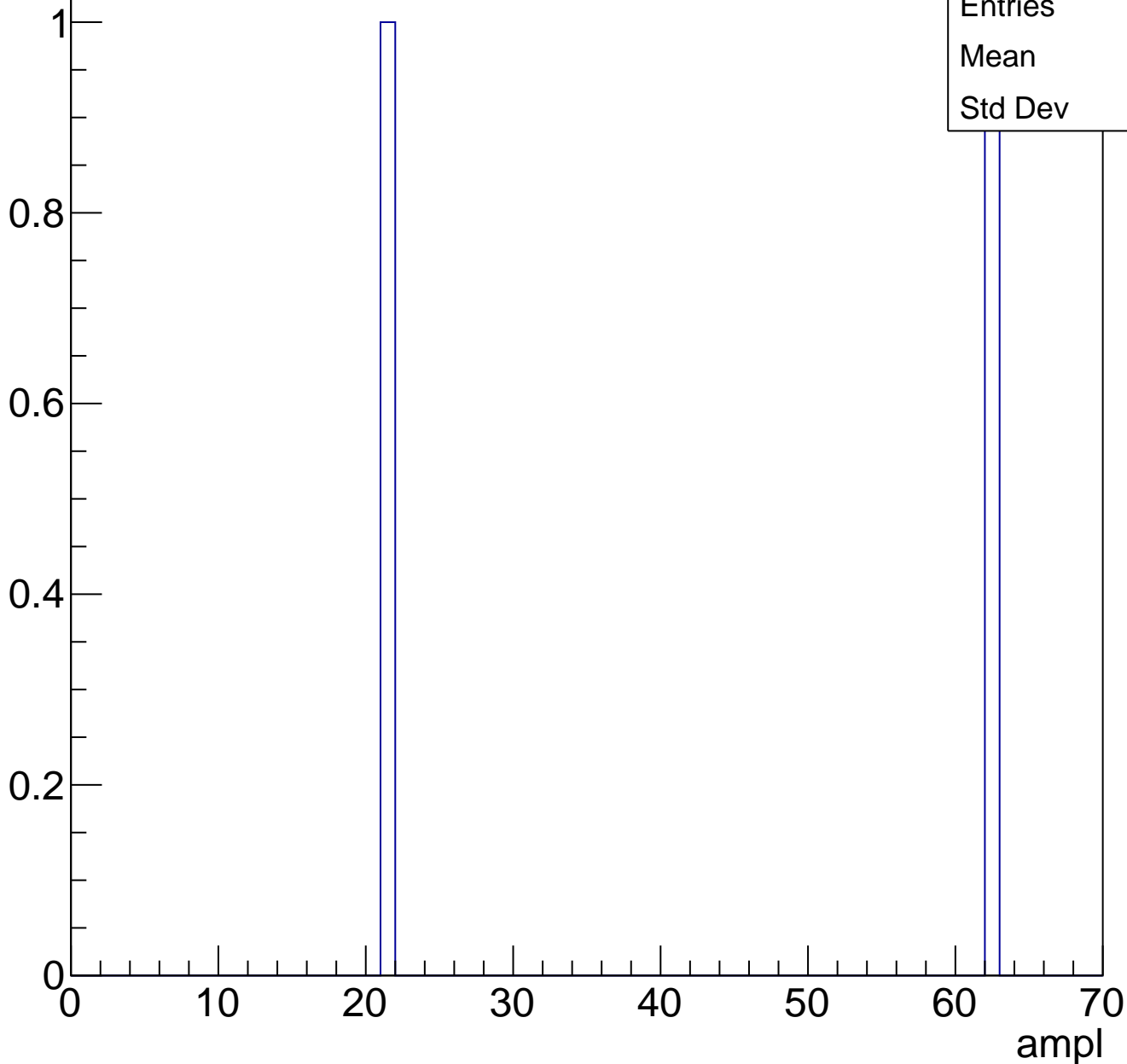
60

70

B1L001S, U21-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch91, adc0

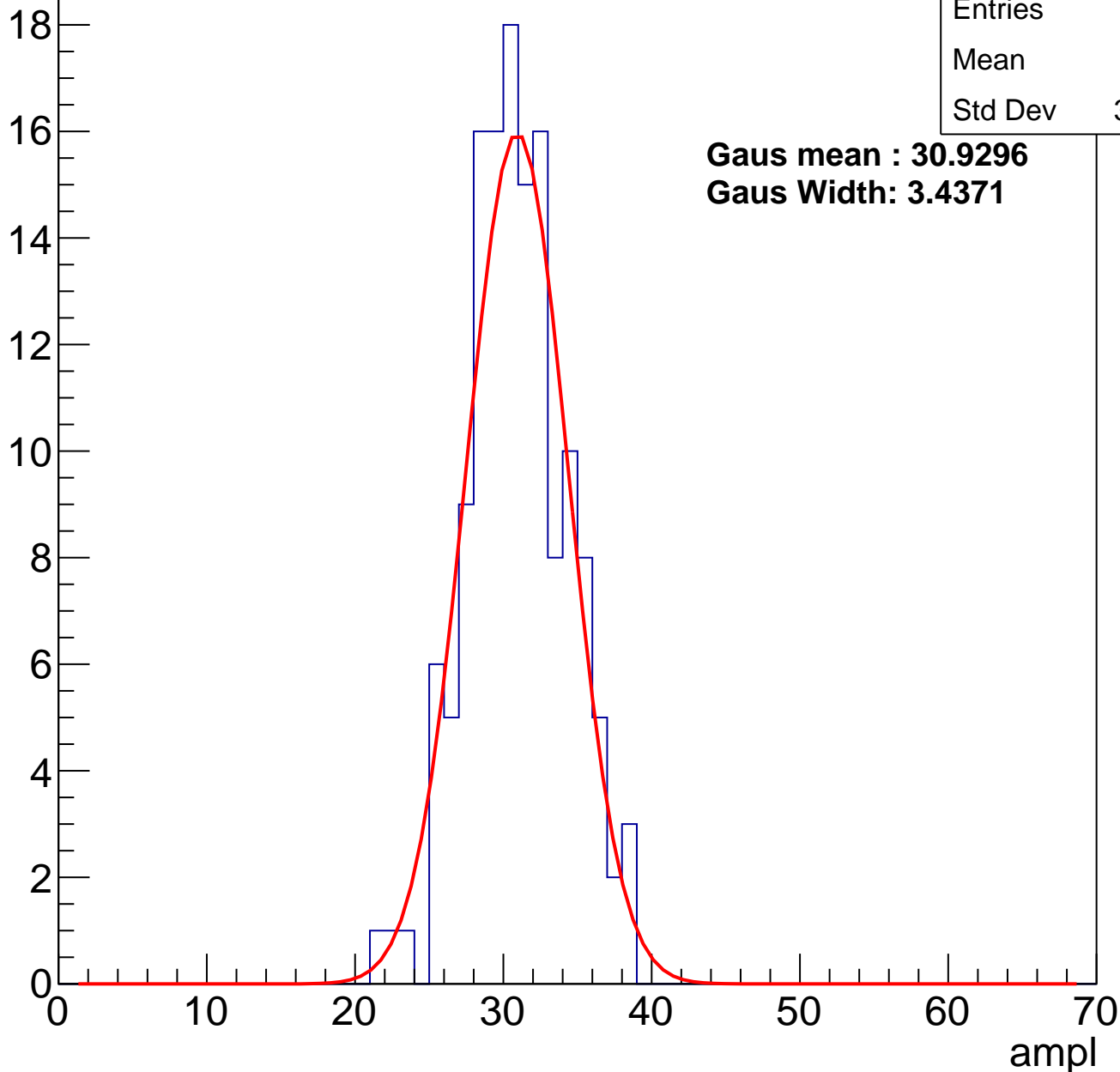
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.5
Std Dev	3.307

Gaus mean : 30.9296

Gaus Width: 3.4371

Entry



B1L001S, U21-ch91, adc1

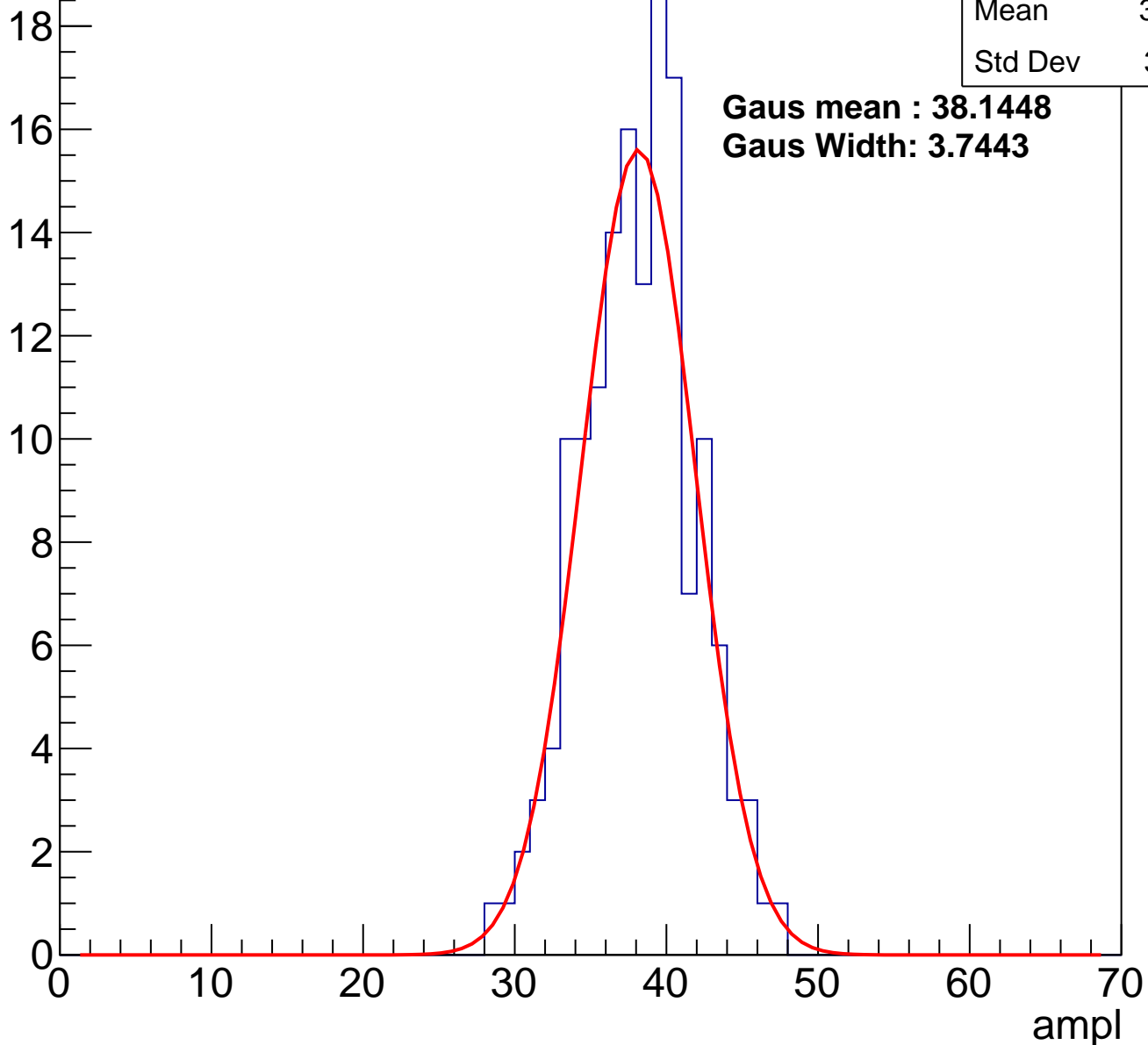
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	37.69
Std Dev	3.651

Gaus mean : 38.1448

Gaus Width: 3.7443

Entry



B1L001S, U21-ch91, adc2

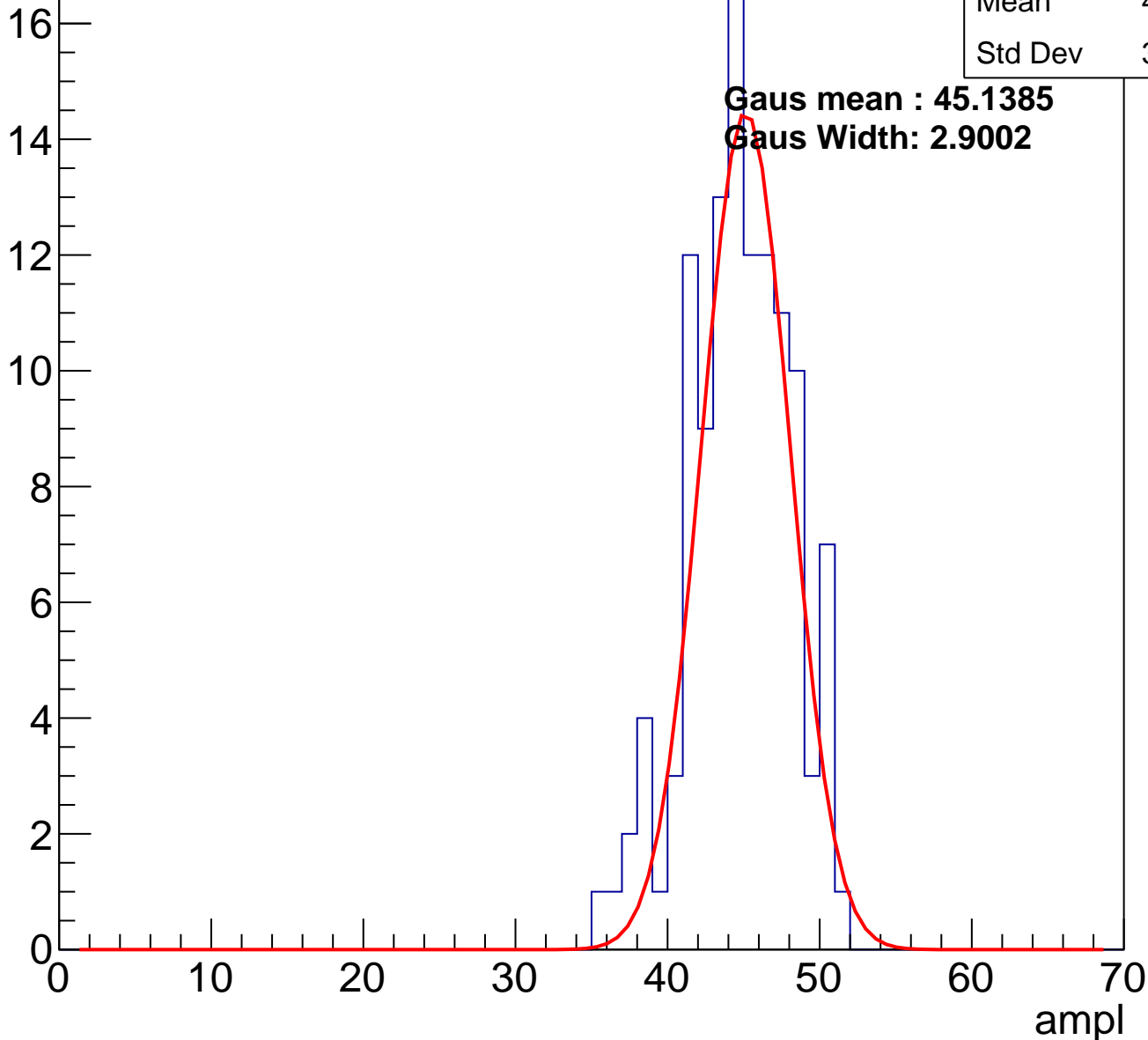
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	119
Mean	44.29
Std Dev	3.308

Gaus mean : 45.1385

Gaus Width: 2.9002



B1L001S, U21-ch91, adc3

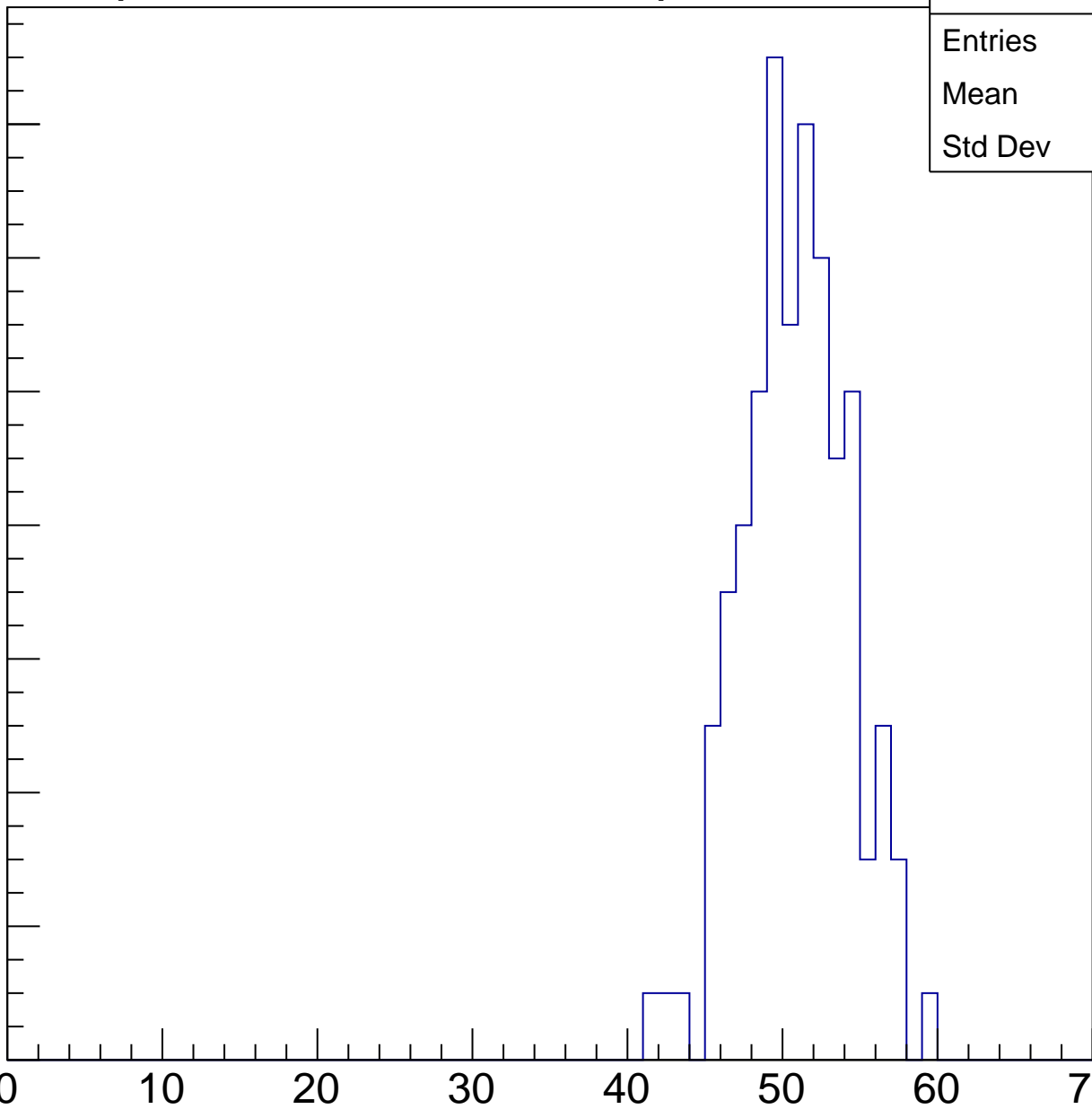
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	116
Mean	50.38
Std Dev	3.383

ampl



B1L001S, U21-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

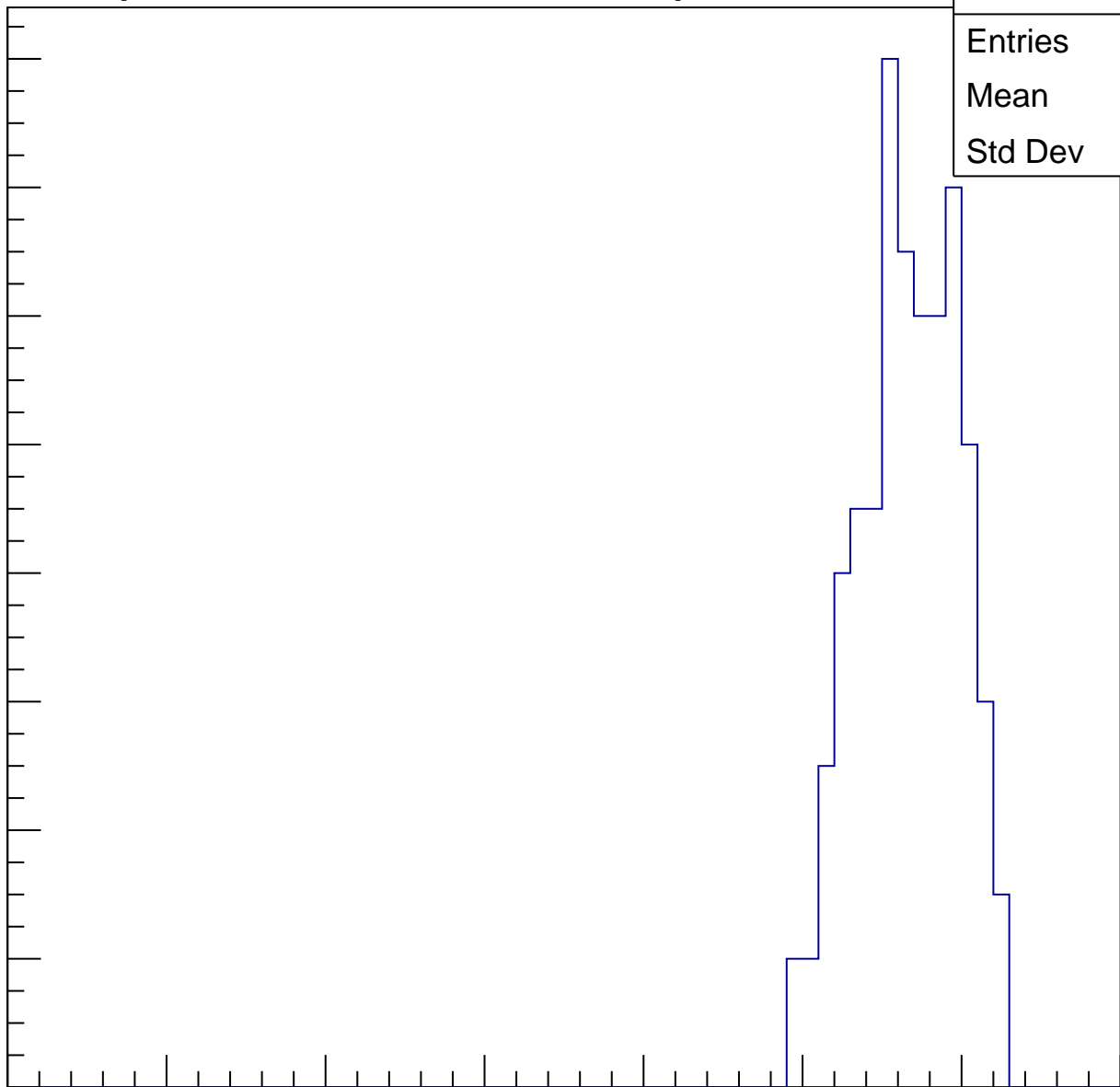
Entries	121
Mean	56.18
Std Dev	3.091

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch91, adc5

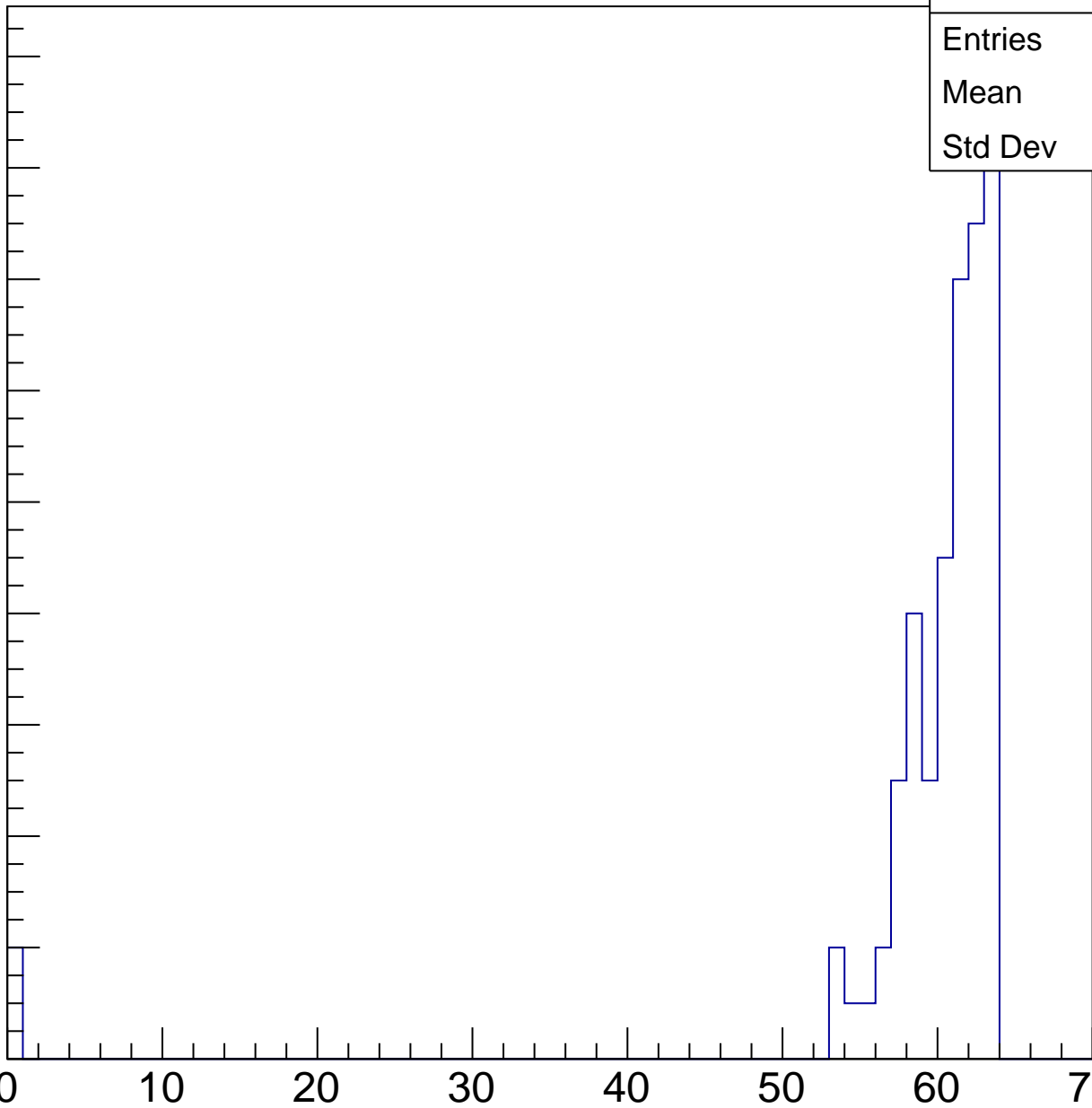
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	82
Mean	58.89
Std Dev	9.632

ampl



B1L001S, U21-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch92, adc0

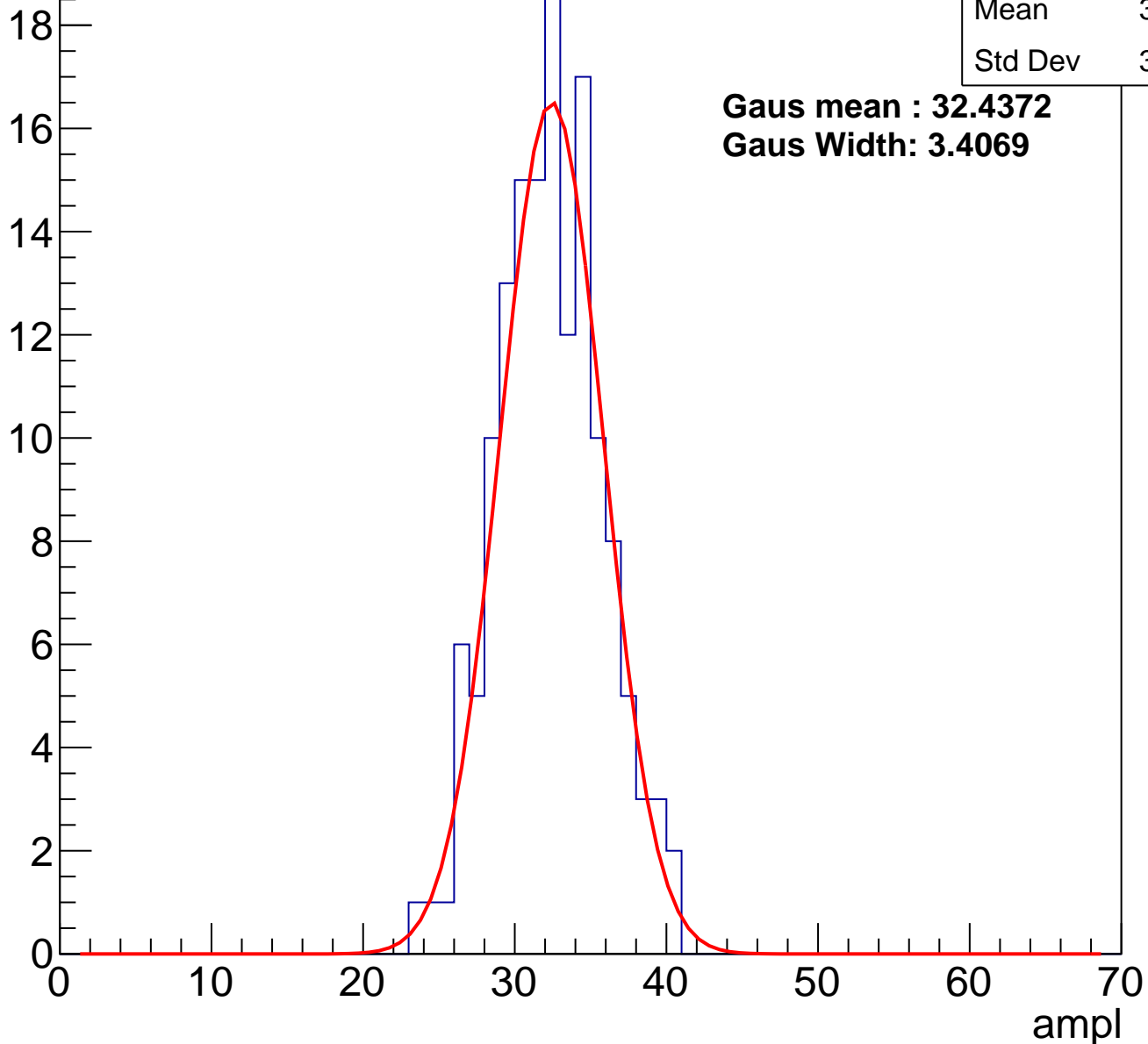
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	31.86
Std Dev	3.402

Gaus mean : 32.4372

Gaus Width: 3.4069

Entry

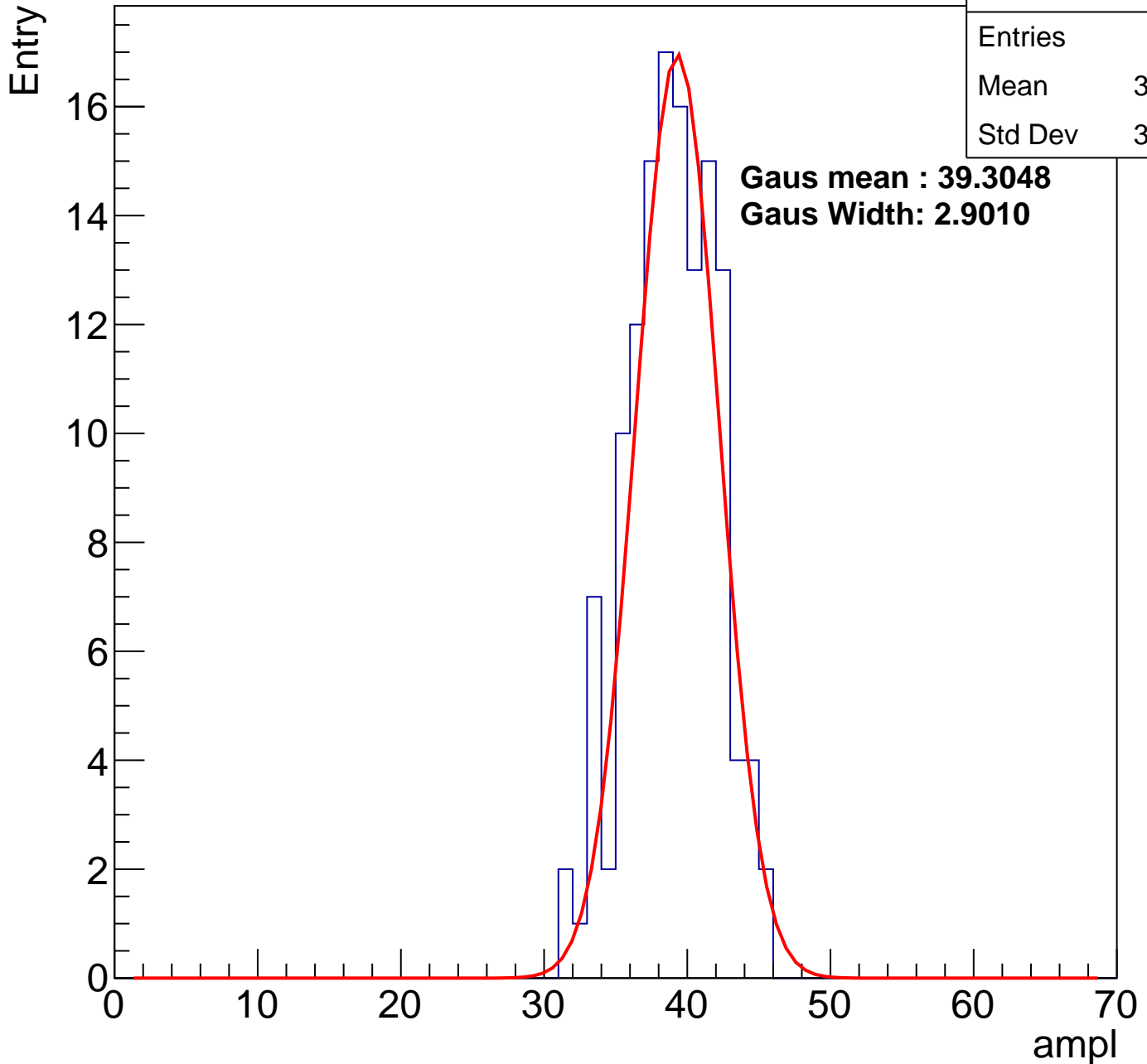


B1L001S, U21-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	38.49
Std Dev	3.023

Gaus mean : 39.3048
Gaus Width: 2.9010



B1L001S, U21-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	45.69
Std Dev	3.126

Gaus mean : 46.0990

Gaus Width: 3.2501

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

B1L001S, U21-ch92, adc3

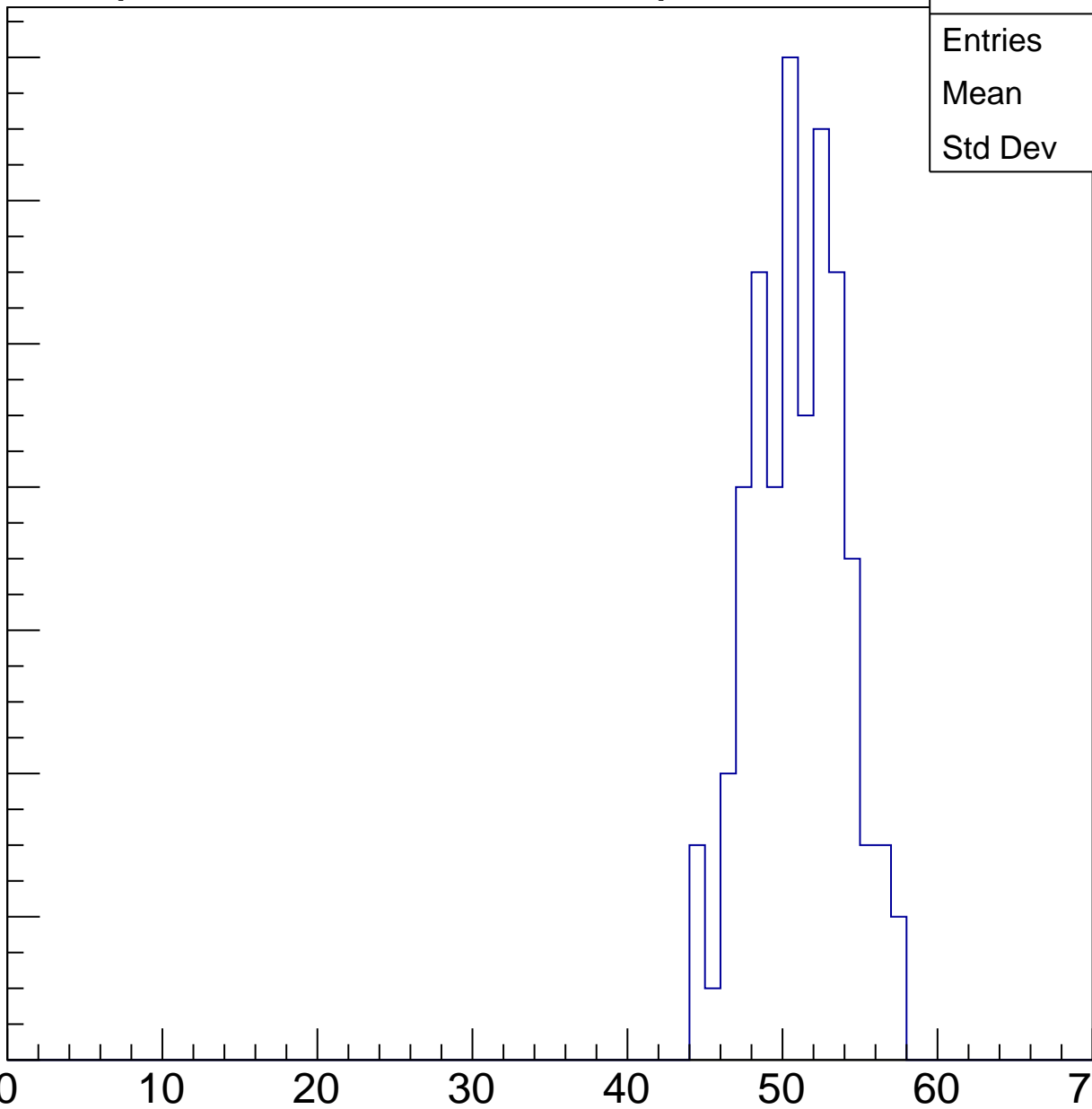
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	50.52
Std Dev	2.957

ampl



B1L001S, U21-ch92, adc4

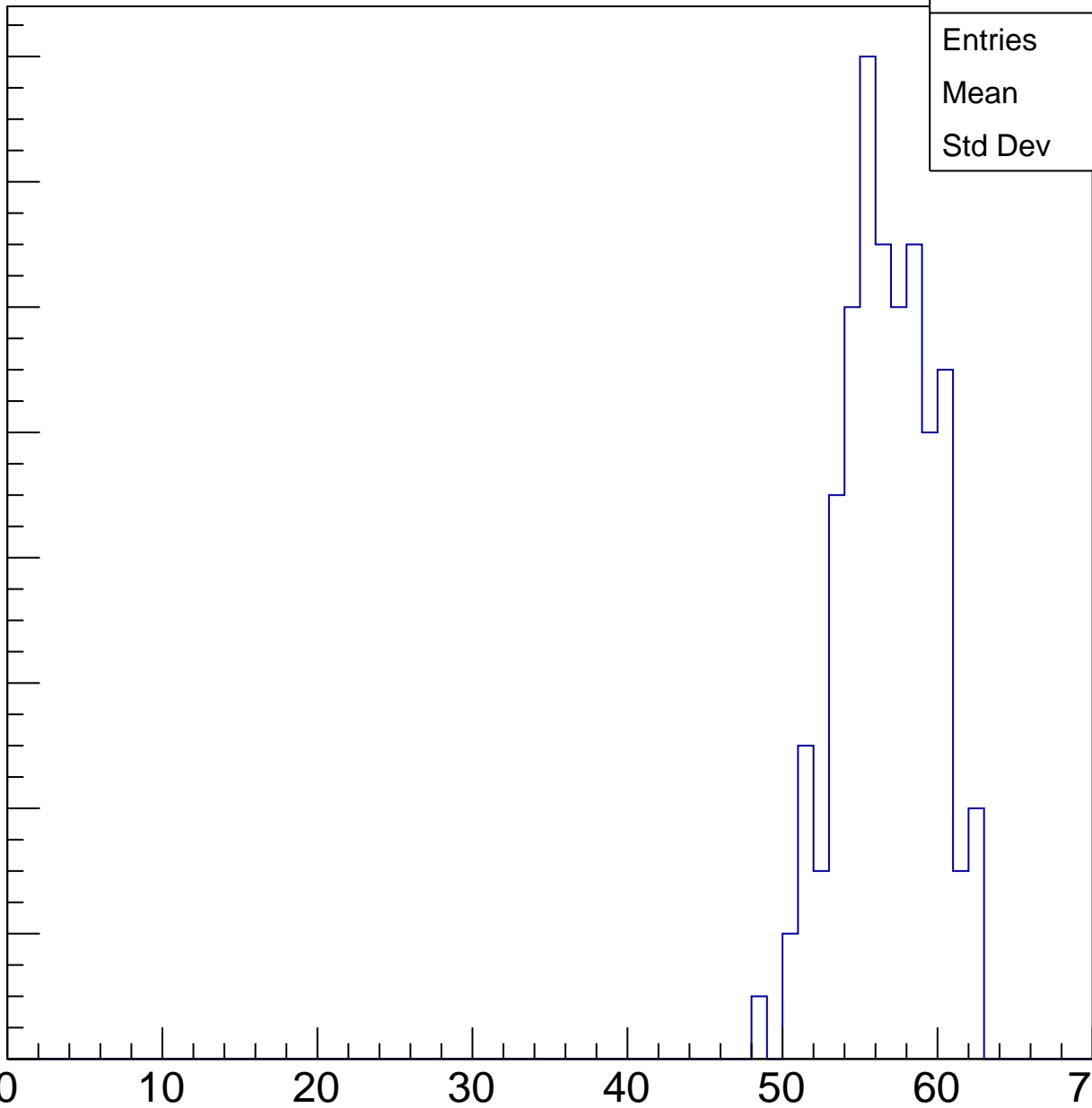
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	56.24
Std Dev	2.957

ampl



B1L001S, U21-ch92, adc5

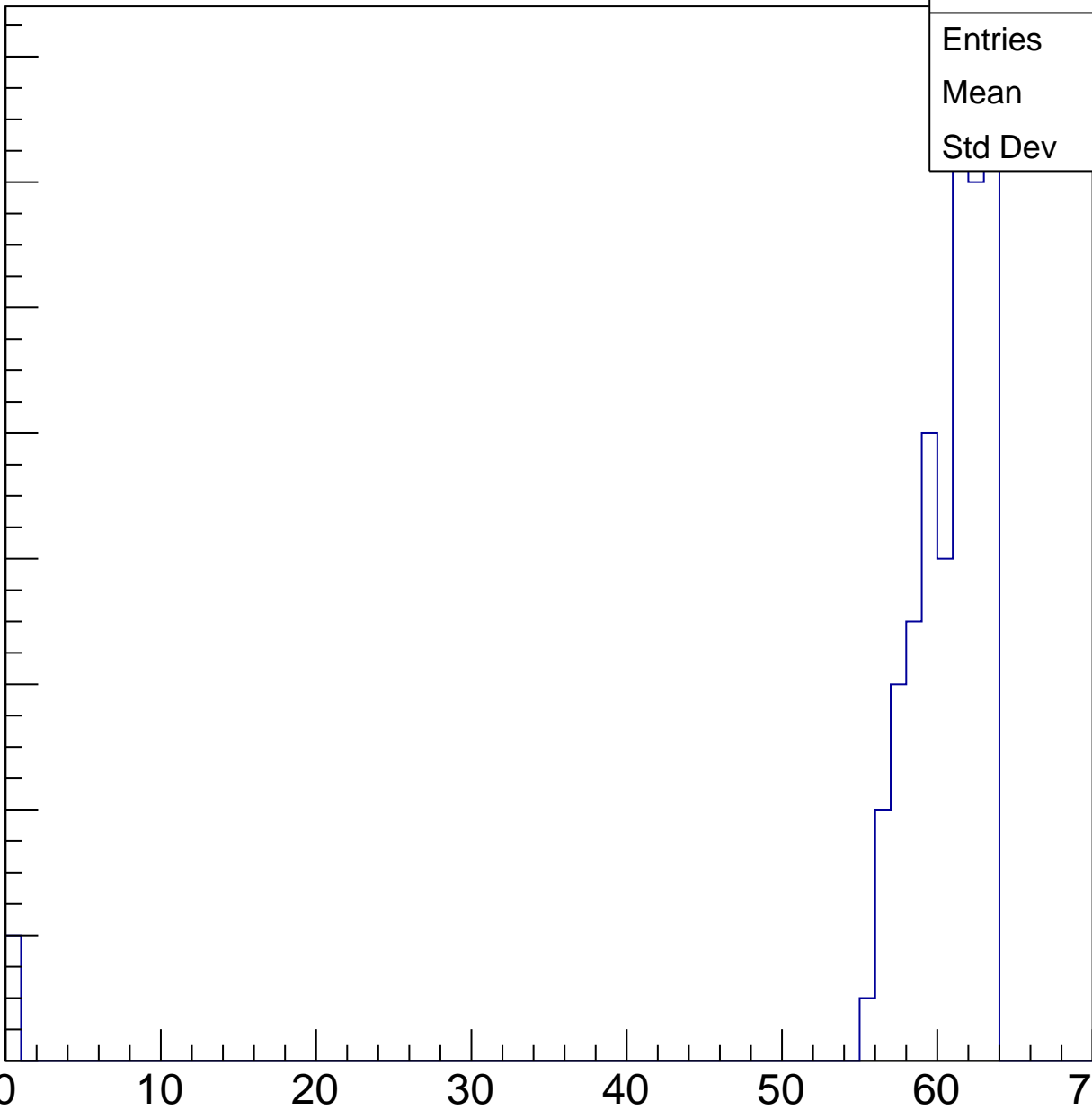
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.92
Std Dev	9.448

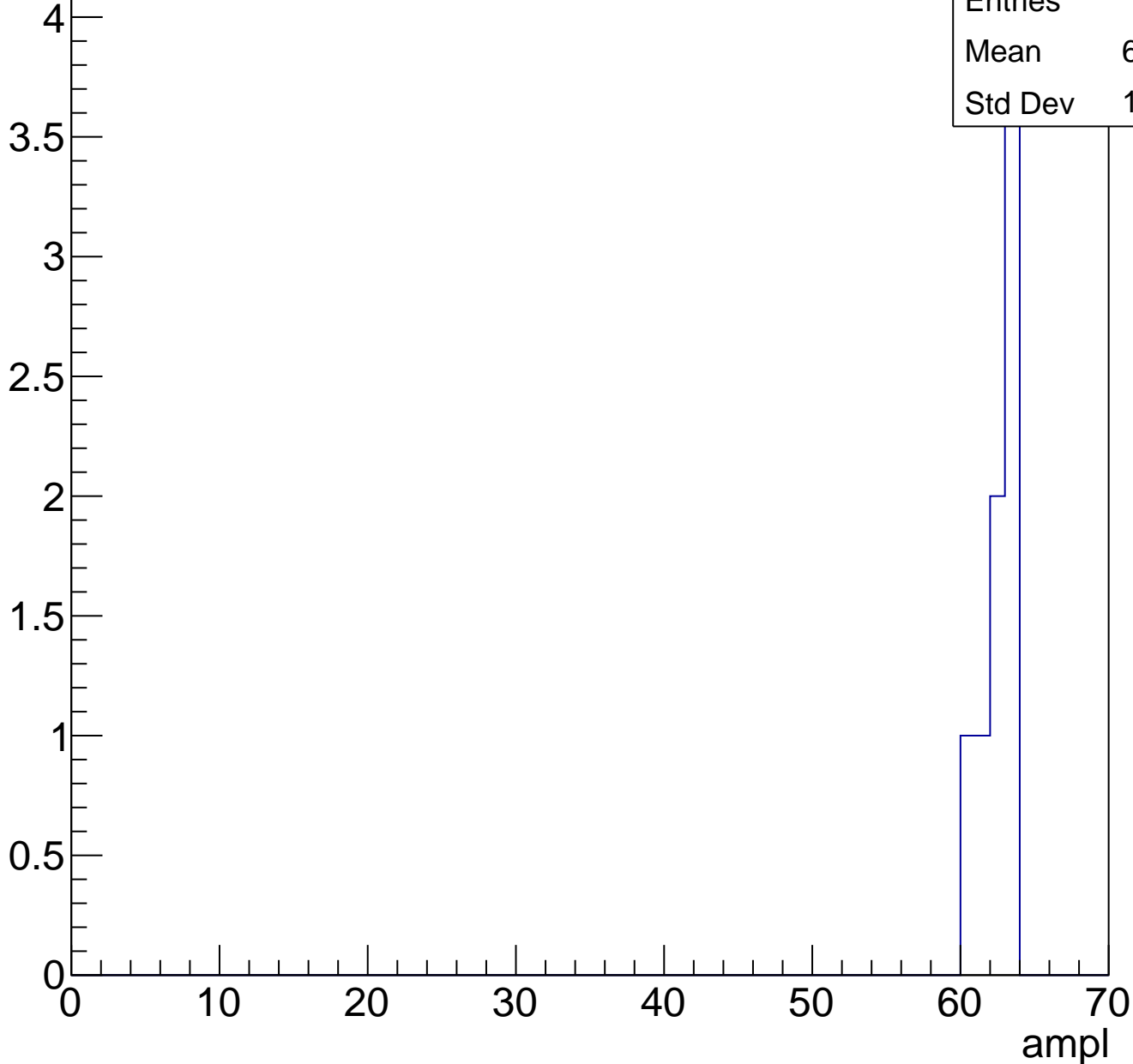
ampl



B1L001S, U21-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch93, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

145

Mean

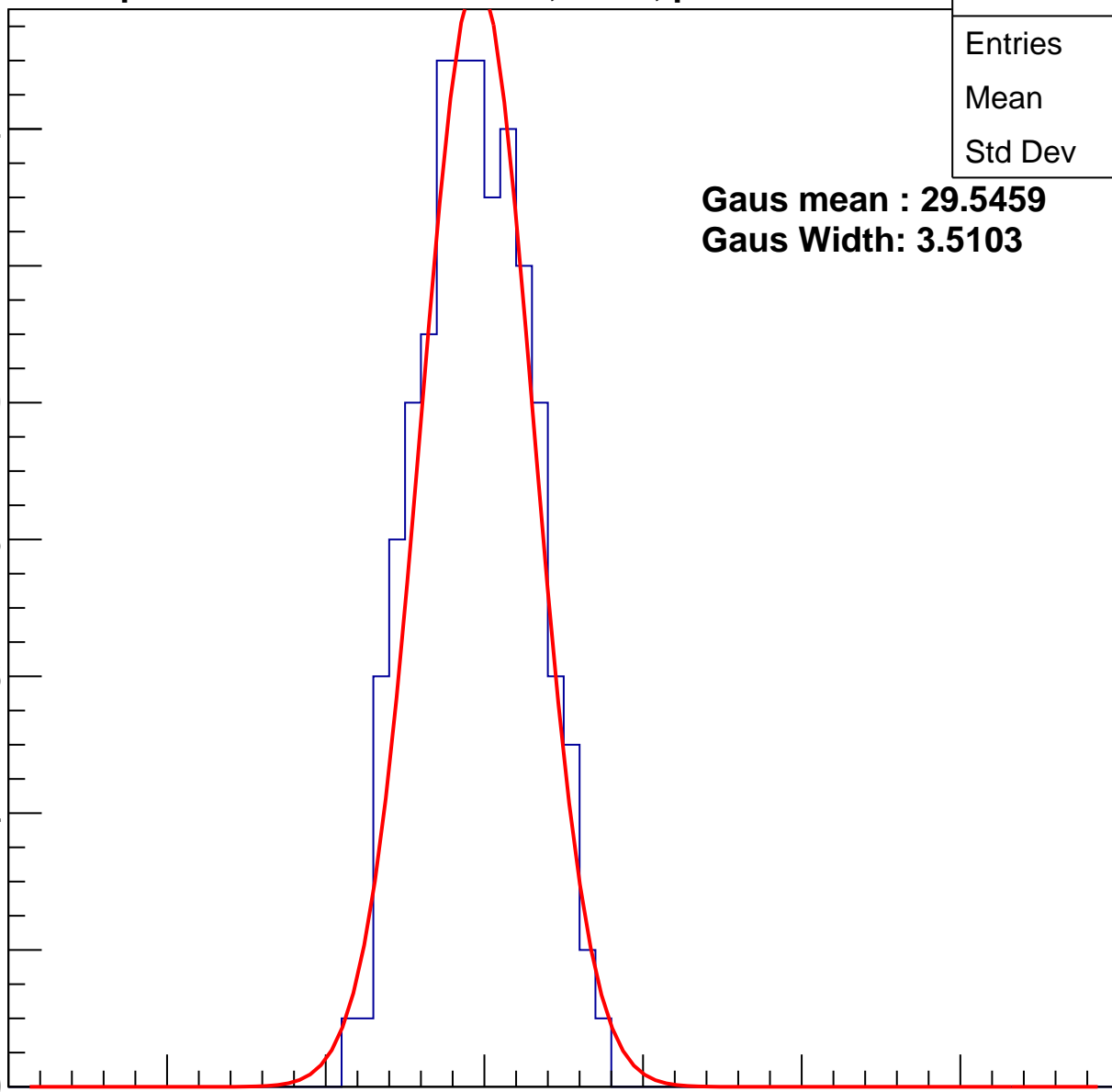
28.93

Std Dev

3.415

Gaus mean : 29.5459

Gaus Width: 3.5103

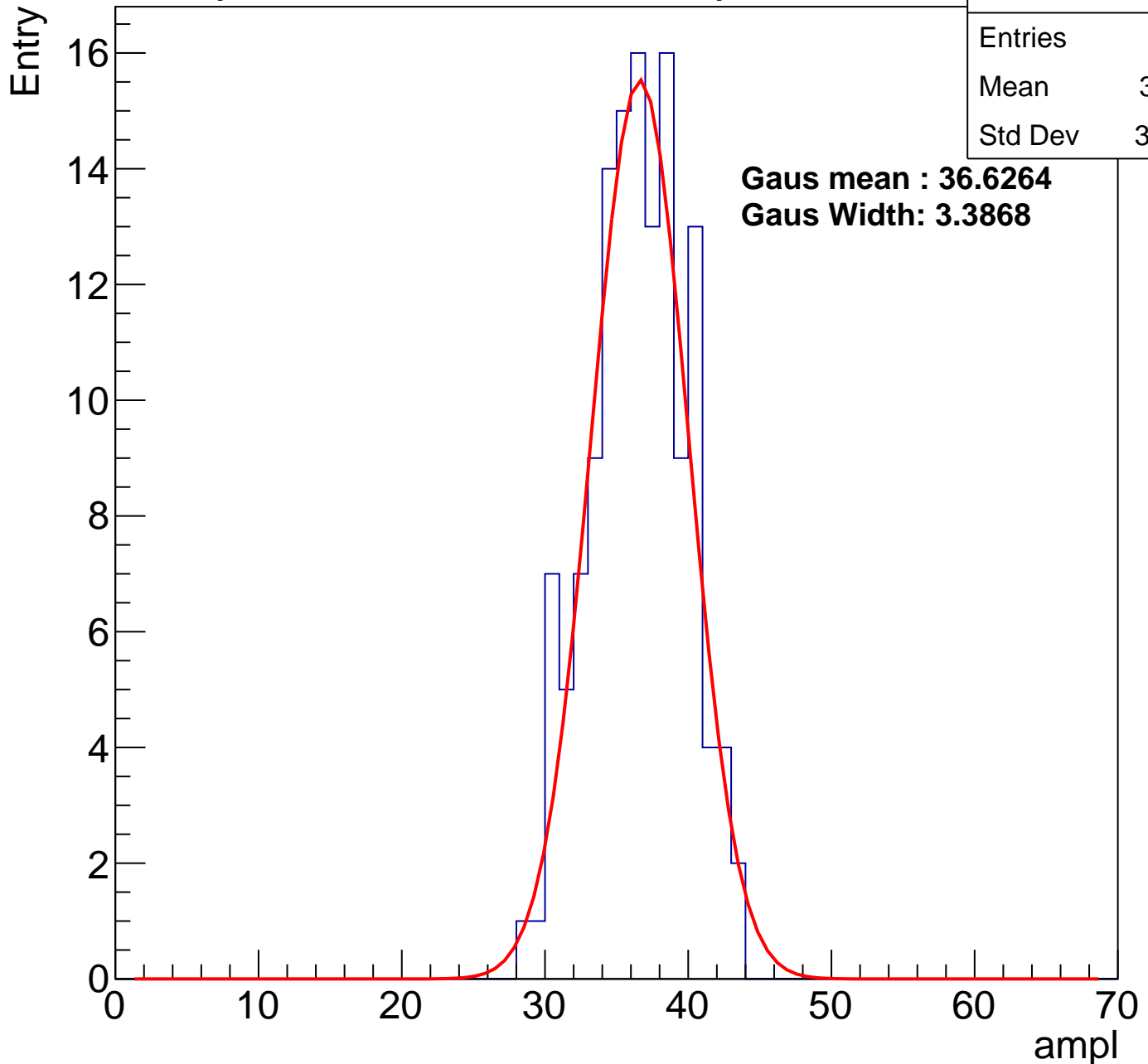


B1L001S, U21-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.01
Std Dev	3.279

Gaus mean : 36.6264
Gaus Width: 3.3868



B1L001S, U21-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	42.75
Std Dev	3.254

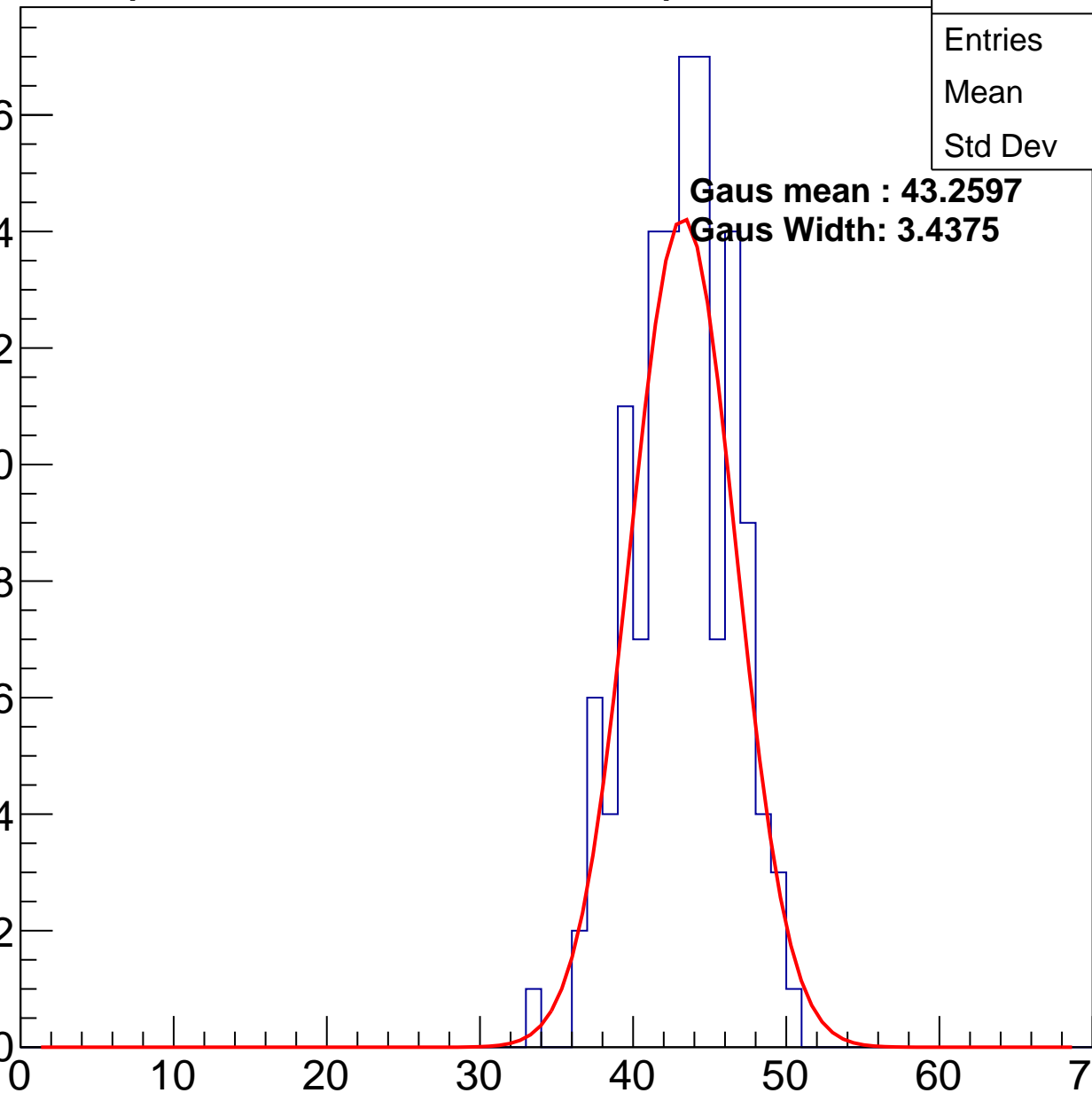
Gaus mean : 43.2597

Gaus Width: 3.4375

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

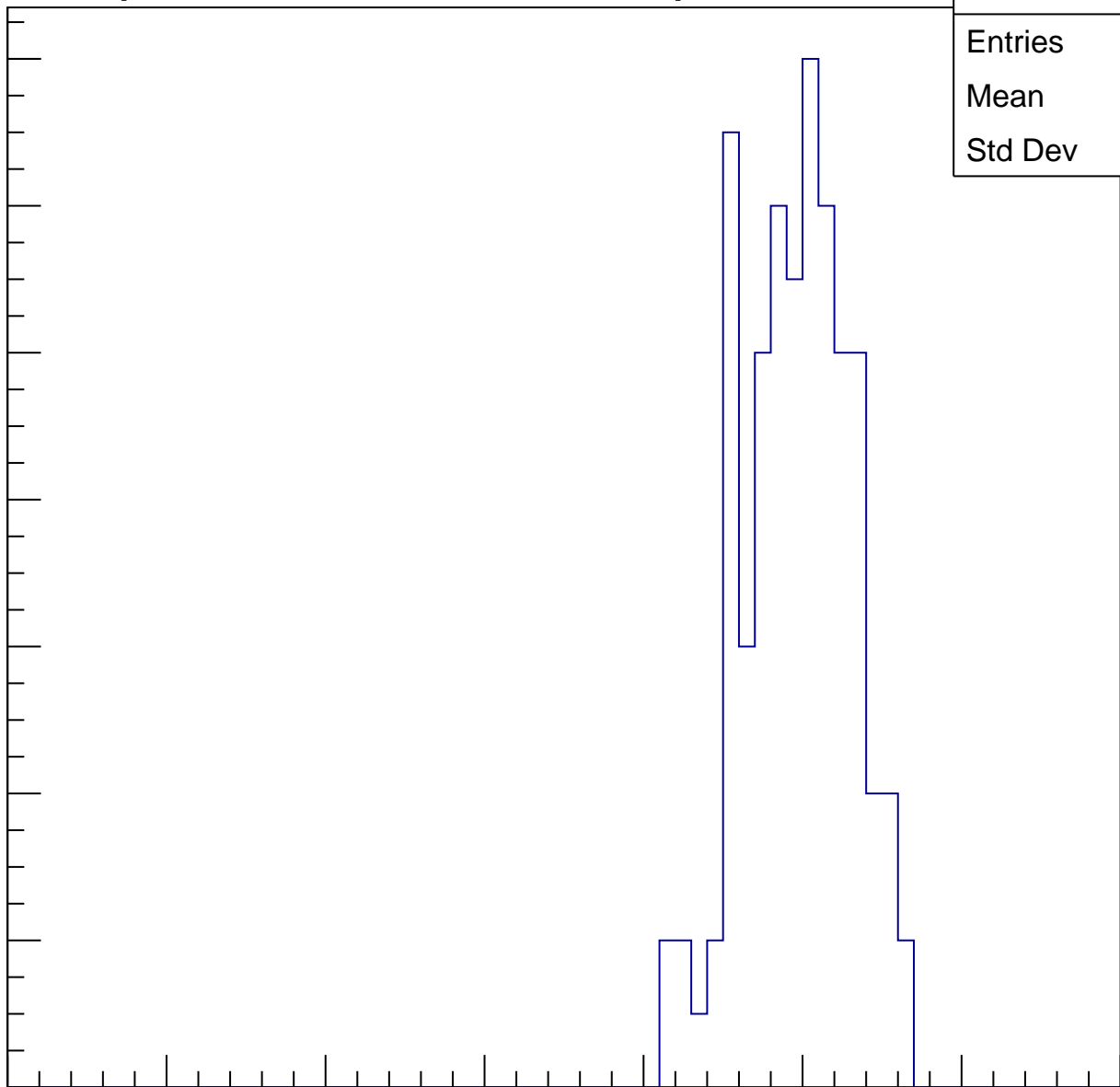
Entries	115
Mean	49.16
Std Dev	3.333

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	54.59
Std Dev	3.601

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

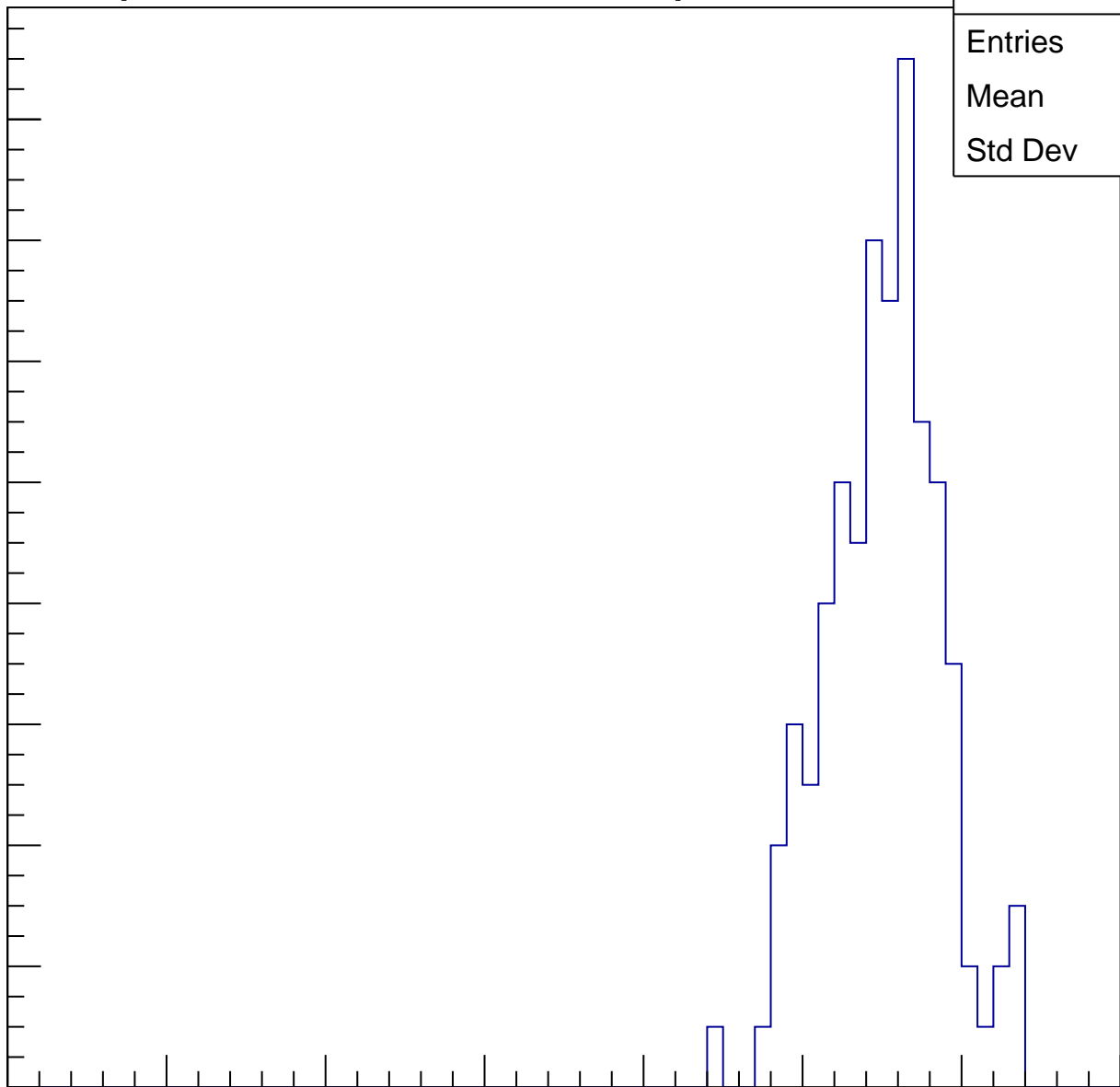
40

50

60

70

ampl



B1L001S, U21-ch93, adc5

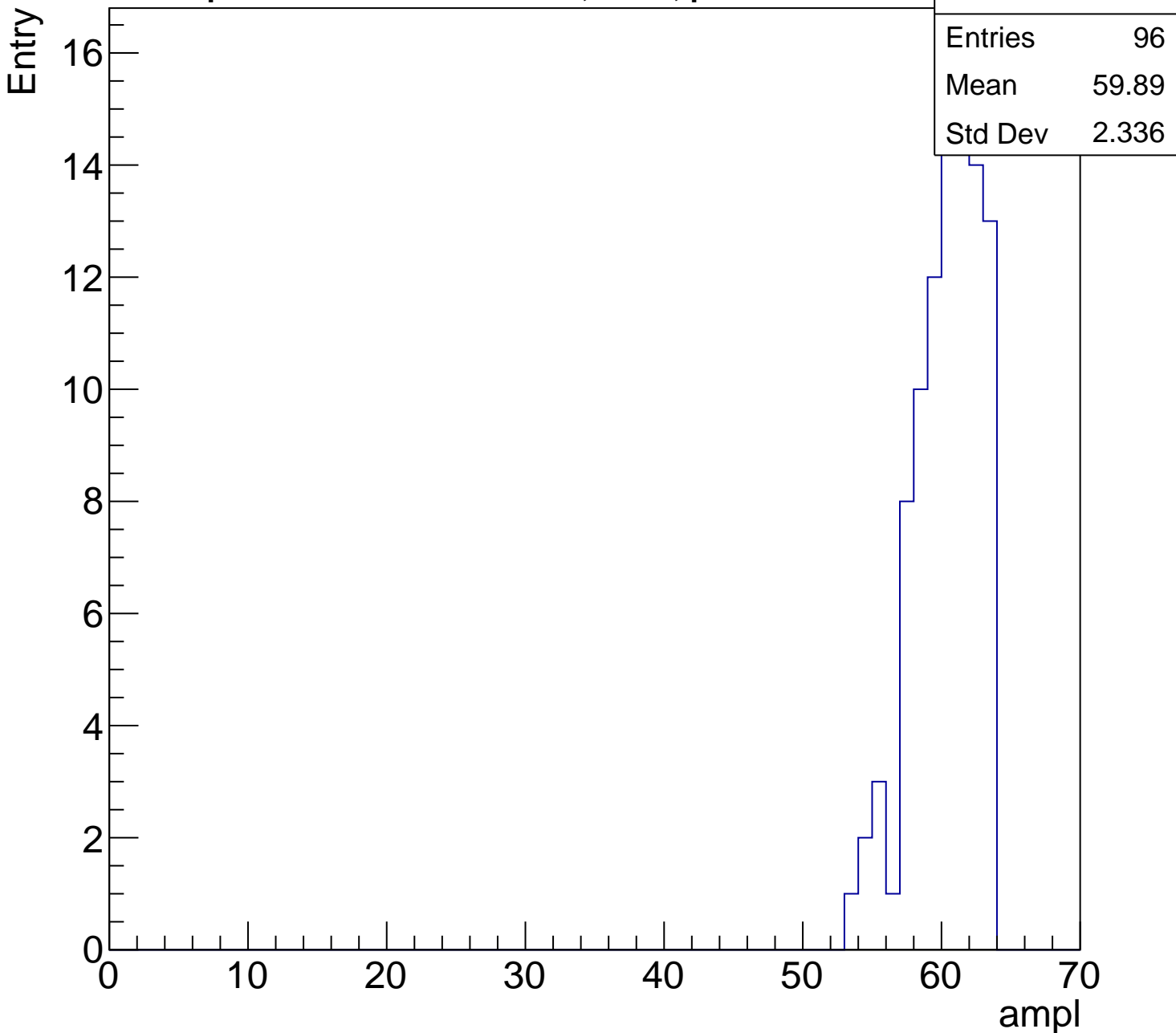
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.89
Std Dev	2.336

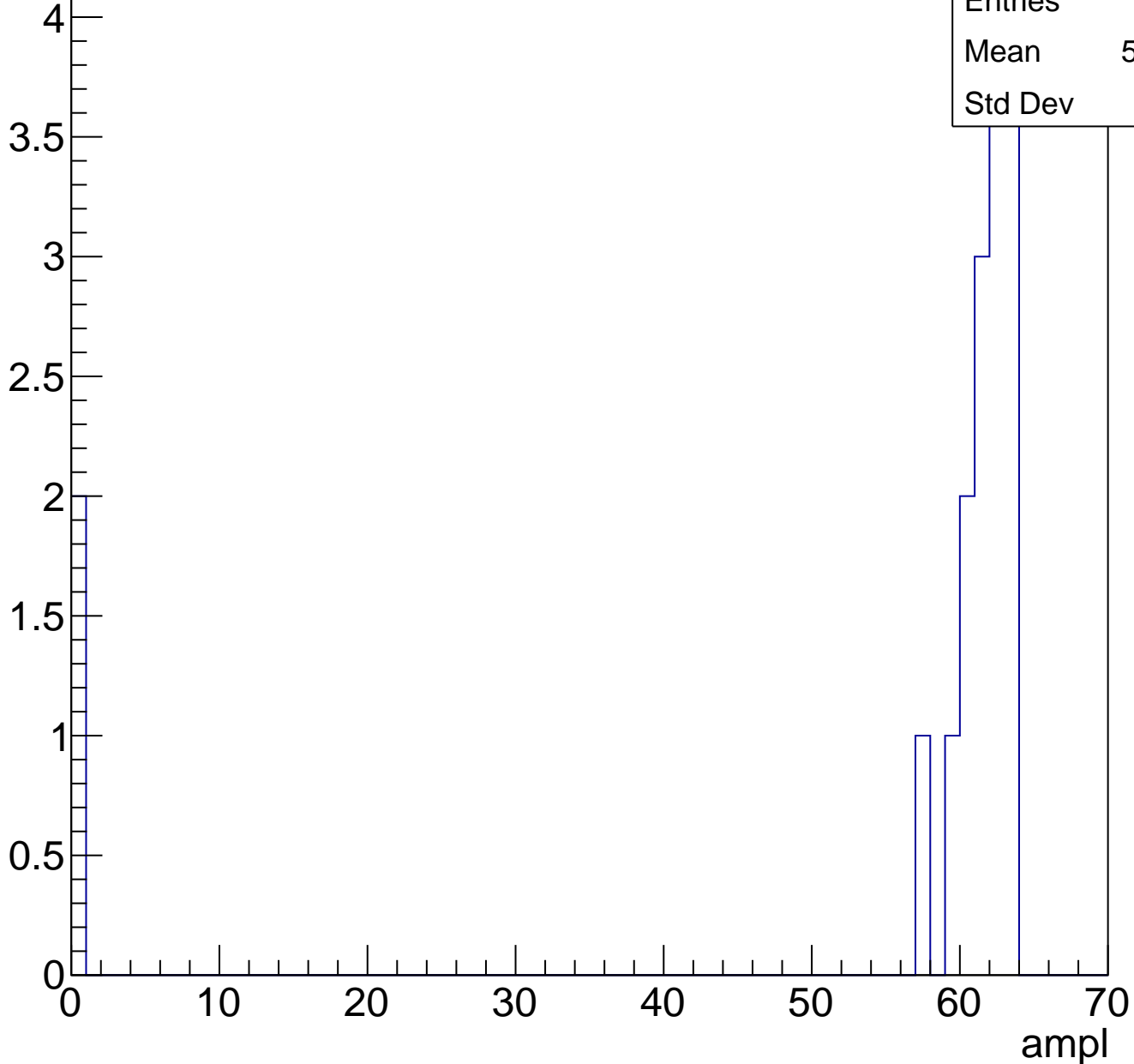
ampl



B1L001S, U21-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U21-ch94, adc0

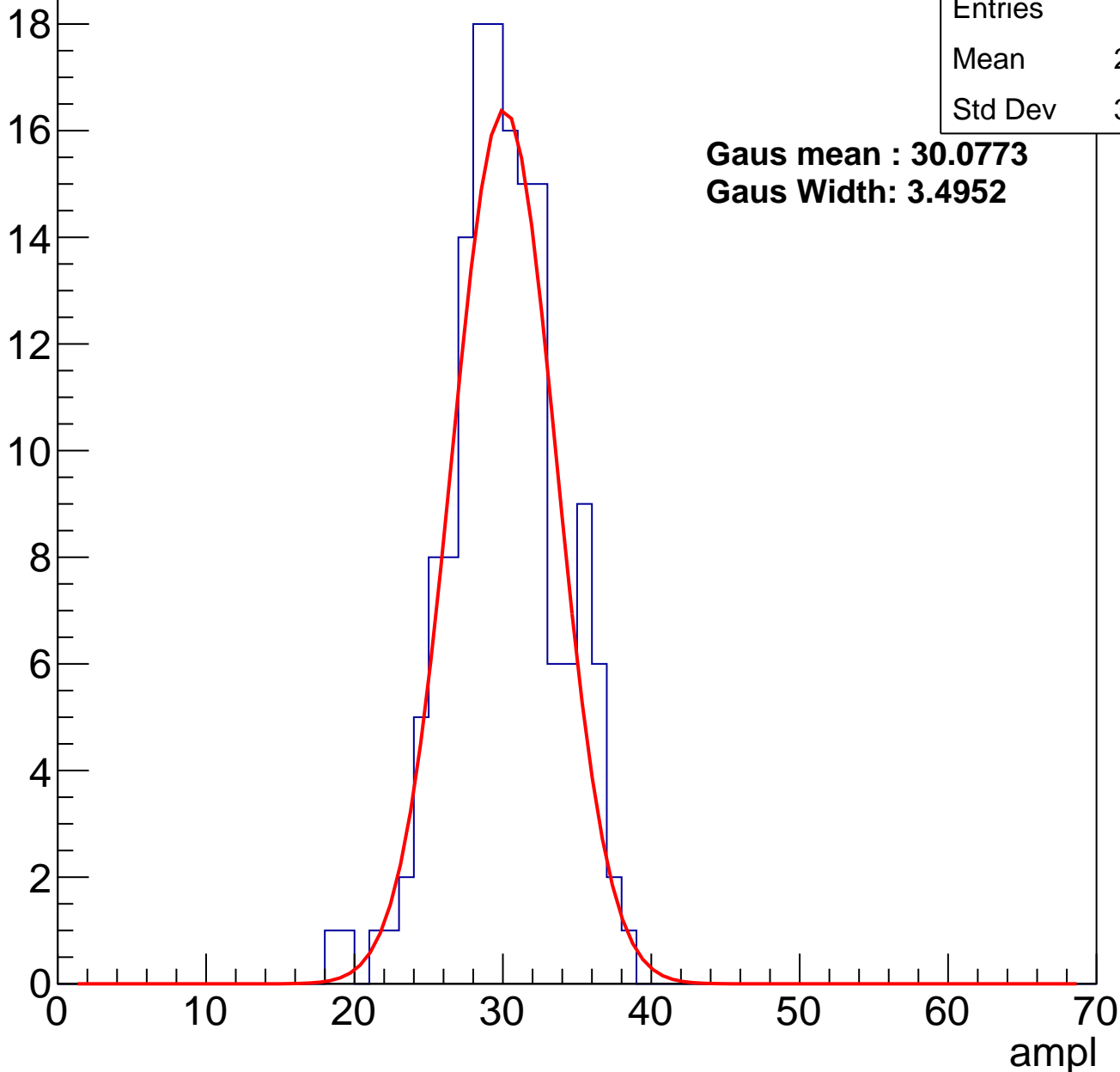
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.59
Std Dev	3.647

Gaus mean : 30.0773

Gaus Width: 3.4952

Entry



B1L001S, U21-ch94, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	36.18
Std Dev	3.174

Gaus mean : 36.4349

Gaus Width: 3.6893

Entry

12

10

8

6

4

2

0

0

10

20

30

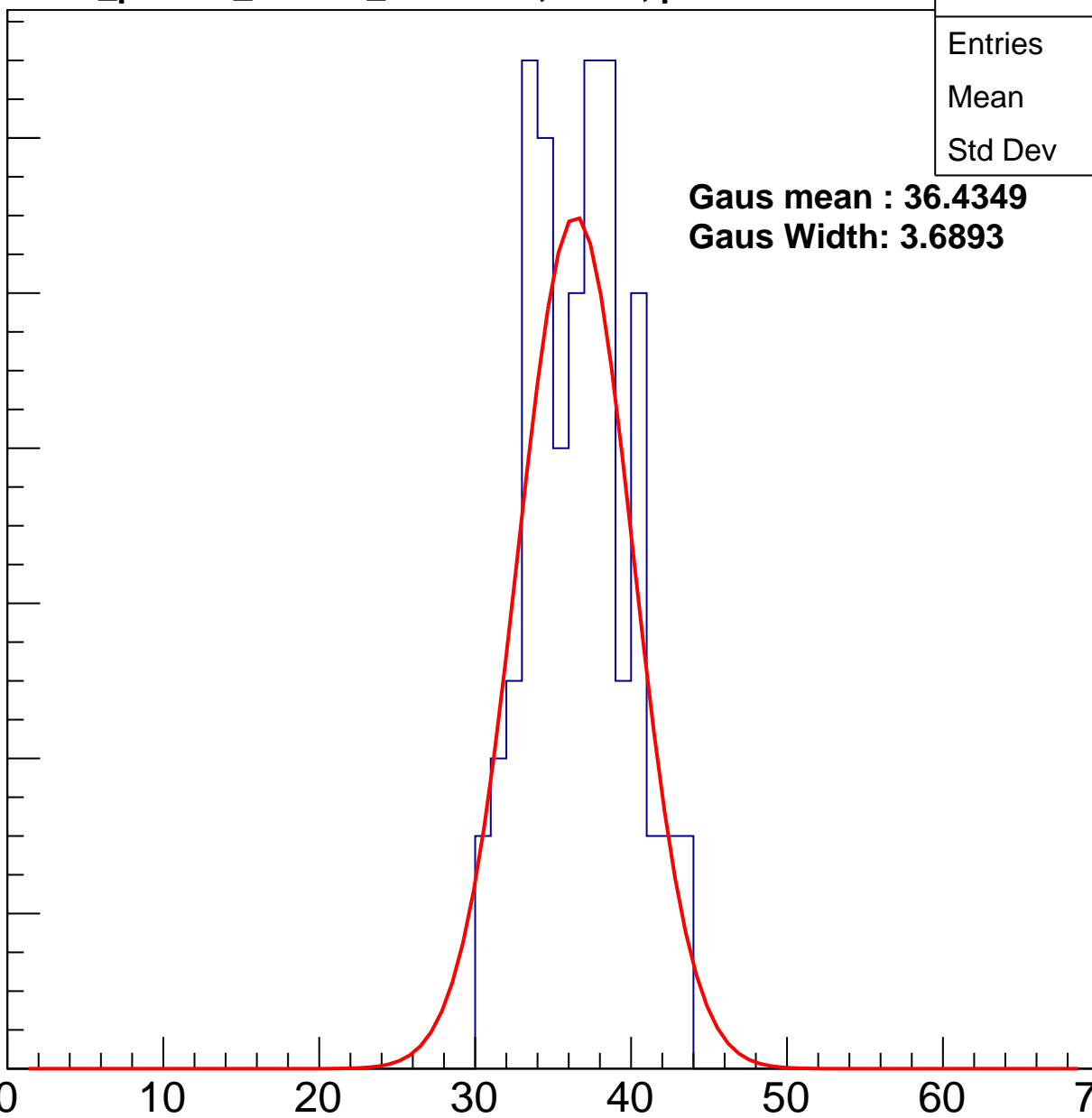
40

50

60

70

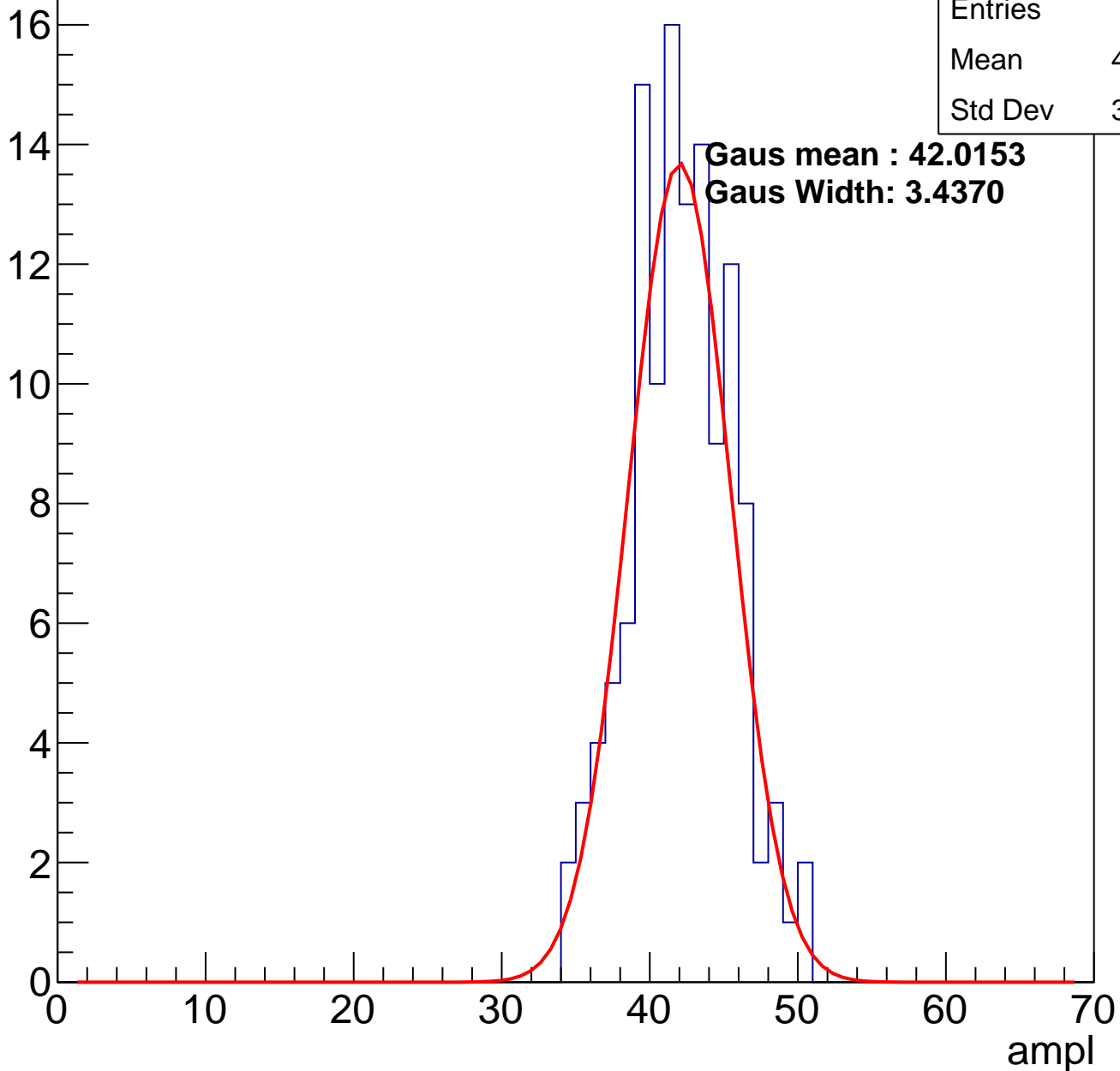
ampl



B1L001S, U21-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch94, adc3

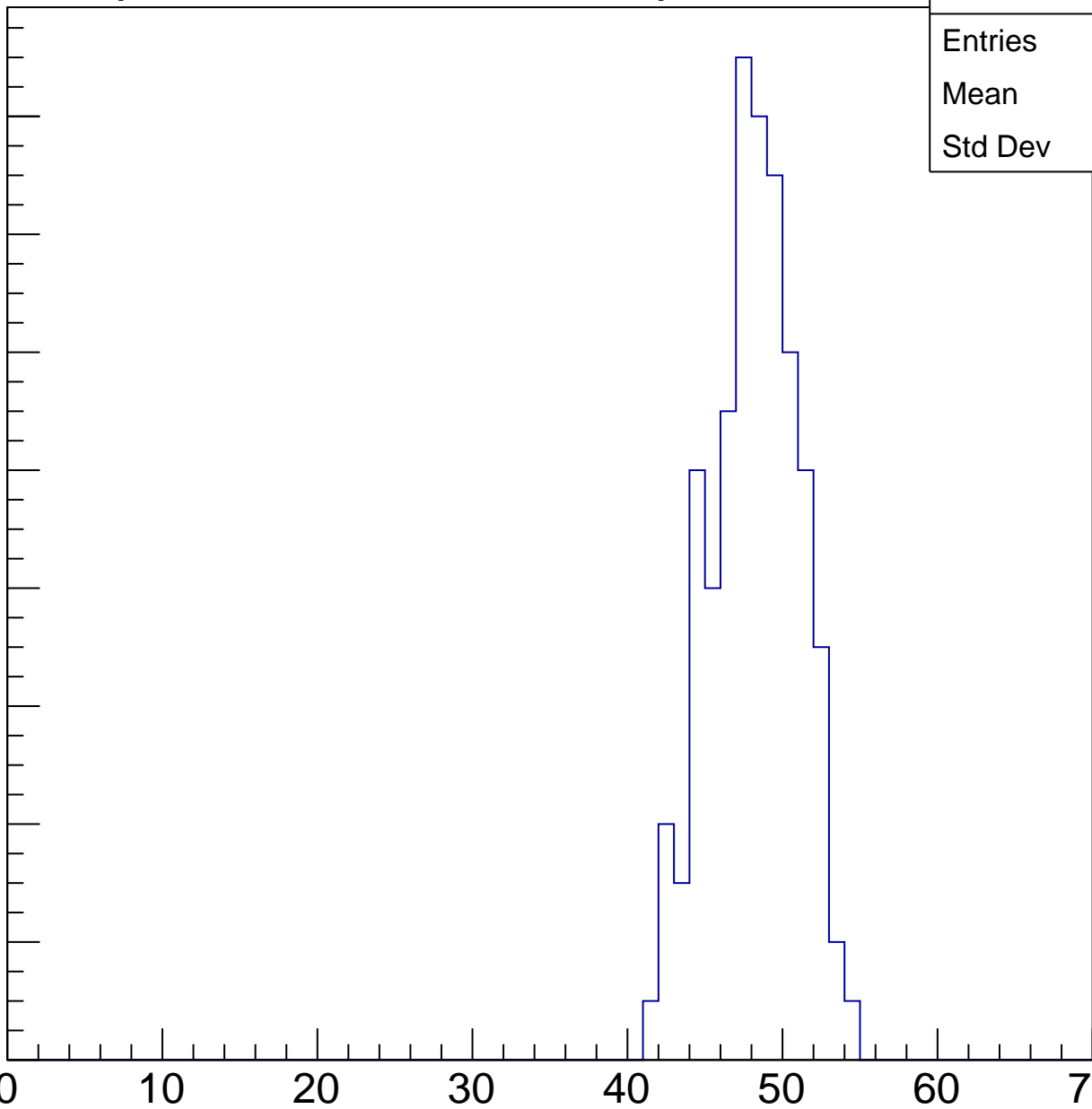
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	47.69
Std Dev	2.775

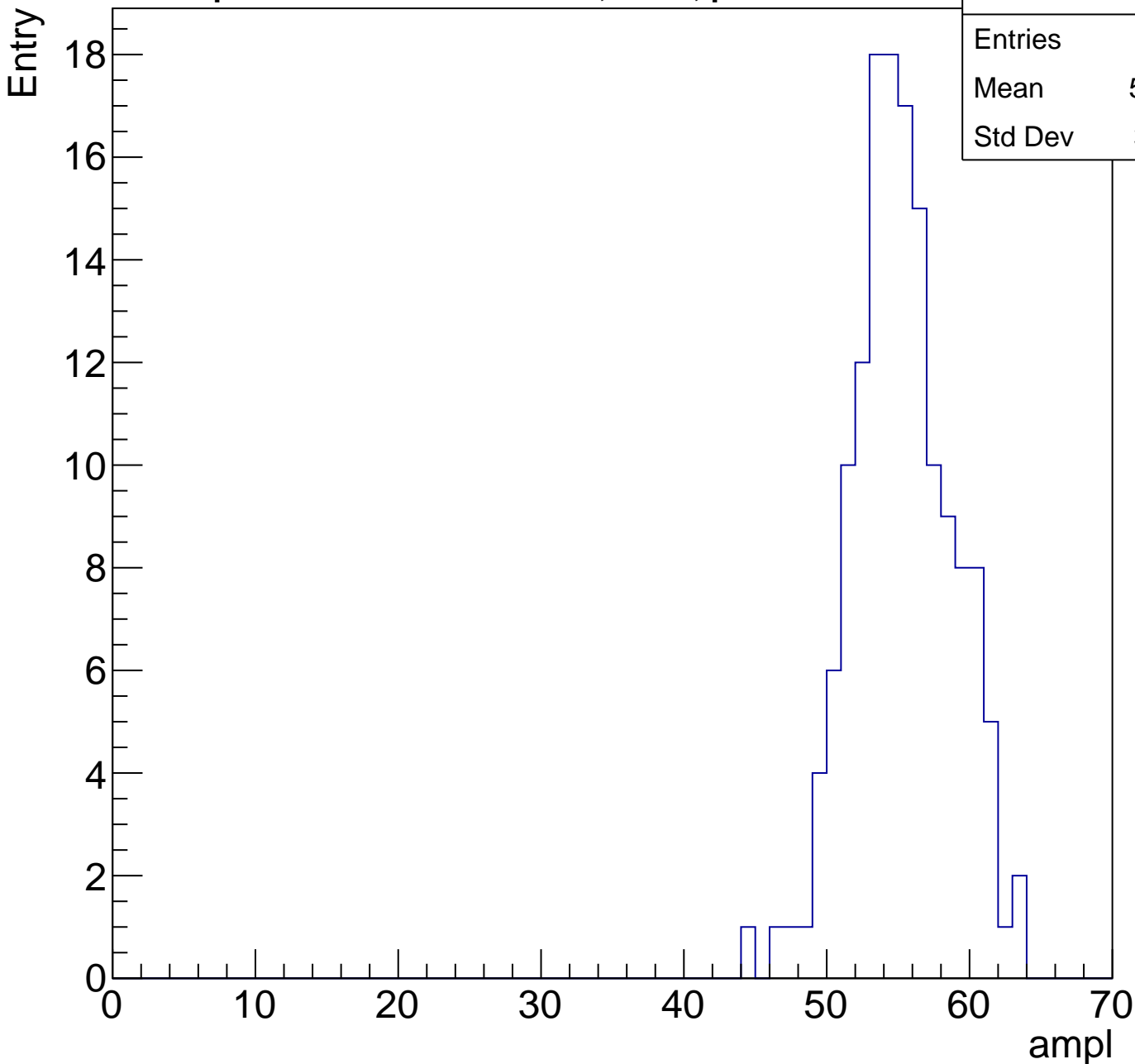
ampl



B1L001S, U21-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	54.78
Std Dev	3.481



B1L001S, U21-ch94, adc5

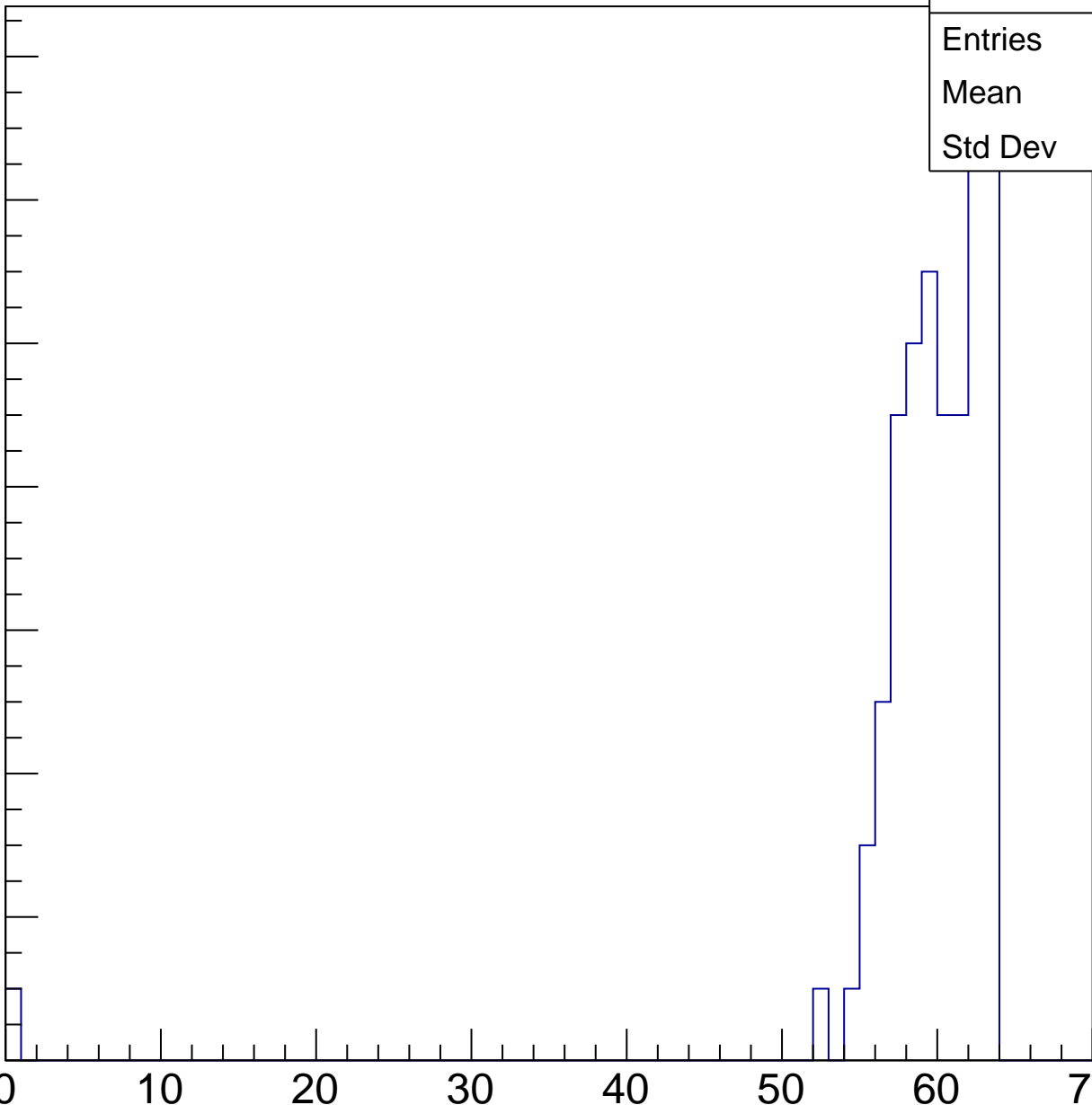
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	58.94
Std Dev	6.882

ampl

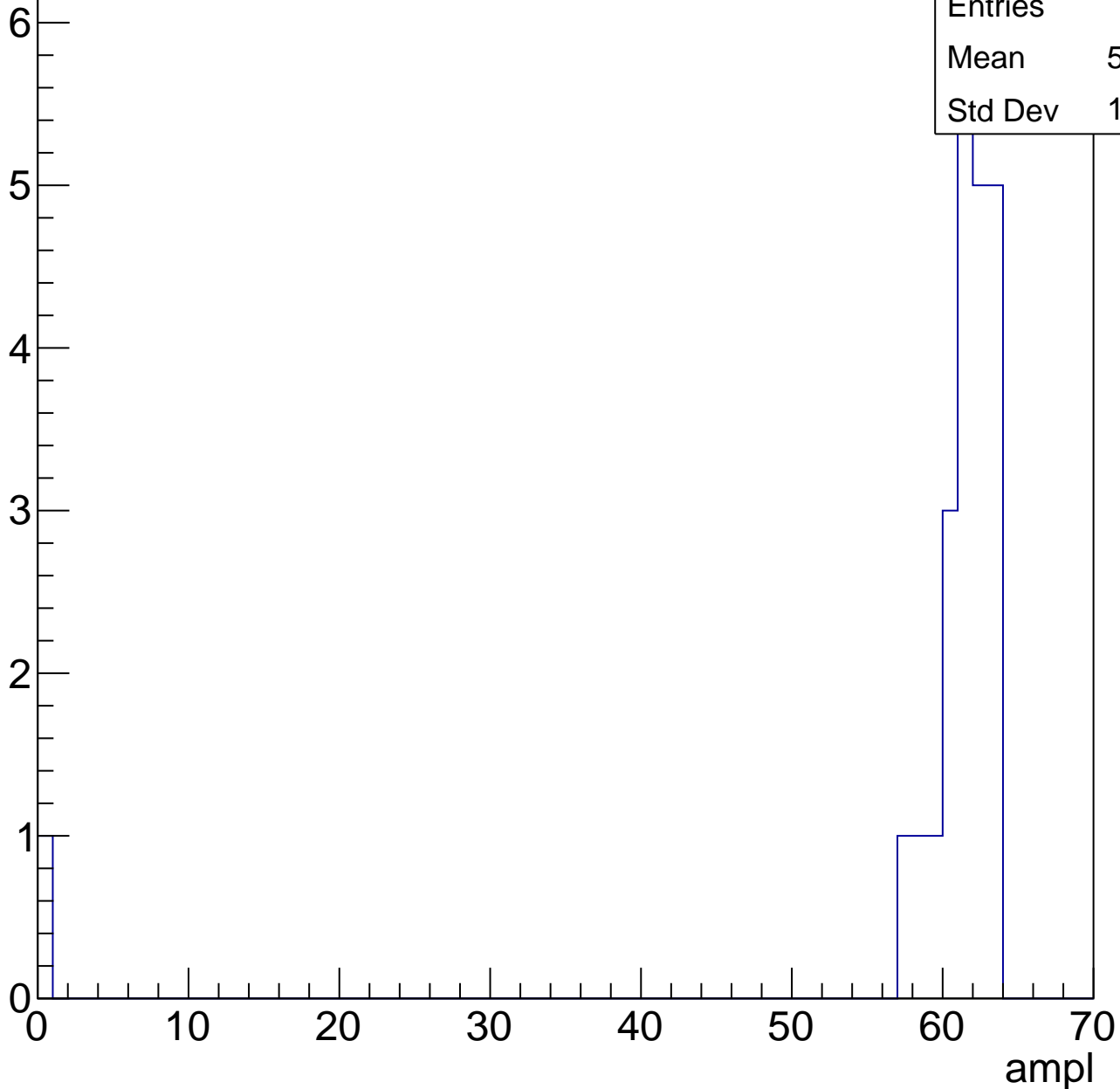


B1L001S, U21-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	58.48
Std Dev	12.57



B1L001S, U21-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U21-ch95, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.18
Std Dev	4.077

Gaus mean : 29.7974

Gaus Width: 3.3924

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

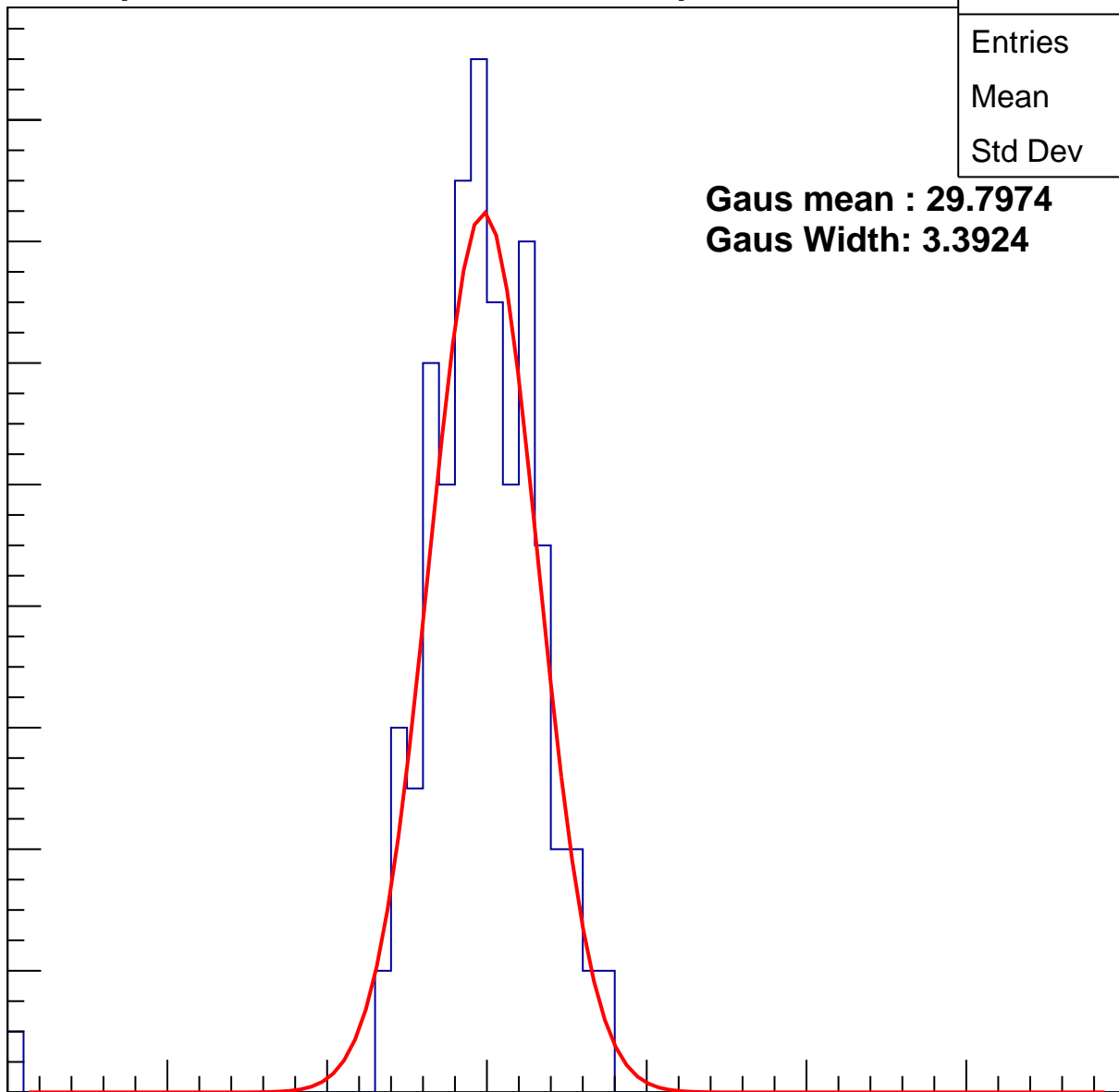
40

50

60

70

ampl



B1L001S, U21-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	36.44
Std Dev	3.653

Gaus mean : 36.9185

Gaus Width: 3.9267

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

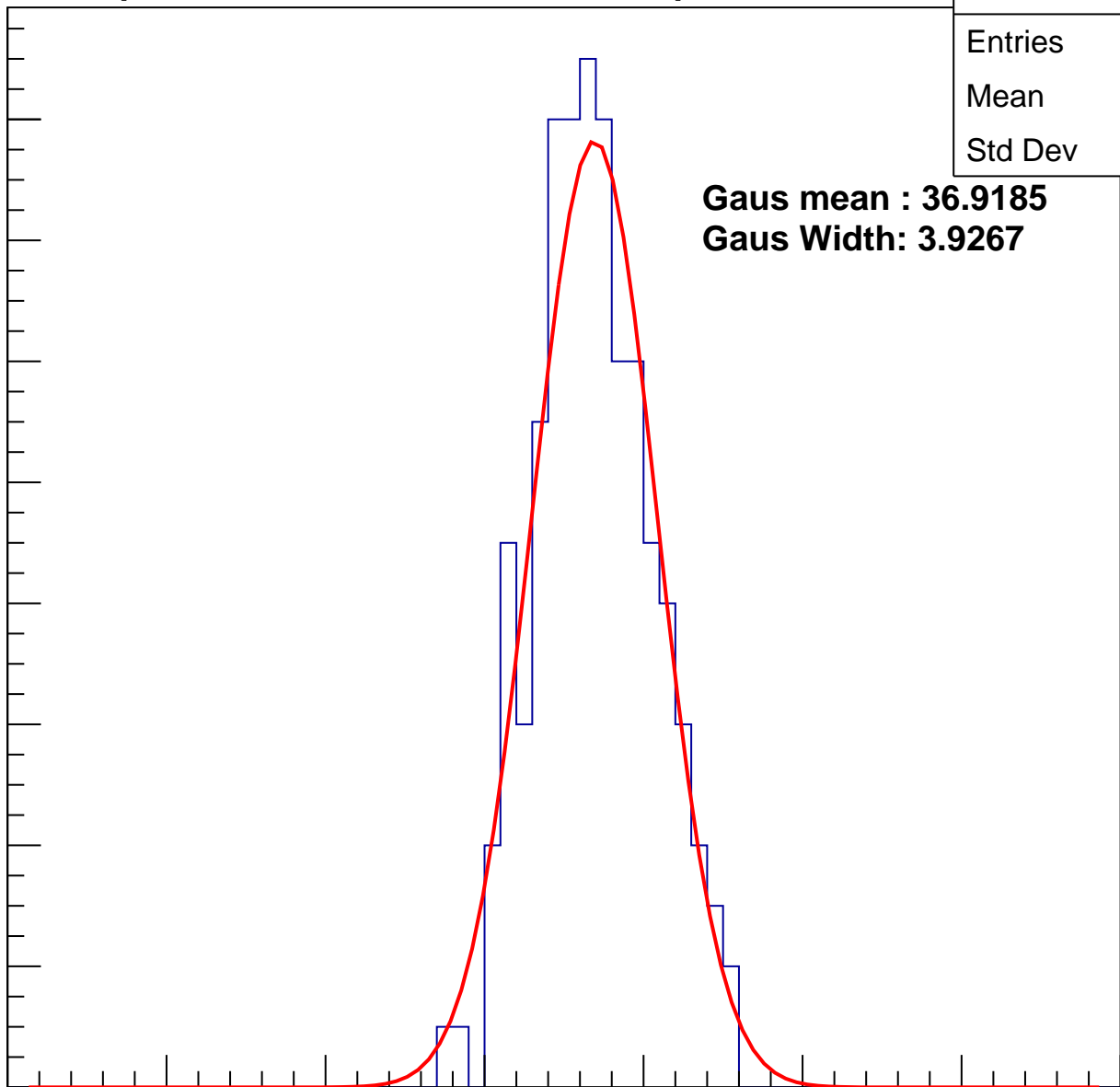
40

50

60

70

ampl



B1L001S, U21-ch95, adc2

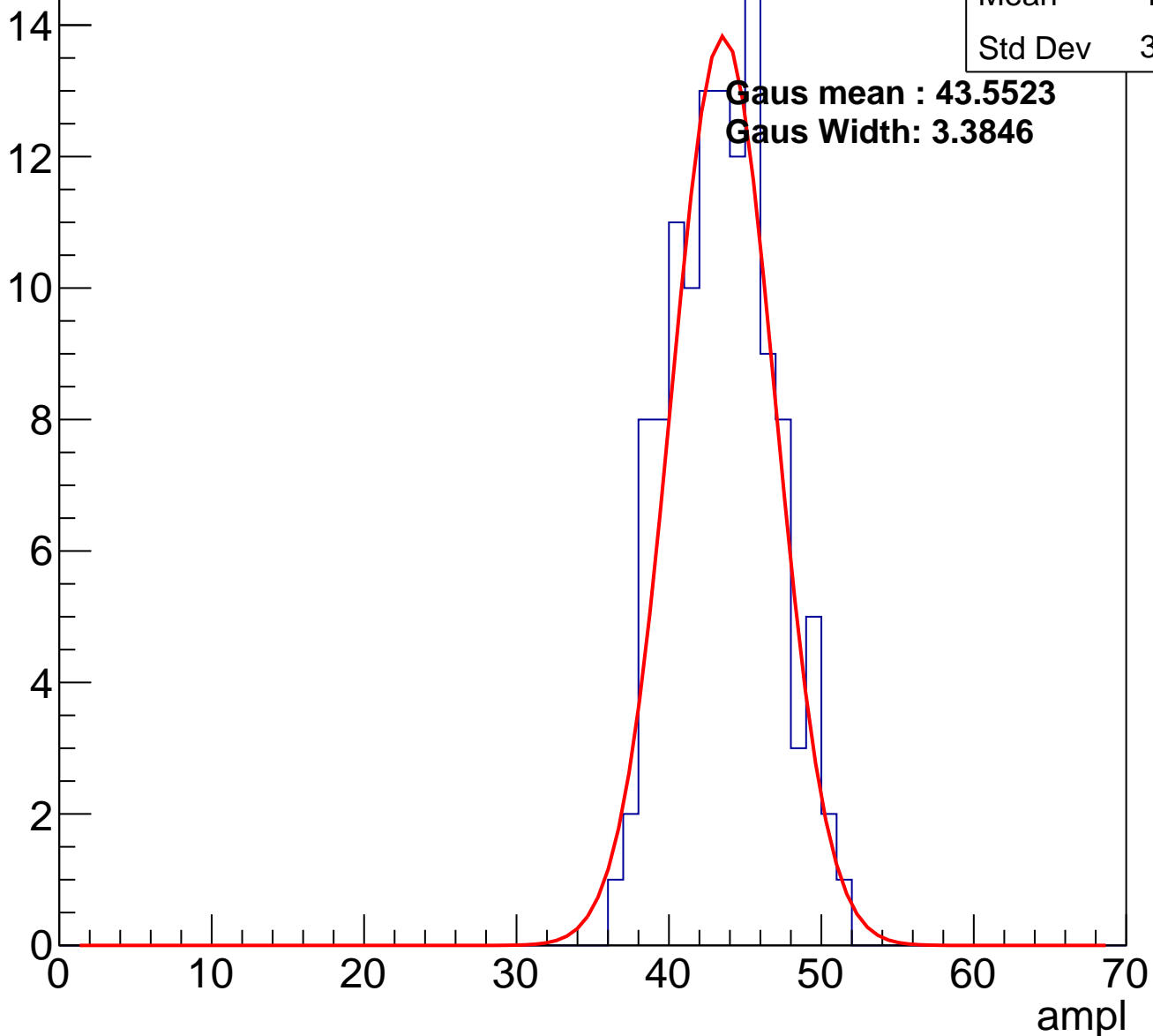
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	43.09
Std Dev	3.275

Gaus mean : 43.5523

Gaus Width: 3.3846

Entry



B1L001S, U21-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

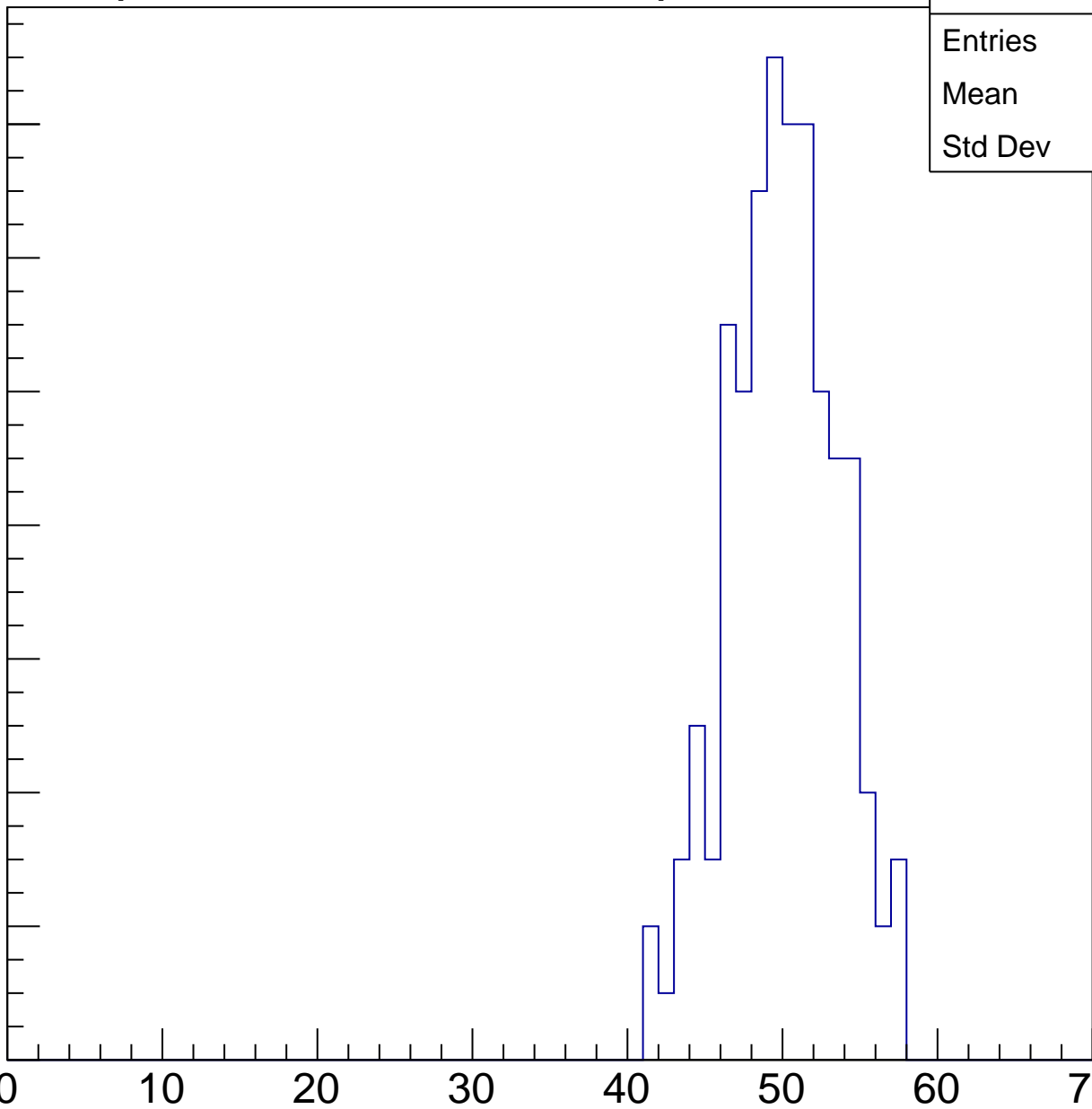
Entries	128
Mean	49.55
Std Dev	3.461

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

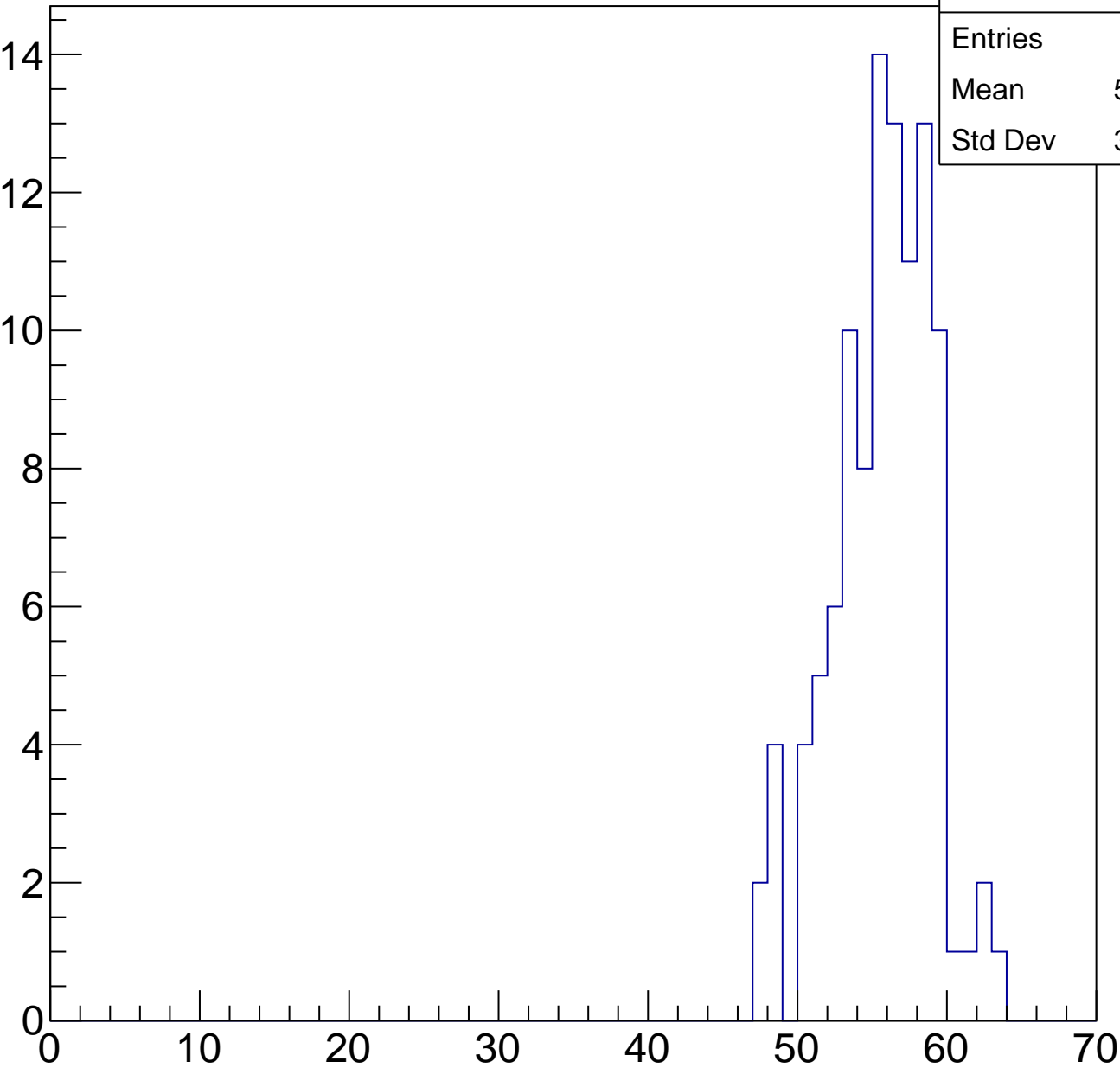
Entries	105
Mean	55.16
Std Dev	3.324

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch95, adc5

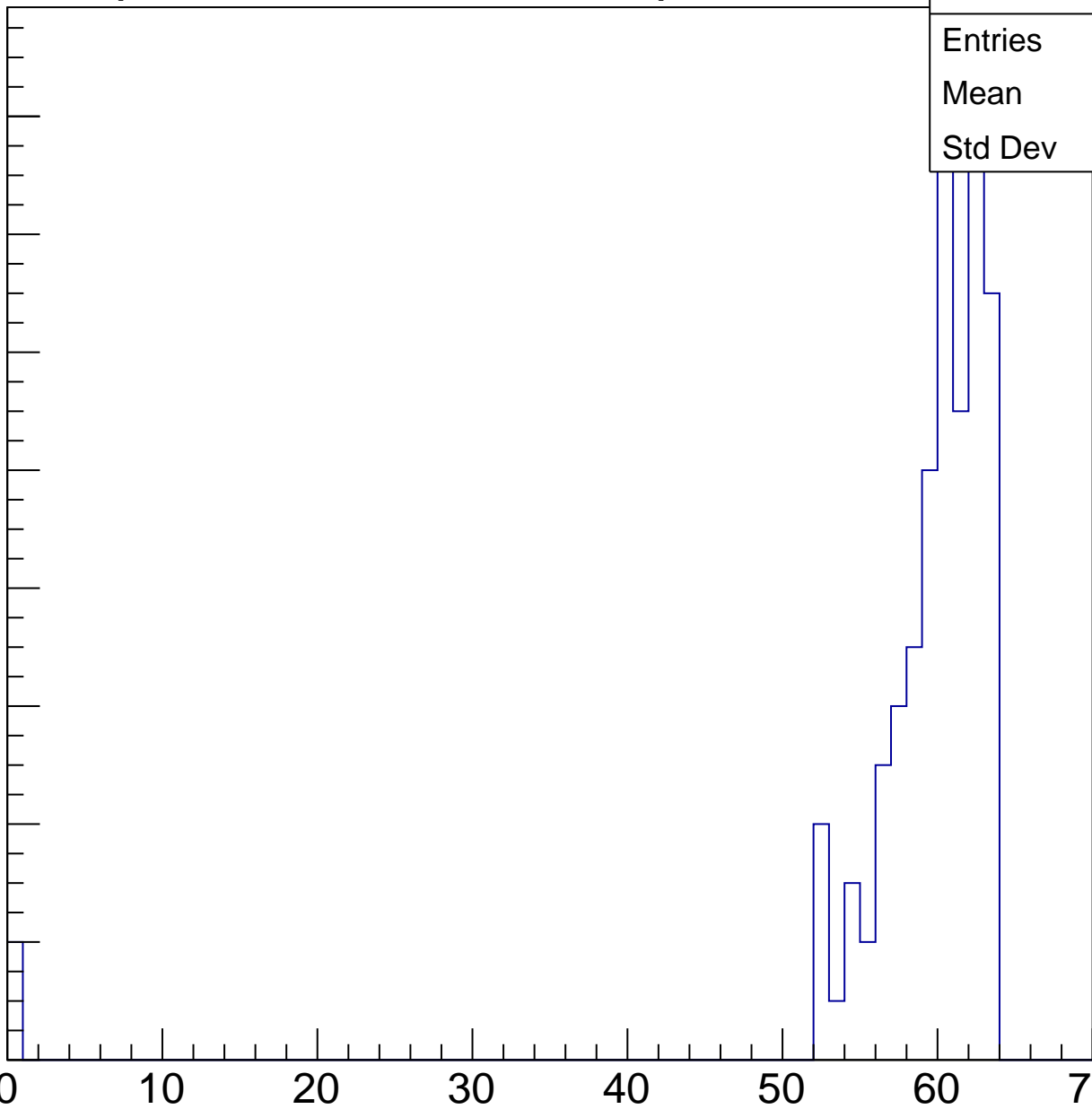
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.28
Std Dev	8.932

ampl

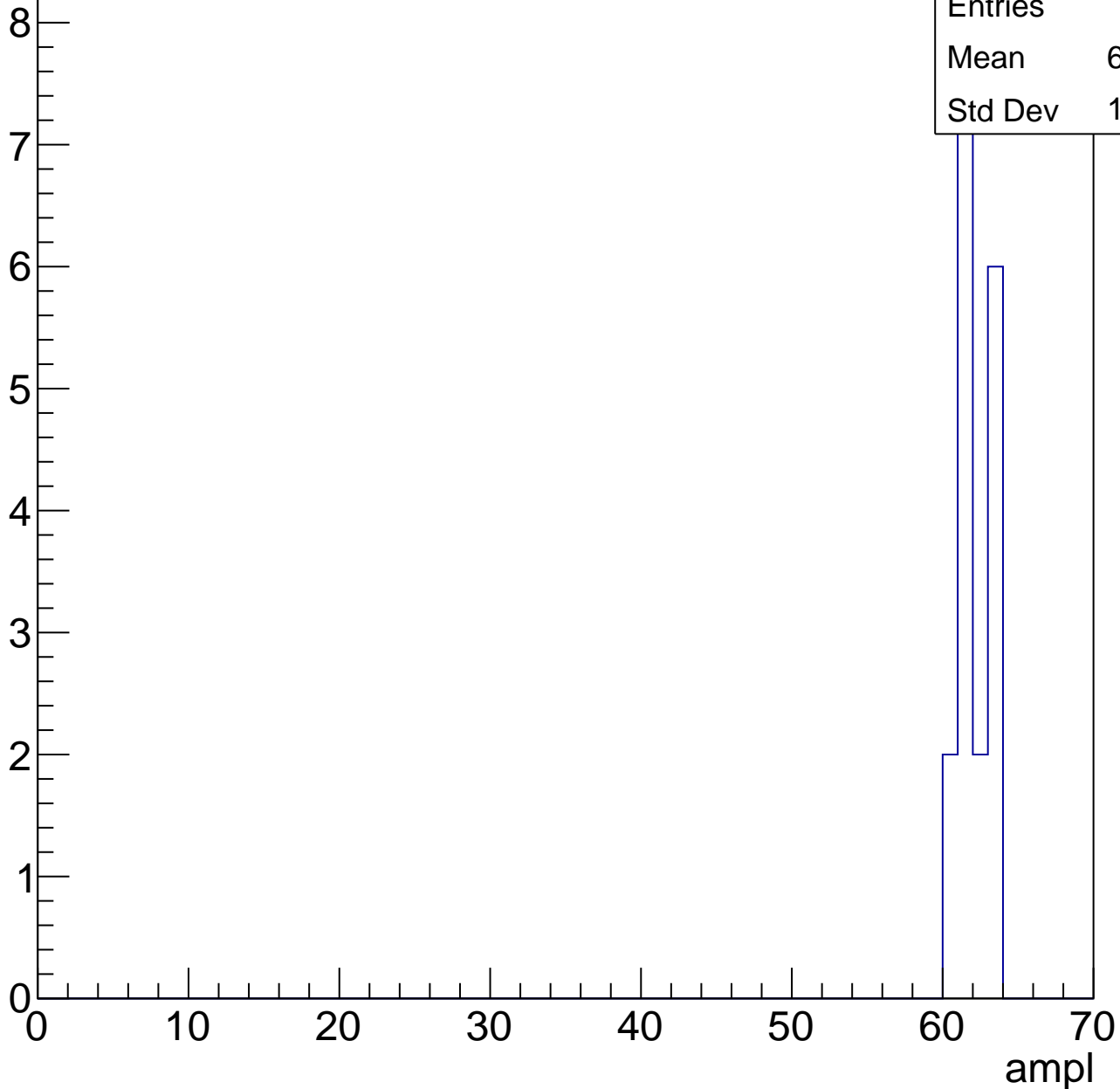


B1L001S, U21-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

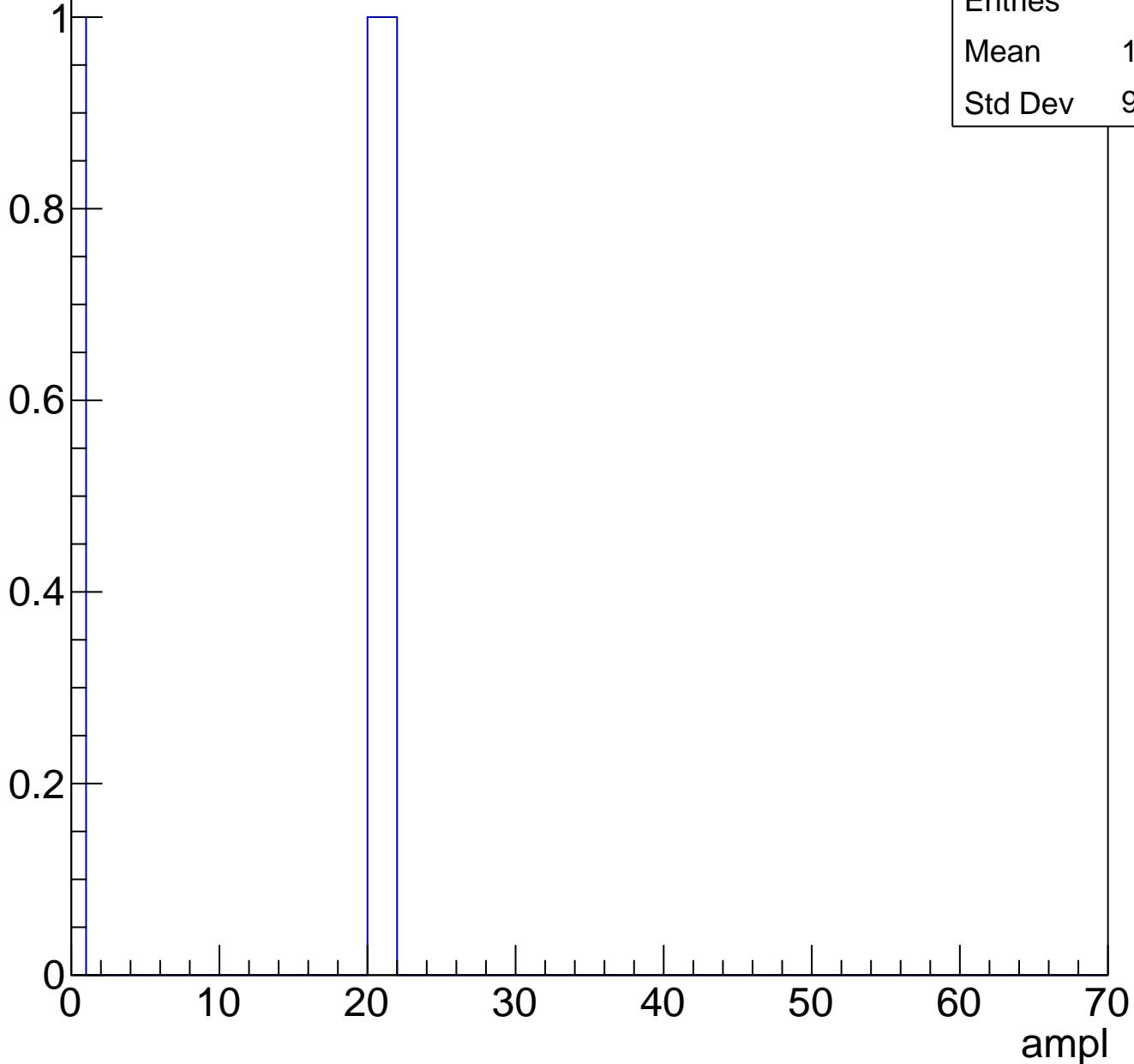
Entries	18
Mean	61.67
Std Dev	1.054



B1L001S, U21-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	13.67
Std Dev	9.672

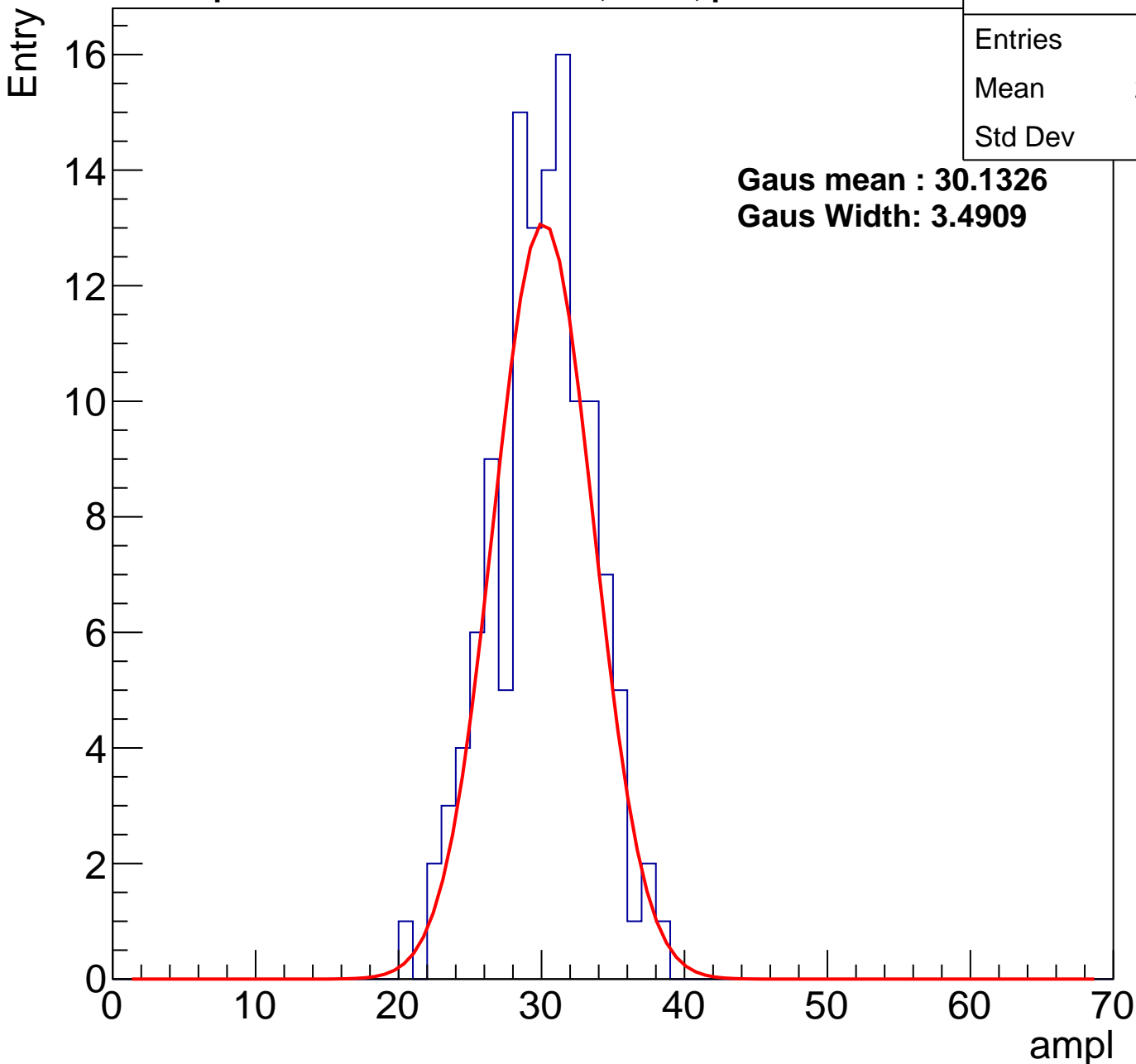
B1L001S, U21-ch96, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.61
Std Dev	3.47

Gaus mean : 30.1326

Gaus Width: 3.4909



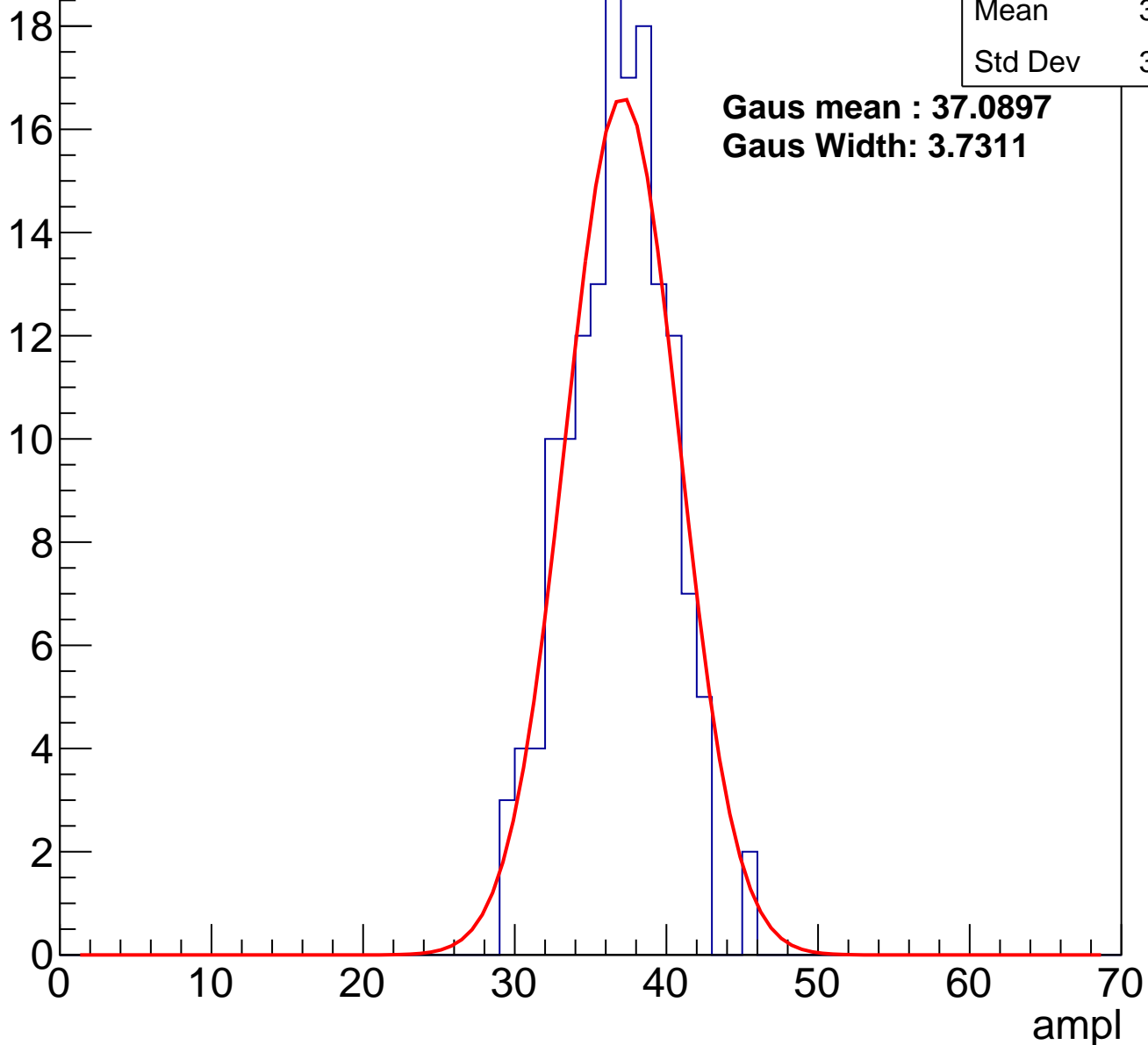
B1L001S, U21-ch96, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	36.34
Std Dev	3.276

Gaus mean : 37.0897
Gaus Width: 3.7311

Entry



B1L001S, U21-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	42.83
Std Dev	2.715

Gaus mean : 43.4031

Gaus Width: 2.3774

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

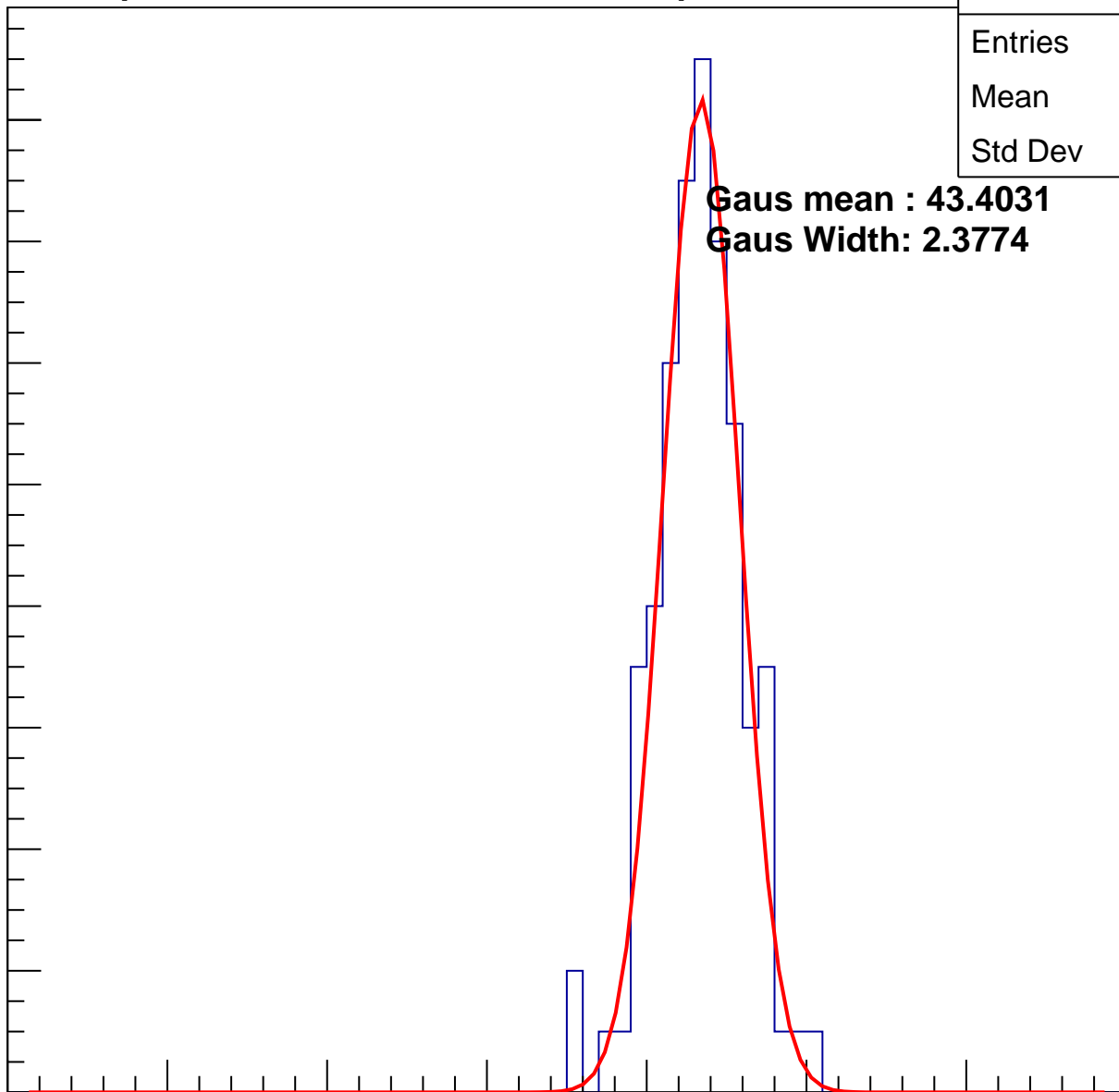
40

50

60

70

ampl

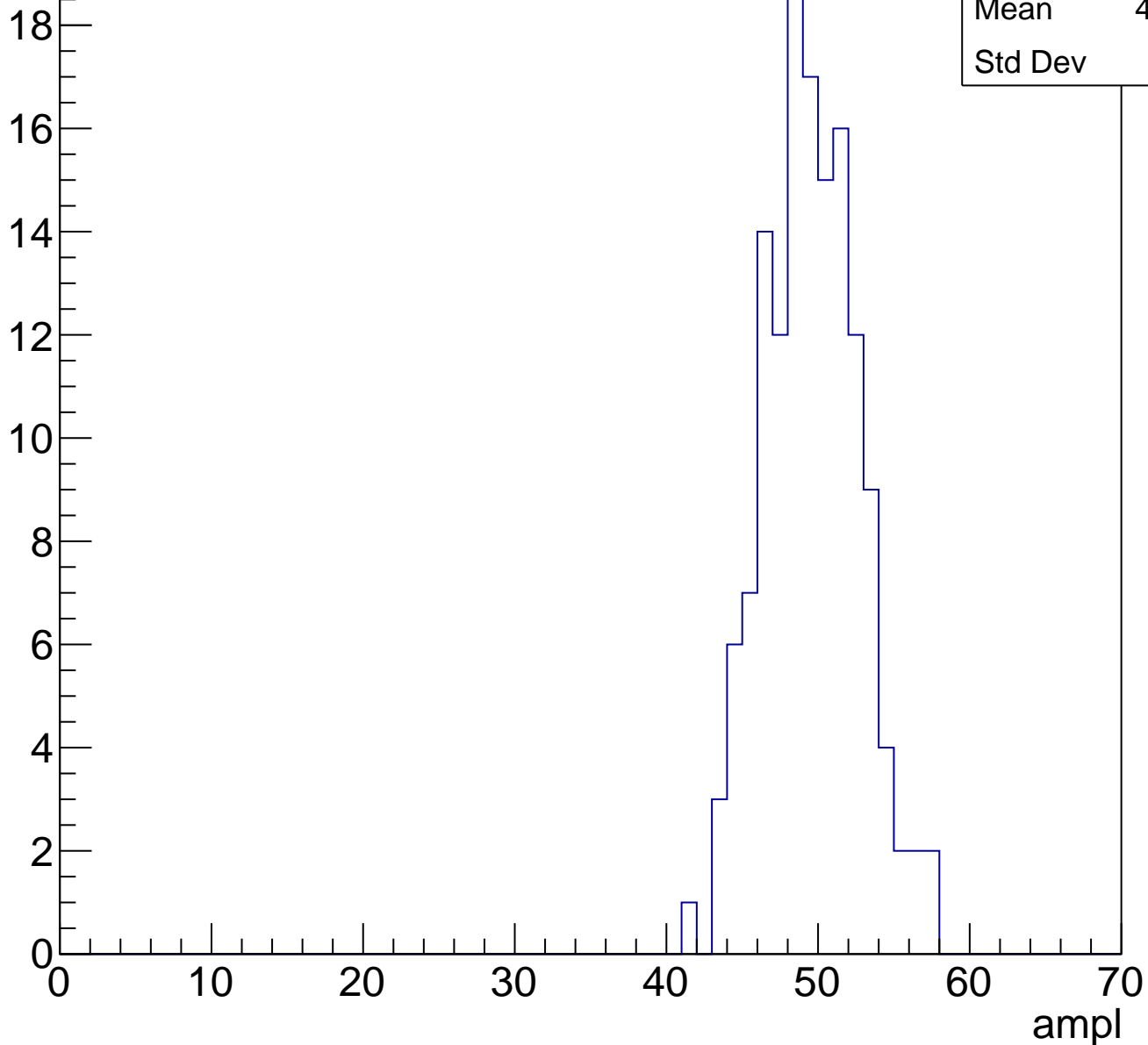


B1L001S, U21-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.09
Std Dev	3.1

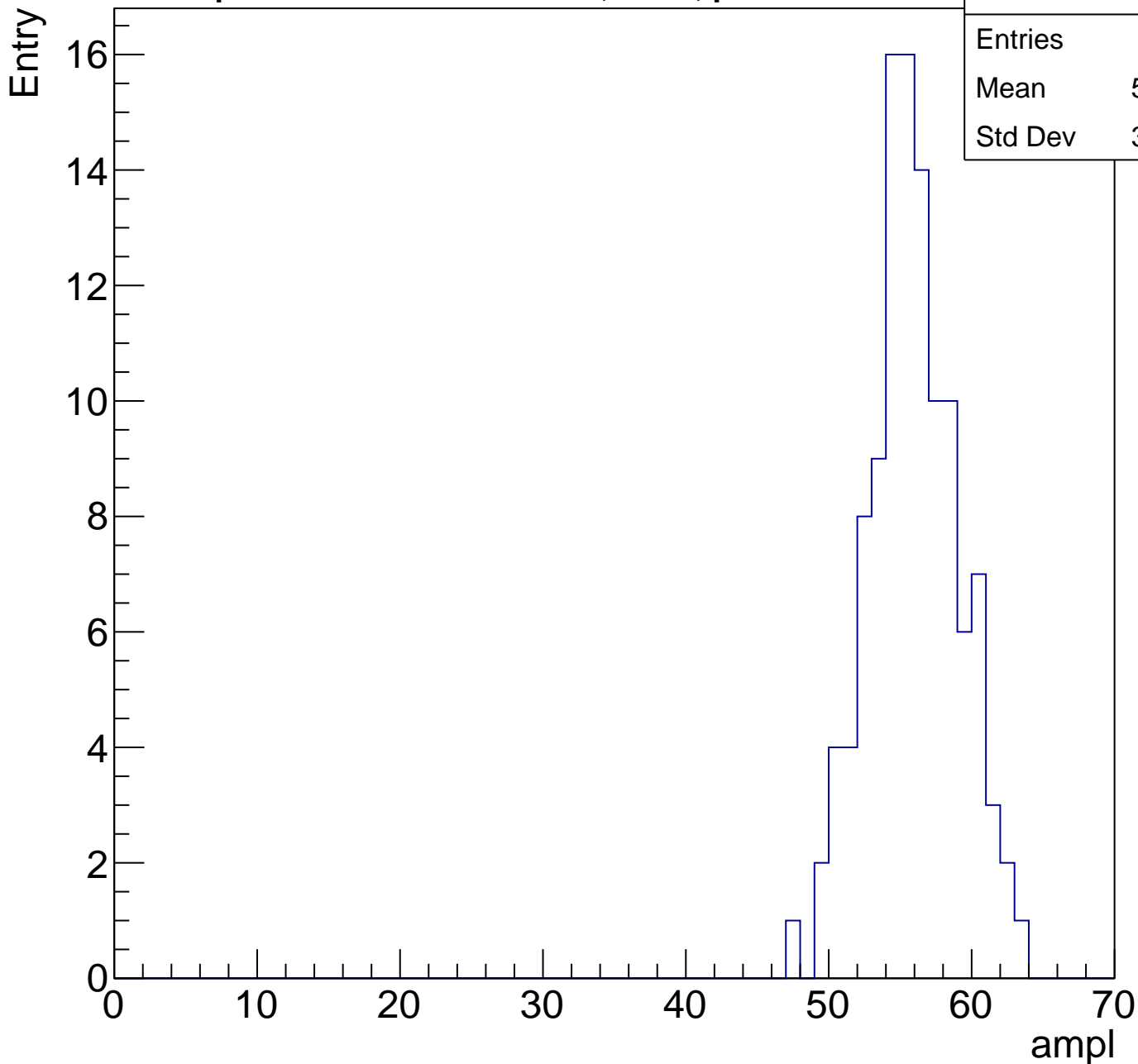
Entry



B1L001S, U21-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	55.43
Std Dev	3.103



B1L001S, U21-ch96, adc5

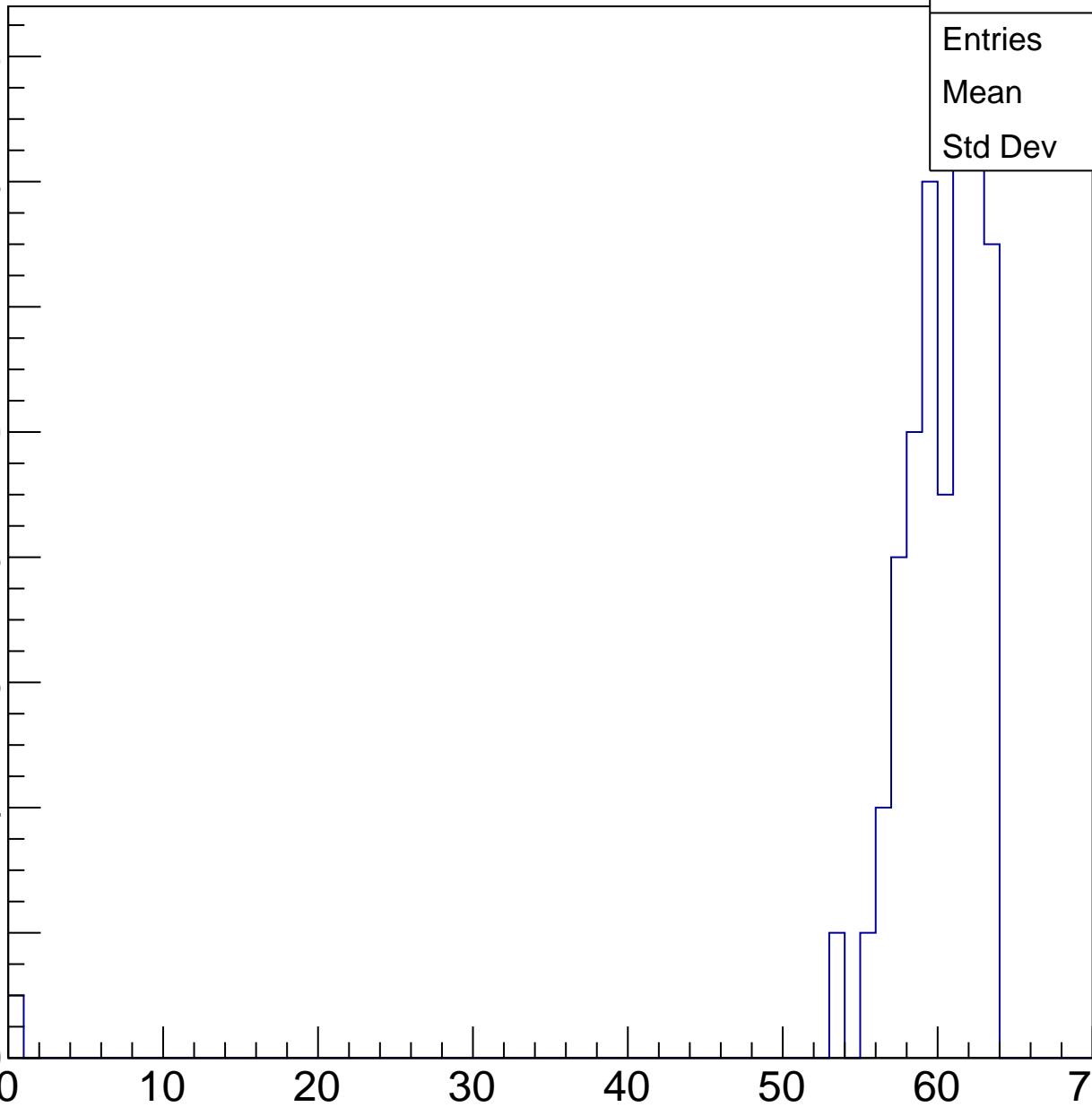
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.22
Std Dev	6.584

ampl

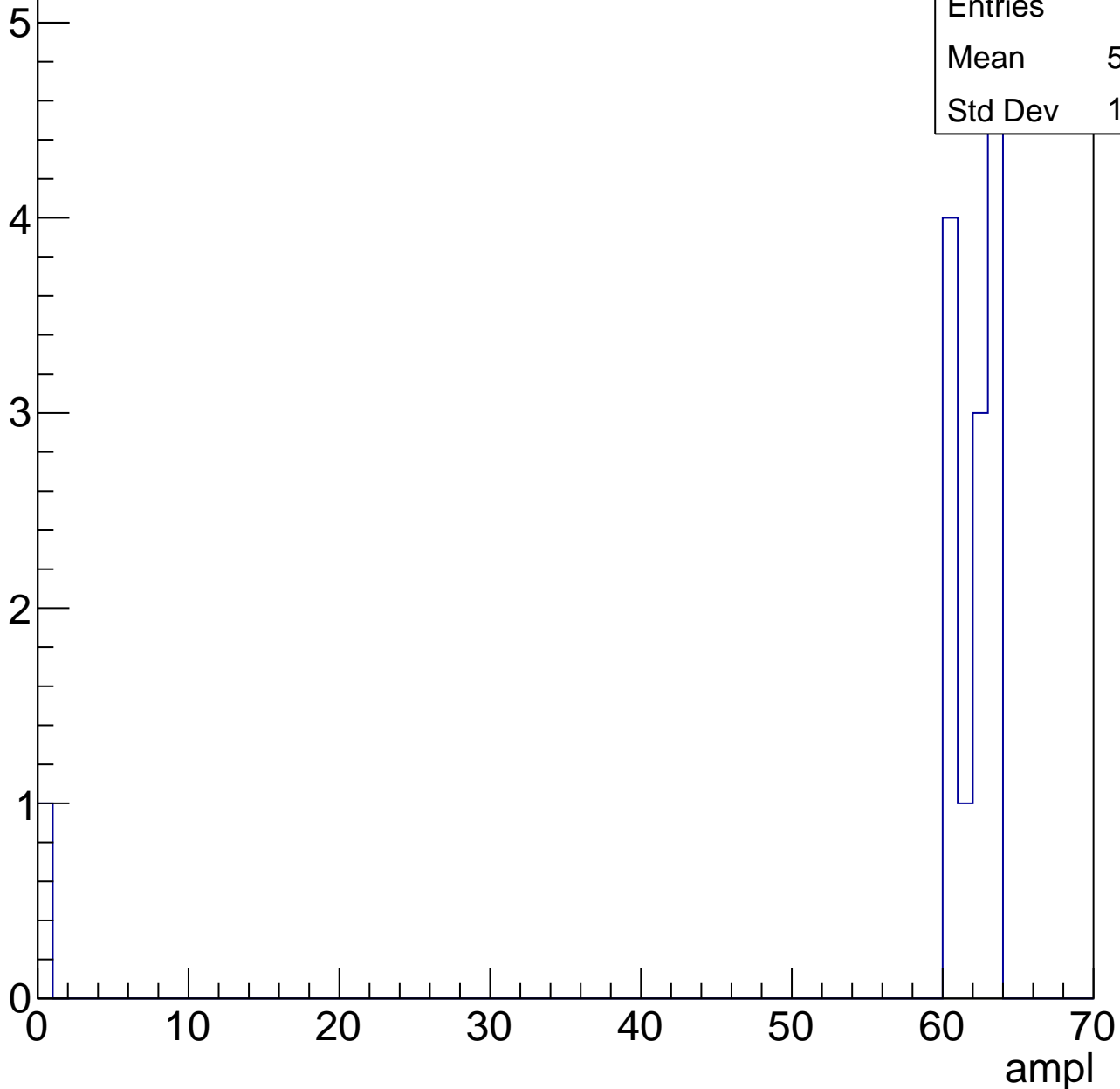


B1L001S, U21-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

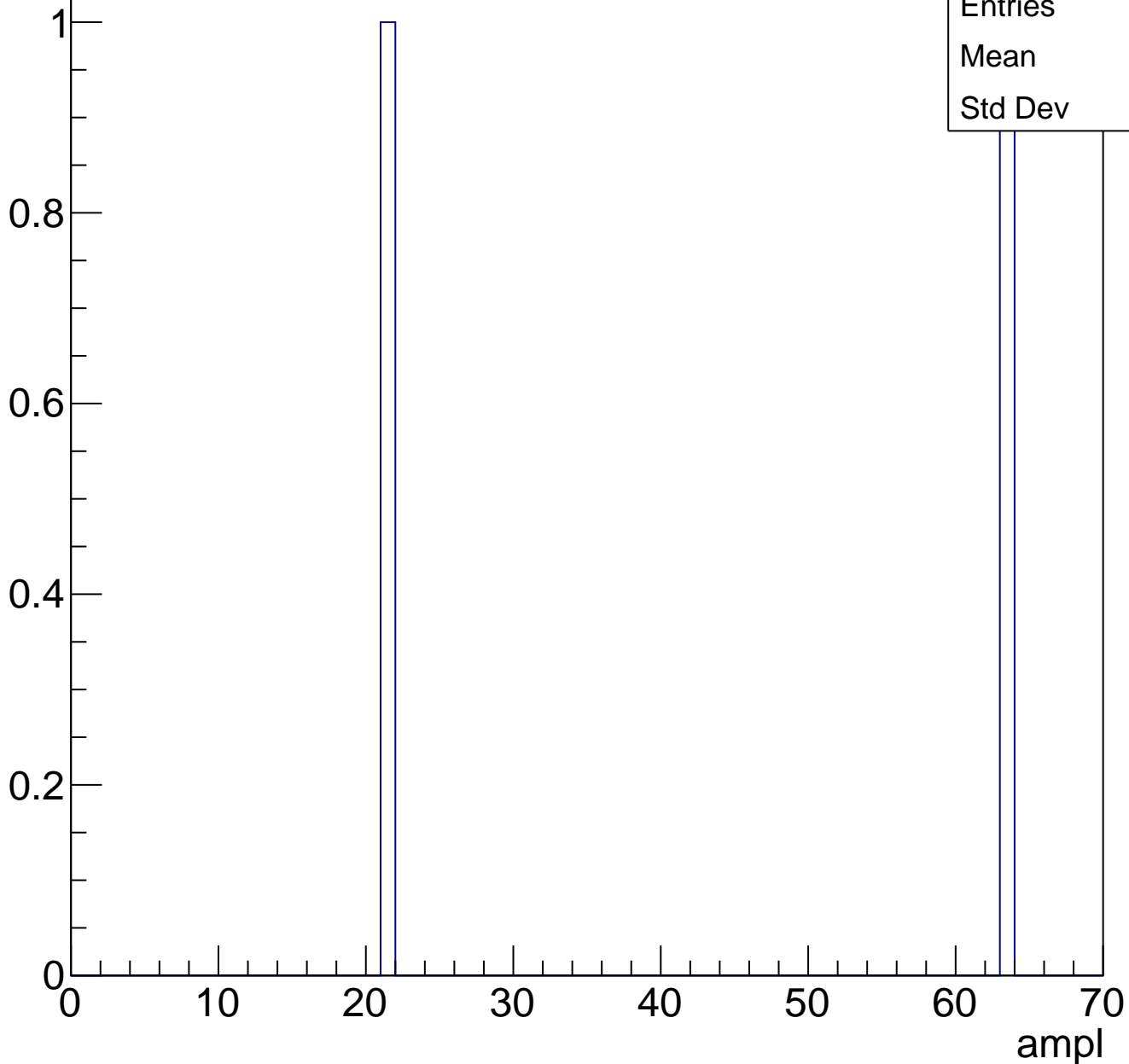
Entries	14
Mean	57.29
Std Dev	15.93



B1L001S, U21-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



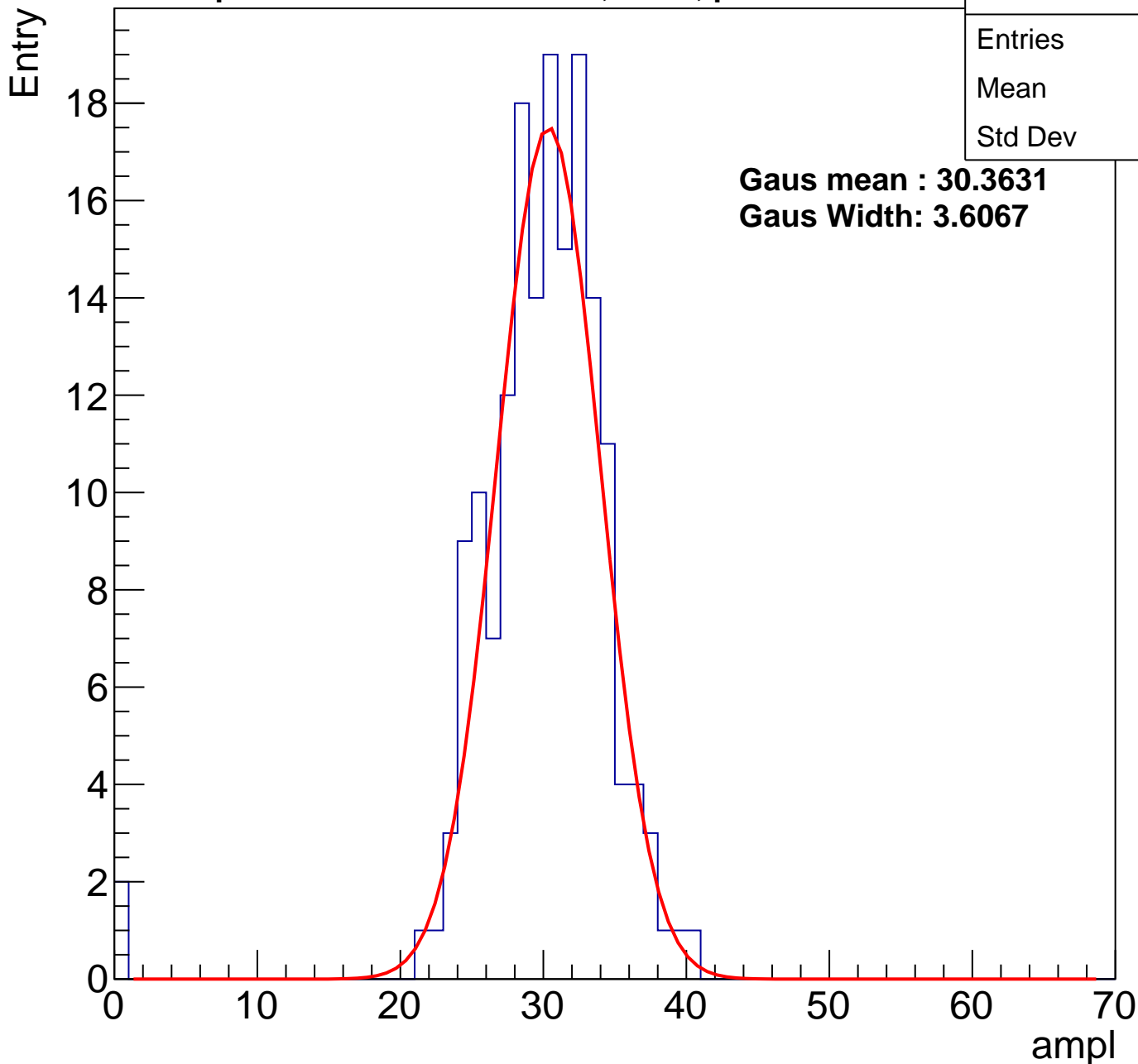
B1L001S, U21-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	29.5
Std Dev	4.84

Gaus mean : 30.3631

Gaus Width: 3.6067



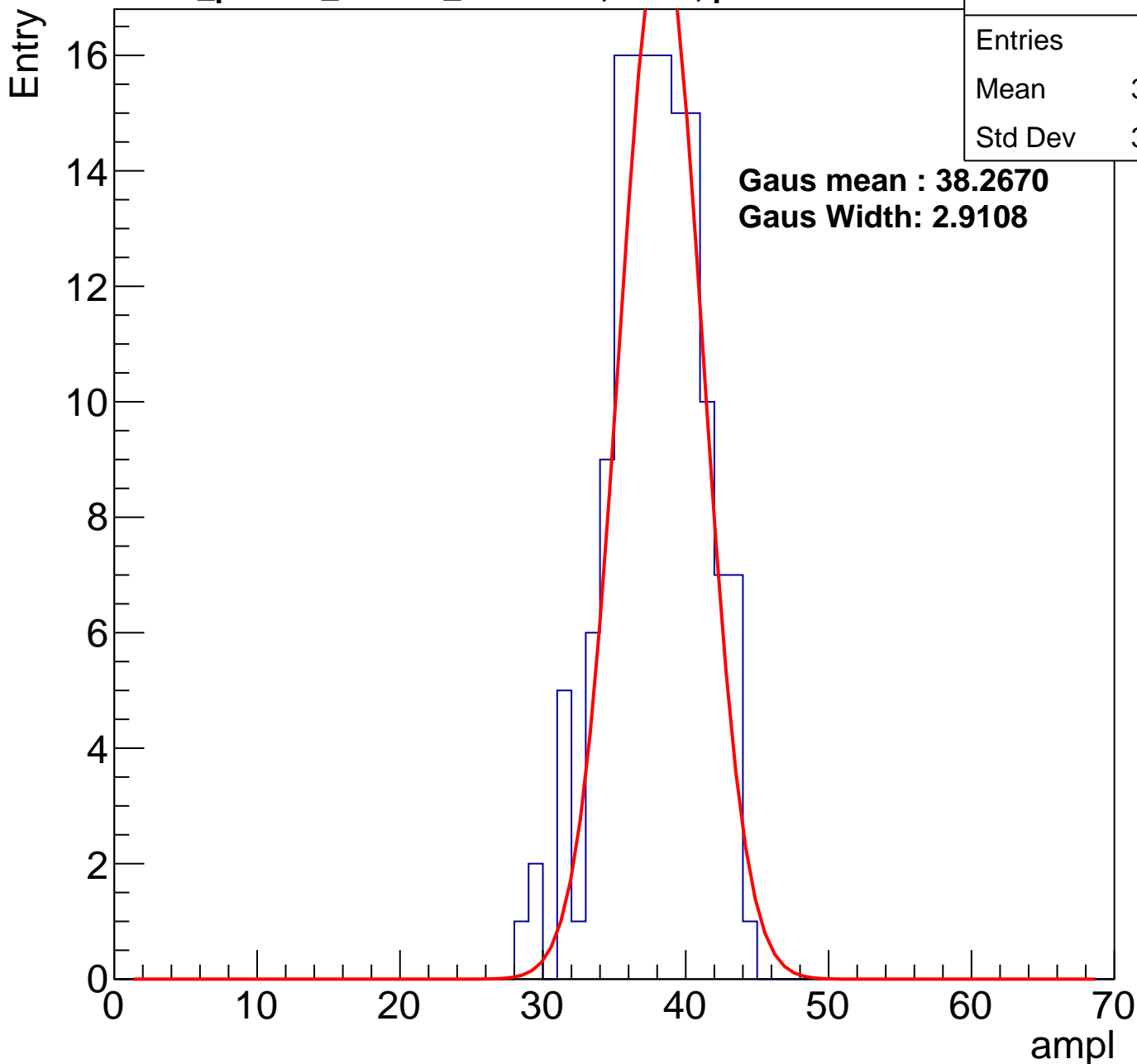
B1L001S, U21-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.39
Std Dev	3.235

Gaus mean : 38.2670

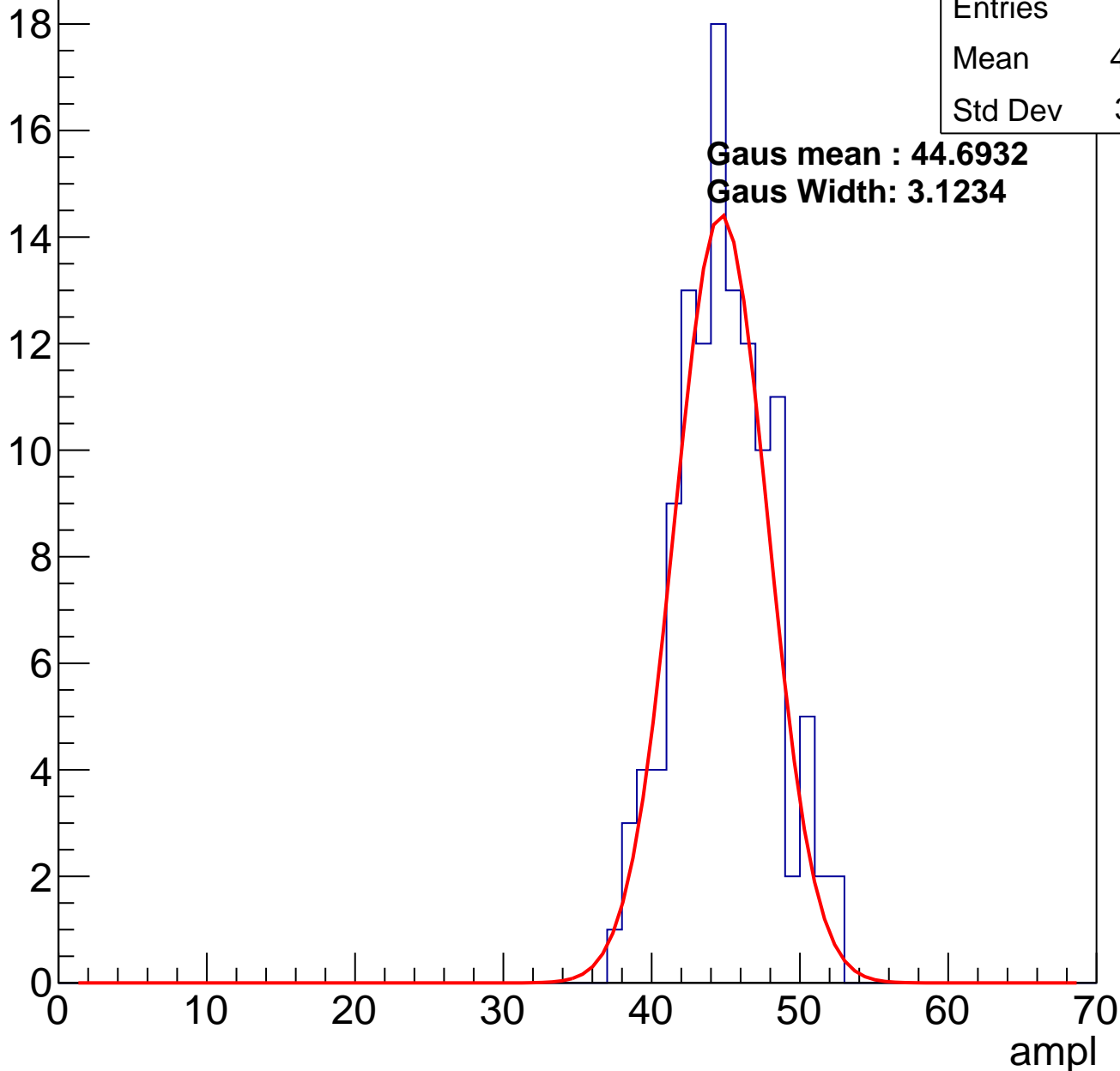
Gaus Width: 2.9108



B1L001S, U21-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

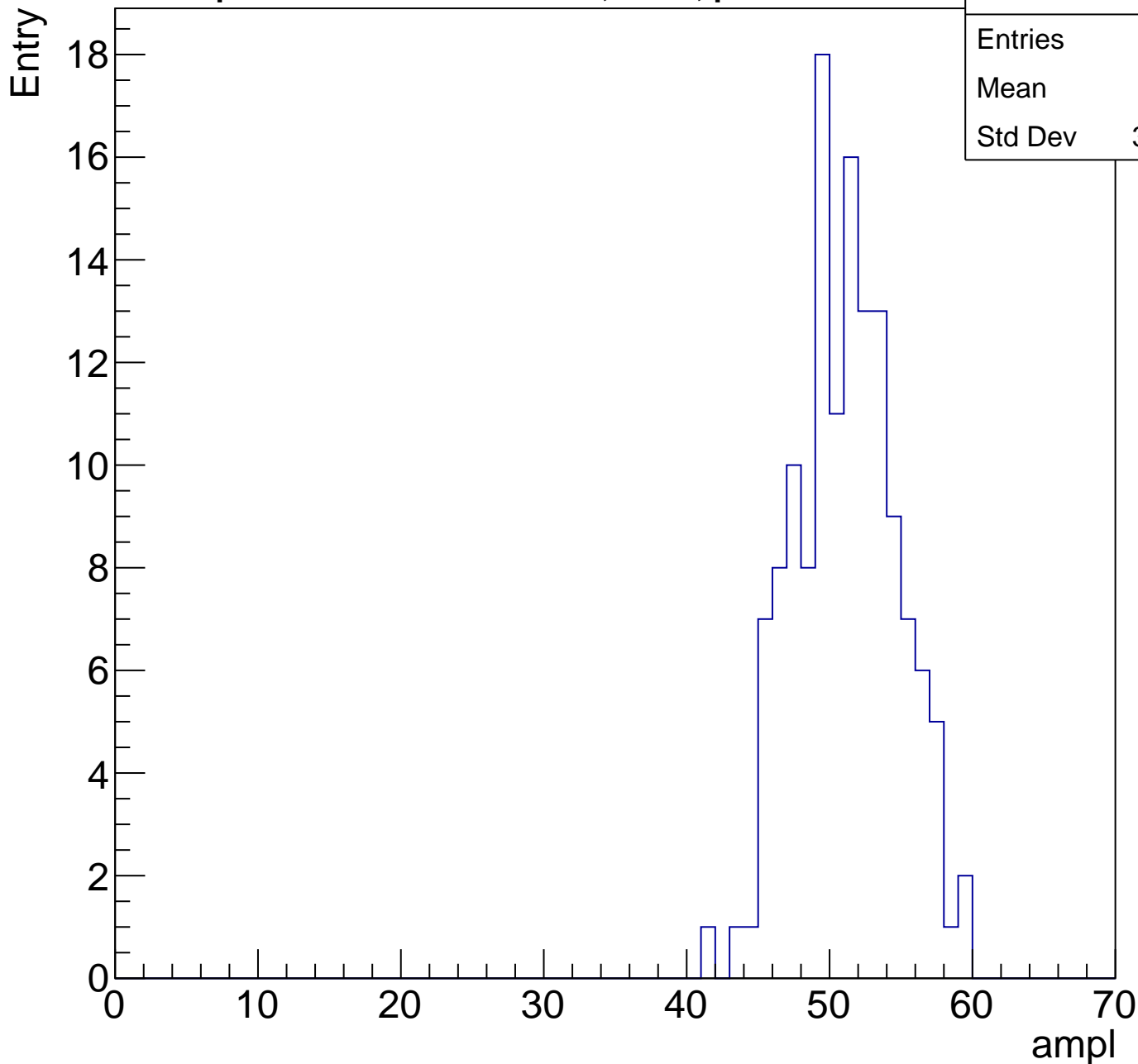
Entry



B1L001S, U21-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	50.7
Std Dev	3.556



B1L001S, U21-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

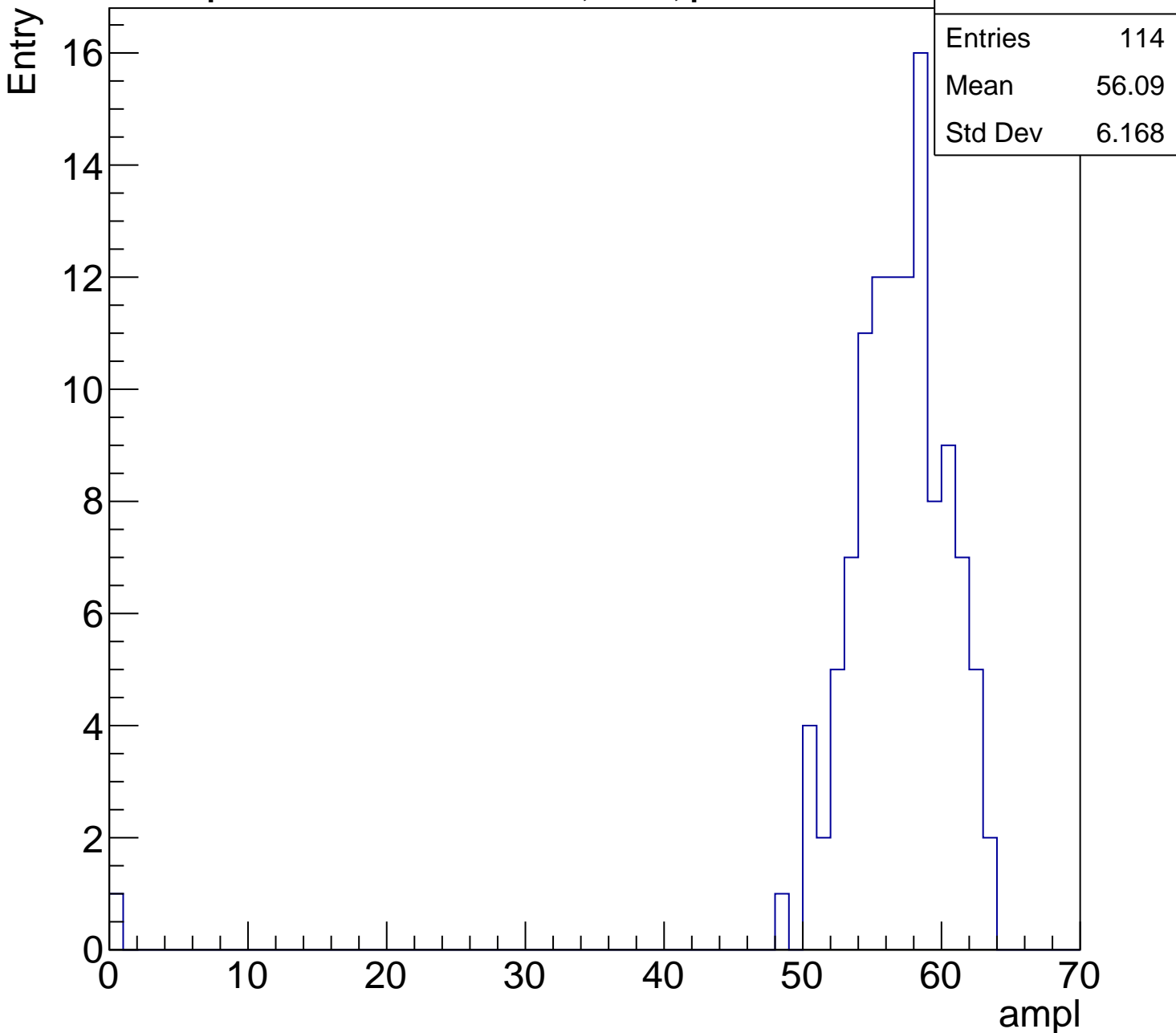
Entries	114
Mean	56.09
Std Dev	6.168

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch97, adc5

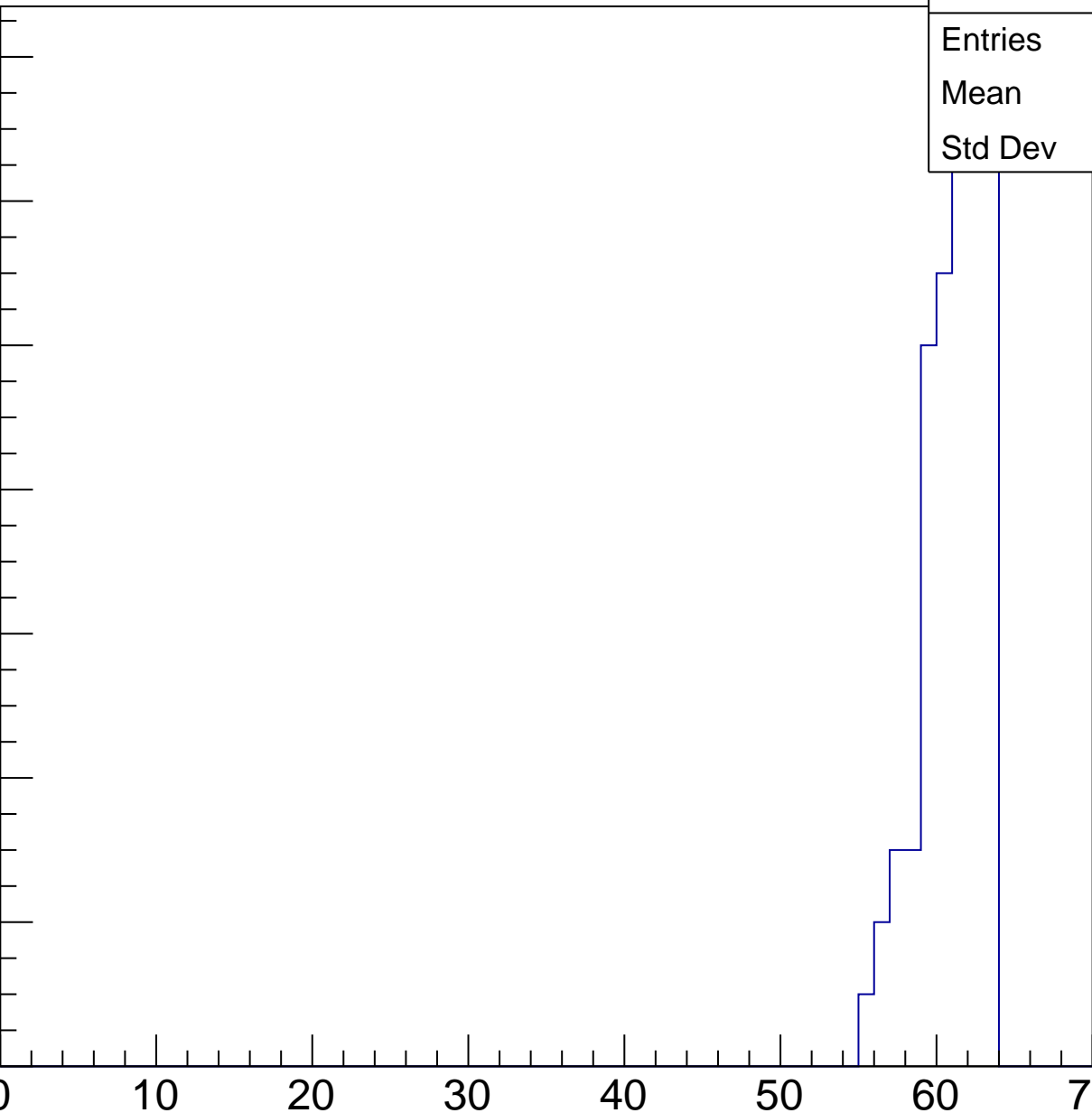
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	60.6
Std Dev	1.952

ampl

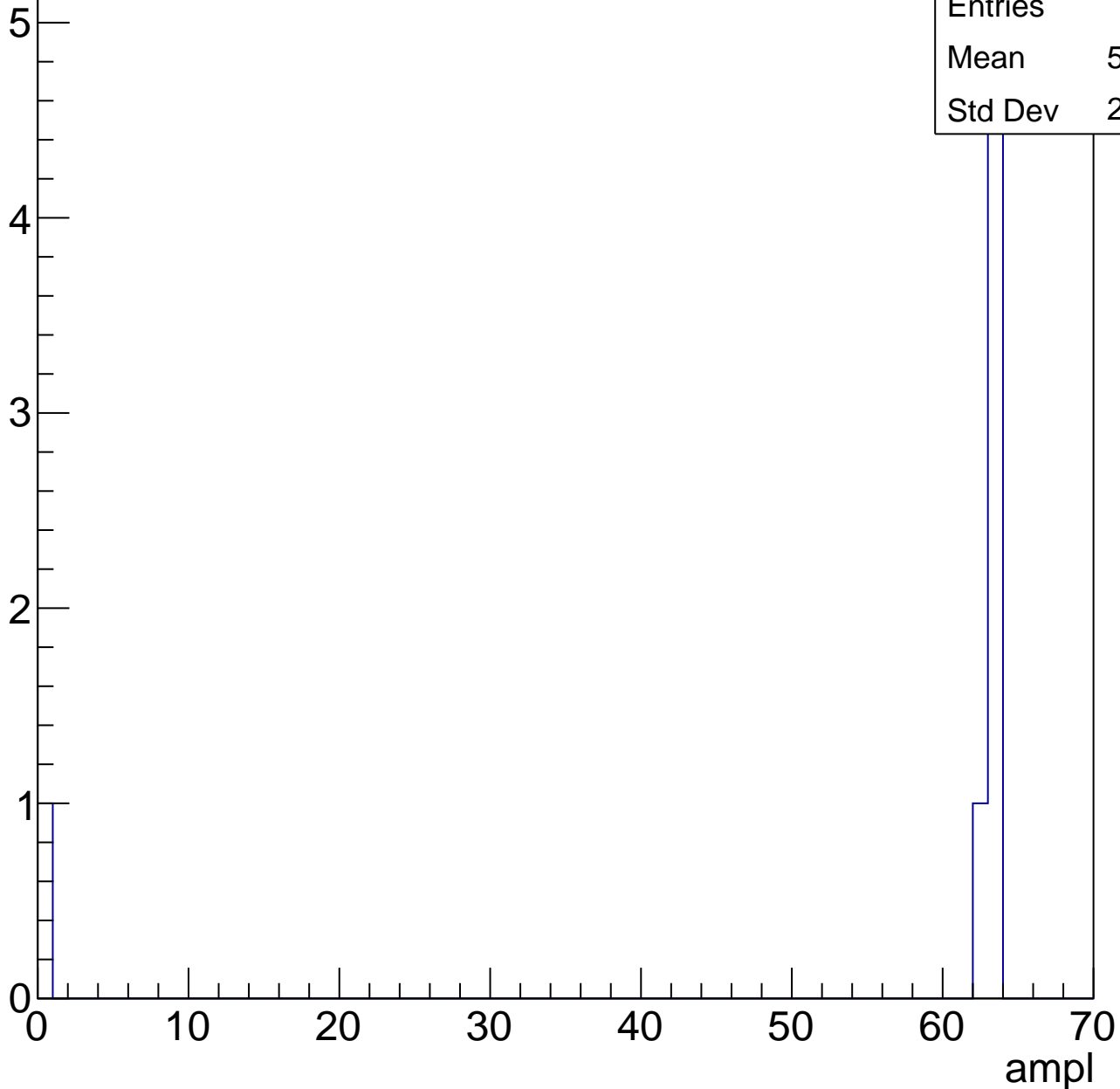


B1L001S, U21-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B1L001S, U21-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L001S, U21-ch98, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	30.43
Std Dev	3.018

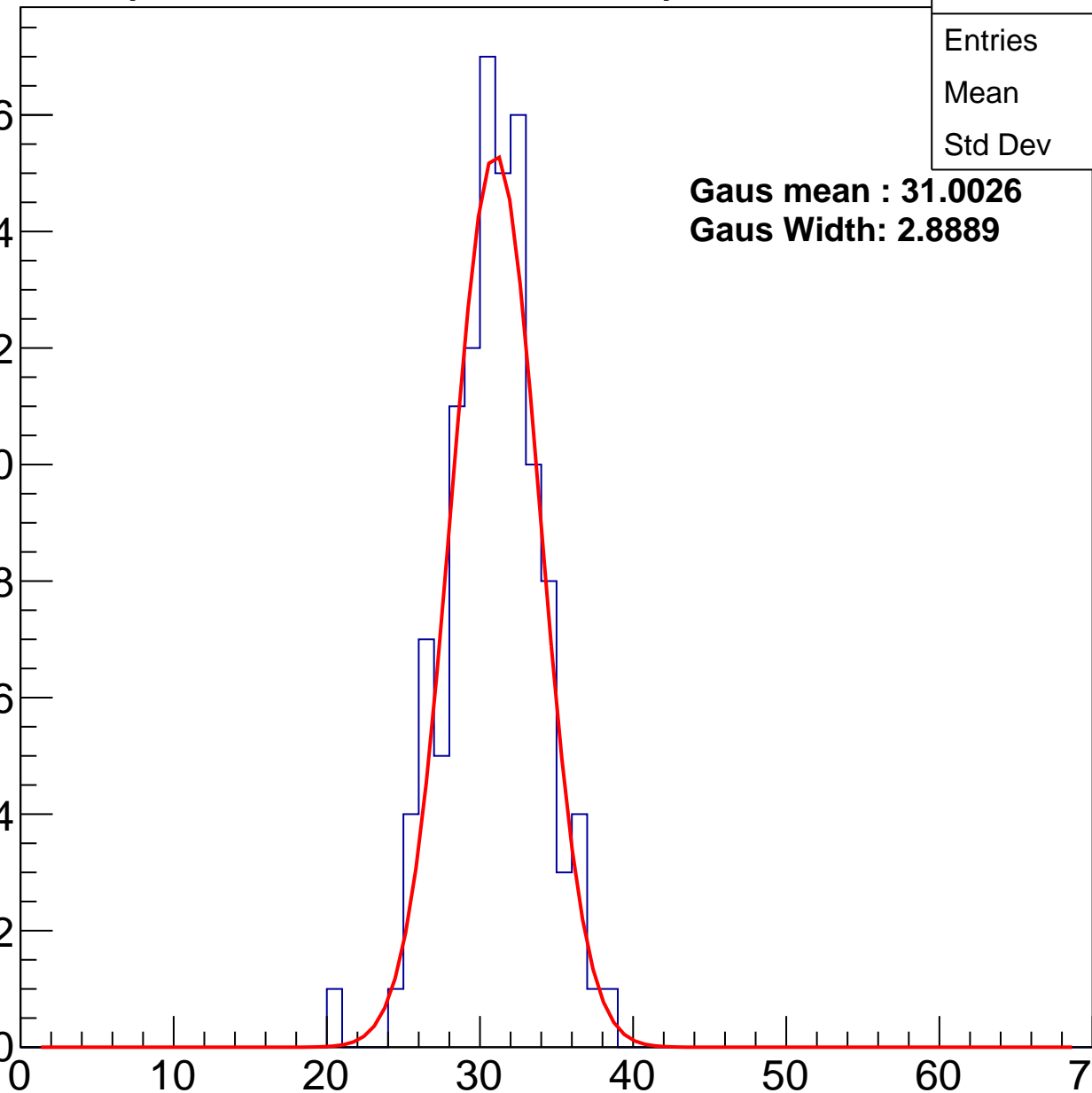
Gaus mean : 31.0026

Gaus Width: 2.8889

Entry

16
14
12
10
8
6
4
2
0

ampl



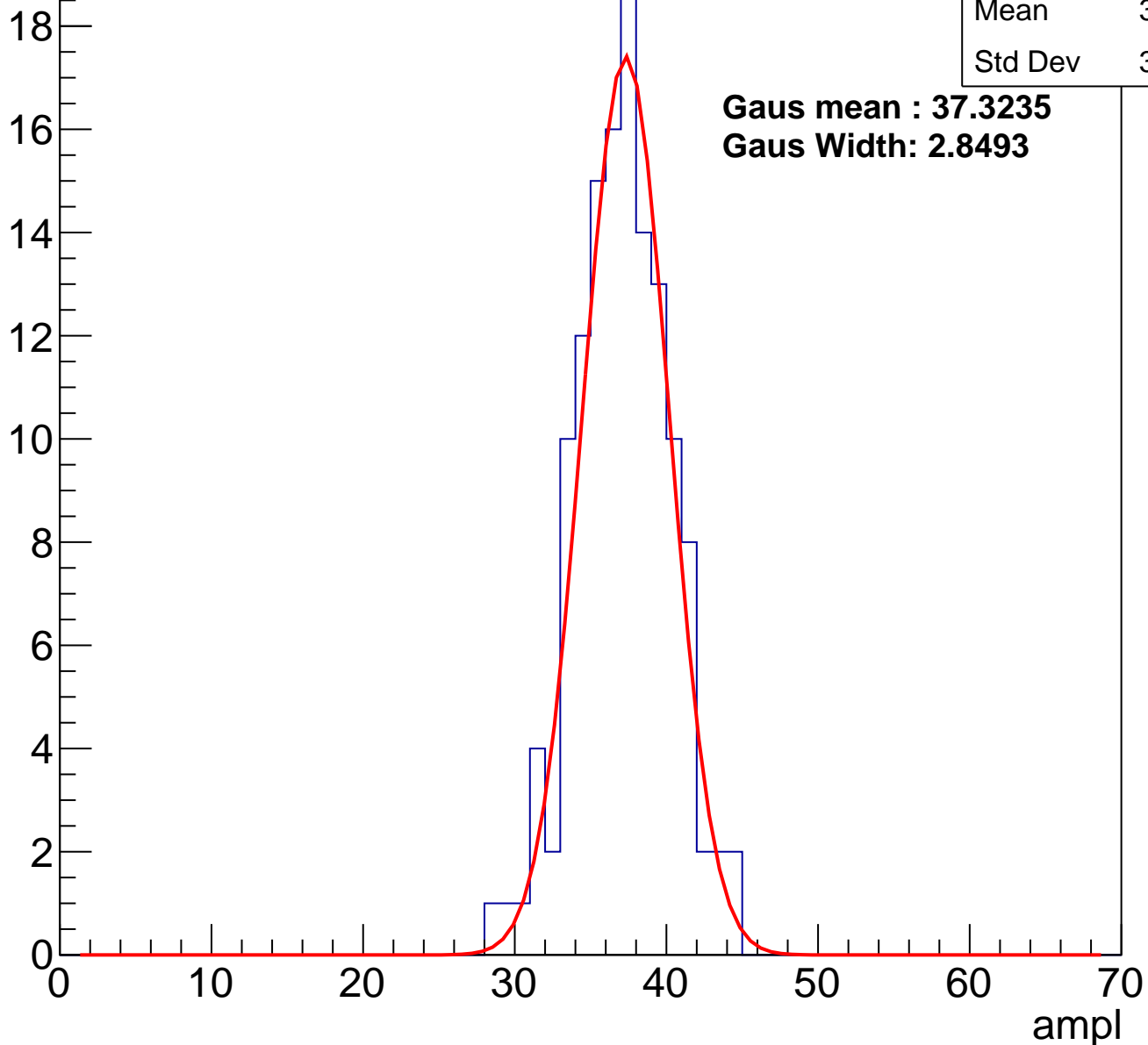
B1L001S, U21-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.68
Std Dev	3.043

Gaus mean : 37.3235
Gaus Width: 2.8493

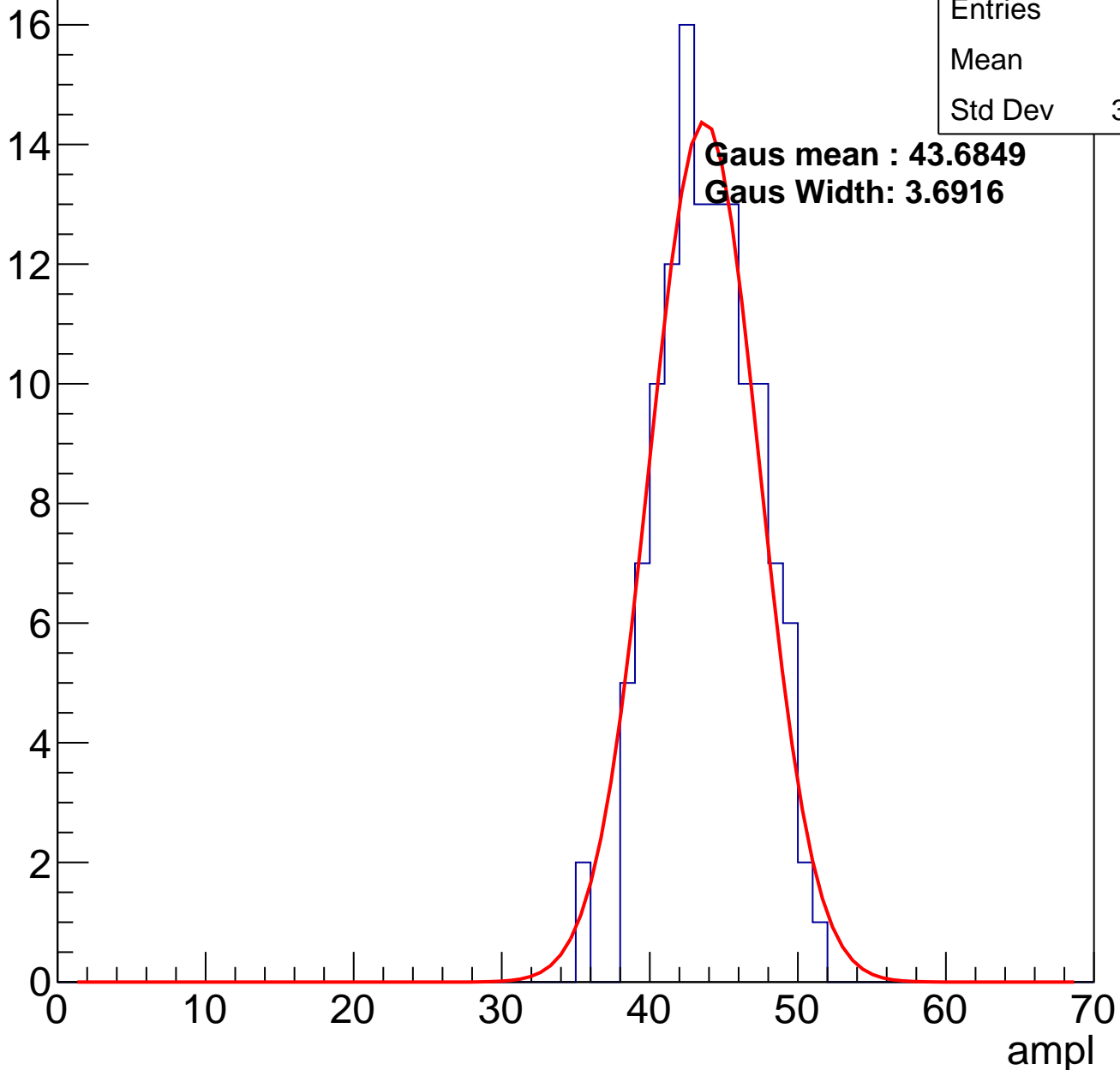
Entry



B1L001S, U21-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

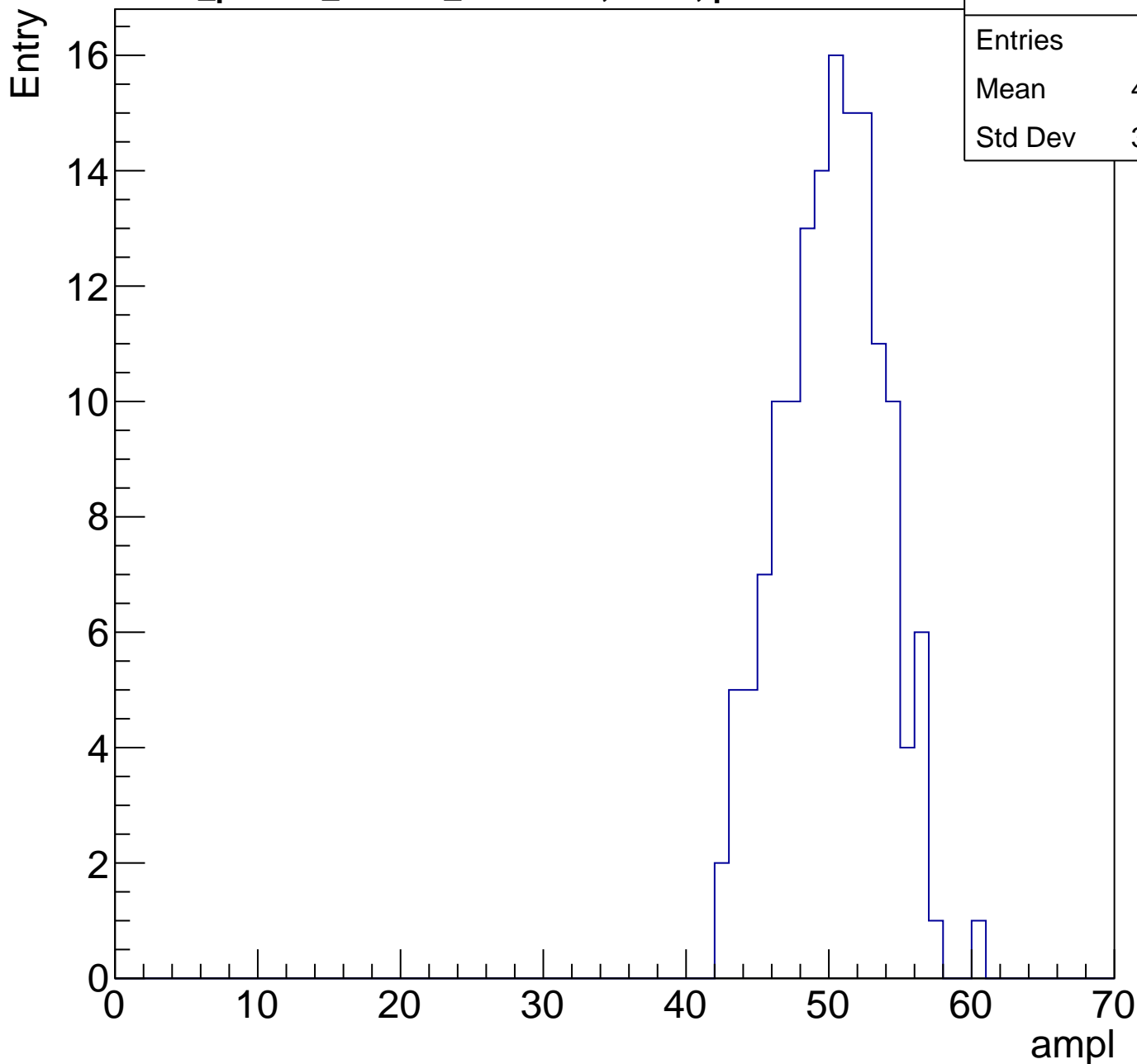
Entry



B1L001S, U21-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

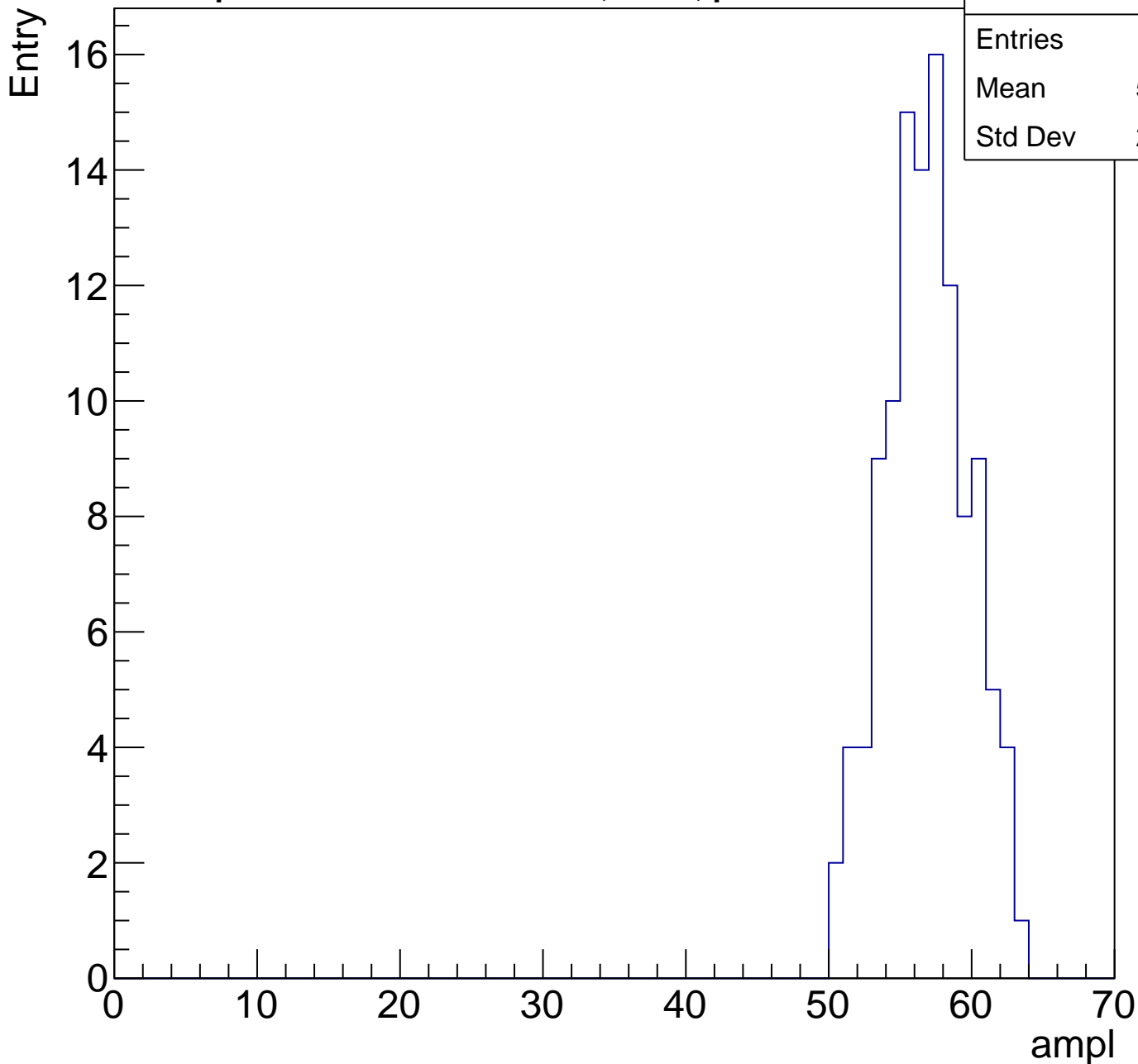
Entries	145
Mean	49.76
Std Dev	3.557



B1L001S, U21-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	56.41
Std Dev	2.901



B1L001S, U21-ch98, adc5

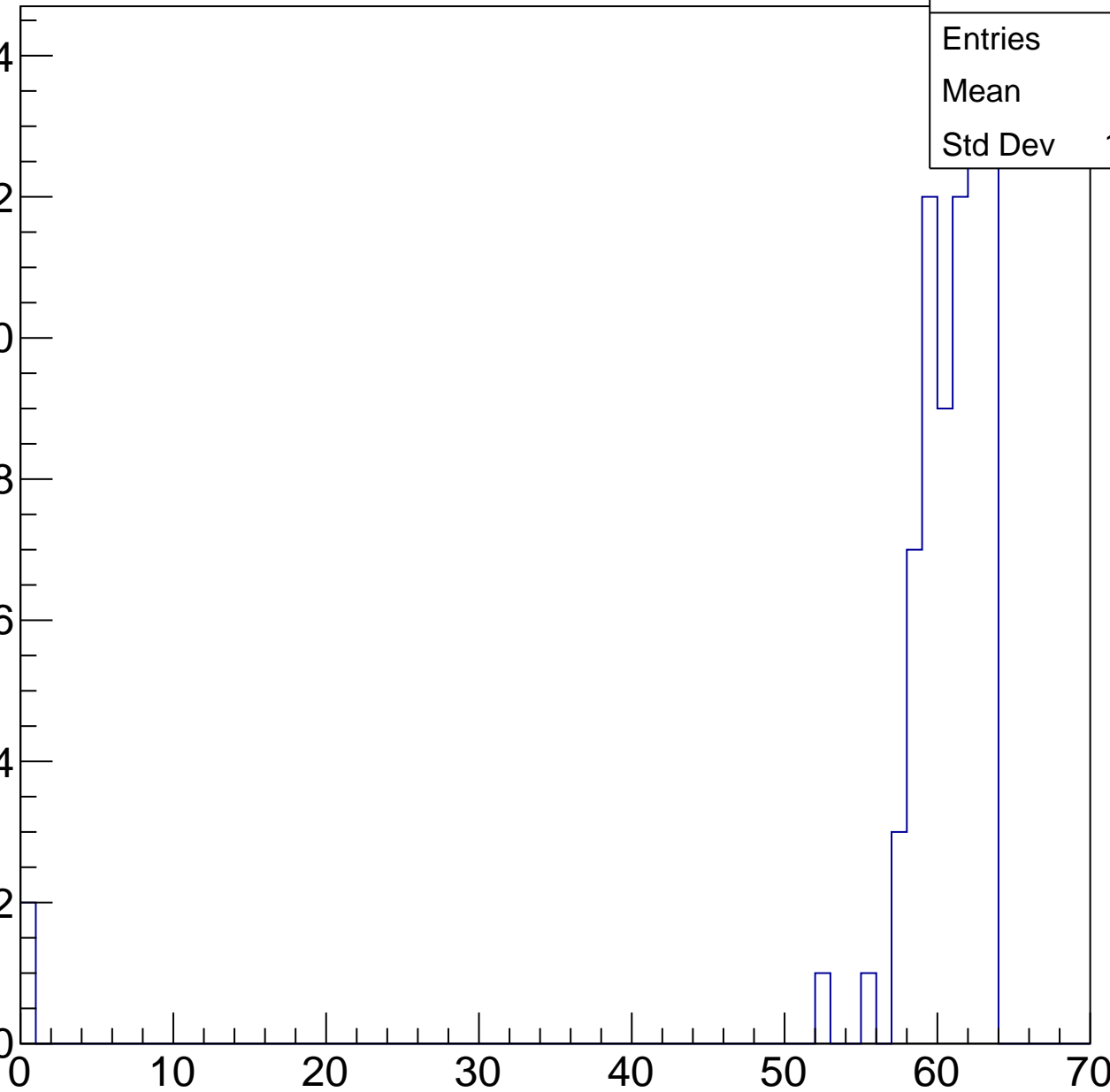
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	58.8
Std Dev	10.02

ampl

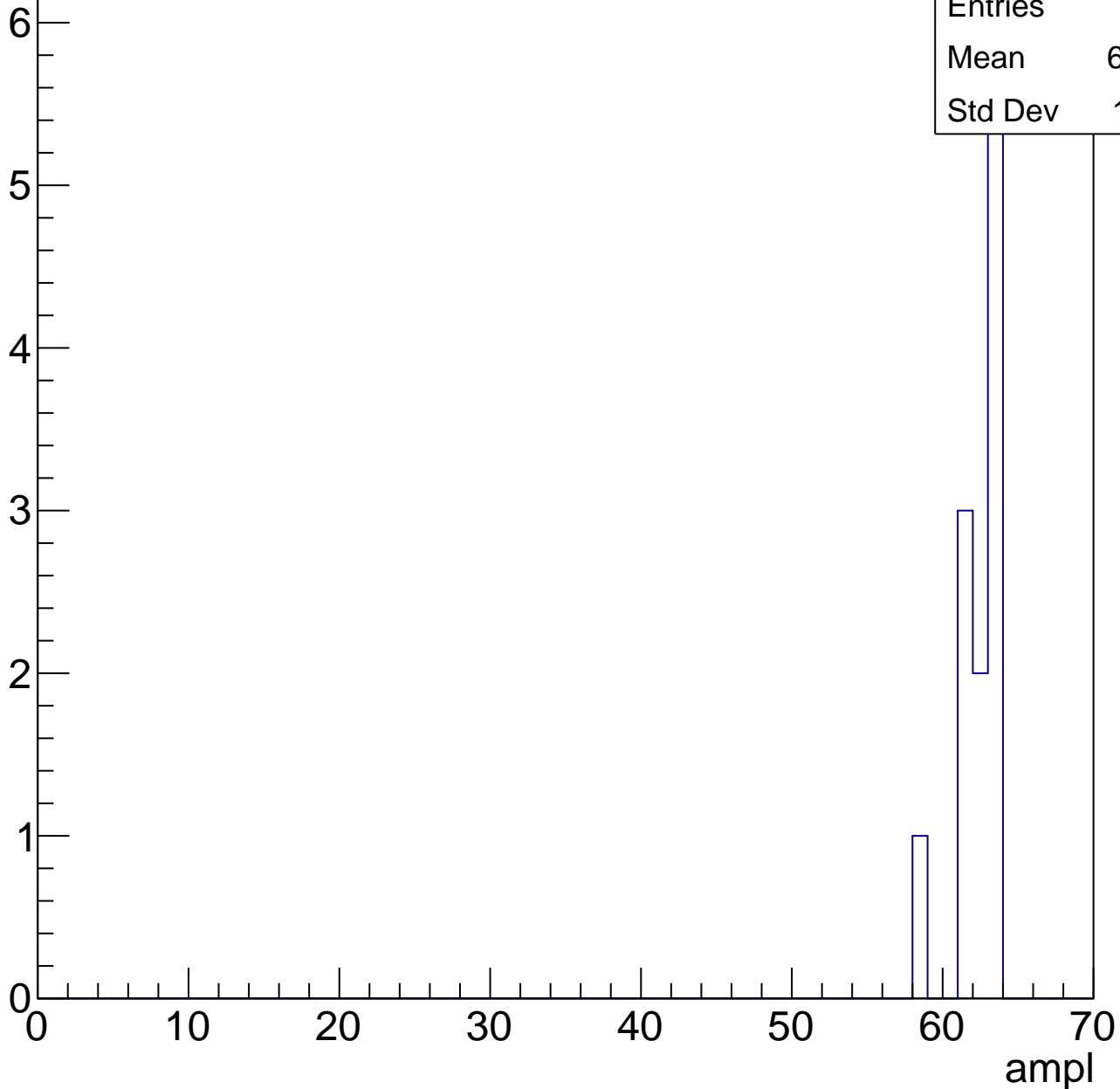


B1L001S, U21-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

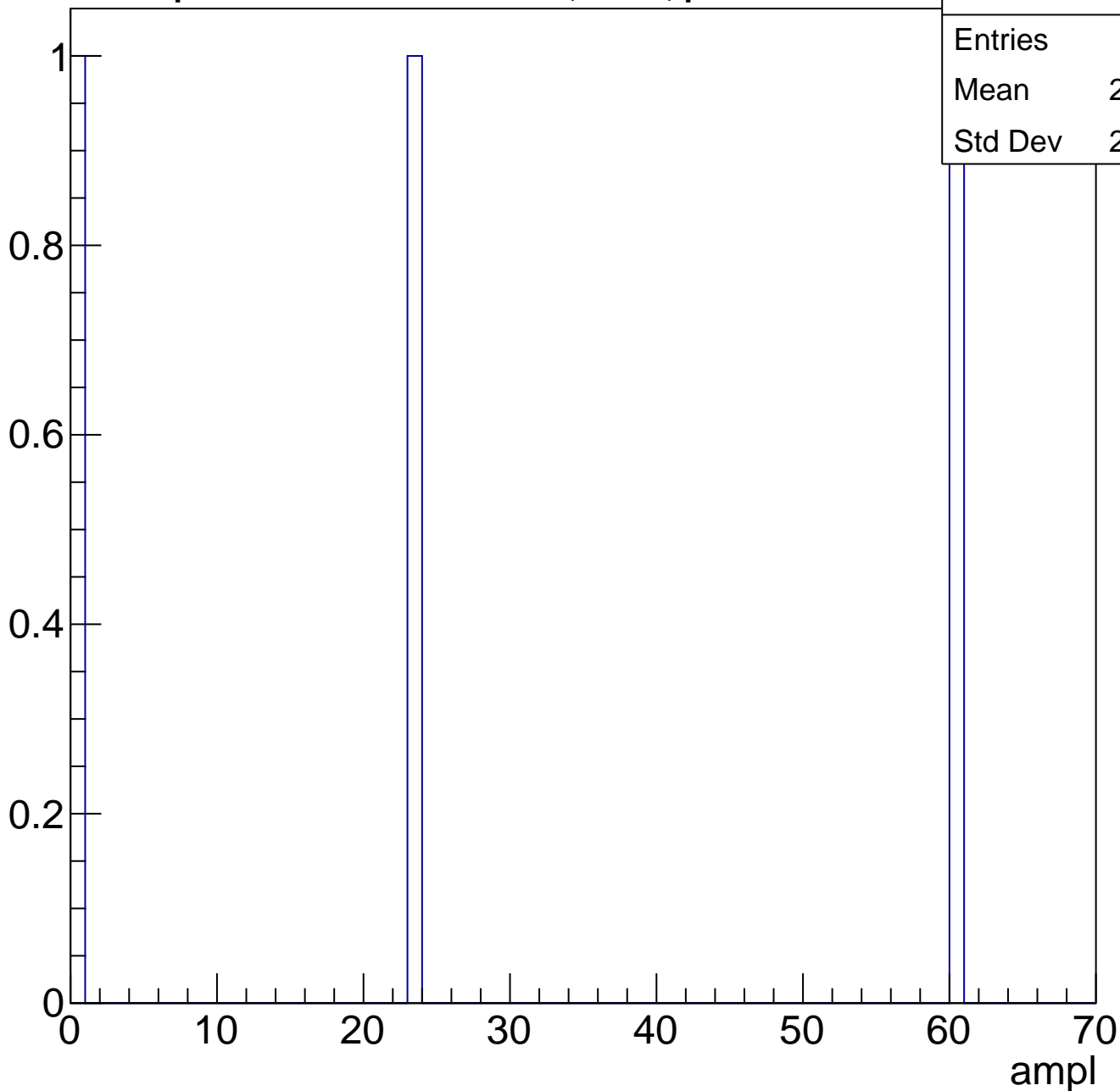
Entries	12
Mean	61.92
Std Dev	1.441



B1L001S, U21-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.67
Std Dev	24.72

B1L001S, U21-ch99, adc0

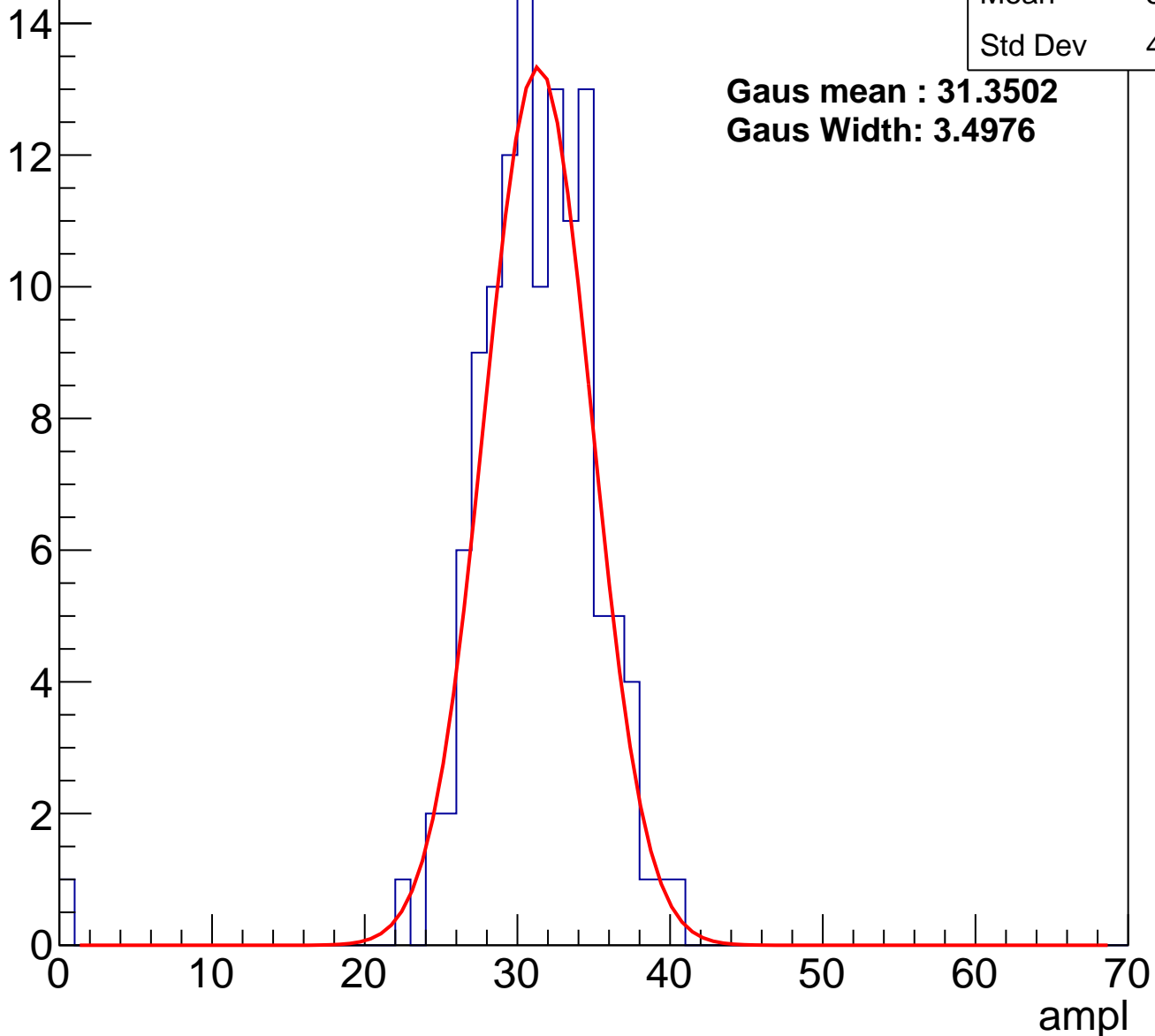
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	30.72
Std Dev	4.404

Gaus mean : 31.3502

Gaus Width: 3.4976

Entry



B1L001S, U21-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	37.34
Std Dev	3.478

Gaus mean : 37.8030

Gaus Width: 3.6790

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

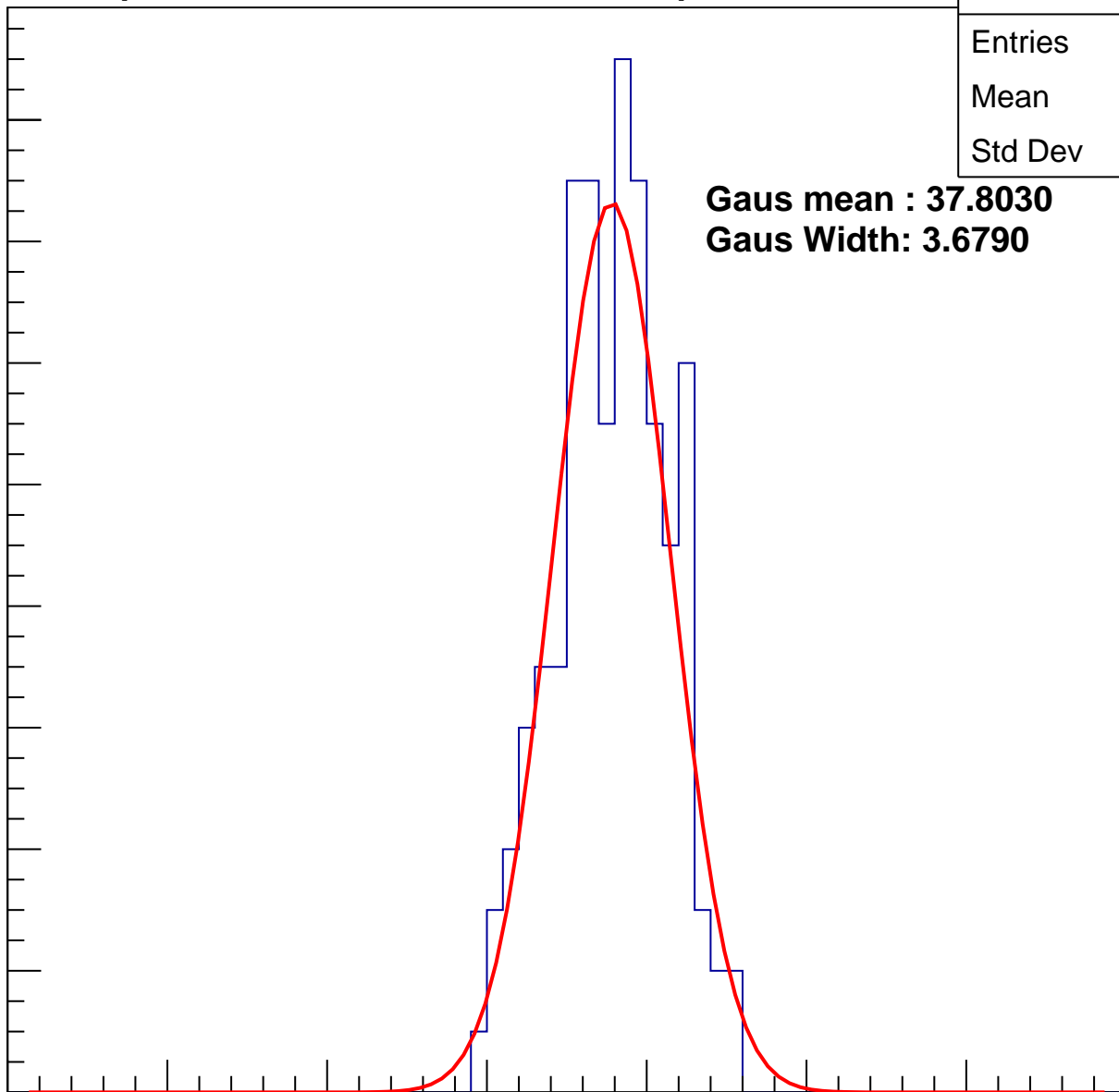
40

50

60

70

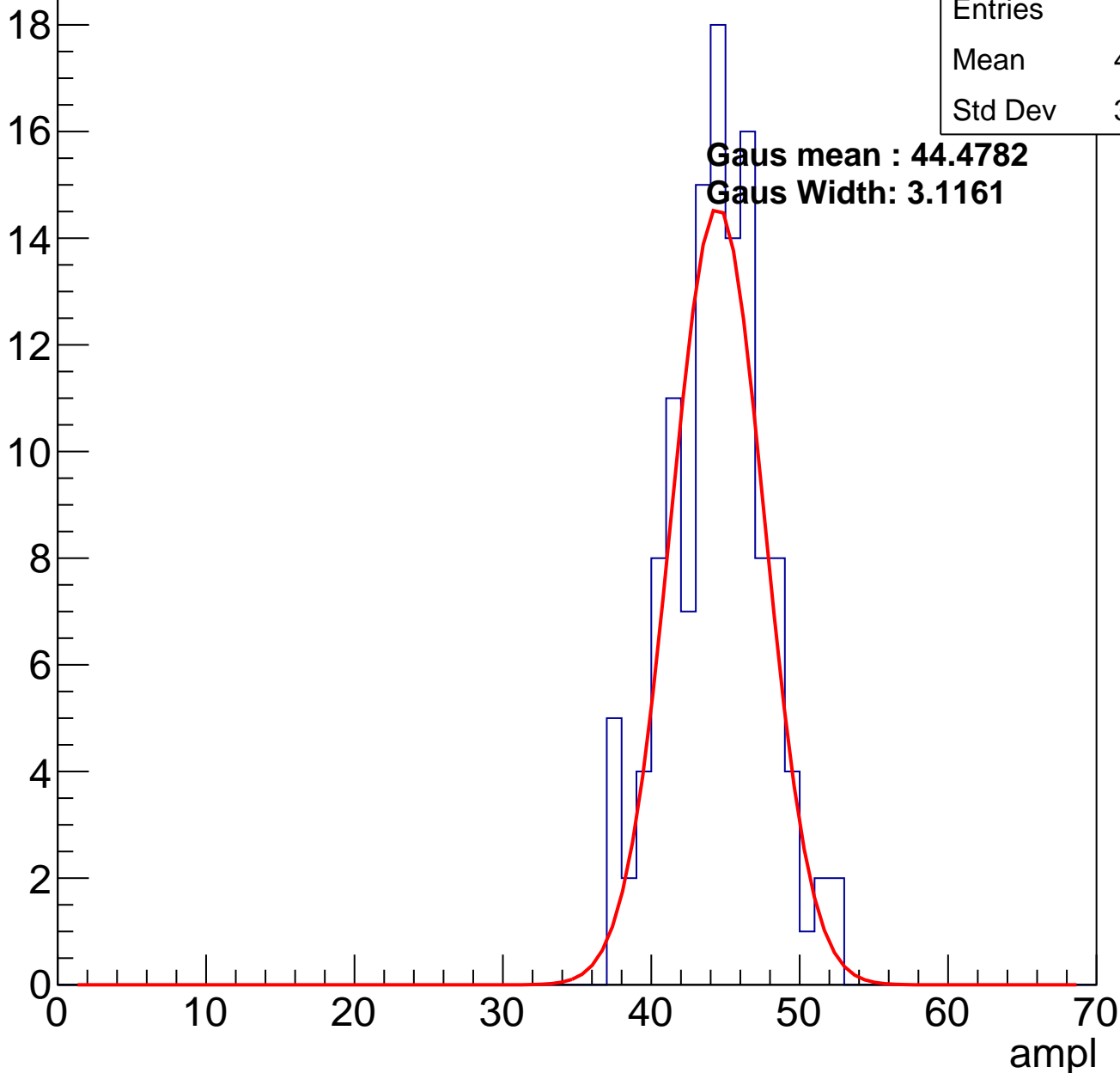
ampl



B1L001S, U21-ch99, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch99, adc3

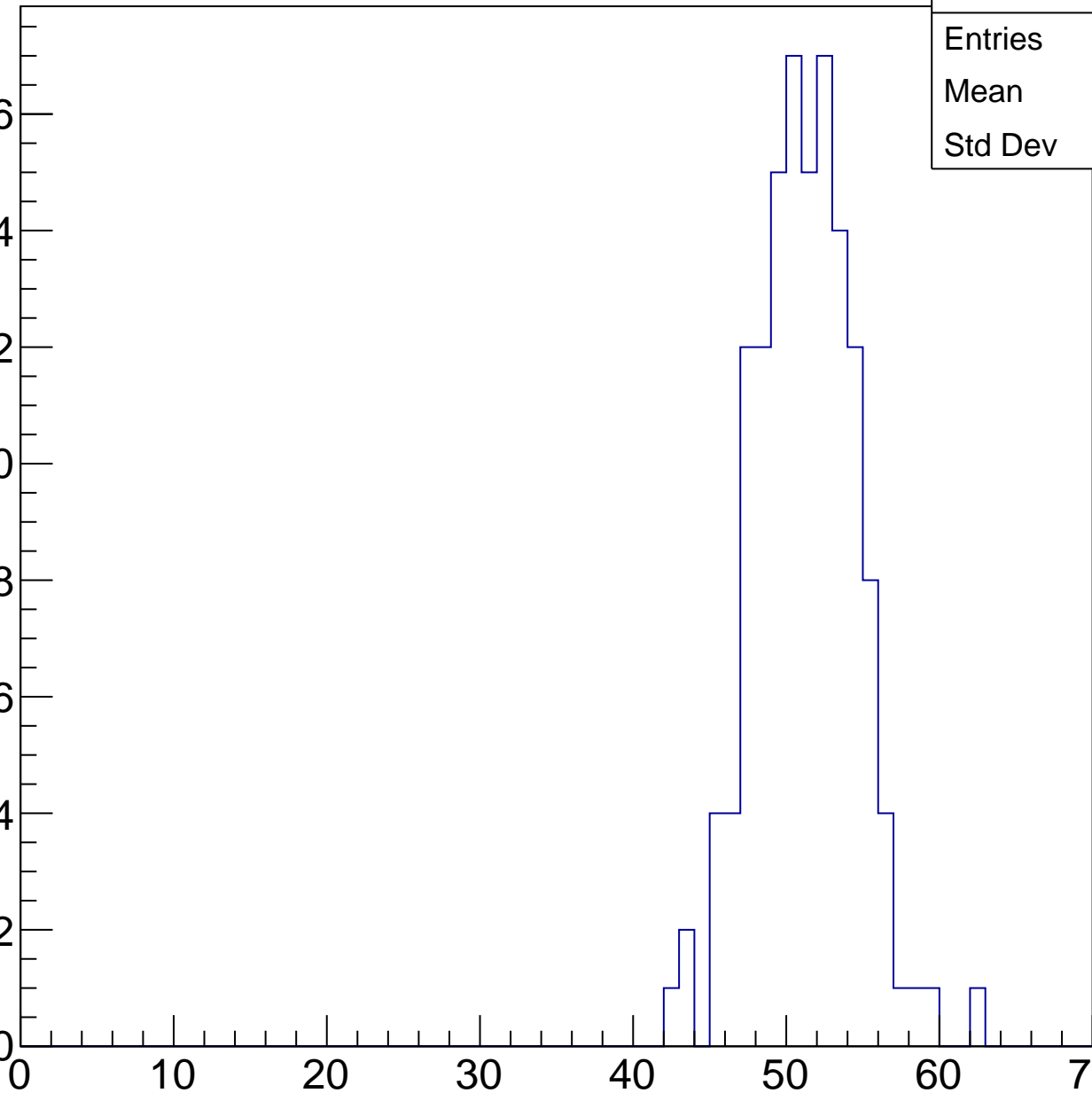
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	50.75
Std Dev	3.262

Entry

16
14
12
10
8
6
4
2
0

ampl

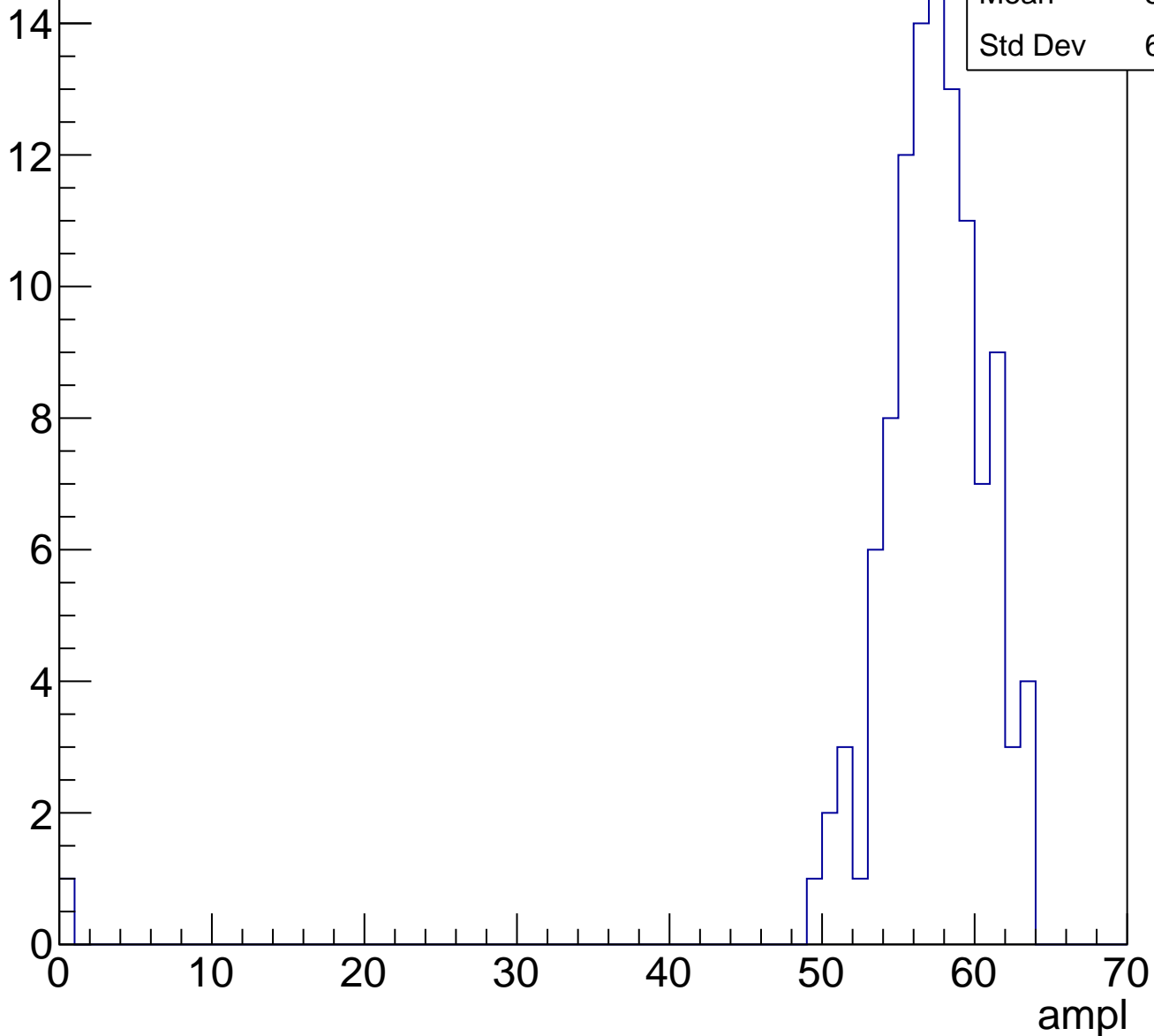


B1L001S, U21-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56.48
Std Dev	6.199

Entry



B1L001S, U21-ch99, adc5

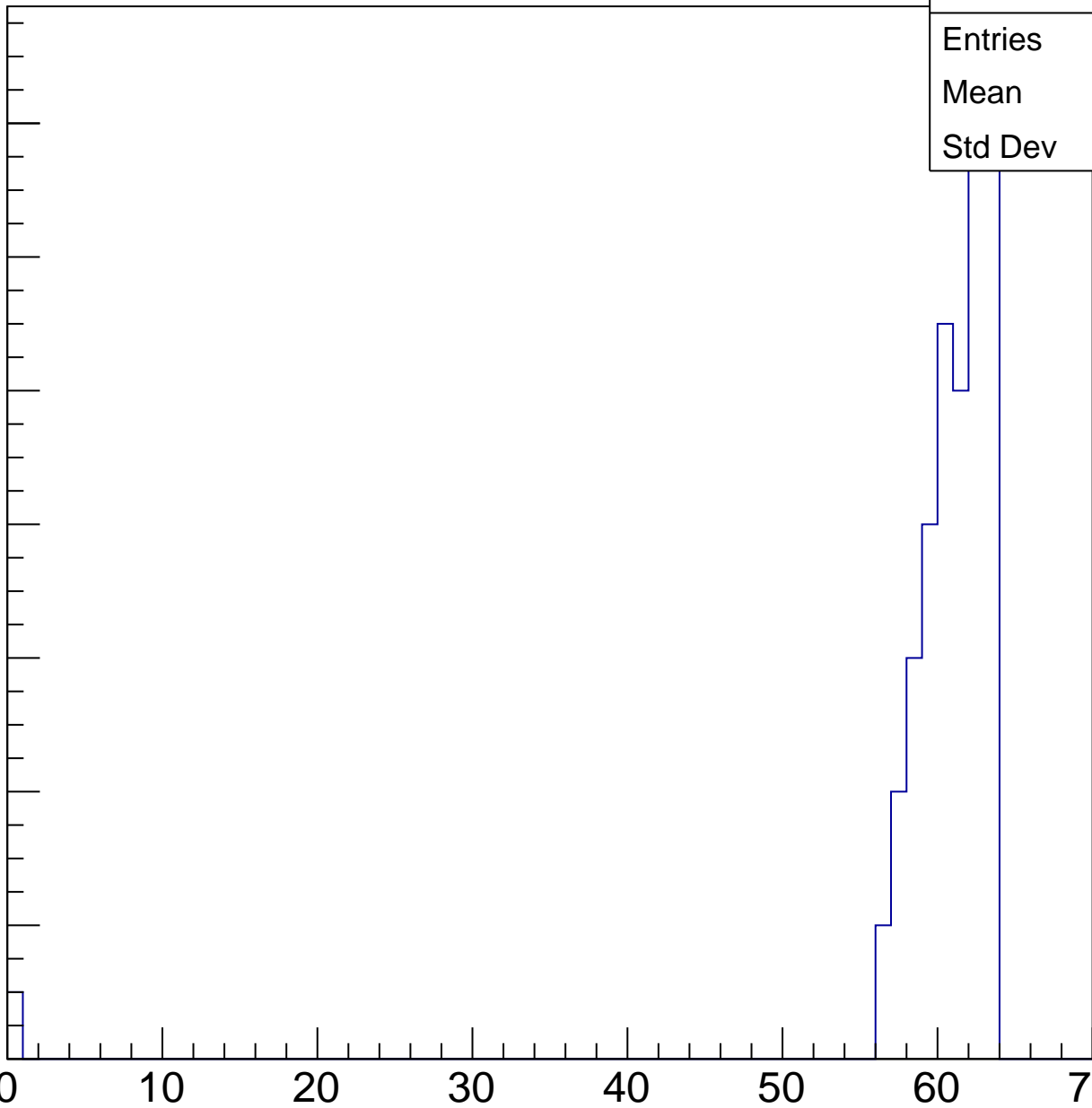
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.79
Std Dev	7.362

ampl



B1L001S, U21-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

B1L001S, U21-ch100, adc0

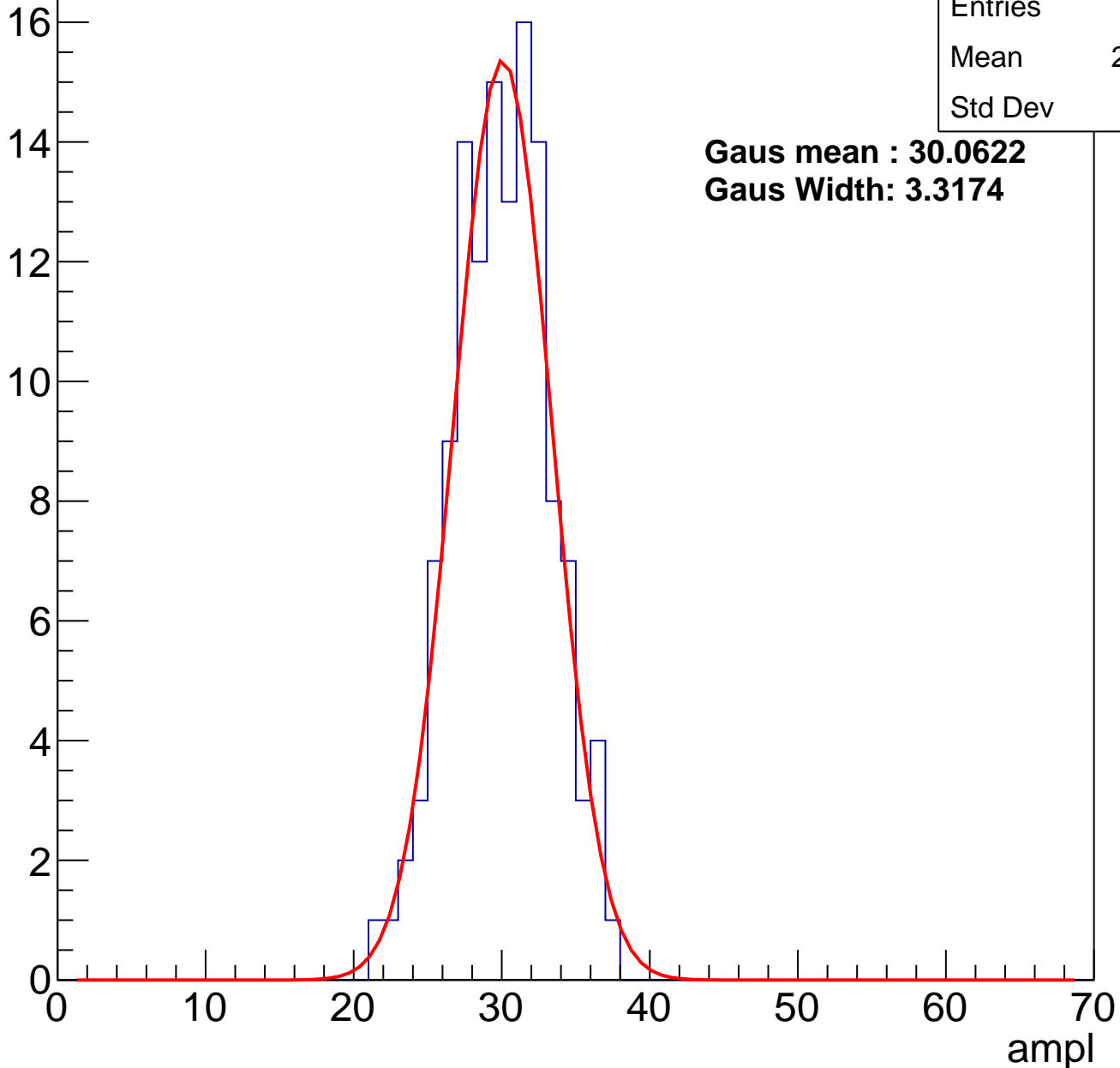
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	29.55
Std Dev	3.23

Gaus mean : 30.0622

Gaus Width: 3.3174

Entry



B1L001S, U21-ch100, adc1

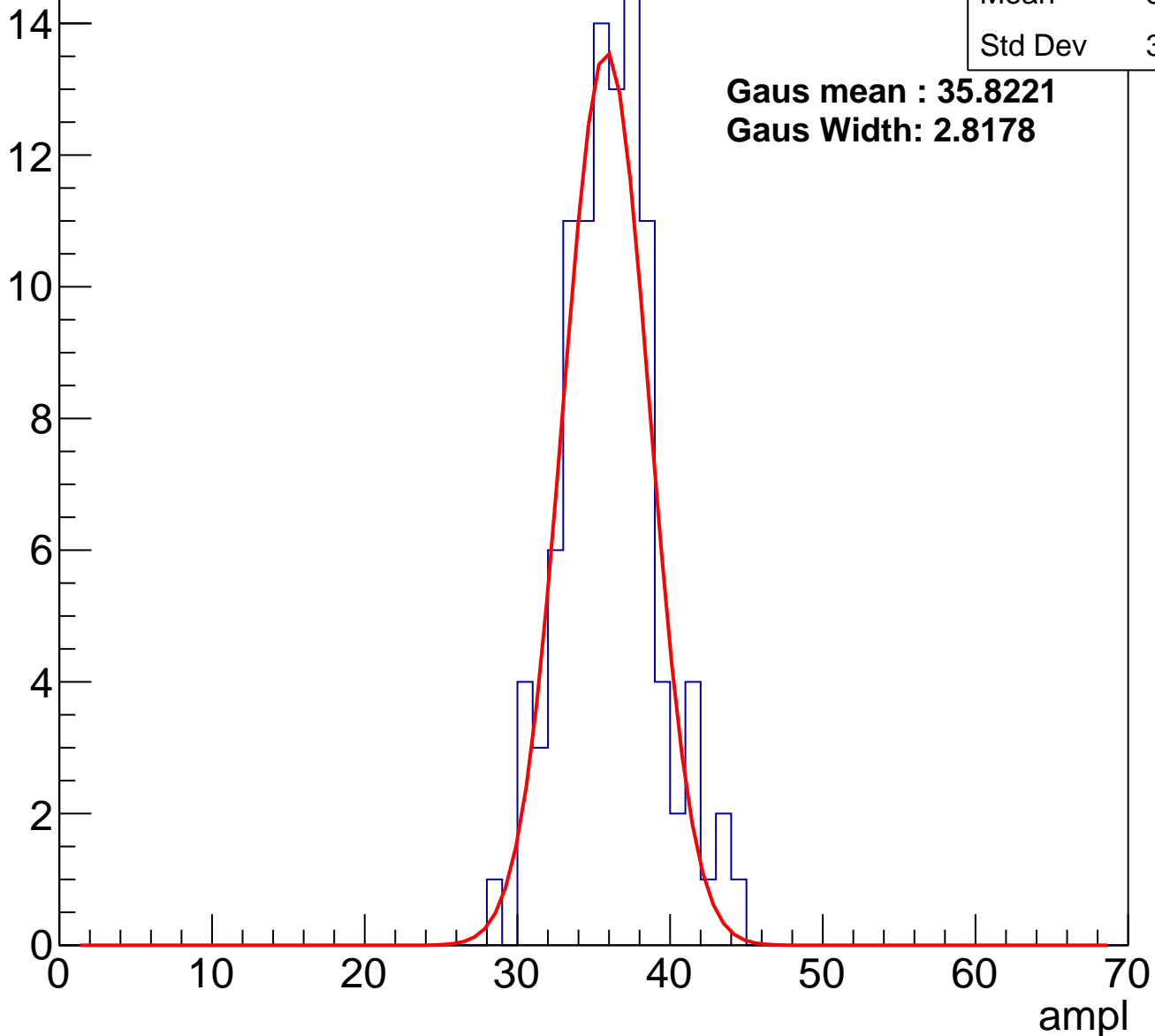
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	35.66
Std Dev	3.036

Gaus mean : 35.8221

Gaus Width: 2.8178

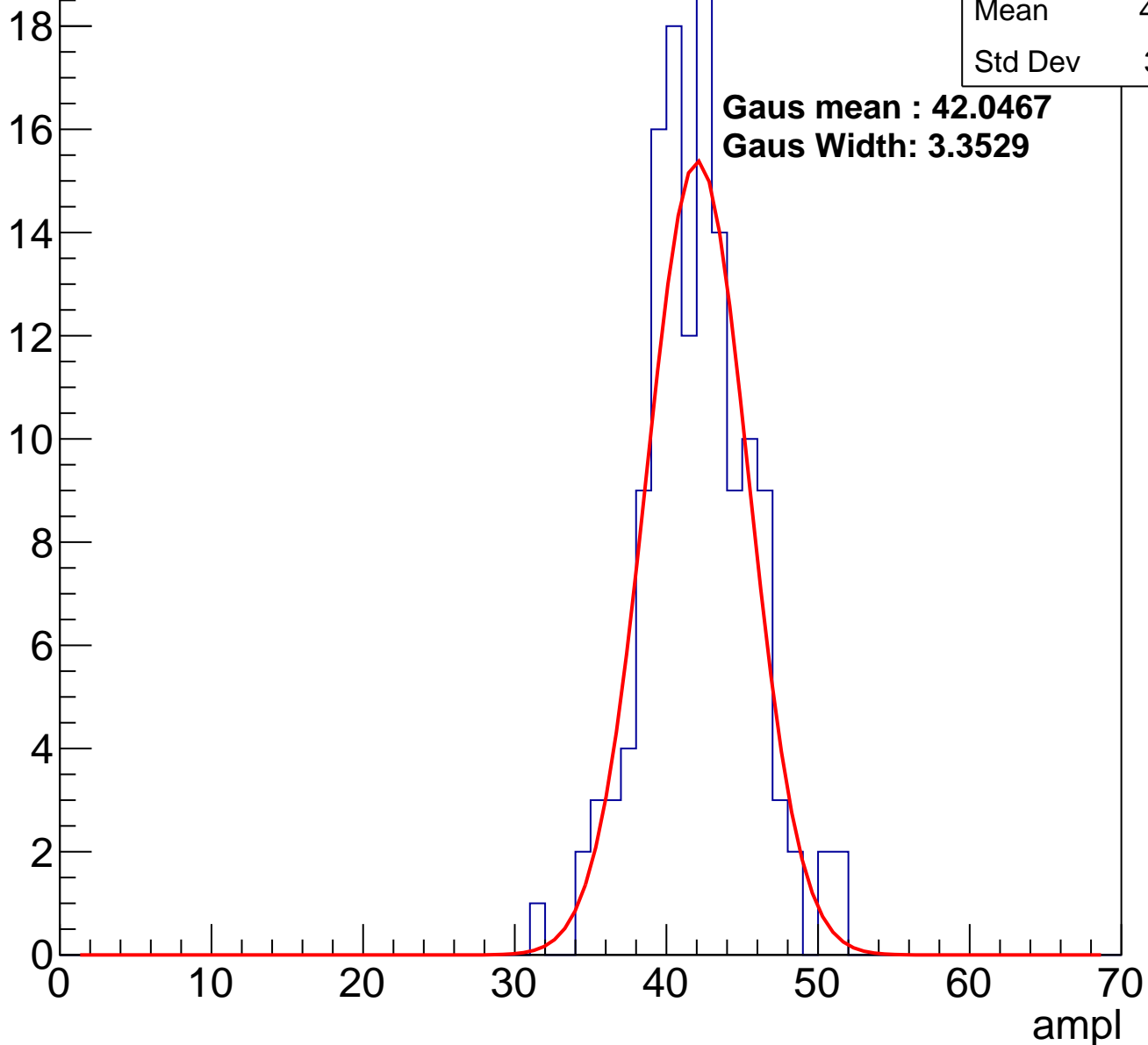
Entry



B1L001S, U21-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

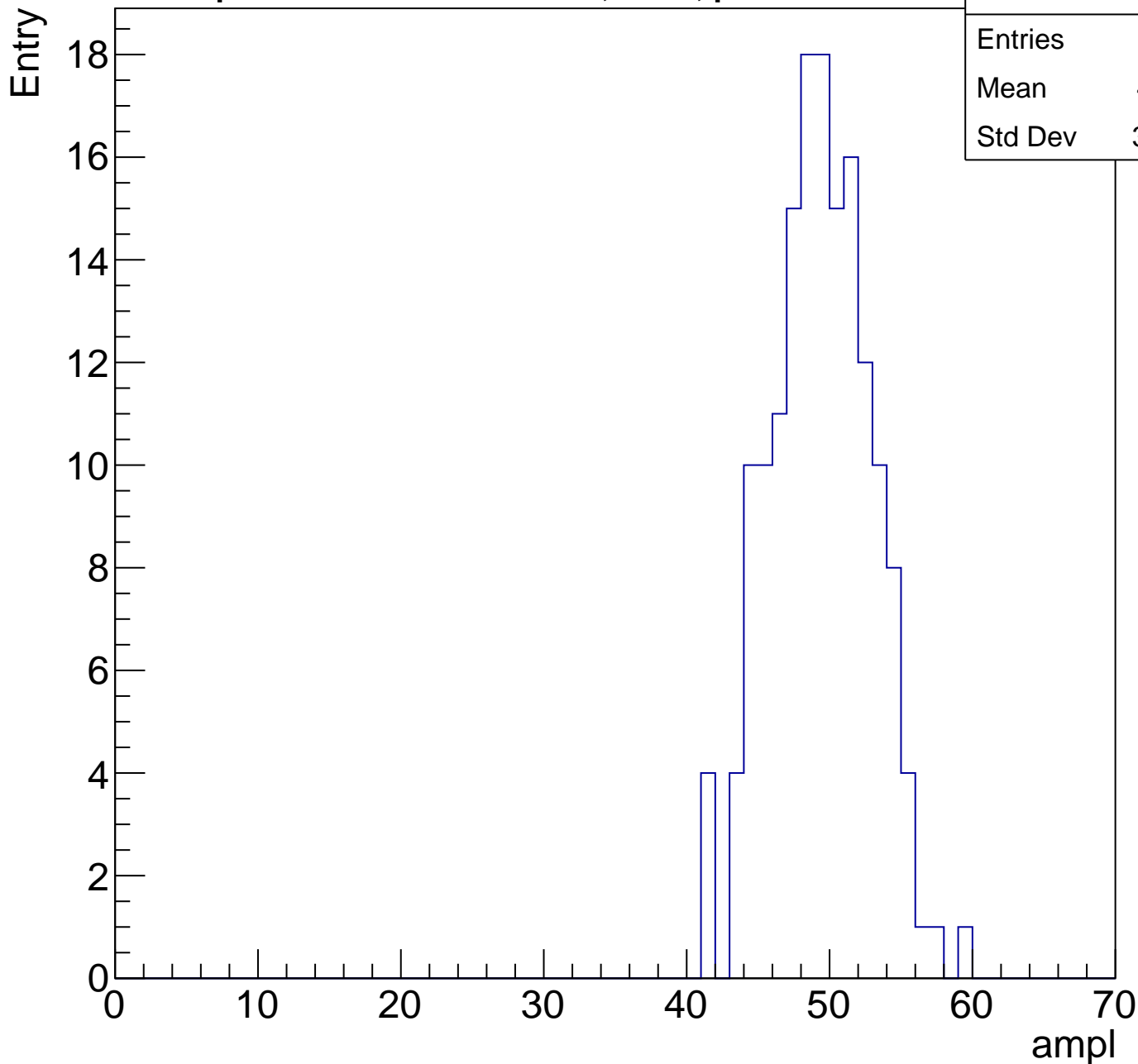
Entry



B1L001S, U21-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

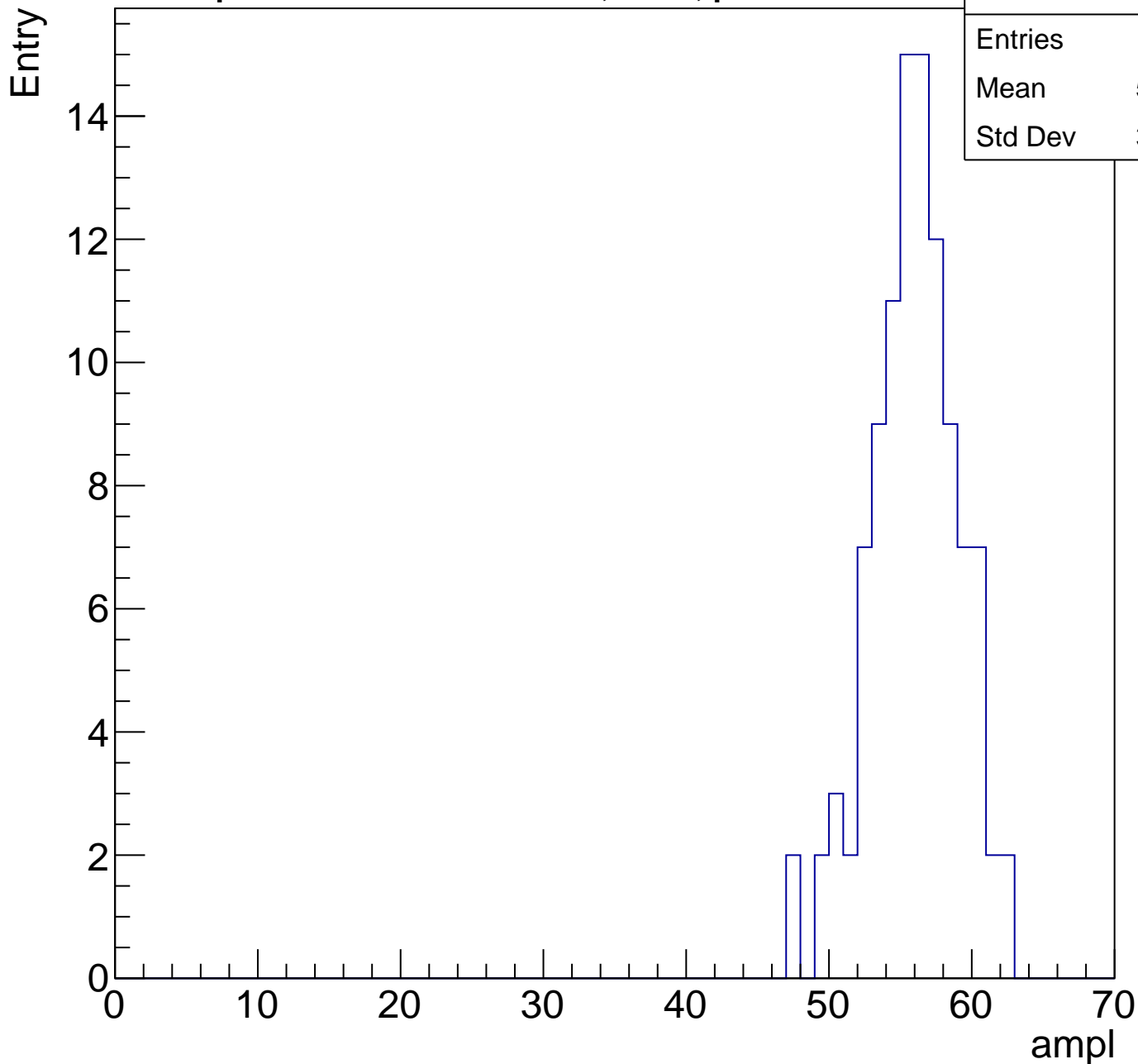
Entries	158
Mean	48.91
Std Dev	3.458



B1L001S, U21-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	55.51
Std Dev	3.071



B1L001S, U21-ch100, adc5

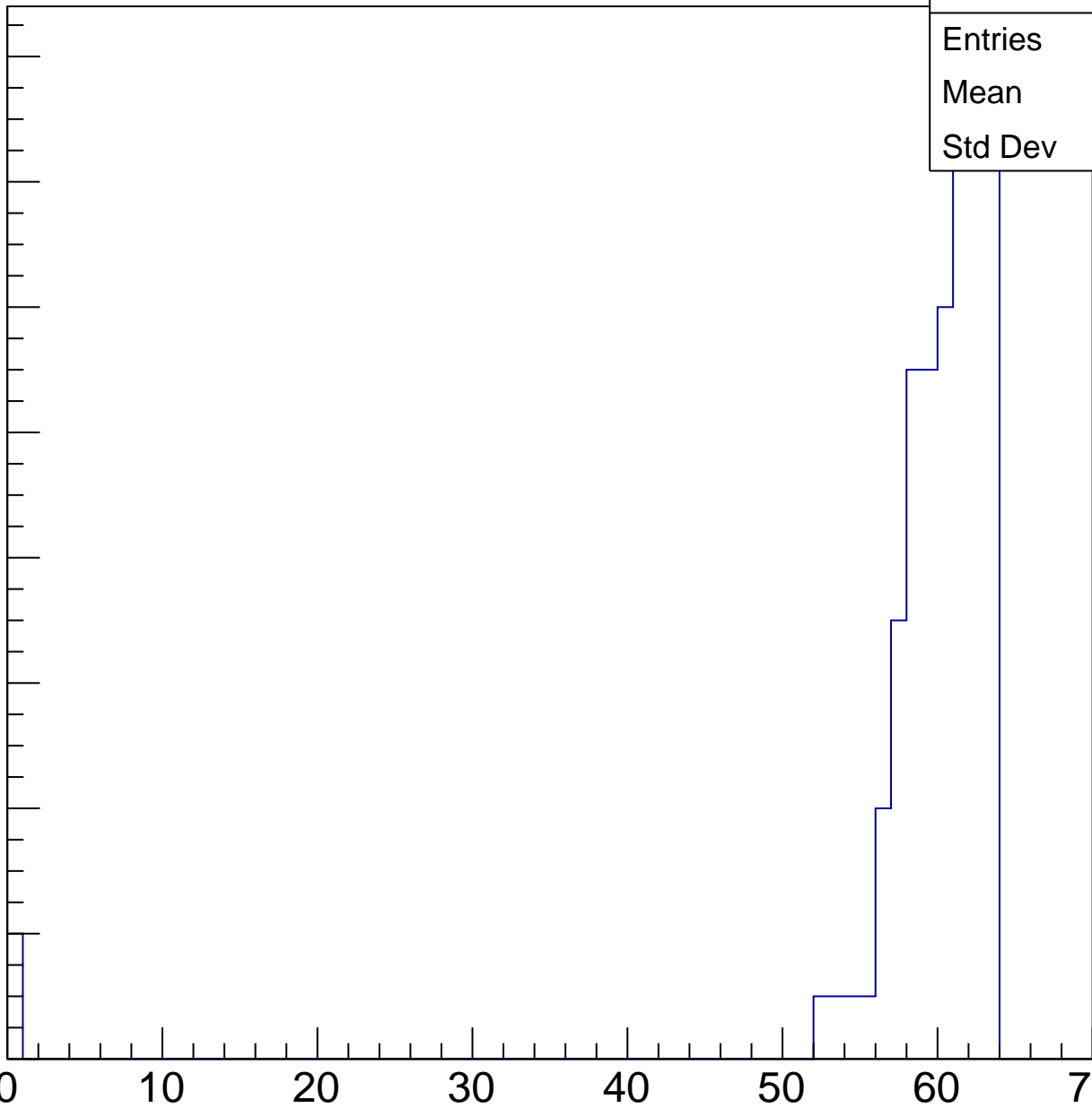
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.74
Std Dev	8.816

ampl

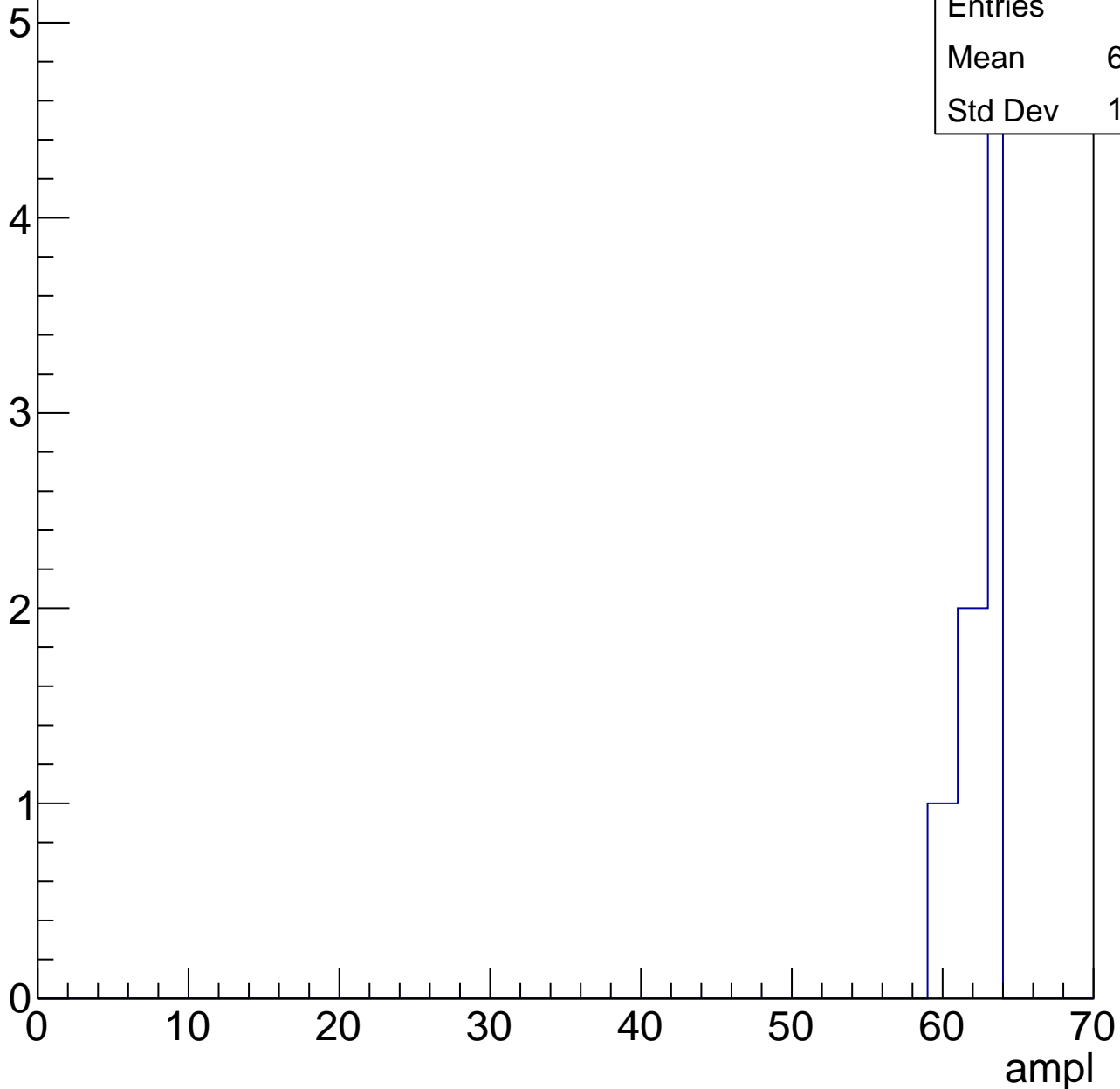


B1L001S, U21-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

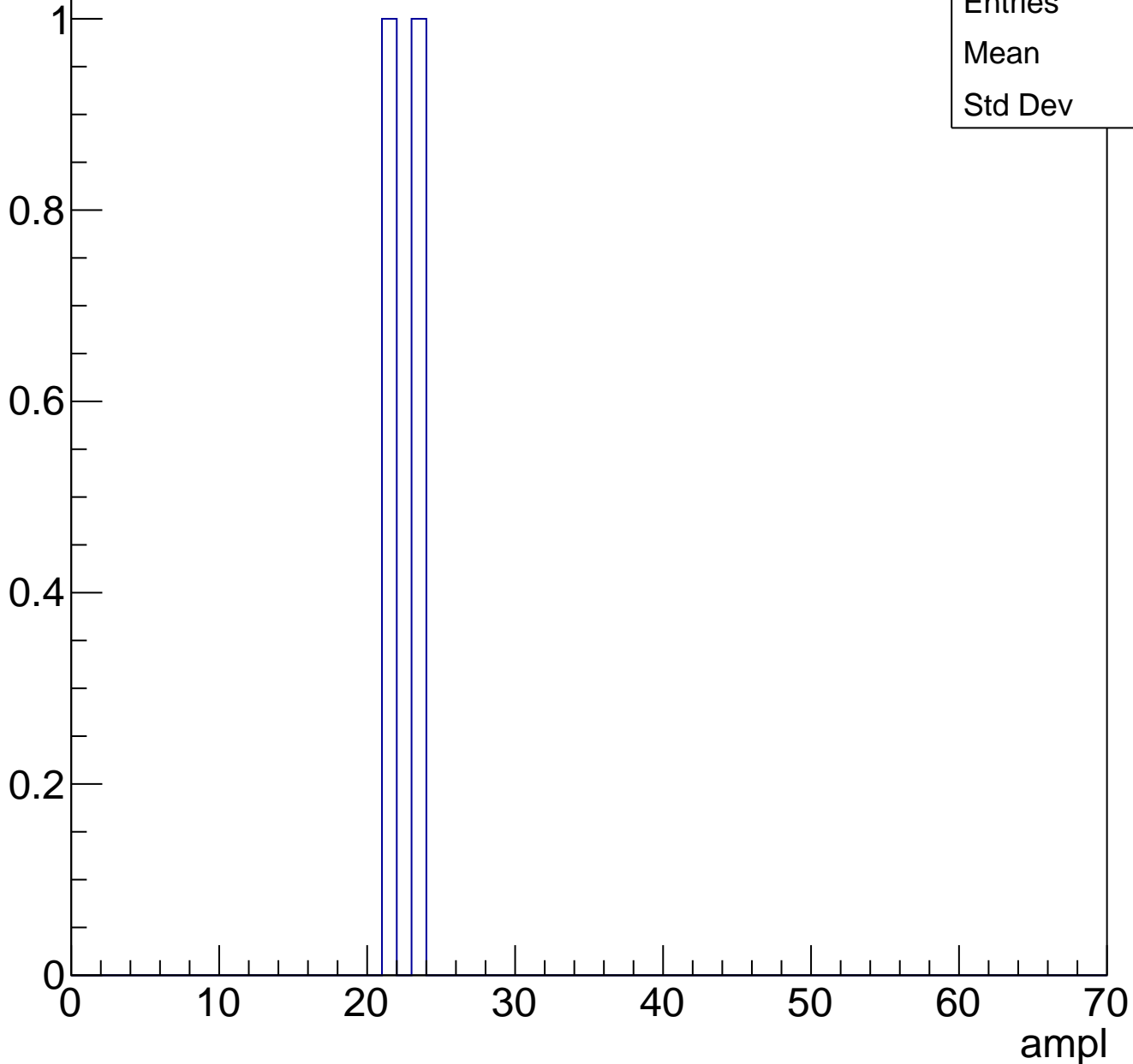
Entries	11
Mean	61.82
Std Dev	1.336



B1L001S, U21-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch101, adc0

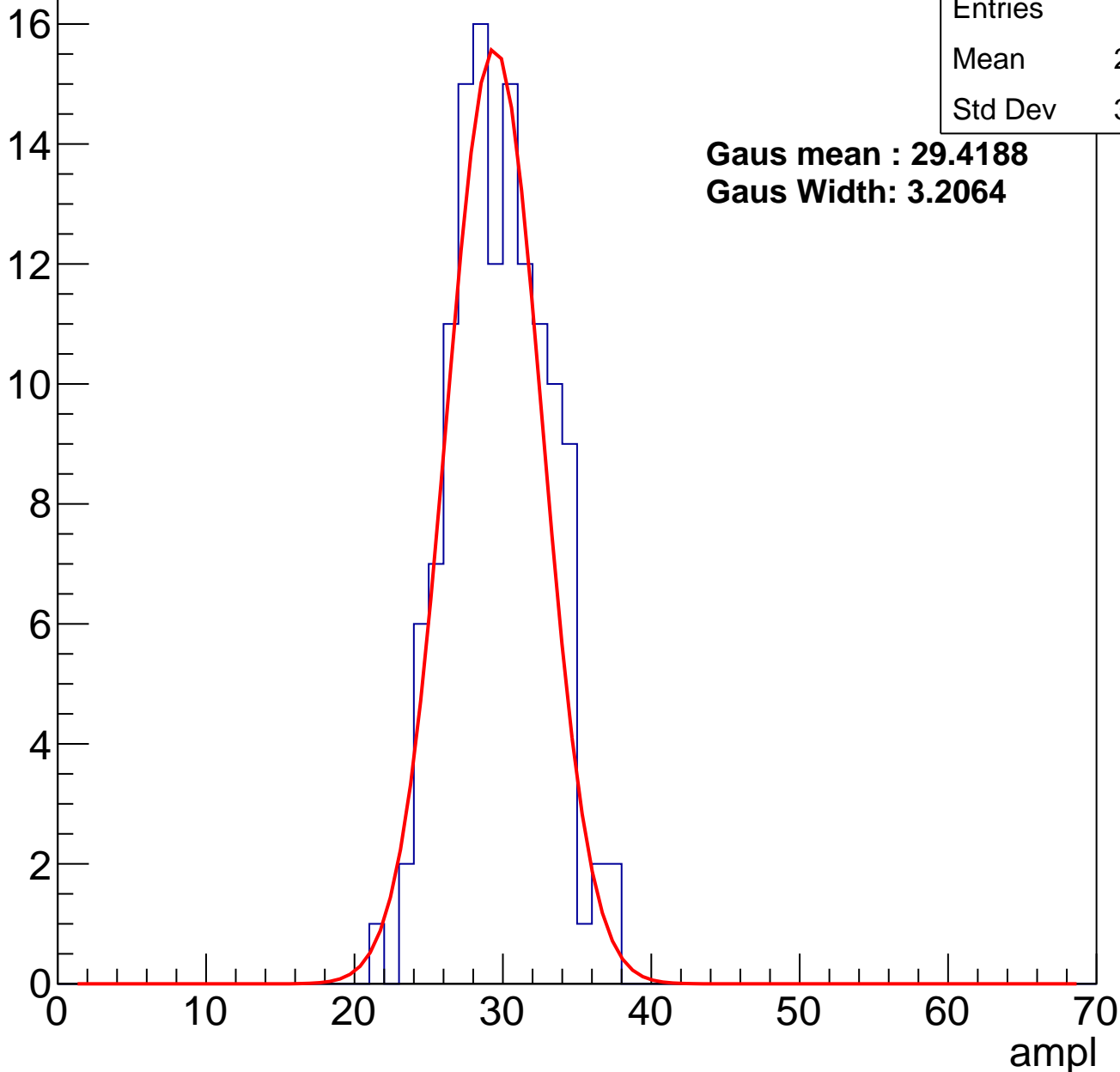
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	29.27
Std Dev	3.224

Gaus mean : 29.4188

Gaus Width: 3.2064

Entry



B1L001S, U21-ch101, adc1

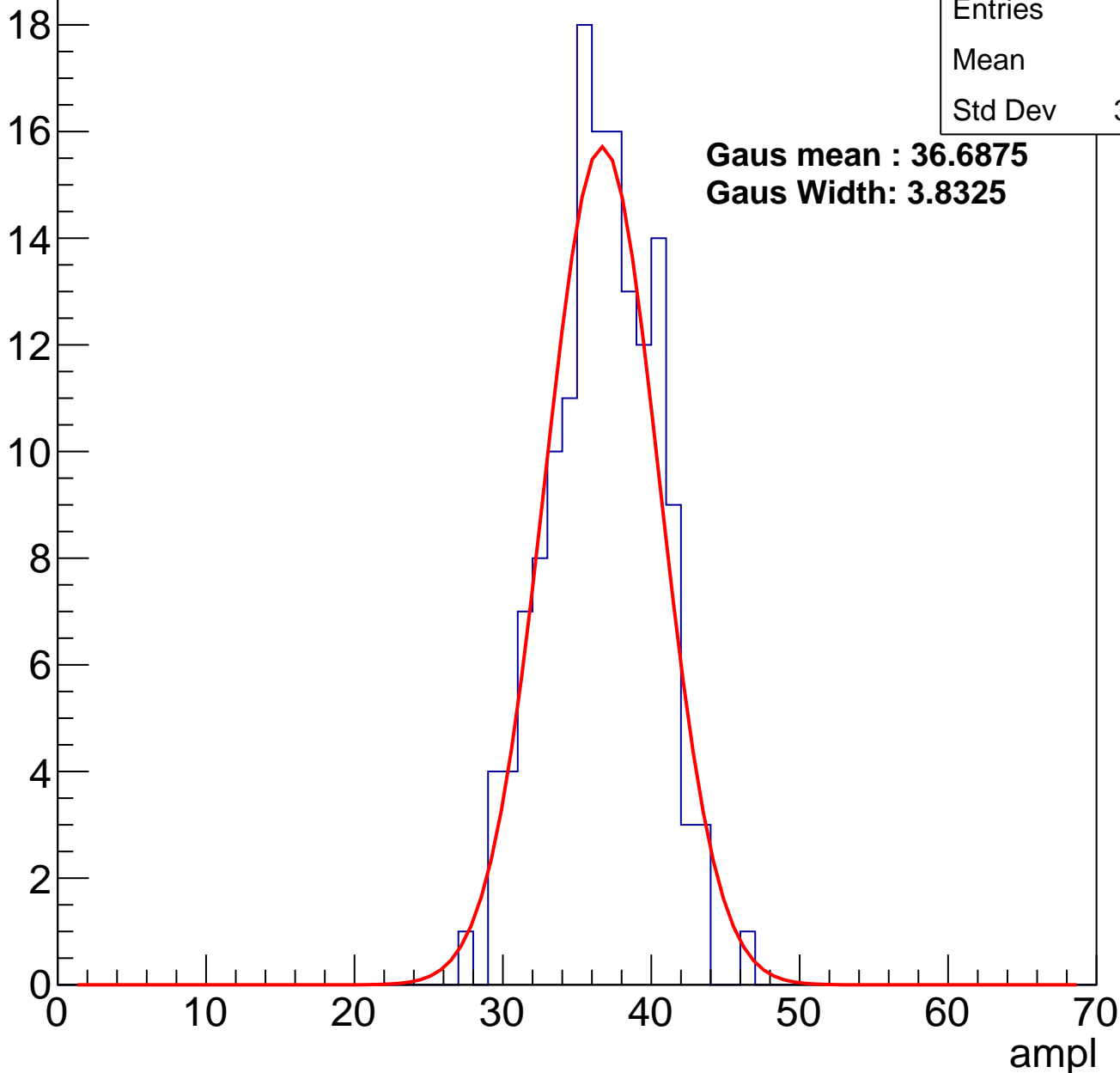
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	36.2
Std Dev	3.512

Gaus mean : 36.6875

Gaus Width: 3.8325

Entry



B1L001S, U21-ch101, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

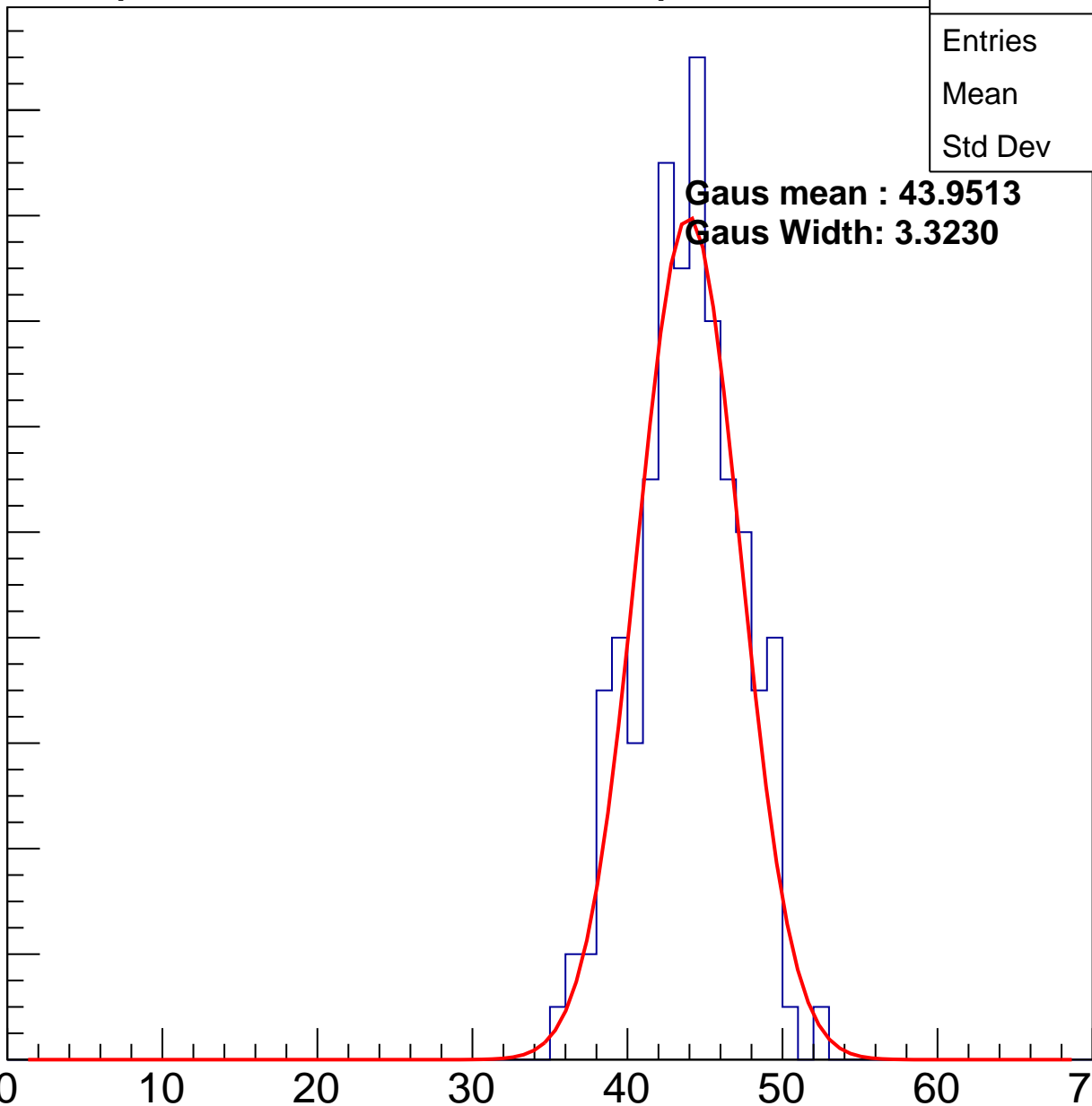
18
16
14
12
10
8
6
4
2
0

Entries	140
Mean	43.44
Std Dev	3.33

Gaus mean : 43.9513

Gaus Width: 3.3230

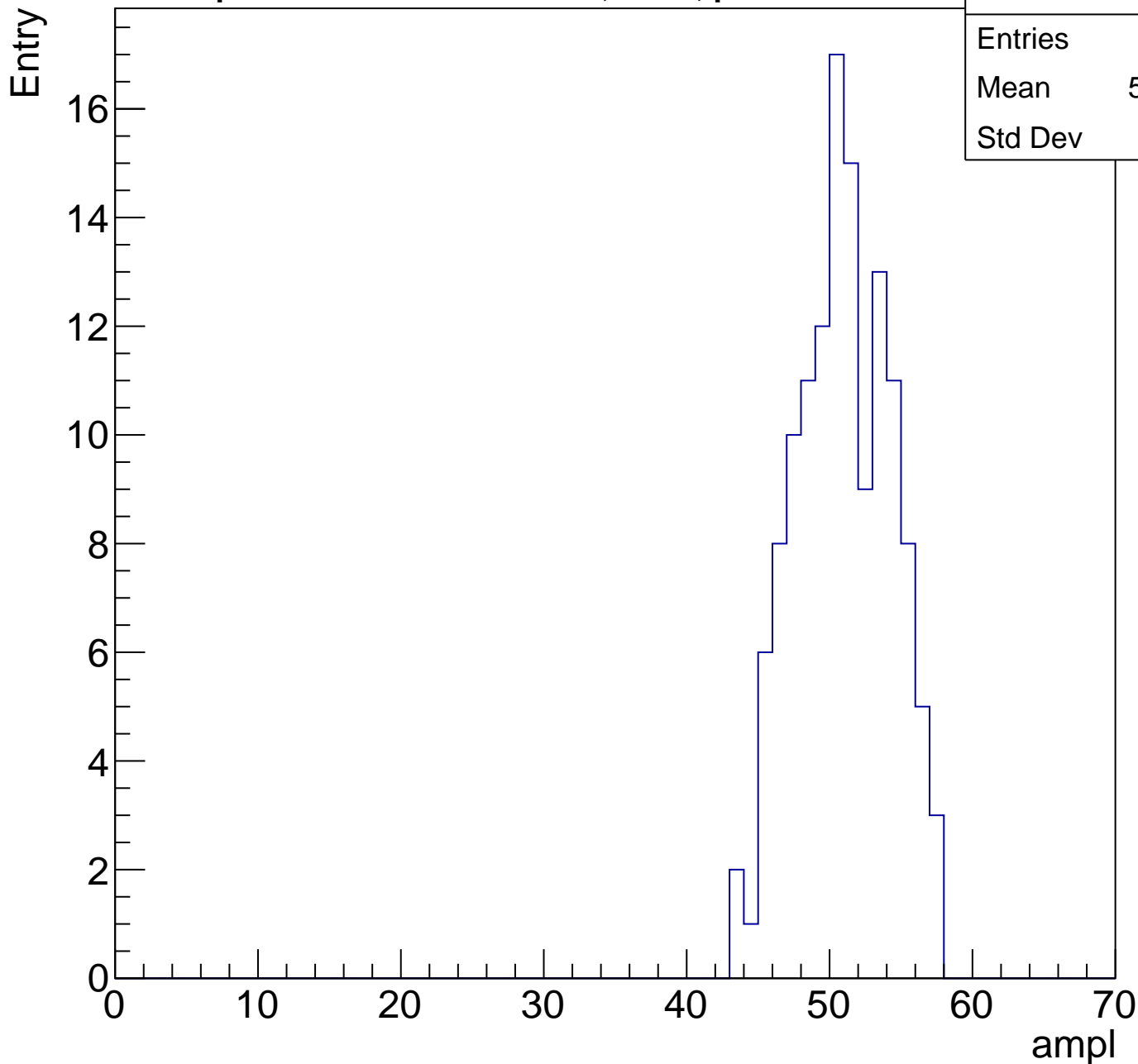
0 10 20 30 40 50 60 70
ampl



B1L001S, U21-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	50.47
Std Dev	3.27



B1L001S, U21-ch101, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	55.99
Std Dev	3.194

Entry

12

10

8

6

4

2

0

0

10

20

30

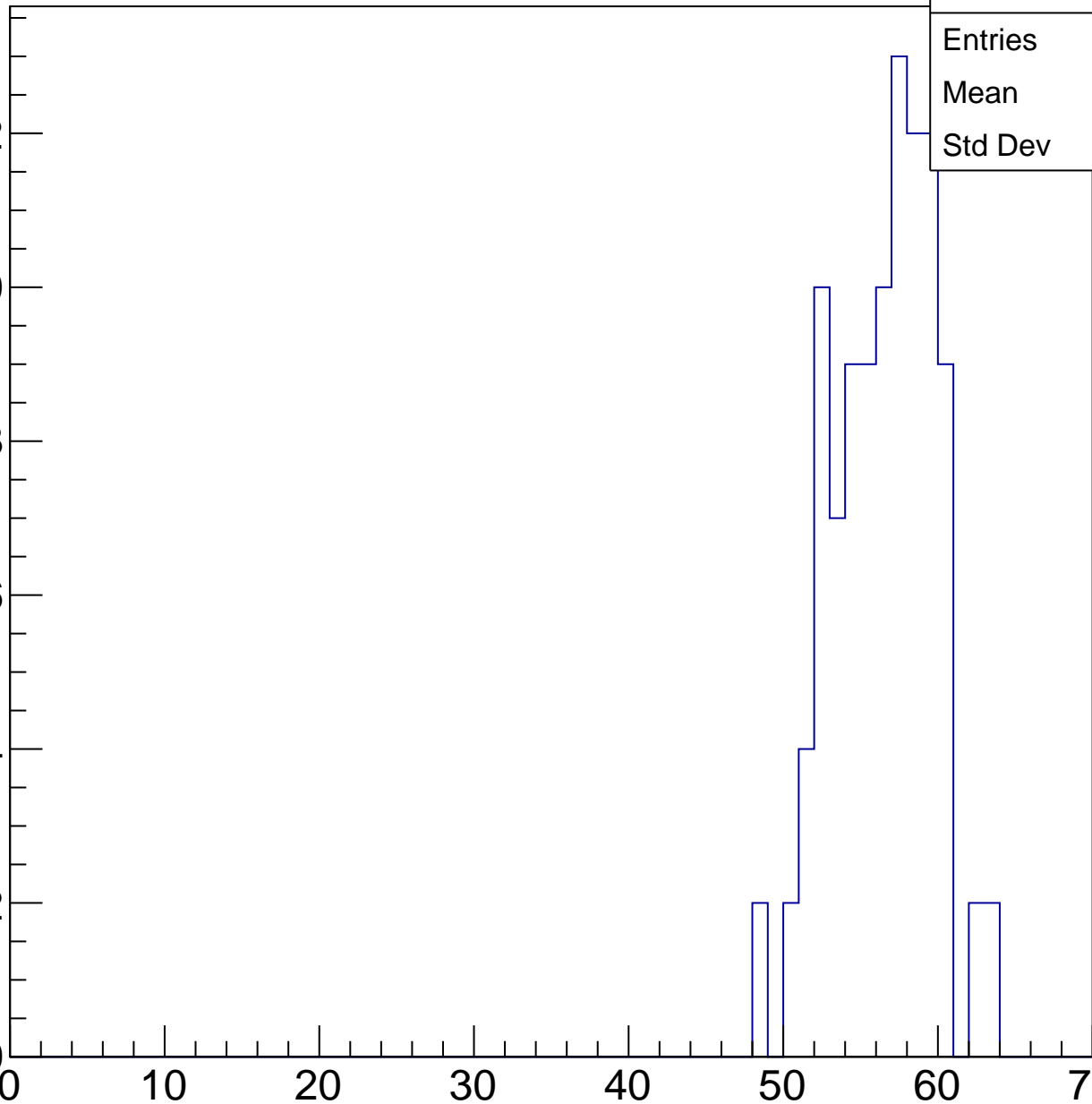
40

50

60

70

ampl



B1L001S, U21-ch101, adc5

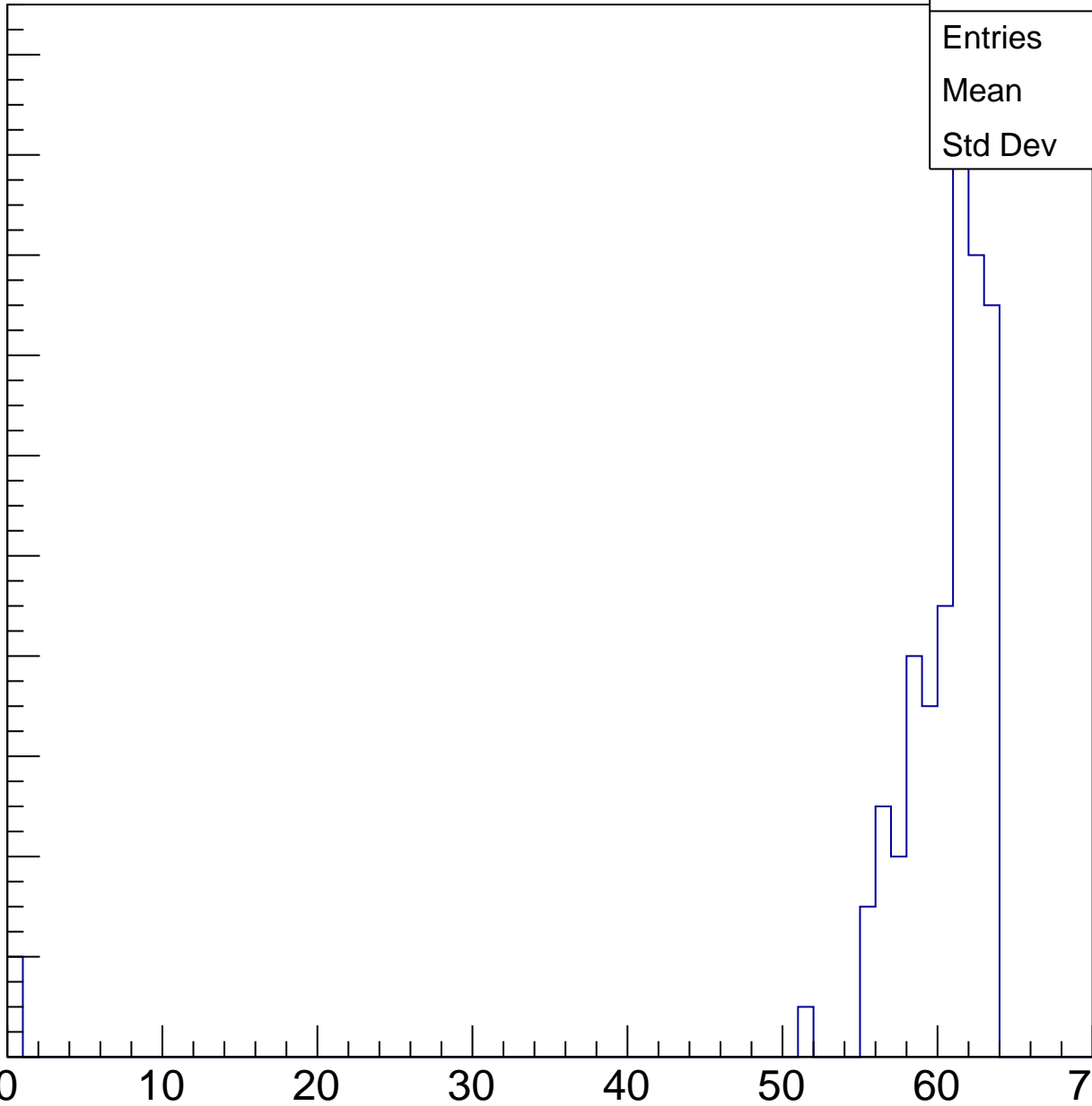
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	90
Mean	58.87
Std Dev	9.198

ampl



B1L001S, U21-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

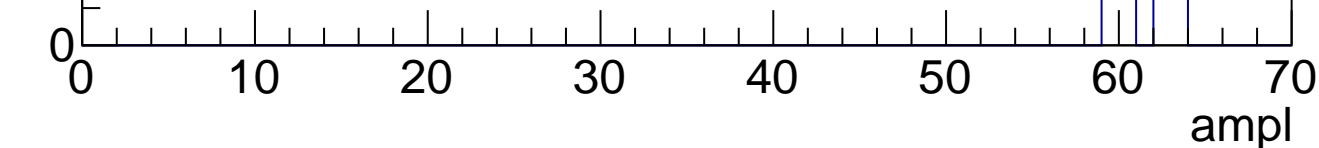
8

Mean

61.5

Std Dev

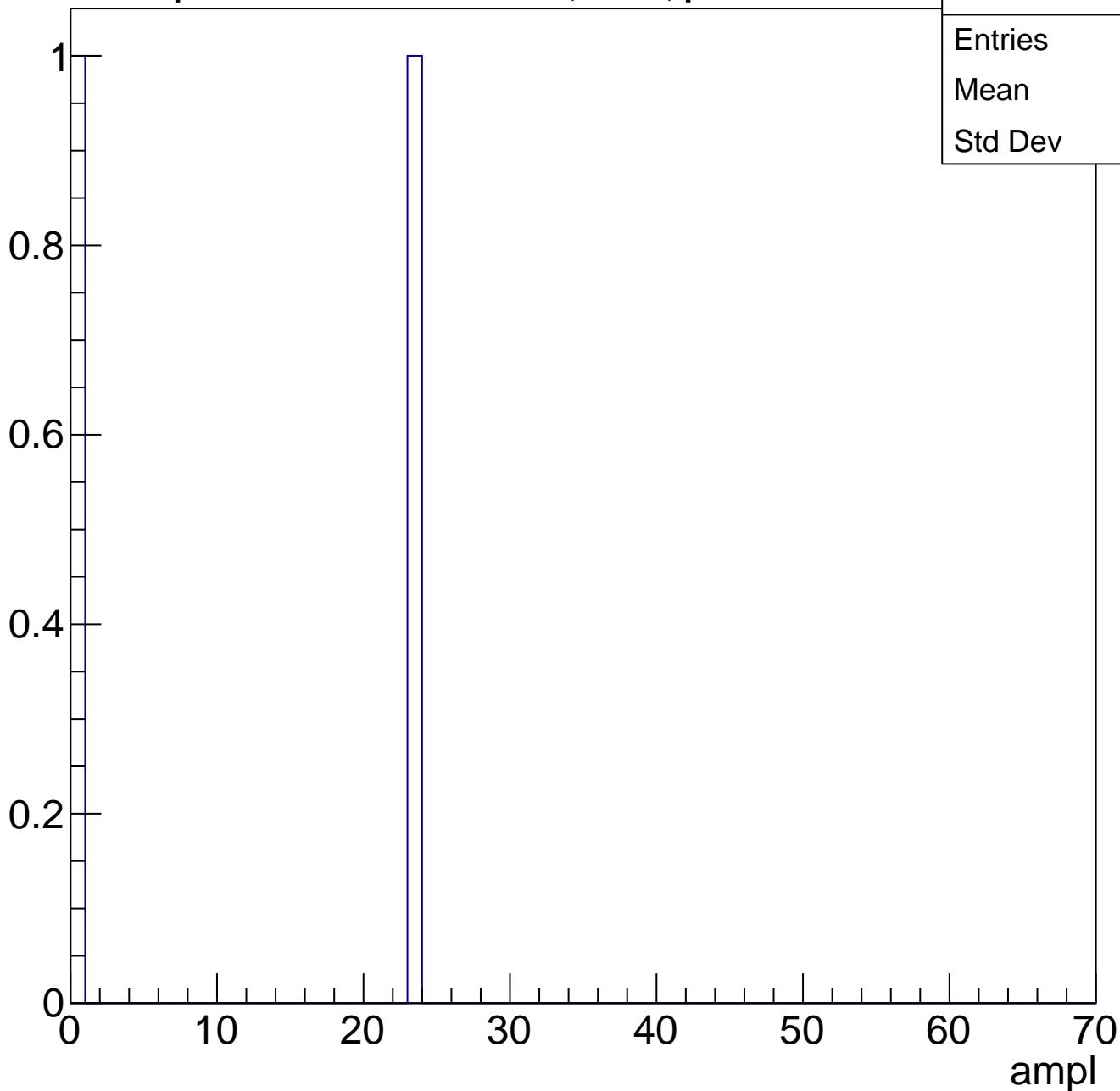
1.5



B1L001S, U21-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch102, adc0

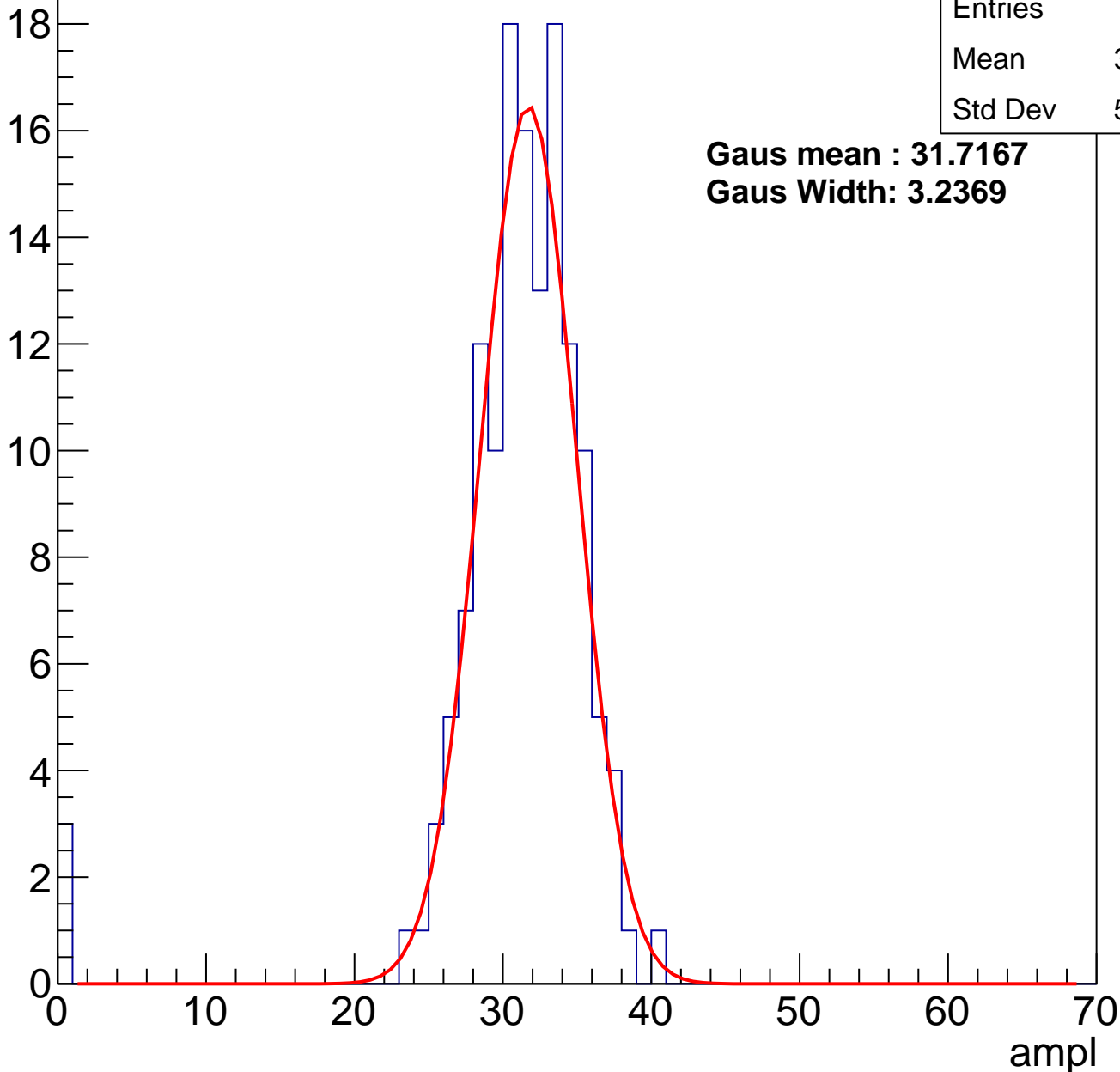
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.55
Std Dev	5.497

Gaus mean : 31.7167

Gaus Width: 3.2369

Entry



B1L001S, U21-ch102, adc1

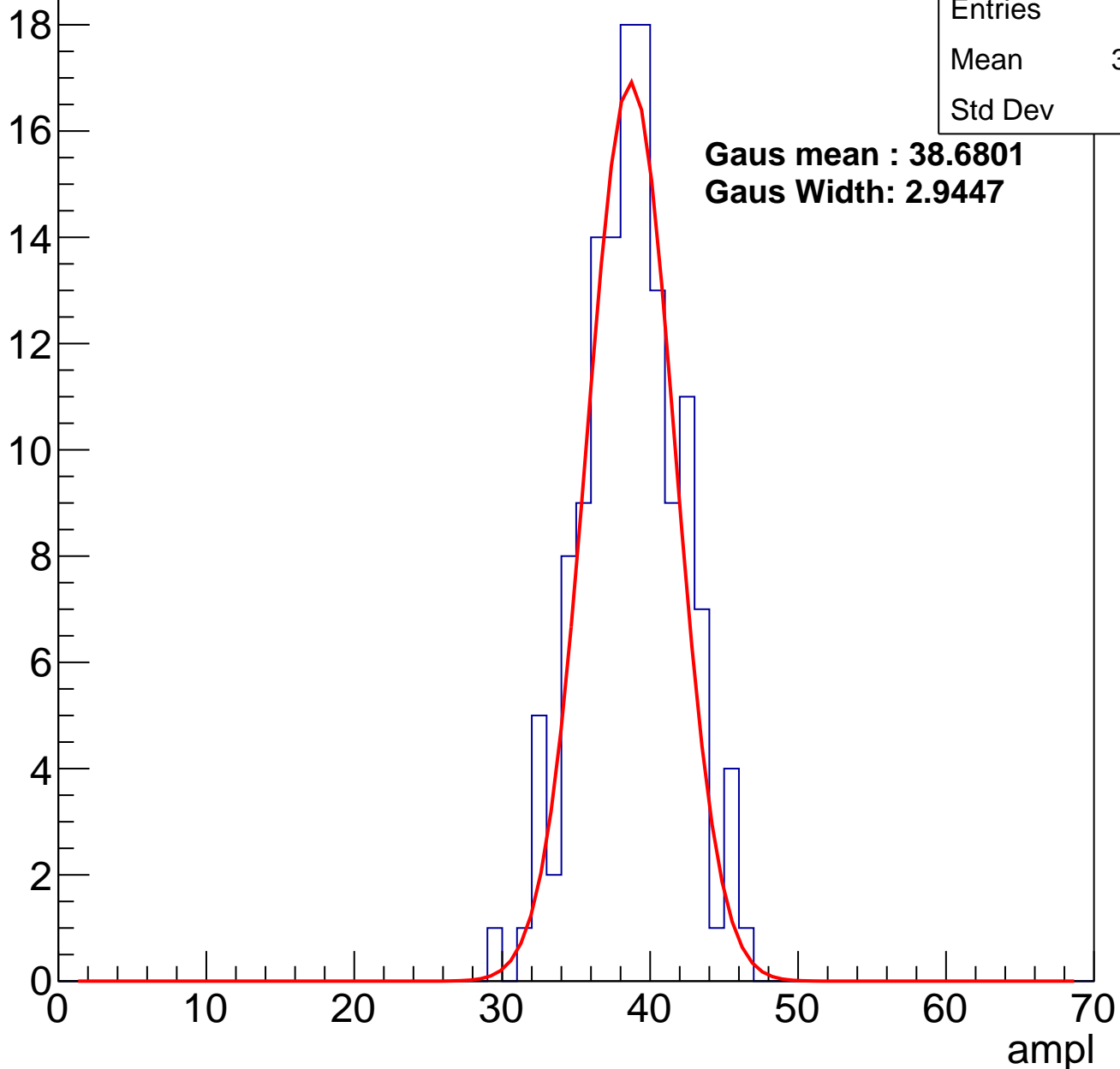
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	38.26
Std Dev	3.24

Gaus mean : 38.6801

Gaus Width: 2.9447

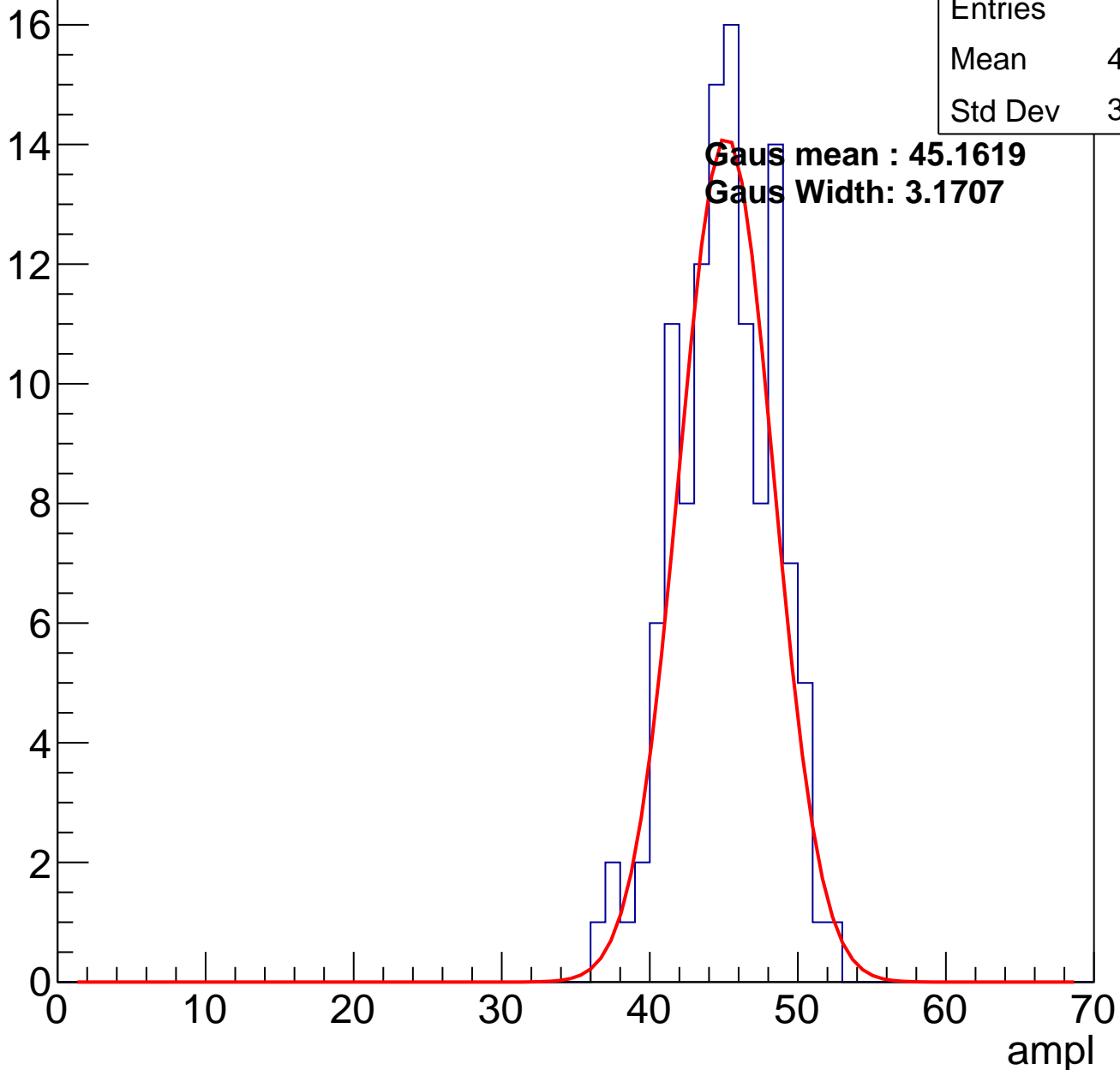
Entry



B1L001S, U21-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

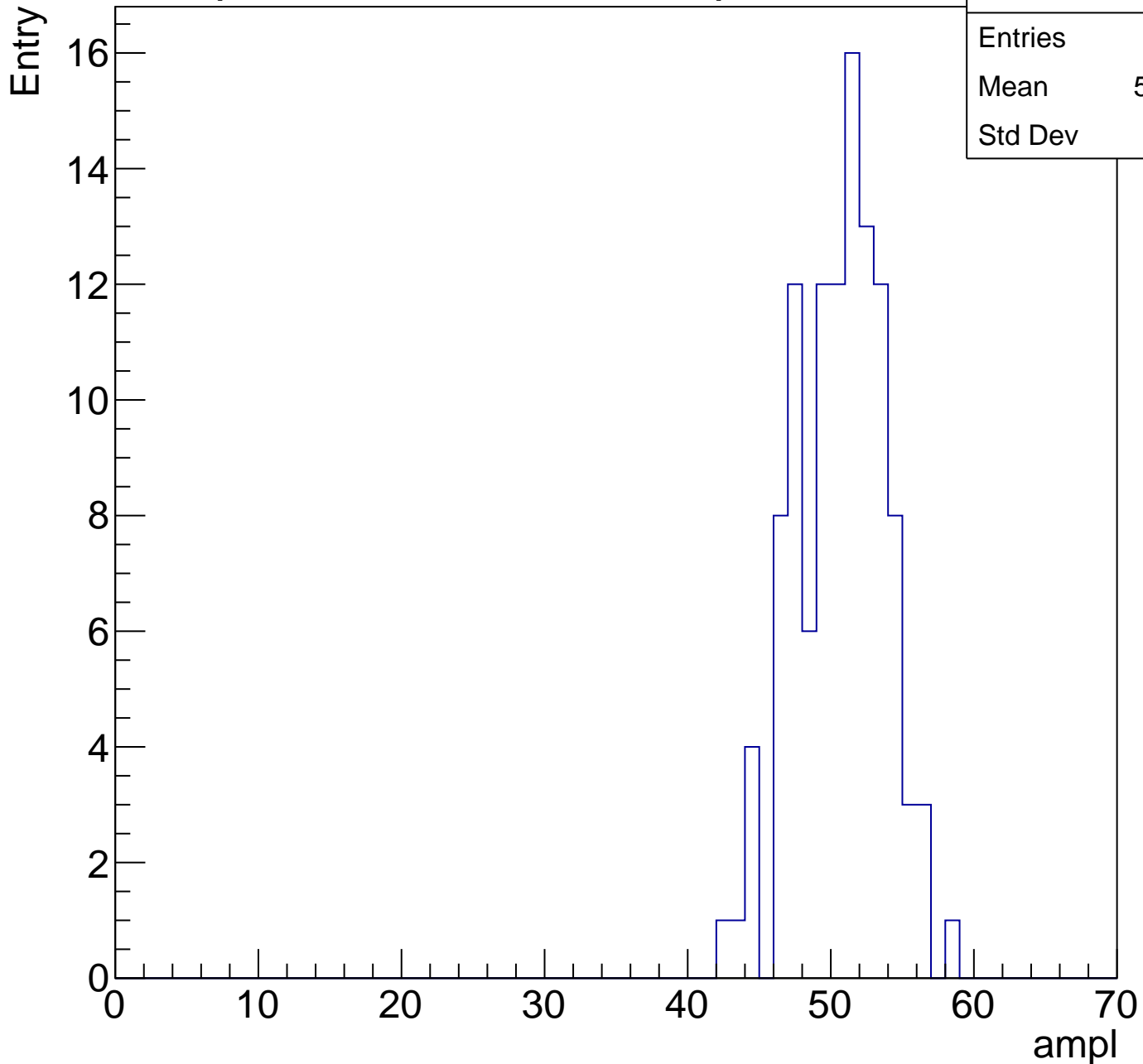
Entry



B1L001S, U21-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	50.18
Std Dev	3.1



B1L001S, U21-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

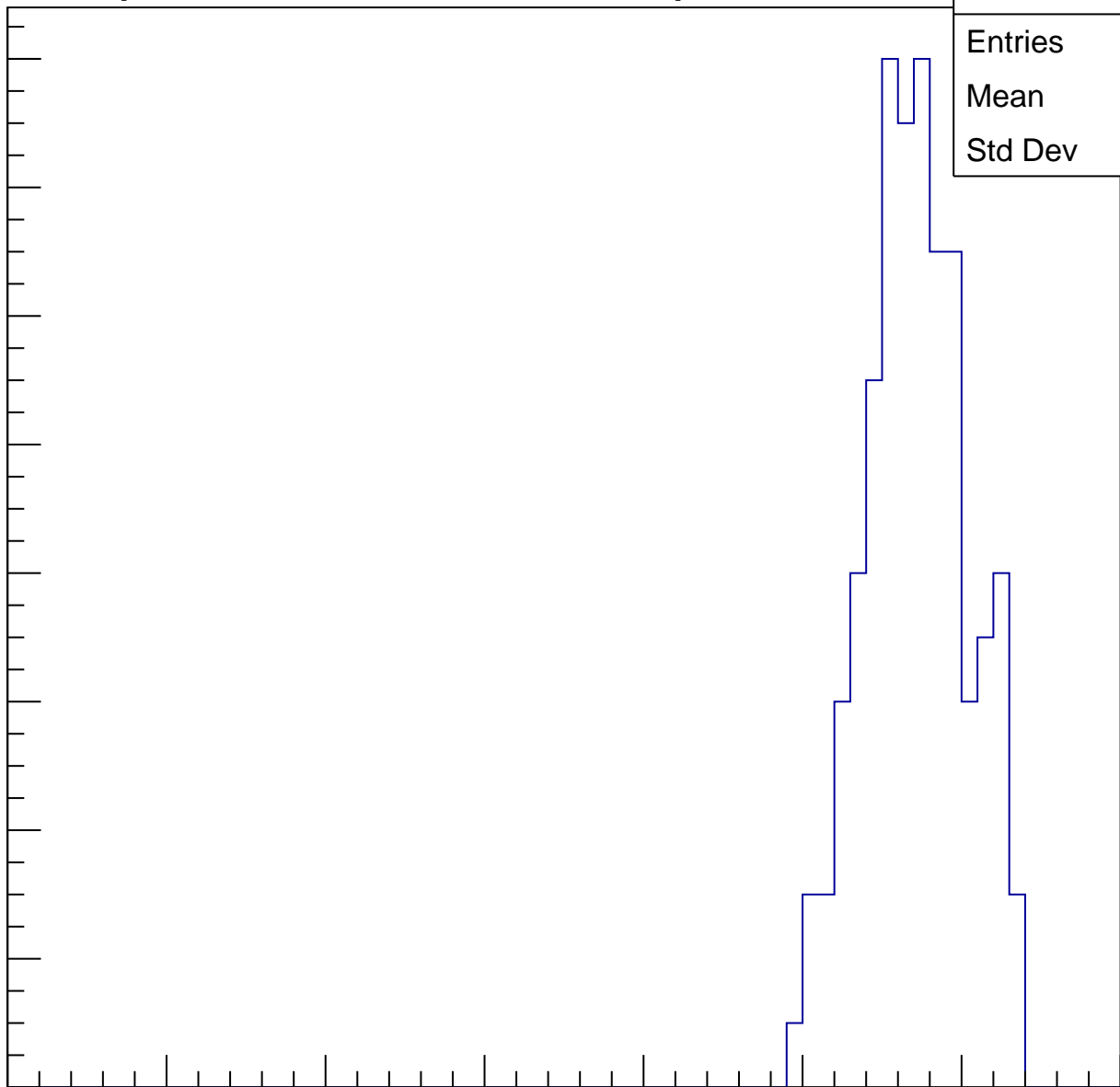
Entries	129
Mean	56.64
Std Dev	3.172

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch102, adc5

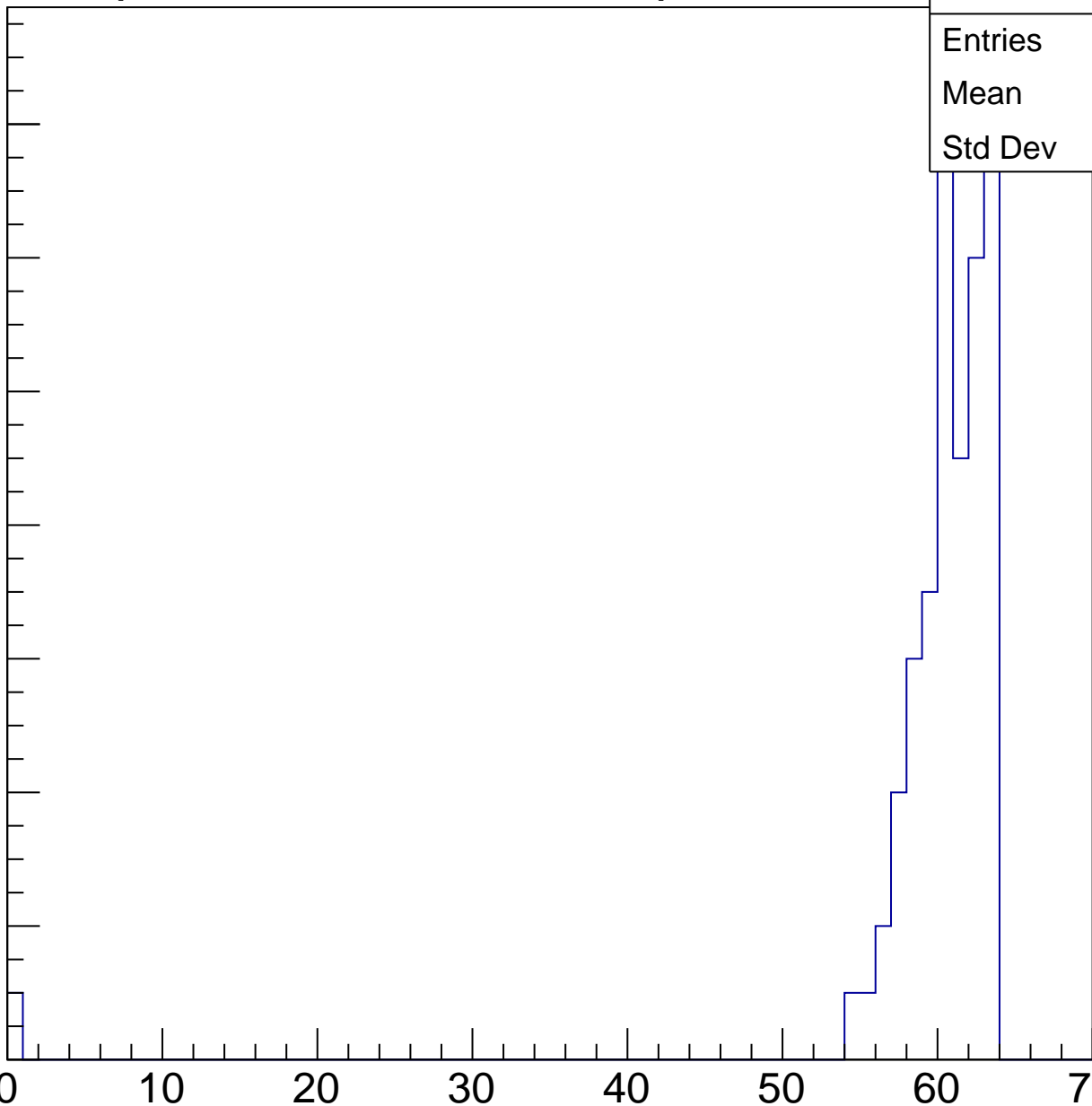
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.56
Std Dev	7.395

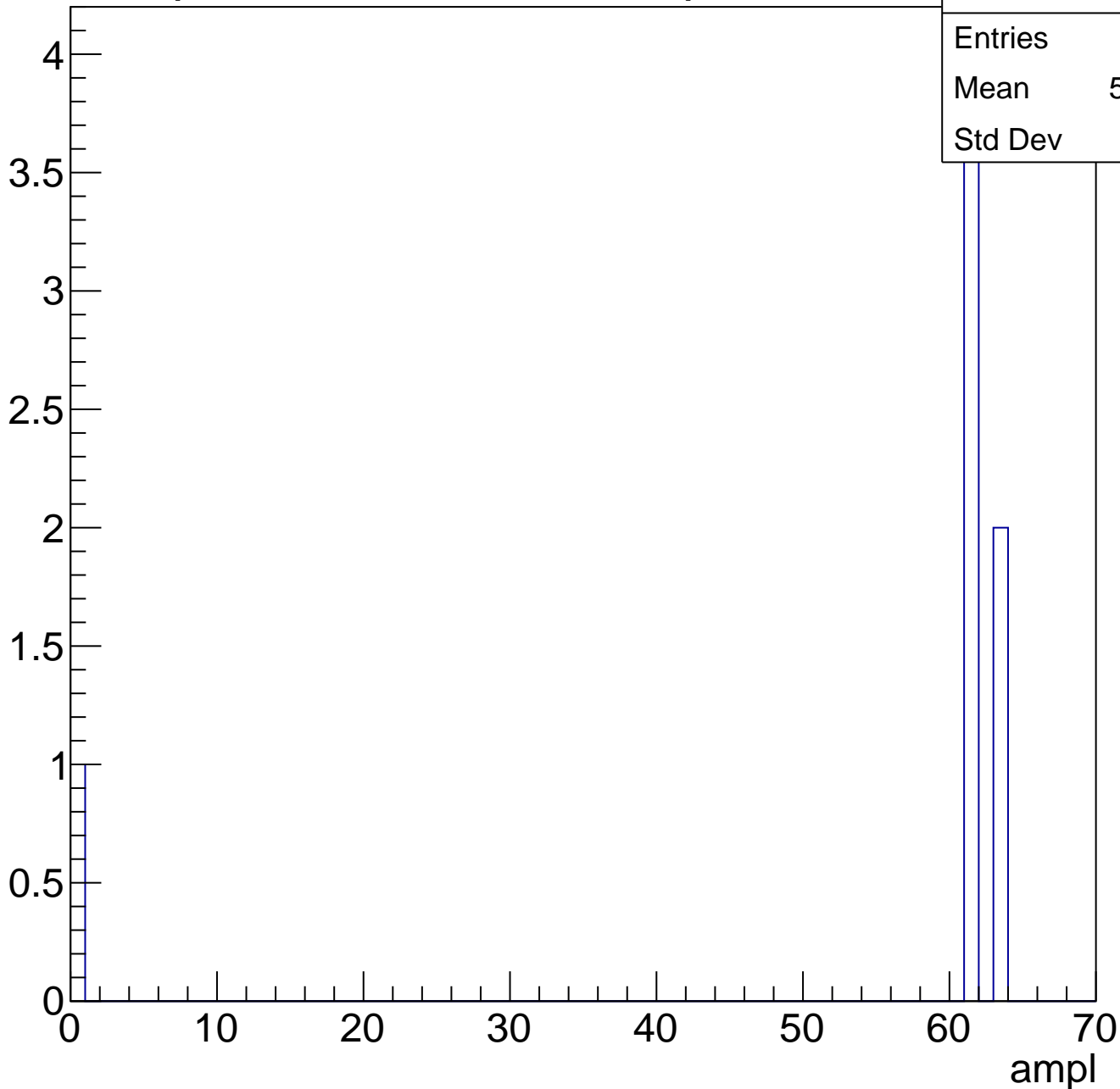
ampl



B1L001S, U21-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U21-ch103, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	29.24
Std Dev	3.578

Gaus mean : 29.9501

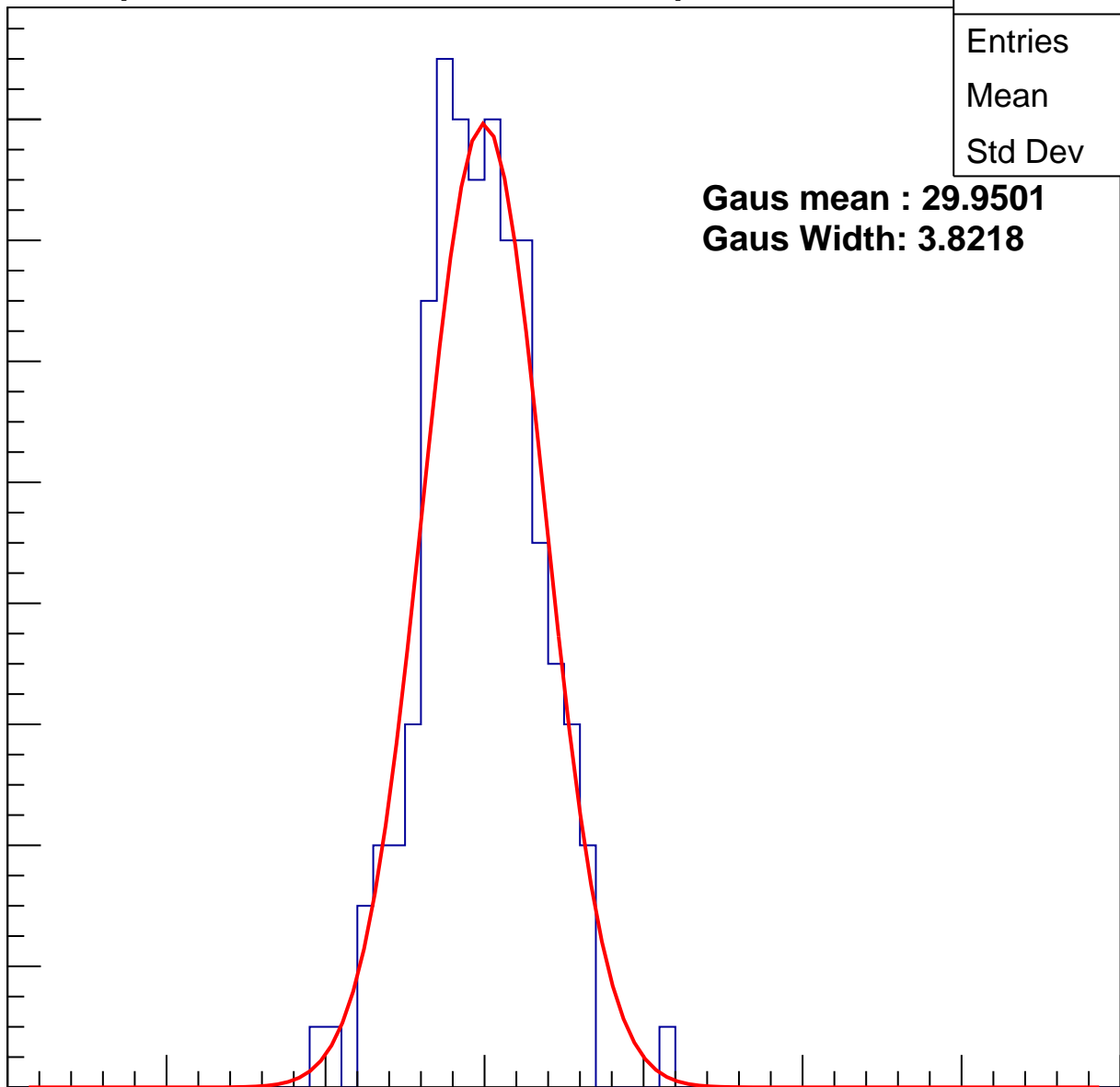
Gaus Width: 3.8218

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch103, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	36.45
Std Dev	3.206

Gaus mean : 36.8179

Gaus Width: 3.4182

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

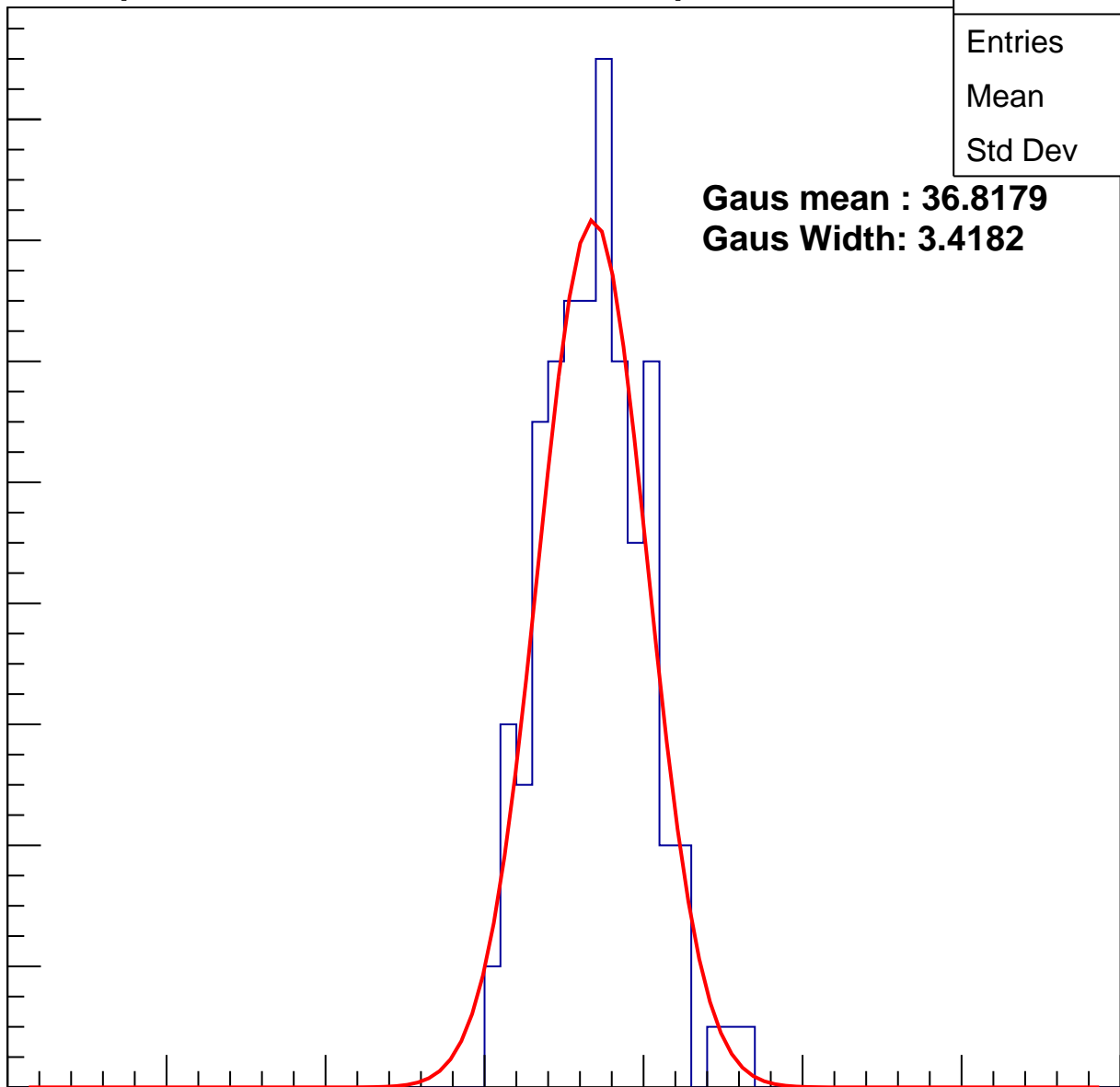
40

50

60

70

ampl



B1L001S, U21-ch103, adc2

calib_packv5_042523_0143.root, FC#2, port C2

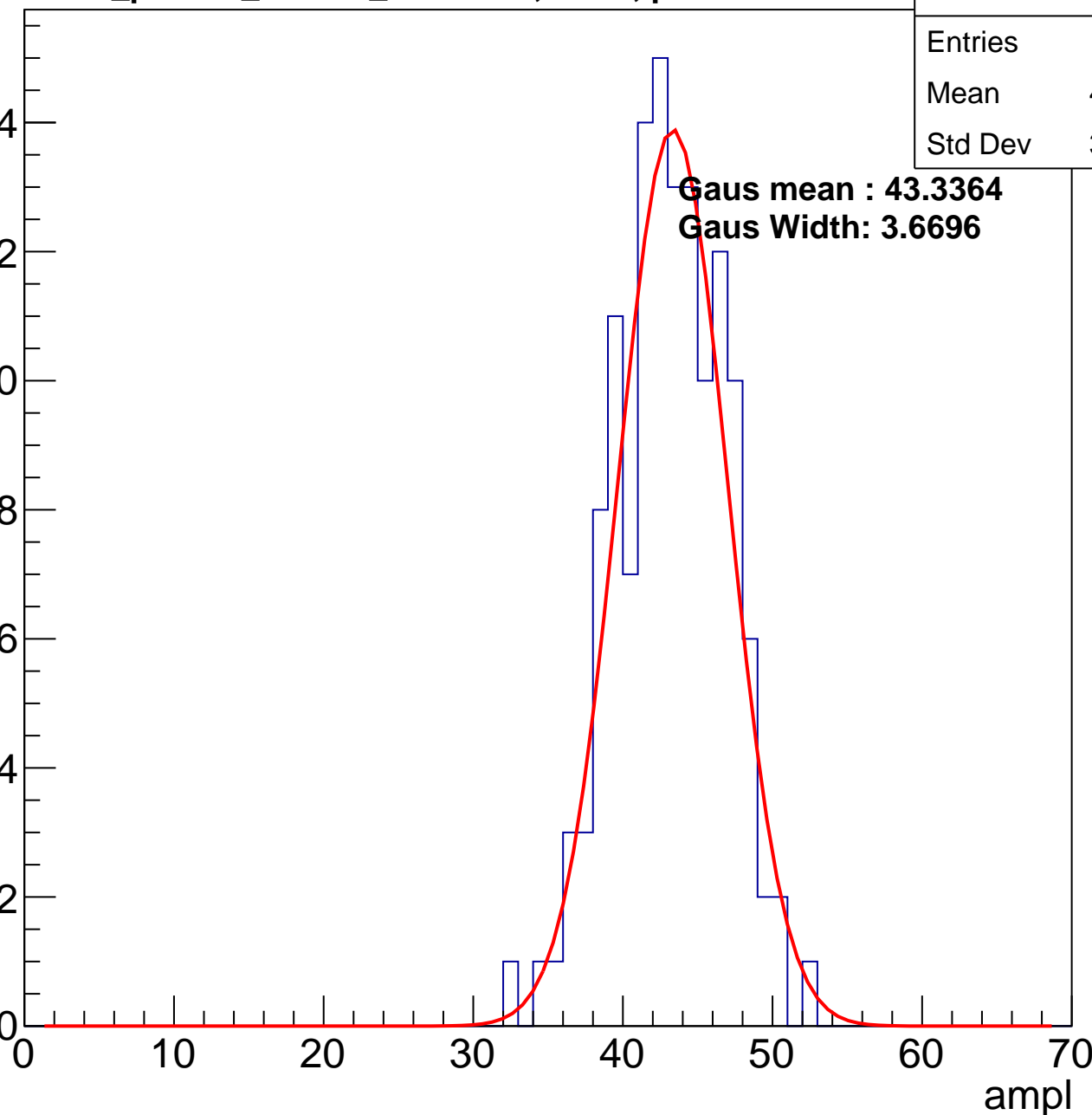
Entry

14
12
10
8
6
4
2
0

Entries	133
Mean	42.69
Std Dev	3.617

Gaus mean : 43.3364

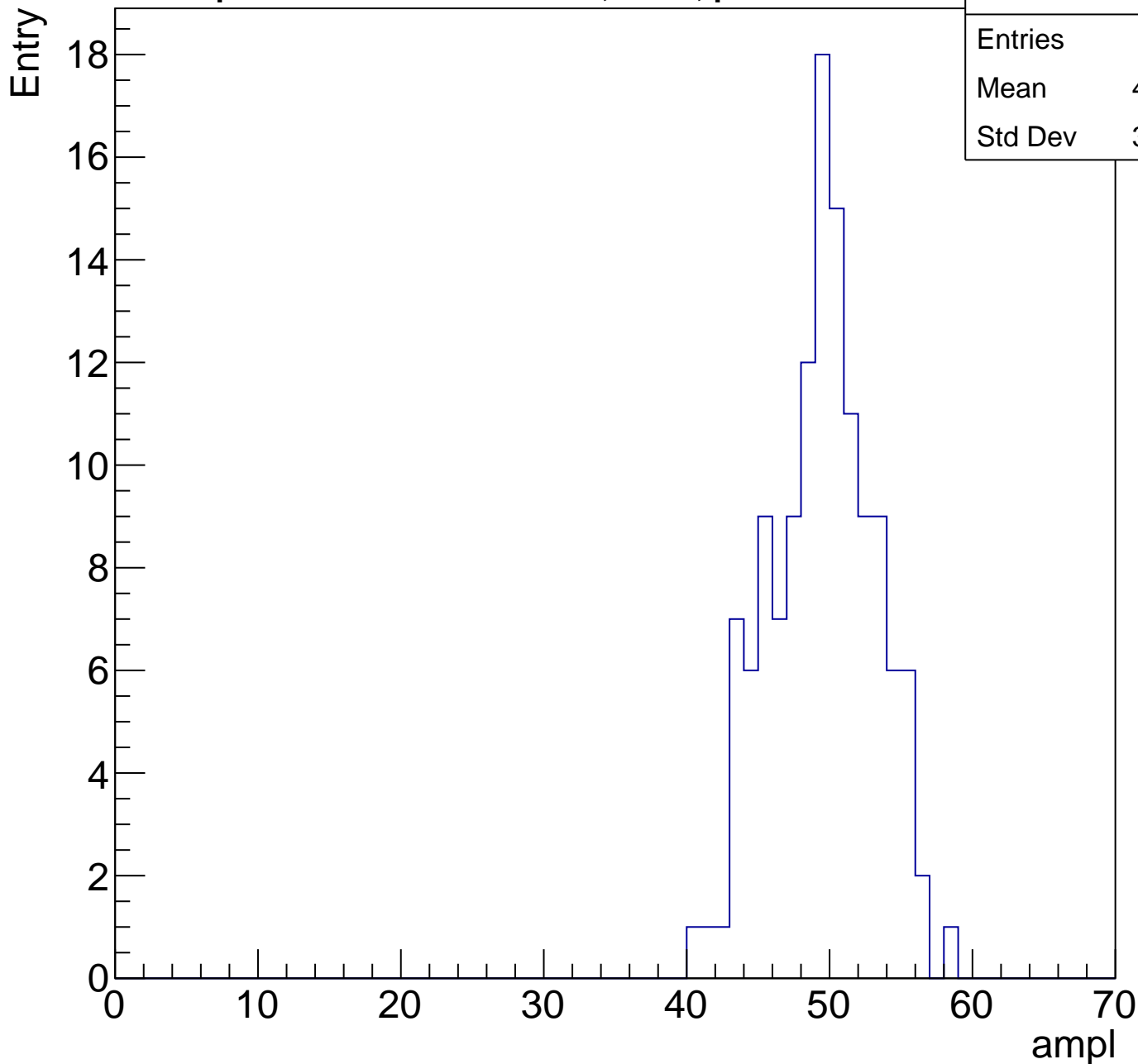
Gaus Width: 3.6696



B1L001S, U21-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	49.05
Std Dev	3.595



B1L001S, U21-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	55.36
Std Dev	3.152

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B1L001S, U21-ch103, adc5

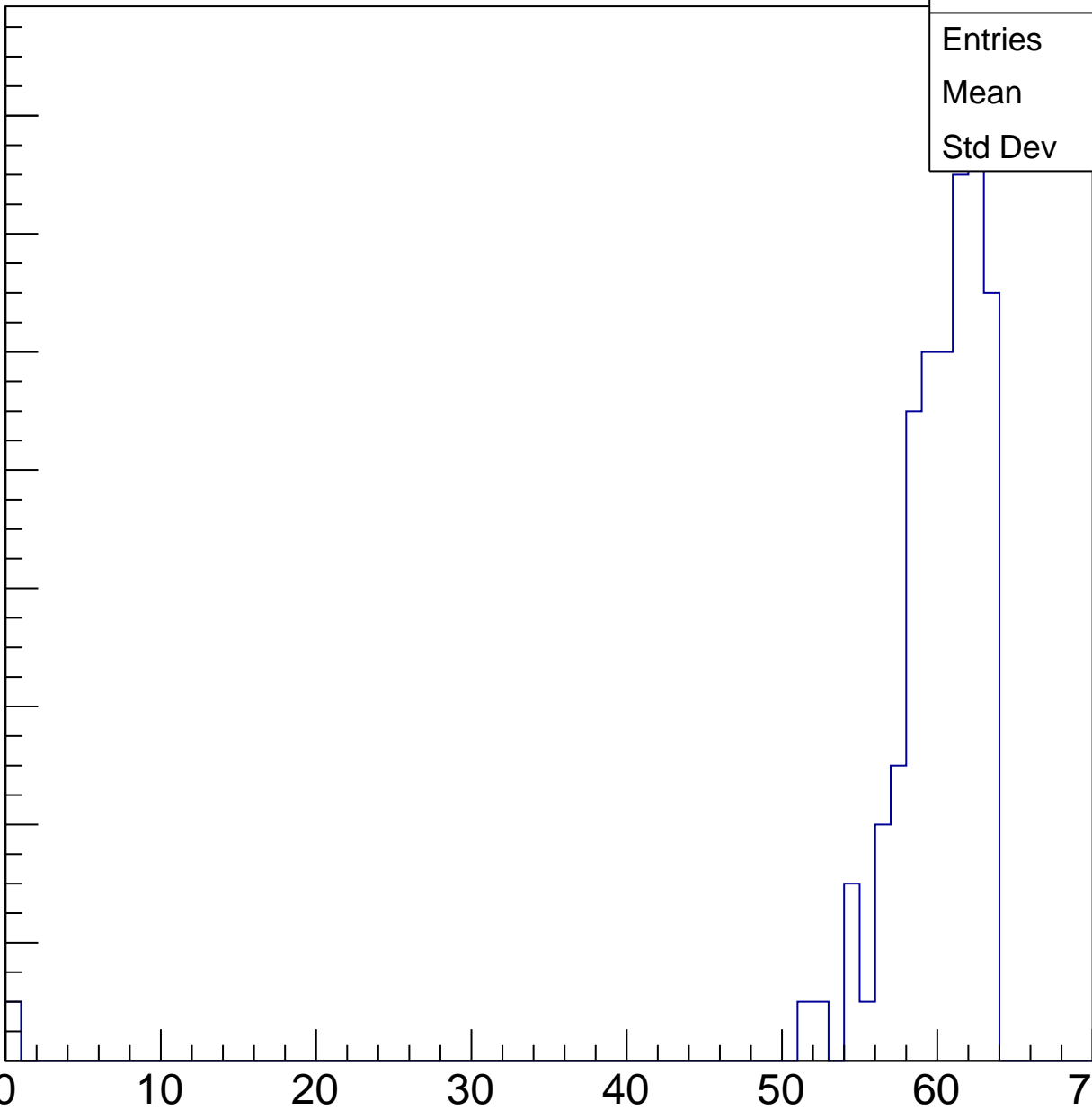
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.2
Std Dev	6.6

ampl

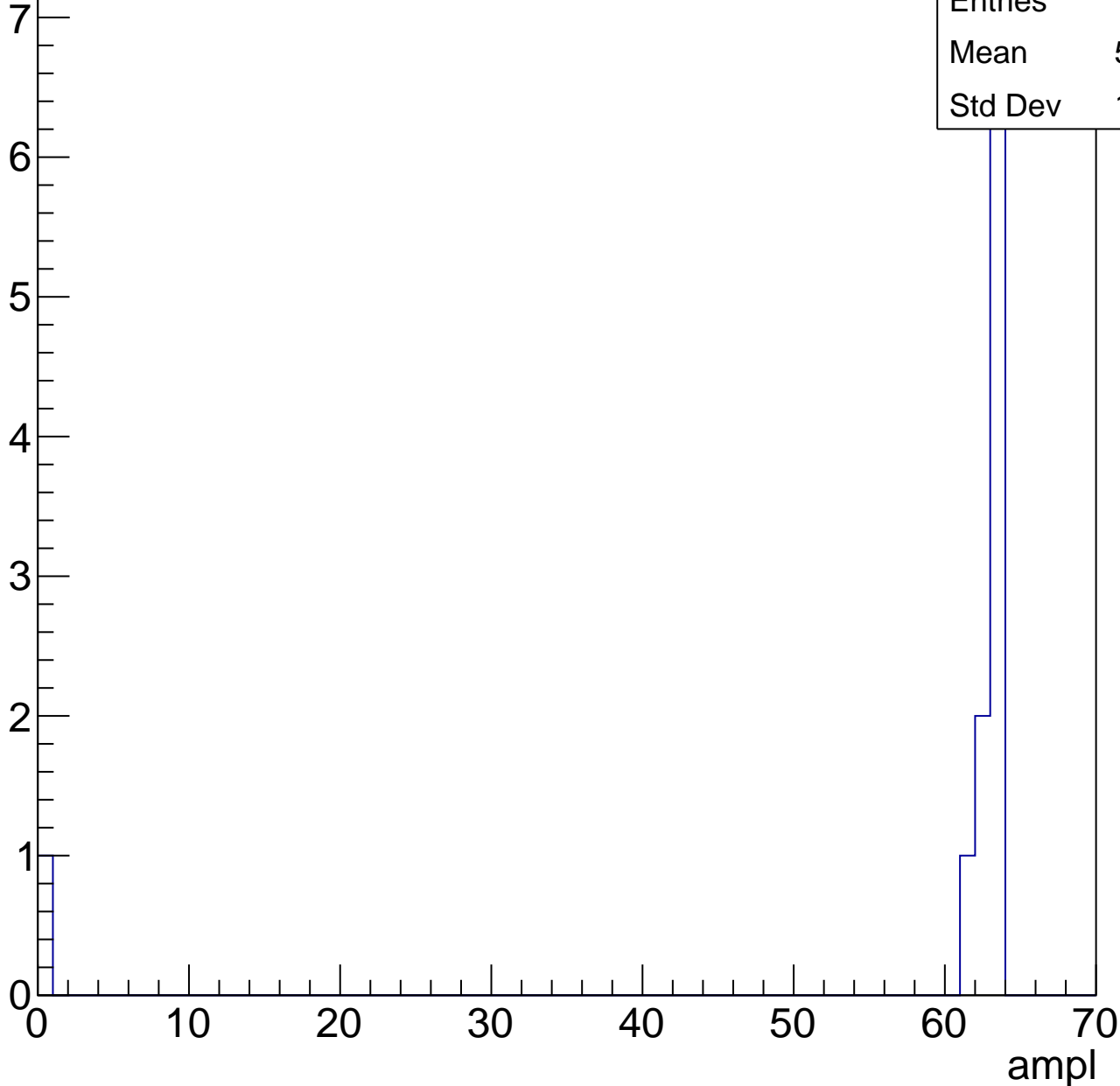


B1L001S, U21-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.91
Std Dev	18.01



B1L001S, U21-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U21-ch104, adc0

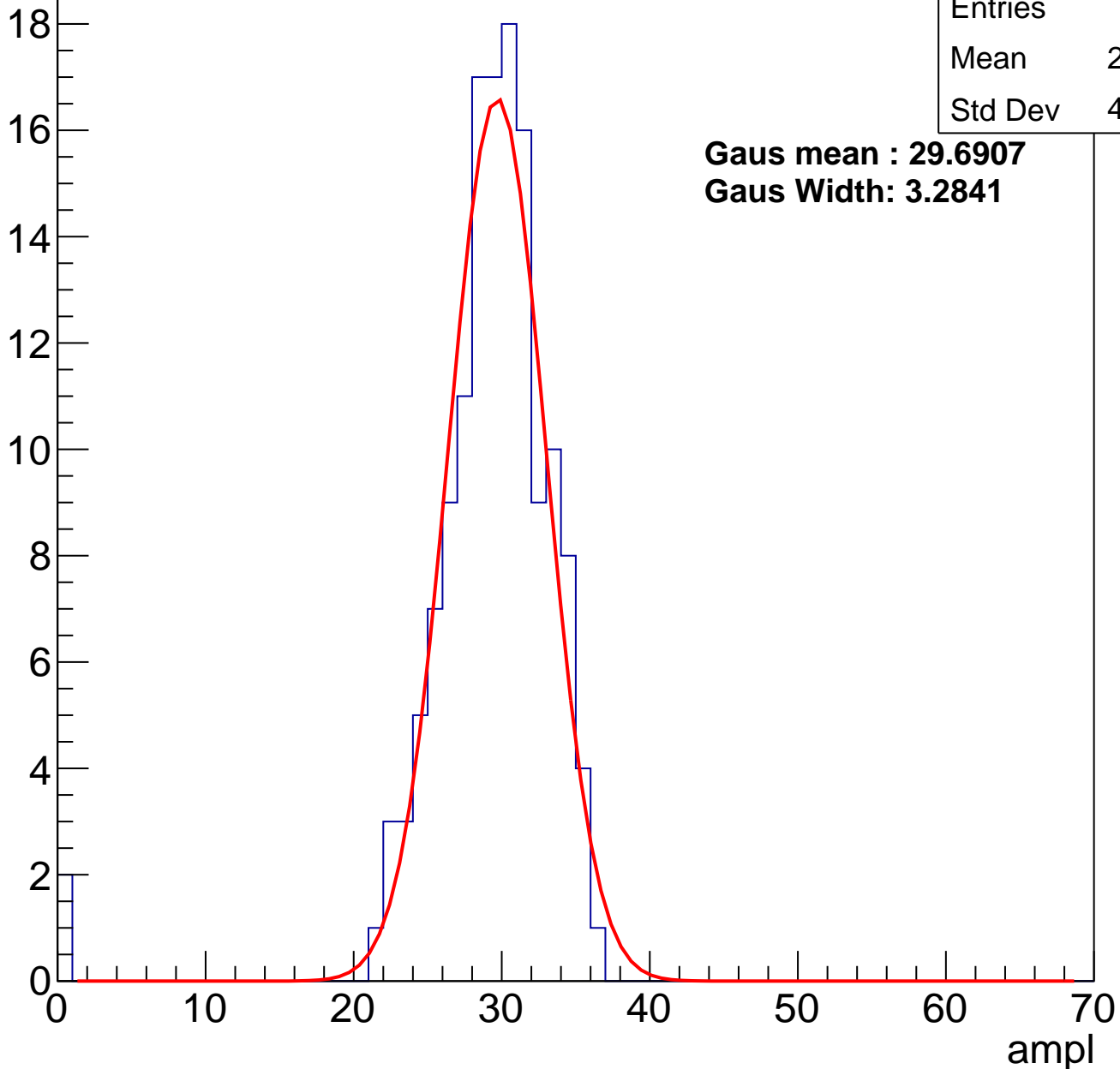
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	28.74
Std Dev	4.679

Gaus mean : 29.6907

Gaus Width: 3.2841

Entry



B1L001S, U21-ch104, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	35.98
Std Dev	3.161

Gaus mean : 36.4524

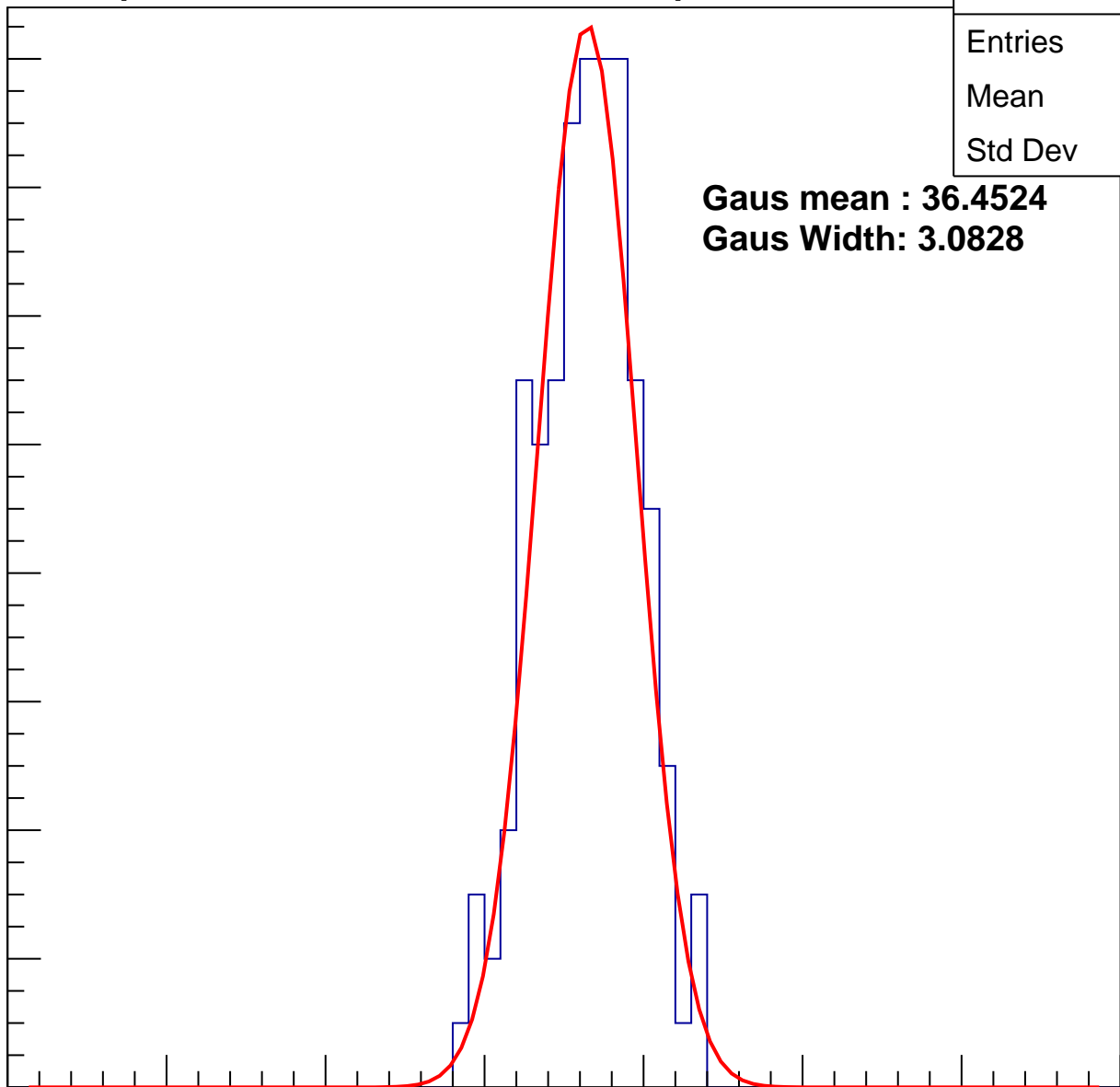
Gaus Width: 3.0828

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U21-ch104, adc2

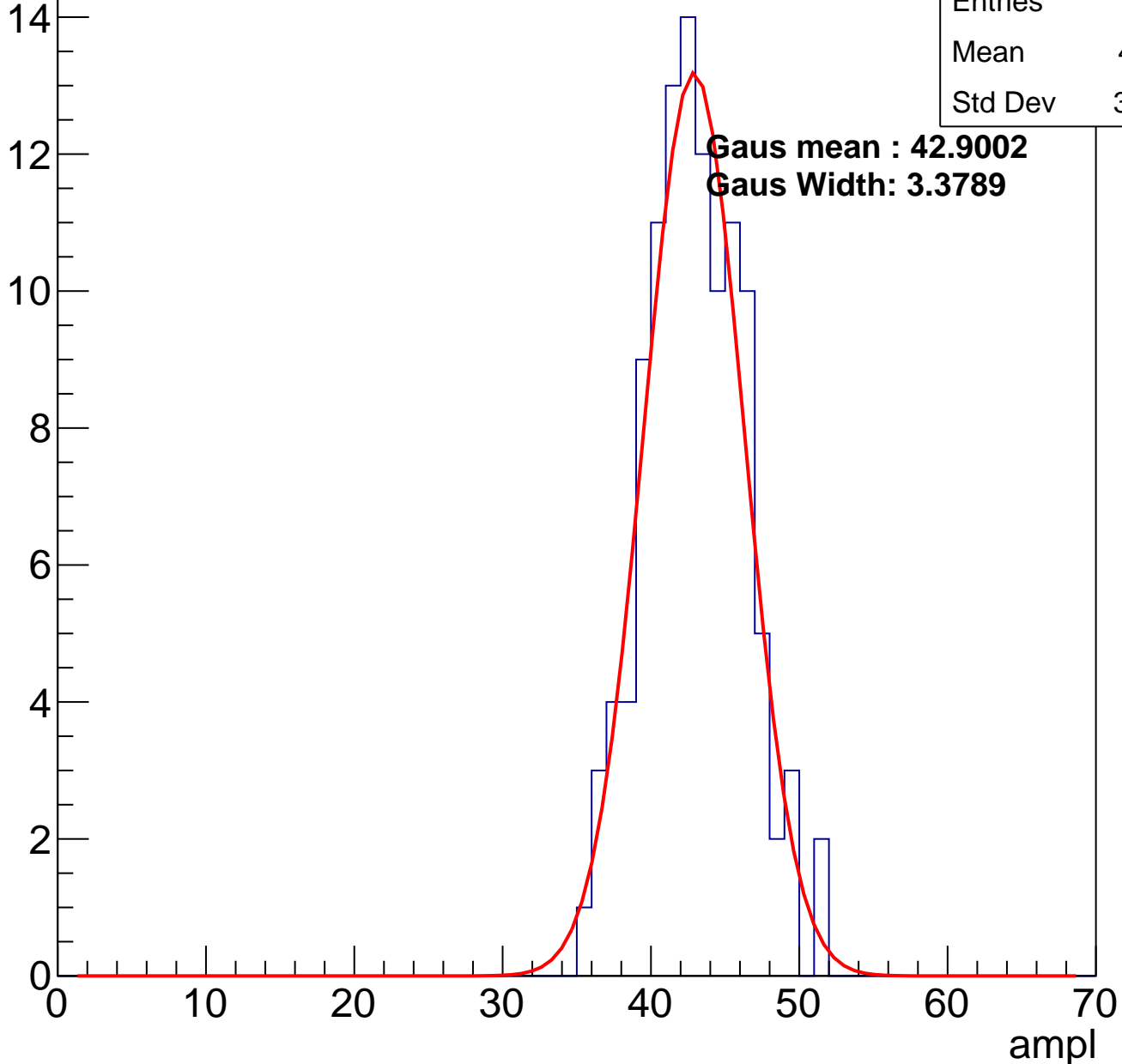
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	42.51
Std Dev	3.307

Entry

Gaus mean : 42.9002

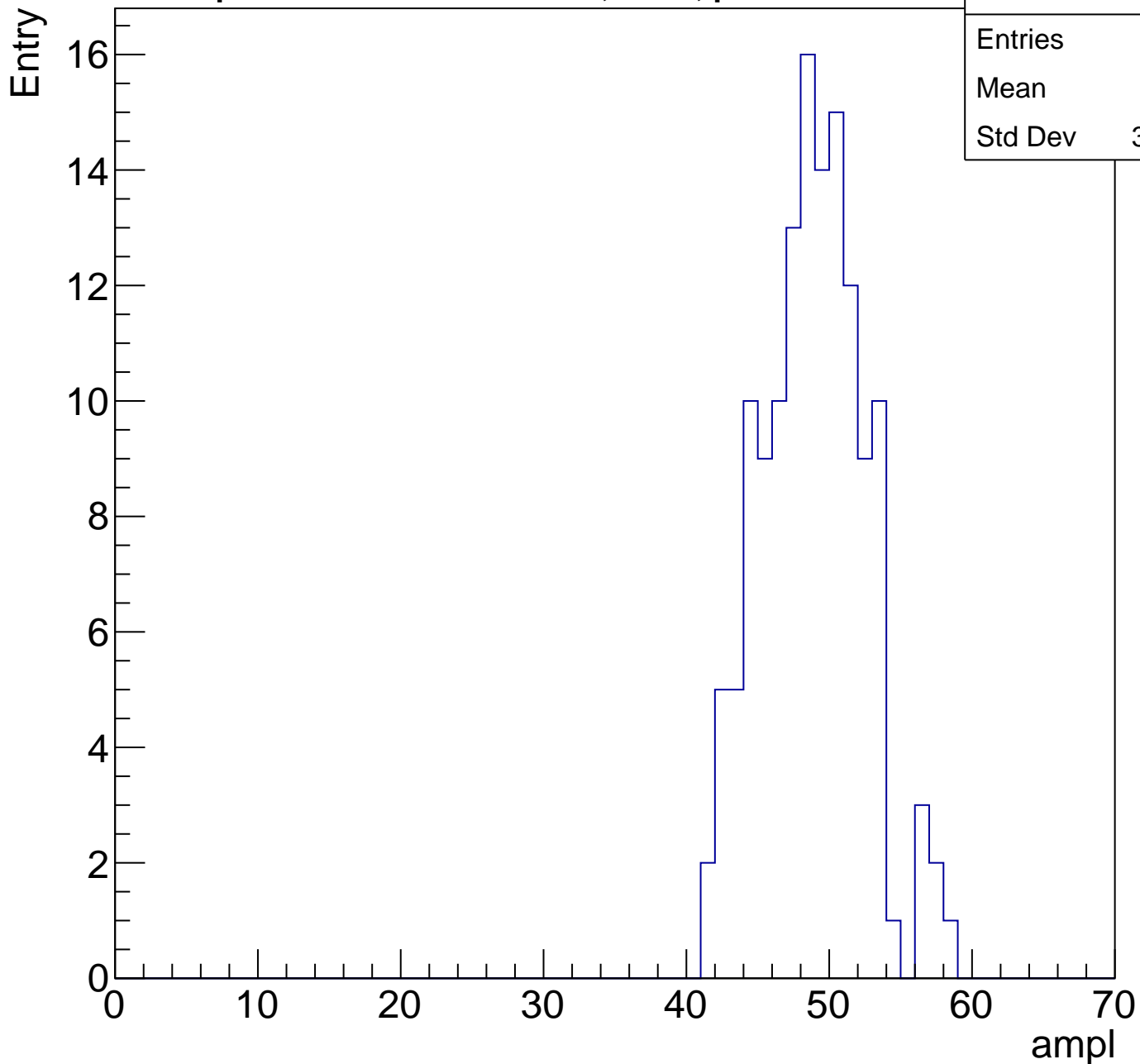
Gaus Width: 3.3789



B1L001S, U21-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	48.4
Std Dev	3.565



B1L001S, U21-ch104, adc4

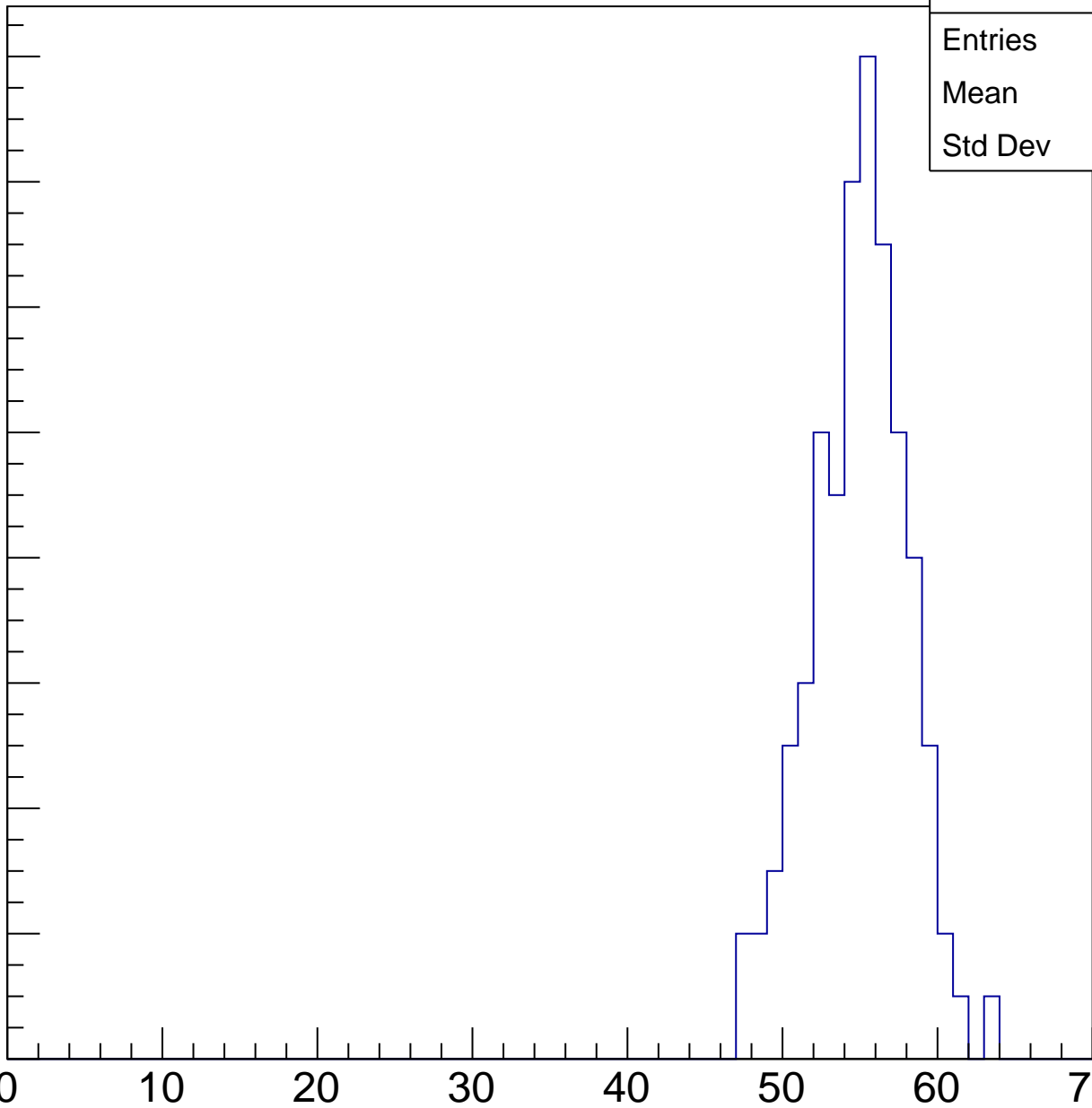
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	54.46
Std Dev	3.085

ampl



B1L001S, U21-ch104, adc5

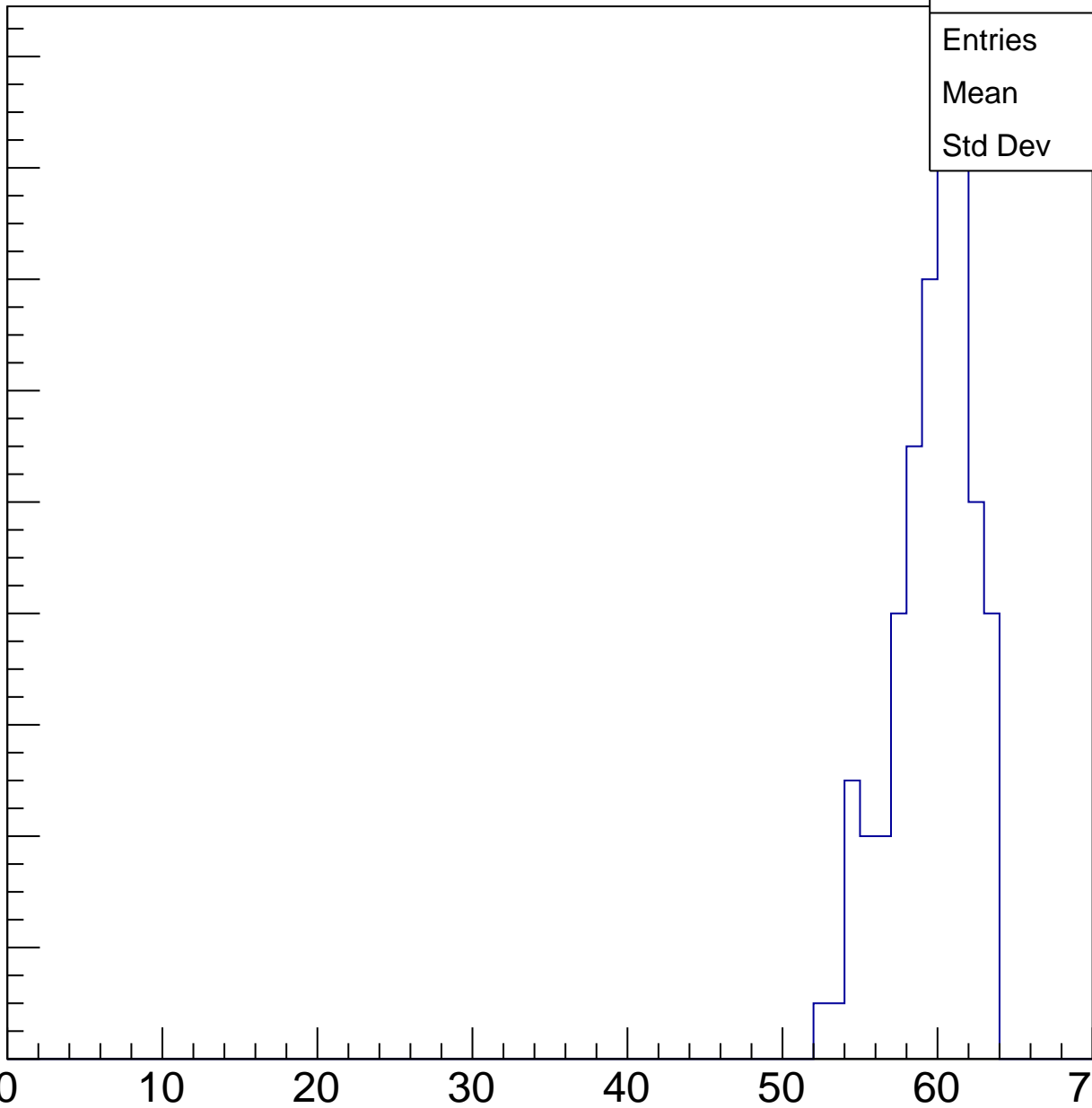
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.21
Std Dev	2.558

ampl



B1L001S, U21-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

27

Mean

57.52

Std Dev

16.3

2

0

2

4

6

8

10

B1L001S, U21-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch105, adc0

calib_packv5_042523_0143.root, FC#2, port C2

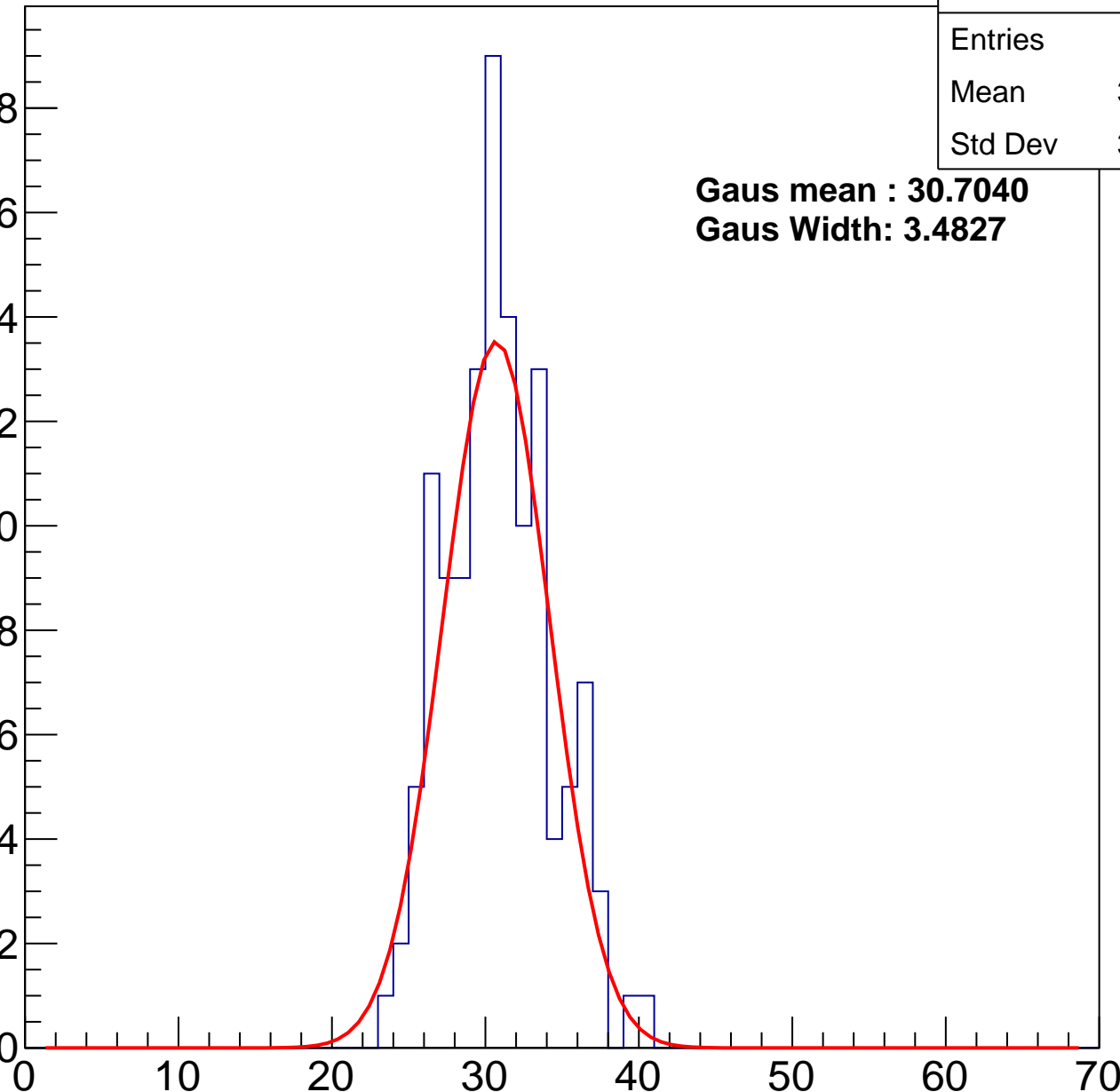
Entry

18
16
14
12
10
8
6
4
2
0

Entries	127
Mean	30.39
Std Dev	3.405

Gaus mean : 30.7040

Gaus Width: 3.4827



ampl

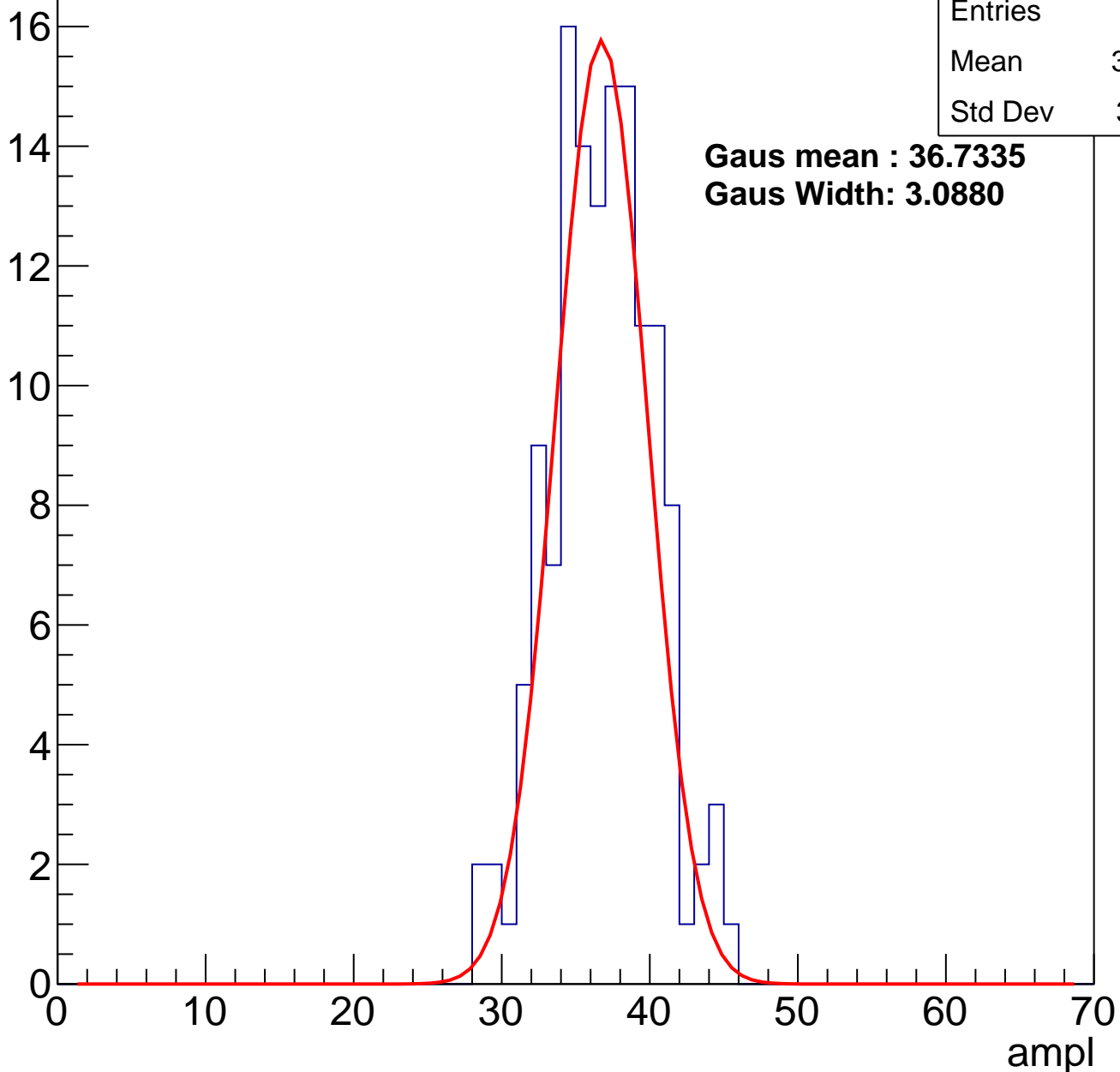
B1L001S, U21-ch105, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.38
Std Dev	3.441

Gaus mean : 36.7335
Gaus Width: 3.0880

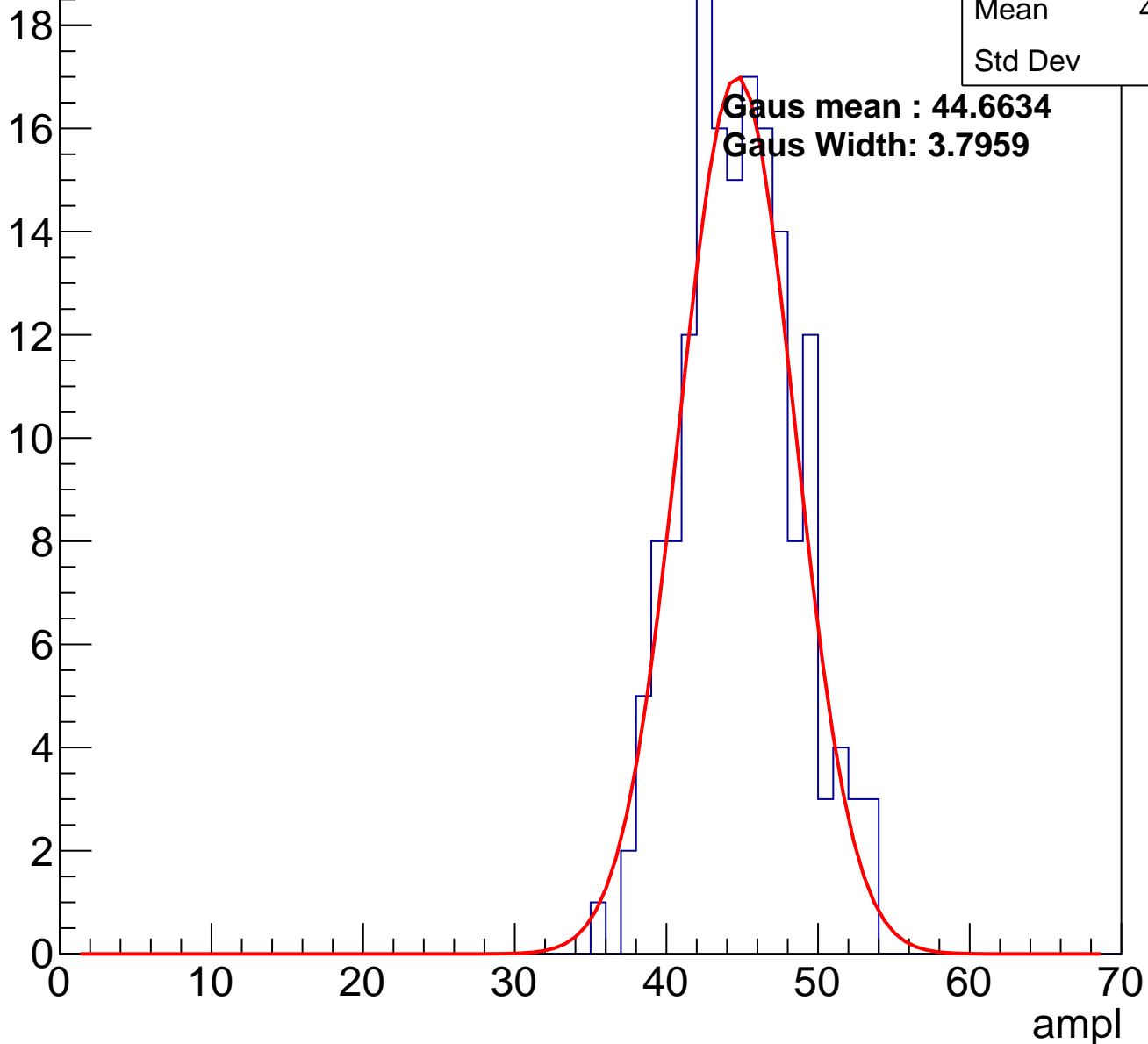
Entry



B1L001S, U21-ch105, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	166
Mean	44.39
Std Dev	3.67

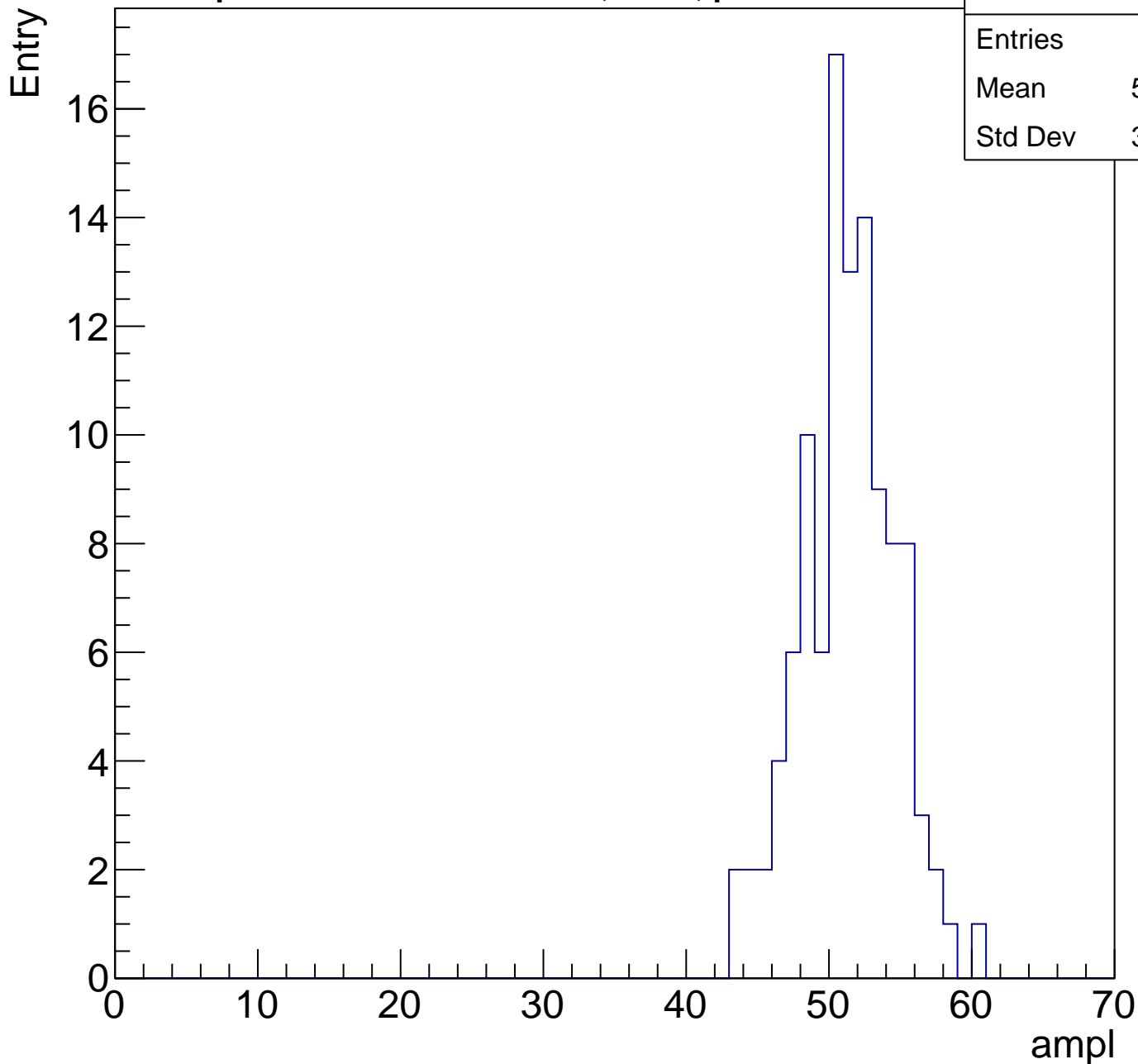
Gaus mean : 44.6634

Gaus Width: 3.7959

B1L001S, U21-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	50.87
Std Dev	3.263



B1L001S, U21-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	56.03
Std Dev	6.294

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

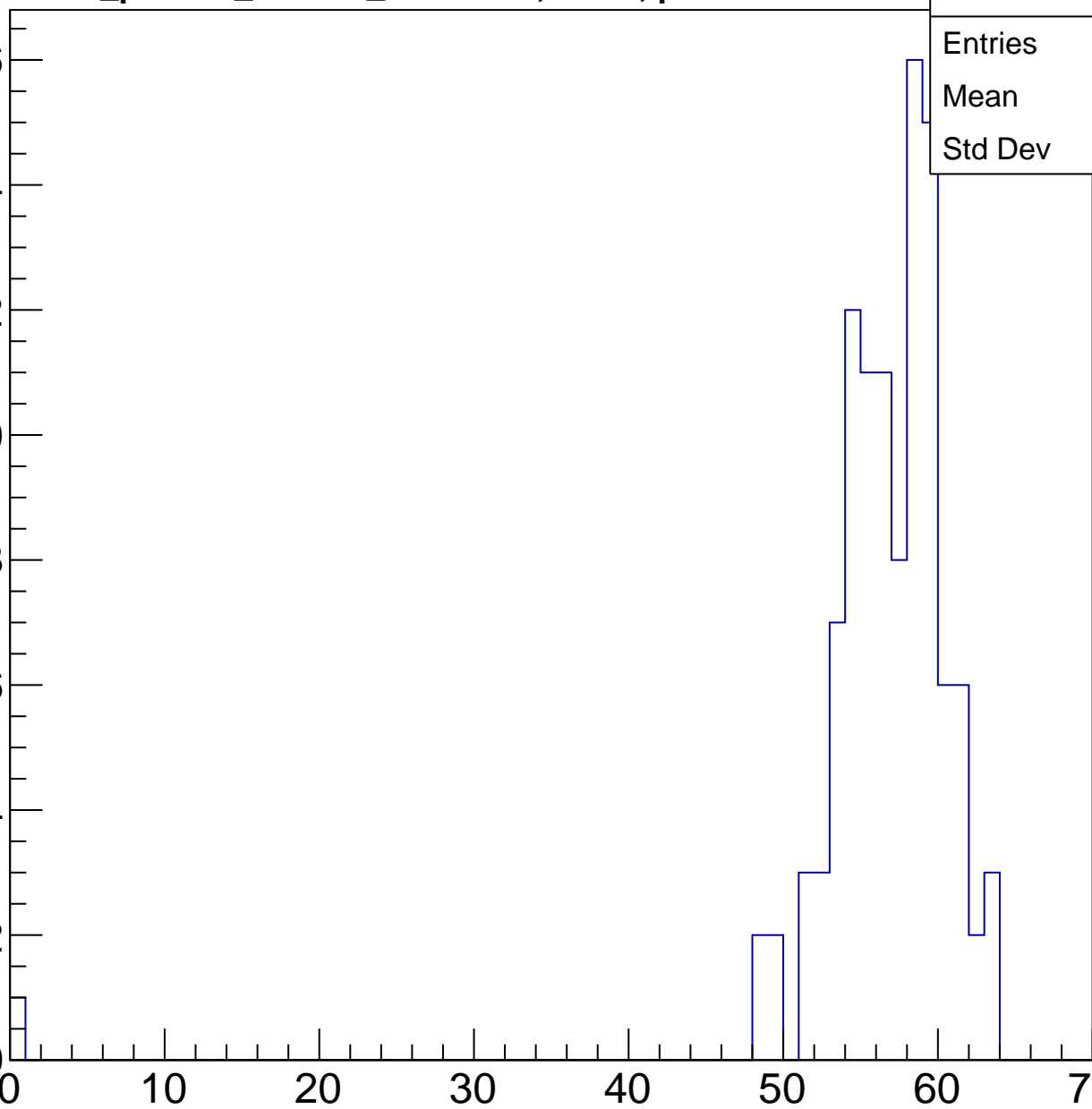
40

50

60

70

ampl



B1L001S, U21-ch105, adc5

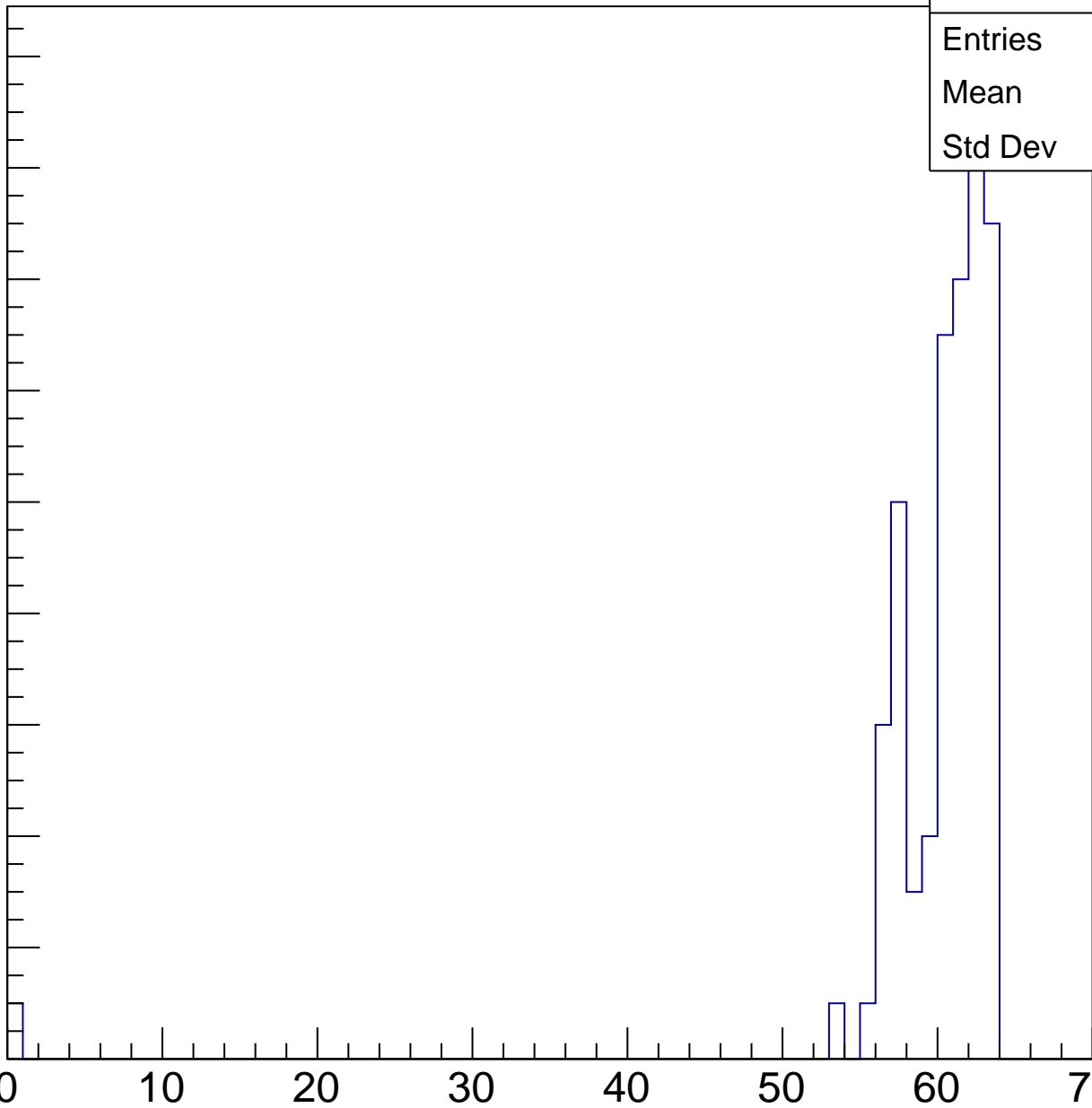
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.52
Std Dev	6.886

ampl



B1L001S, U21-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

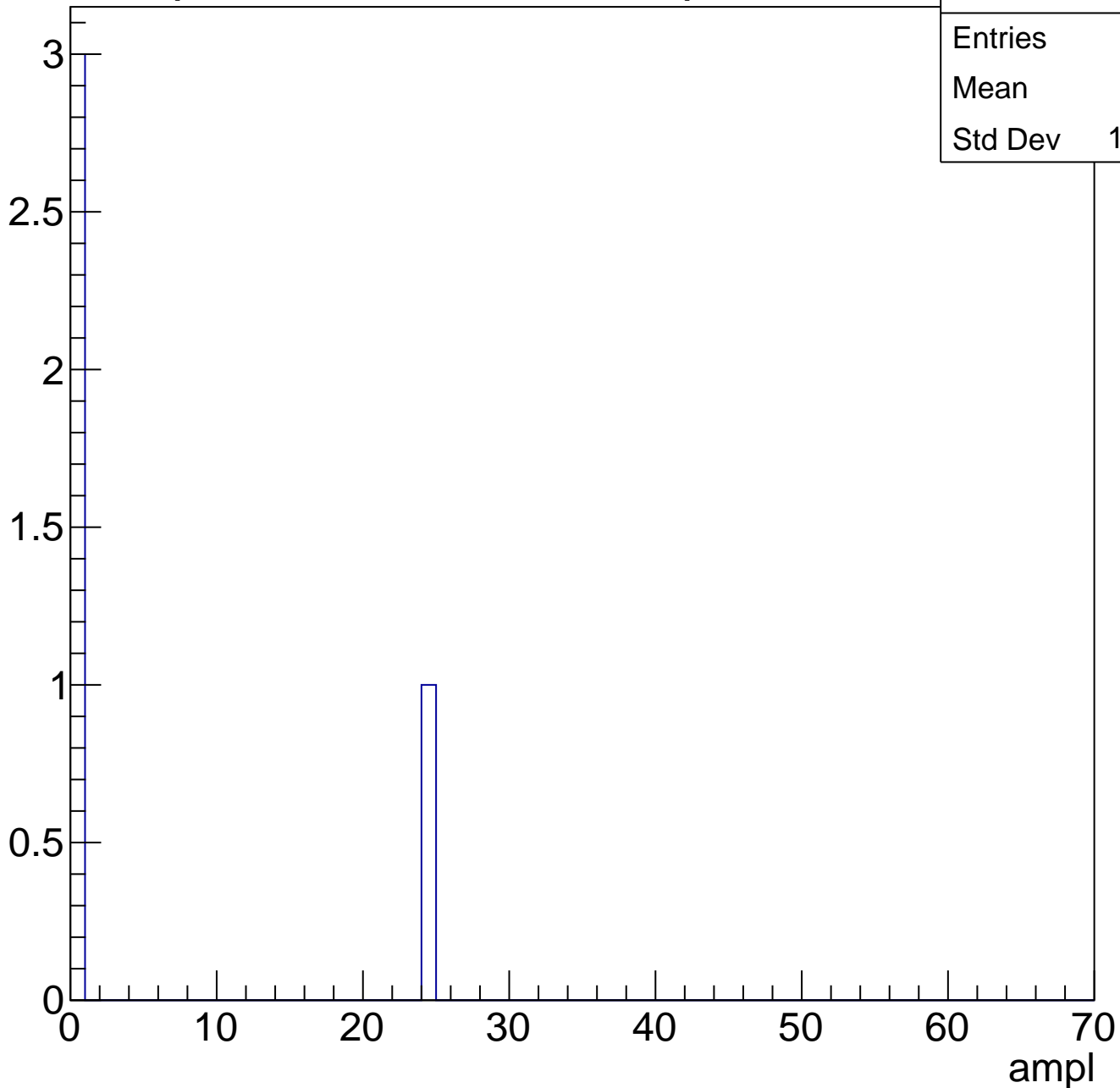
Entry



B1L001S, U21-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch106, adc0

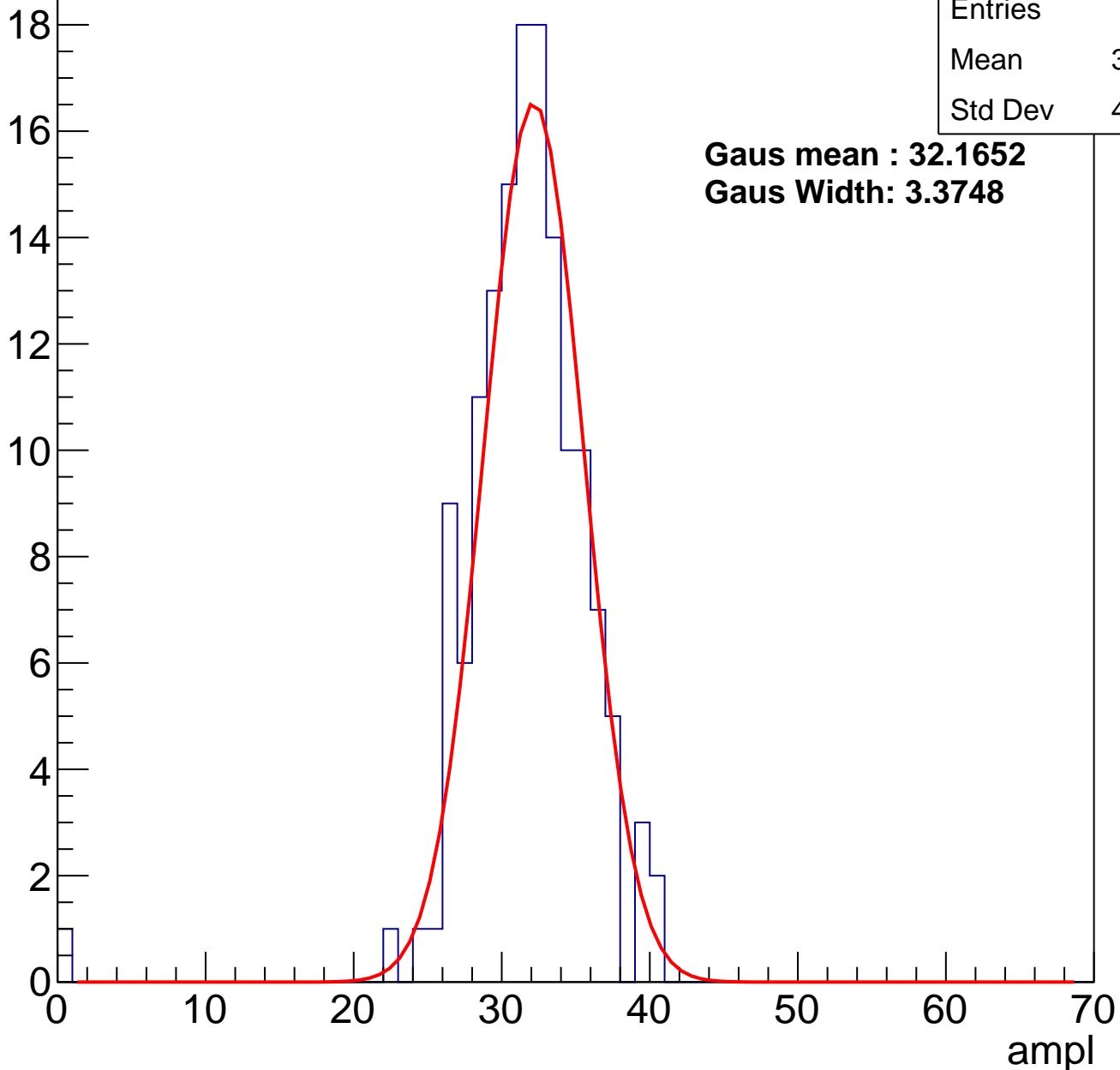
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	31.19
Std Dev	4.269

Gaus mean : 32.1652

Gaus Width: 3.3748

Entry



B1L001S, U21-ch106, adc1

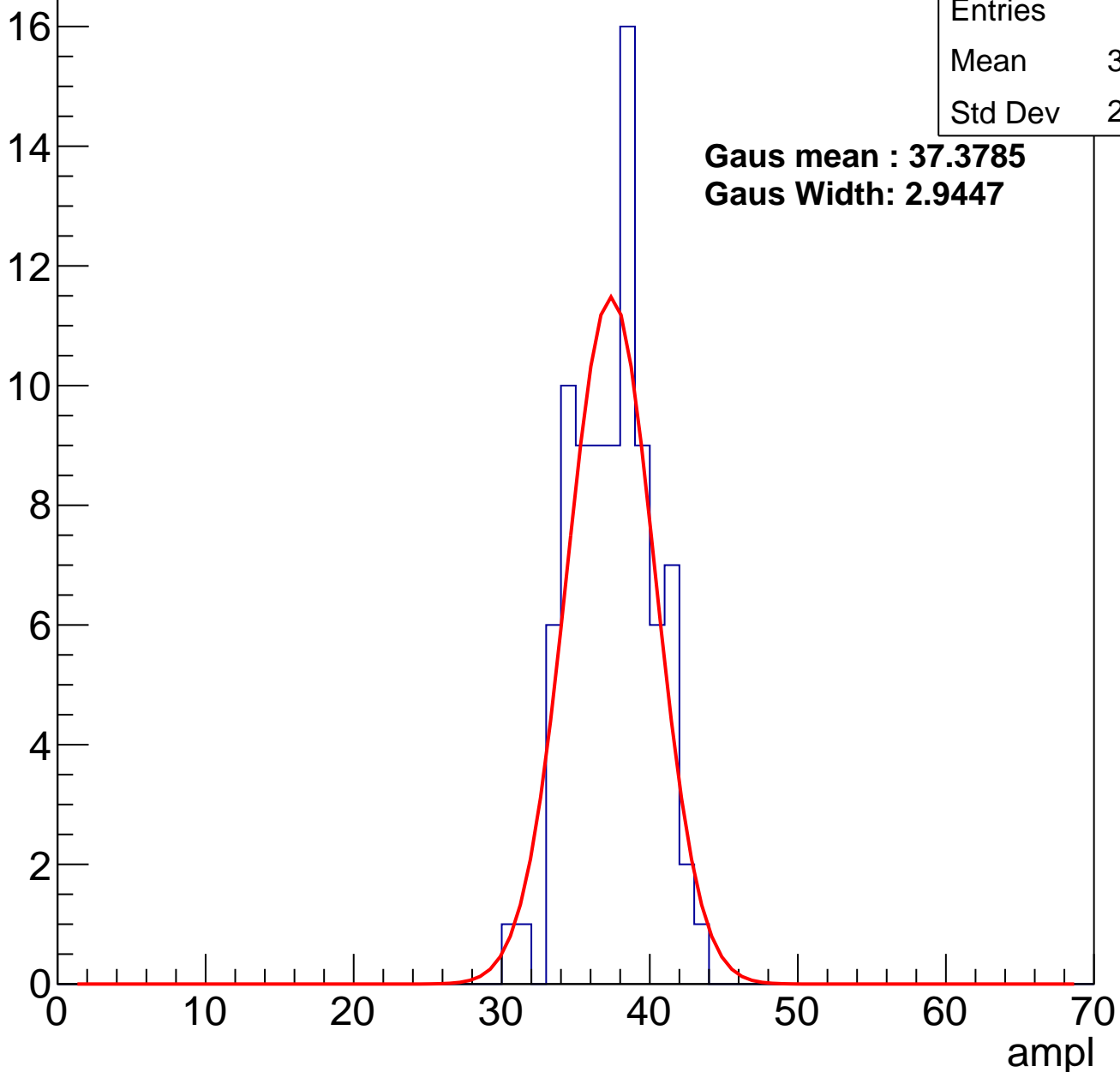
calib_packv5_042523_0143.root, FC#2, port C2

Entries	86
Mean	37.02
Std Dev	2.685

Gaus mean : 37.3785

Gaus Width: 2.9447

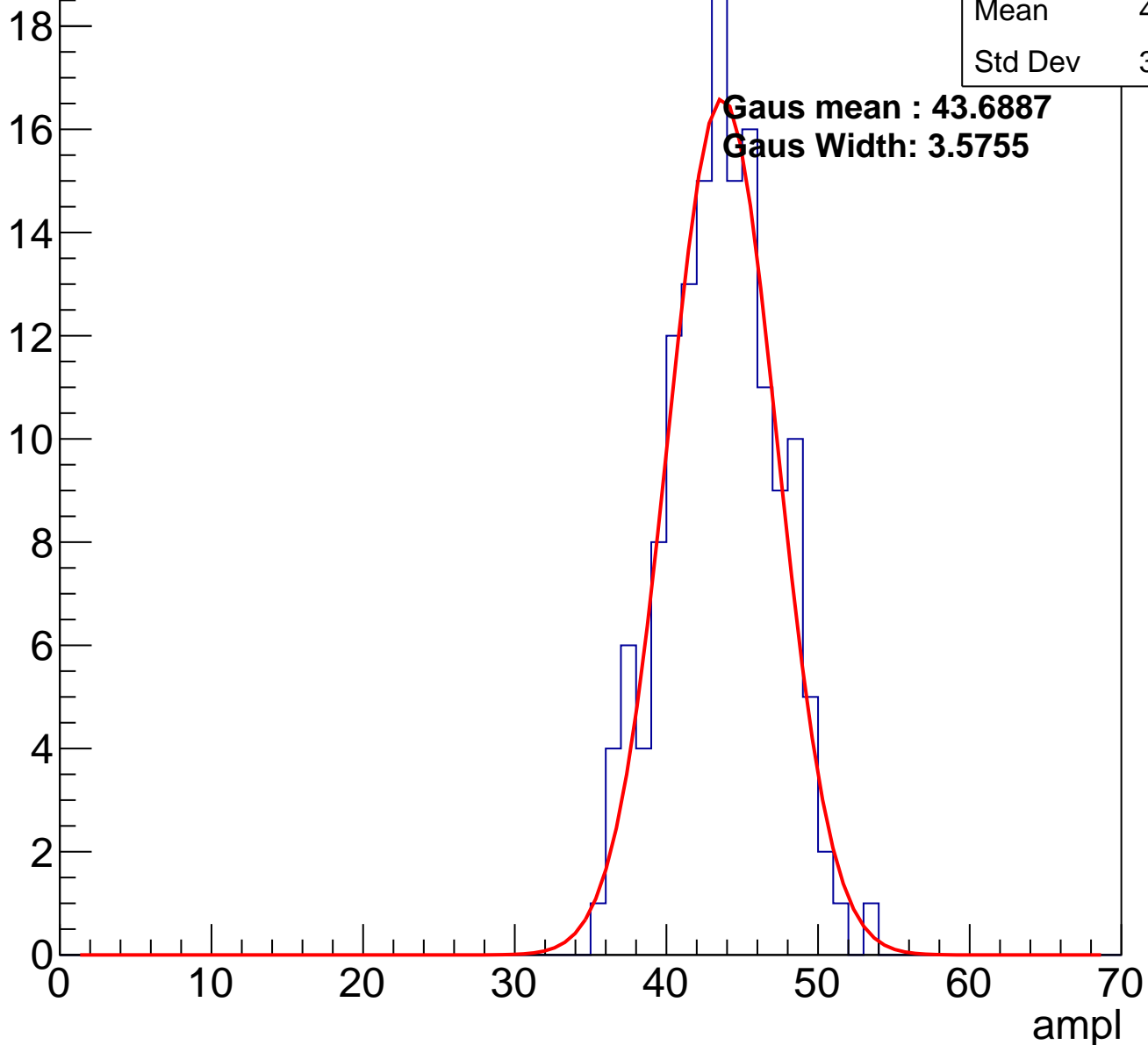
Entry



B1L001S, U21-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

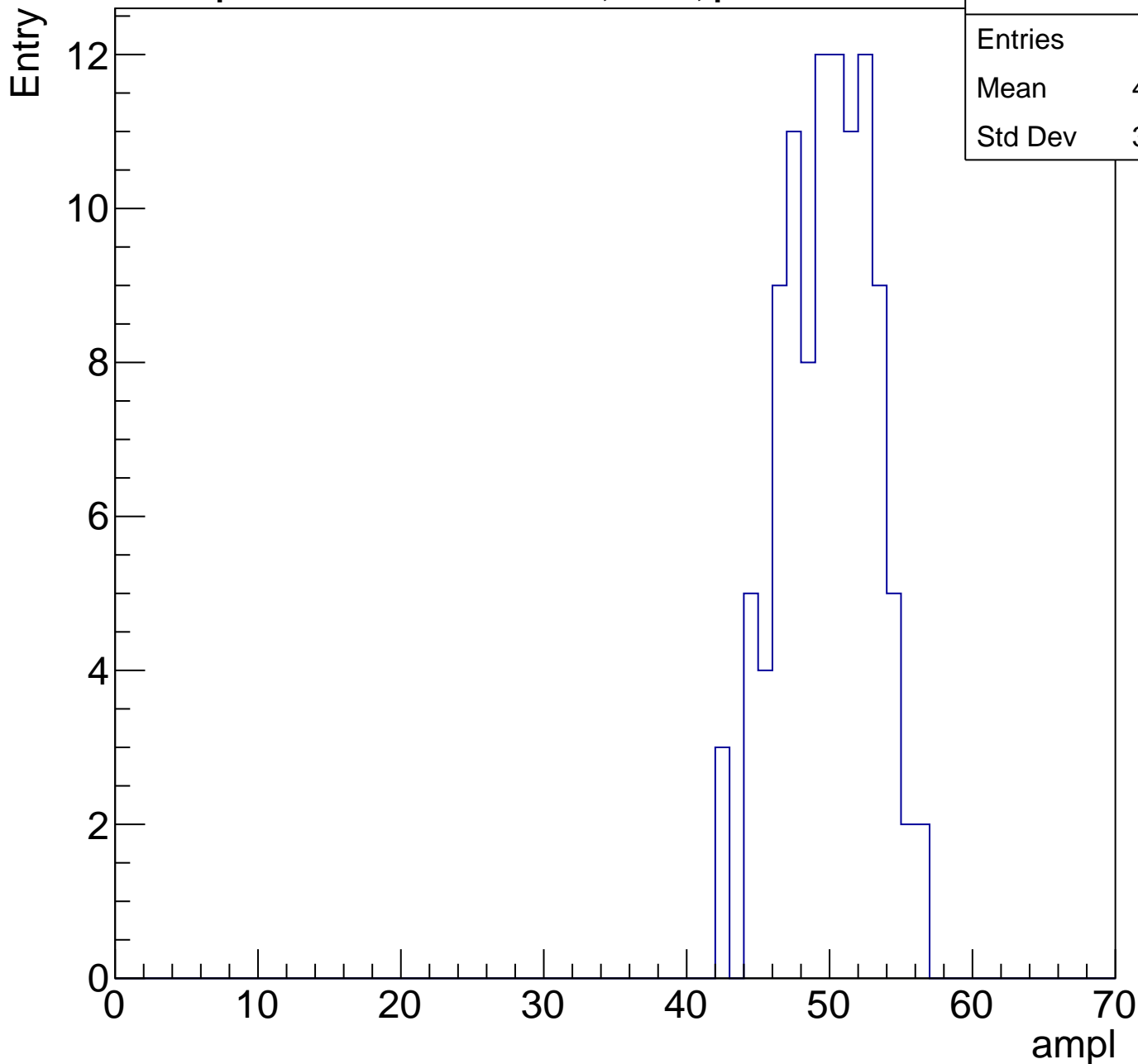
Entry



B1L001S, U21-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

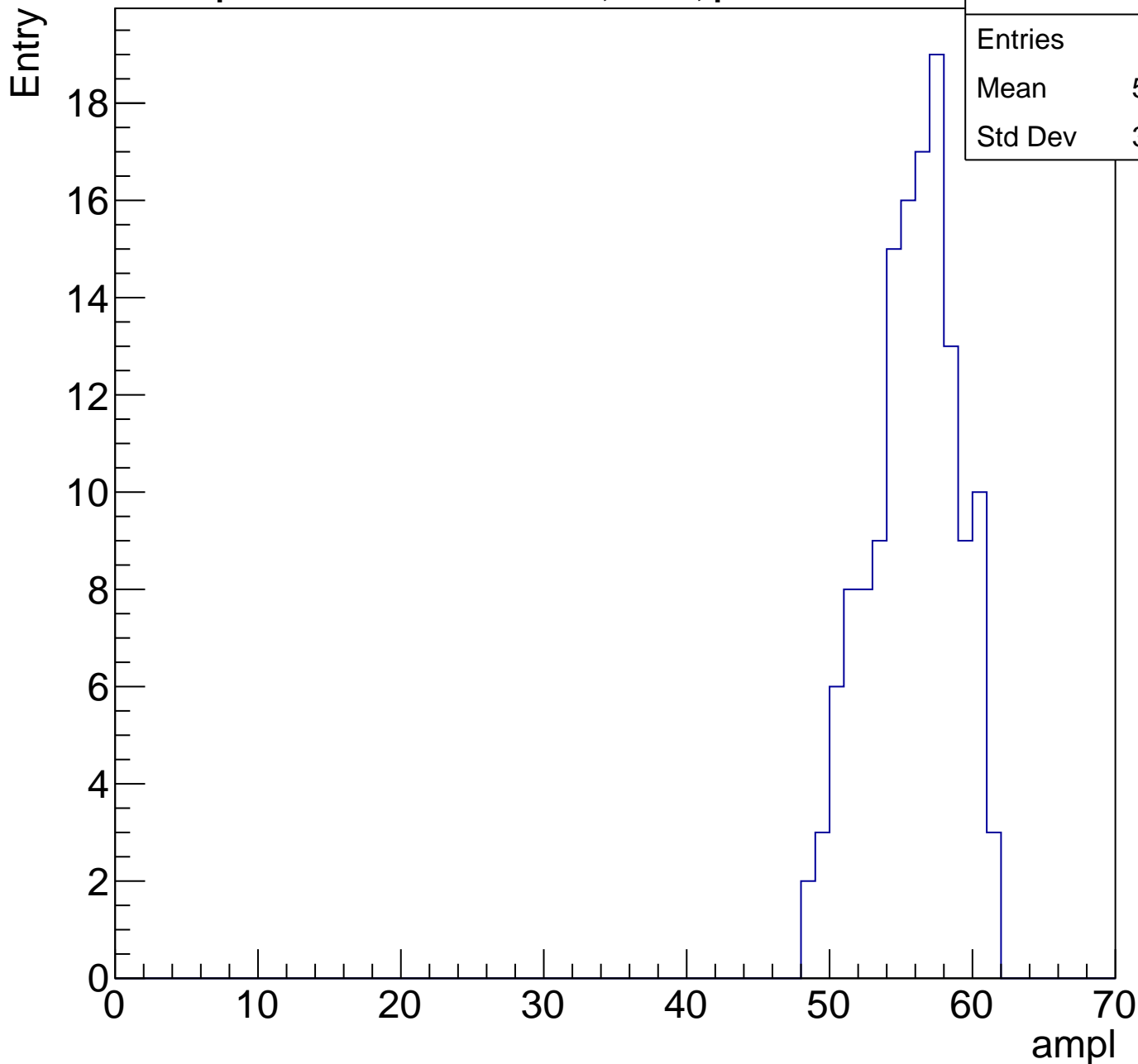
Entries	105
Mean	49.36
Std Dev	3.175



B1L001S, U21-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

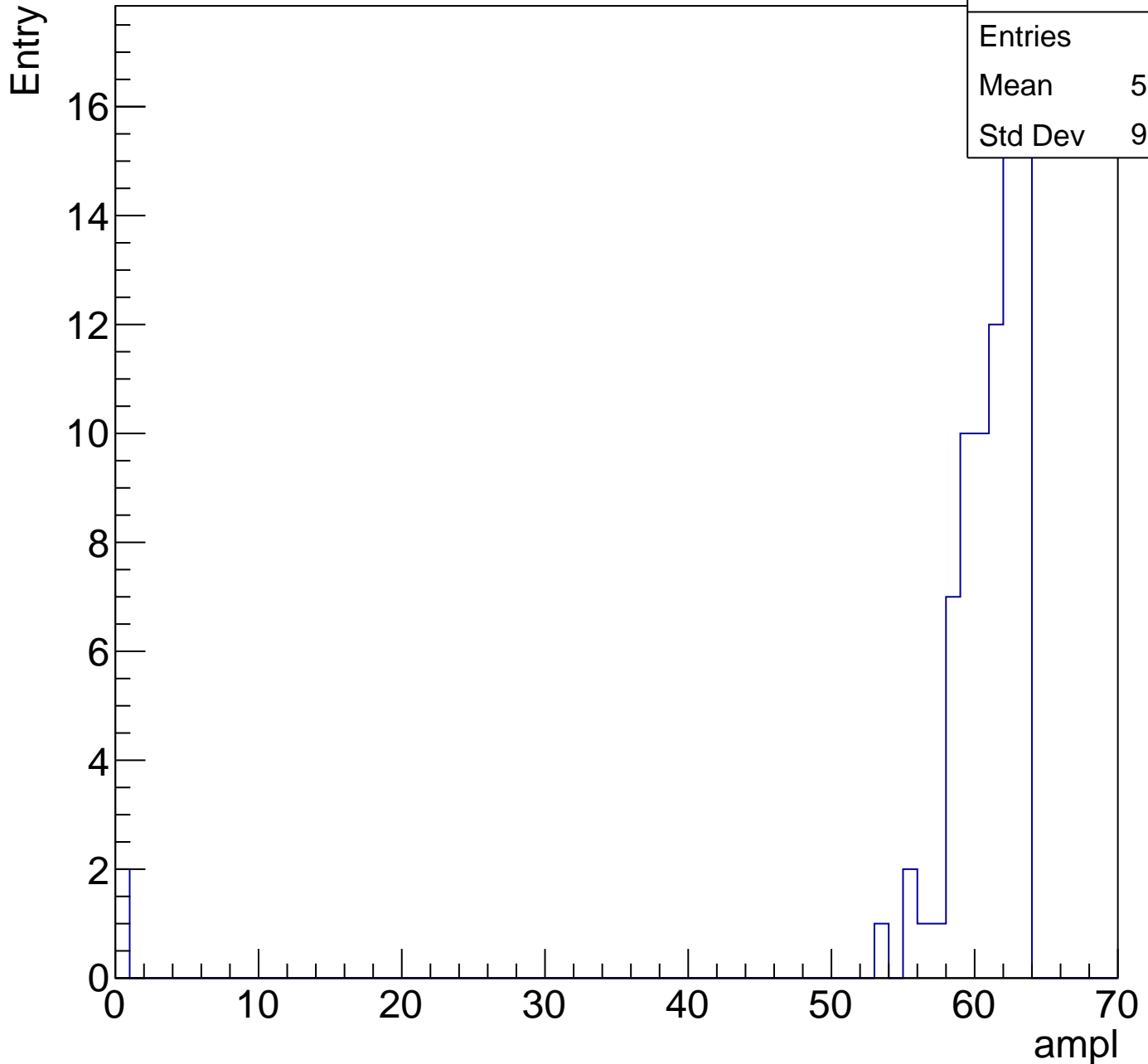
Entries	138
Mean	55.34
Std Dev	3.073



B1L001S, U21-ch106, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	59.06
Std Dev	9.754

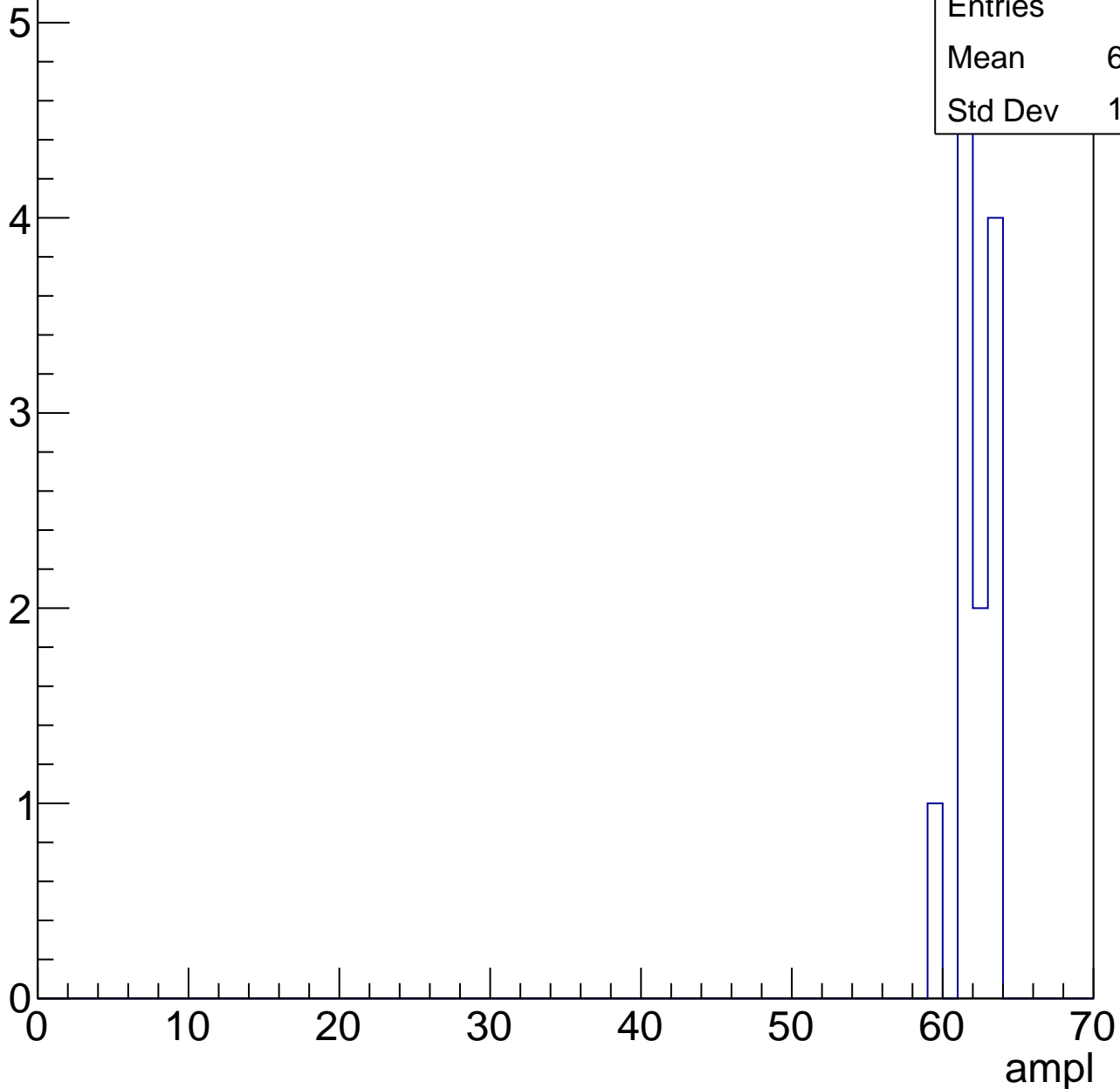


B1L001S, U21-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.67
Std Dev	1.179



B1L001S, U21-ch106, adc7

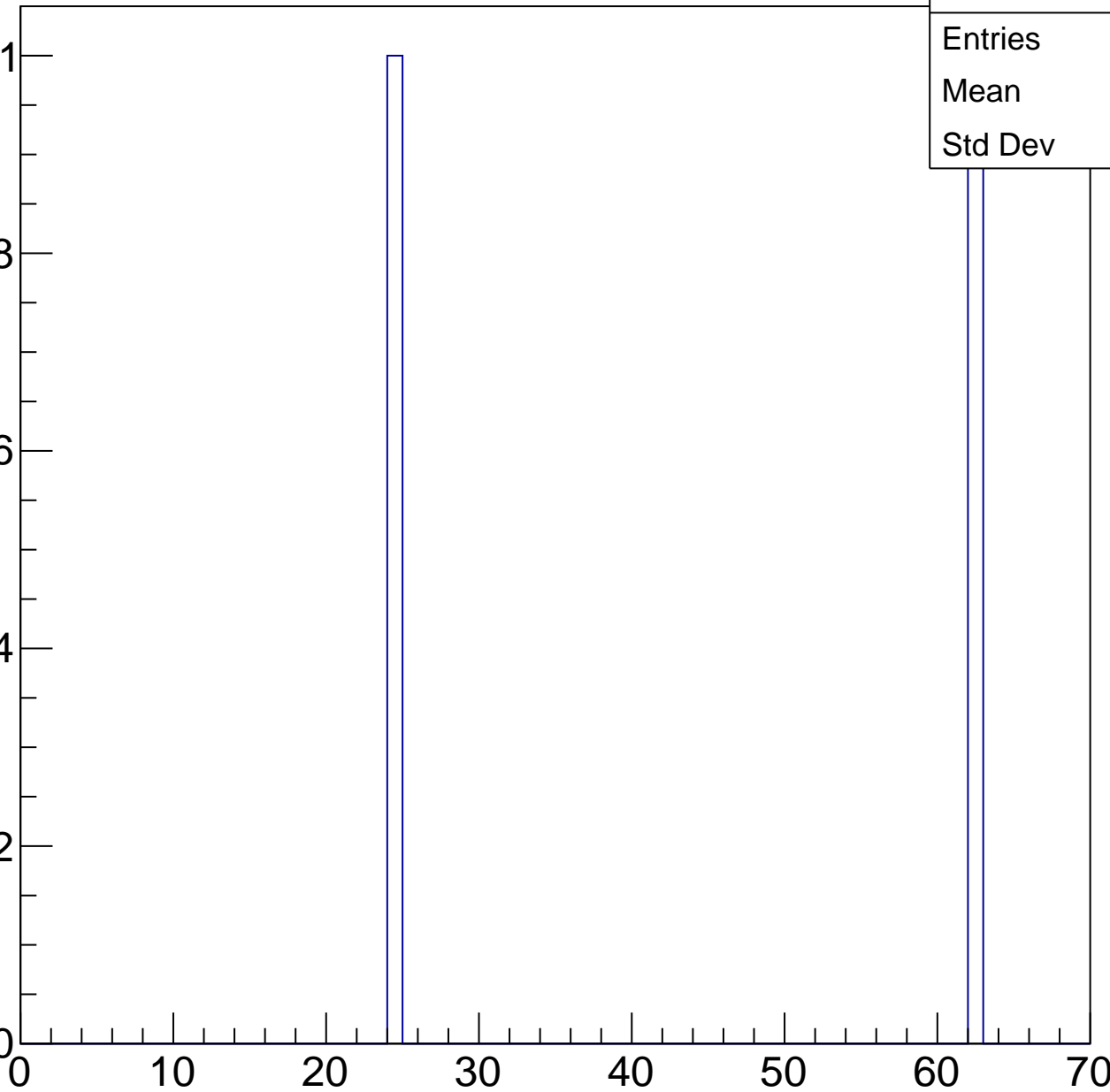
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	19

ampl



B1L001S, U21-ch107, adc0

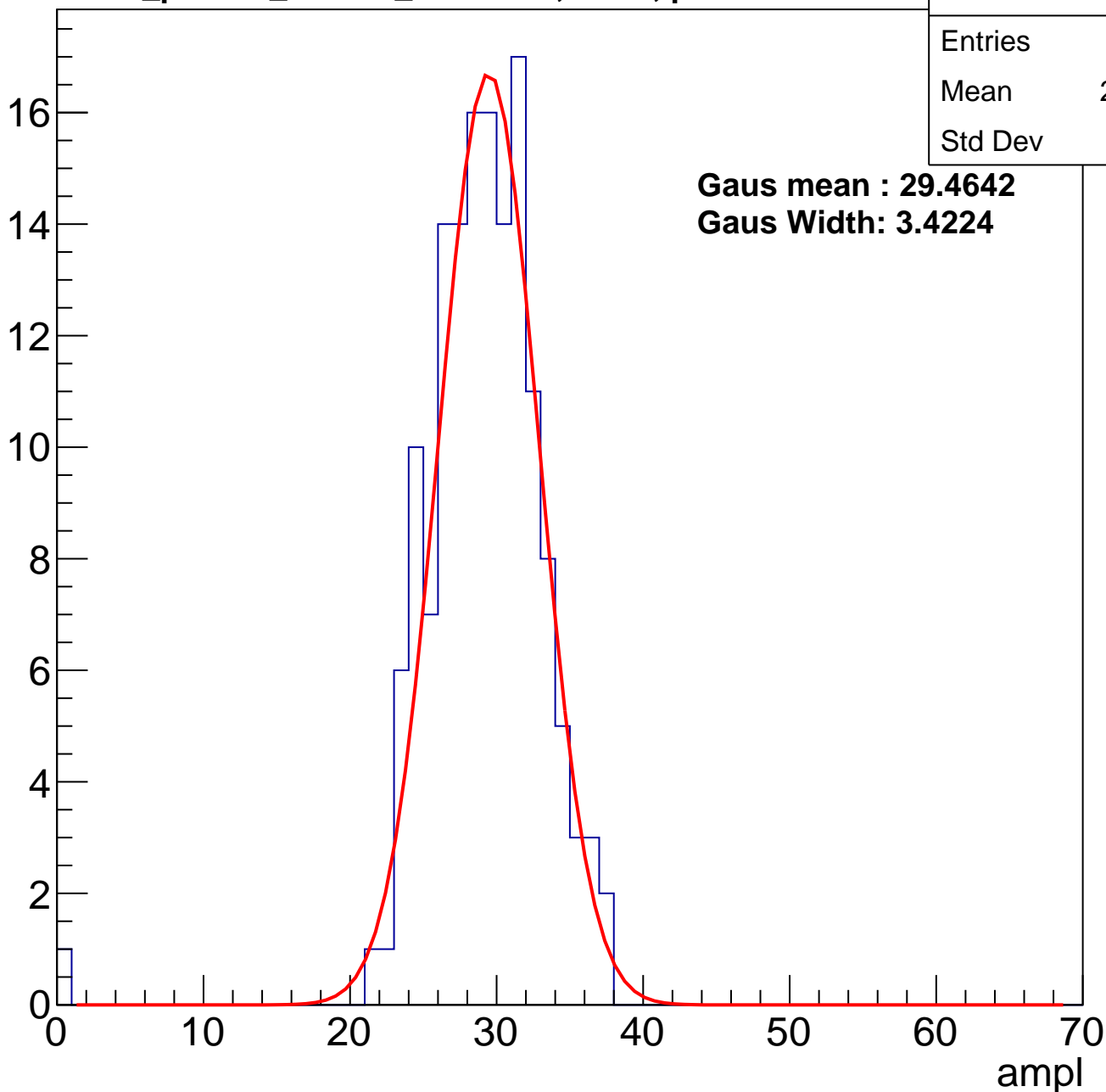
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	28.66
Std Dev	4.12

Gaus mean : 29.4642

Gaus Width: 3.4224

Entry



B1L001S, U21-ch107, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	36.53
Std Dev	3.346

Gaus mean : 36.9706

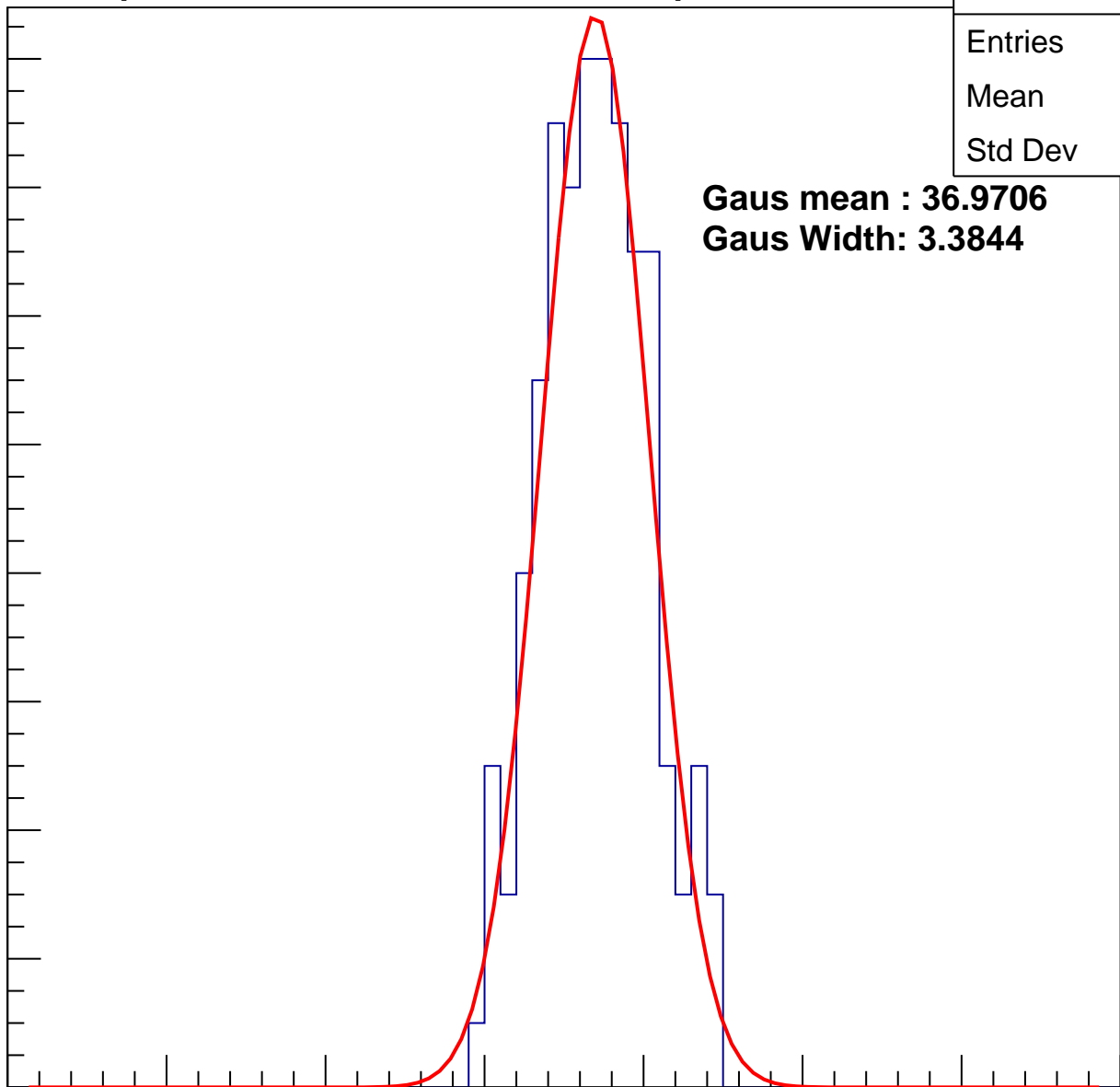
Gaus Width: 3.3844

Entry

16
14
12
10
8
6
4
2
0

ampl

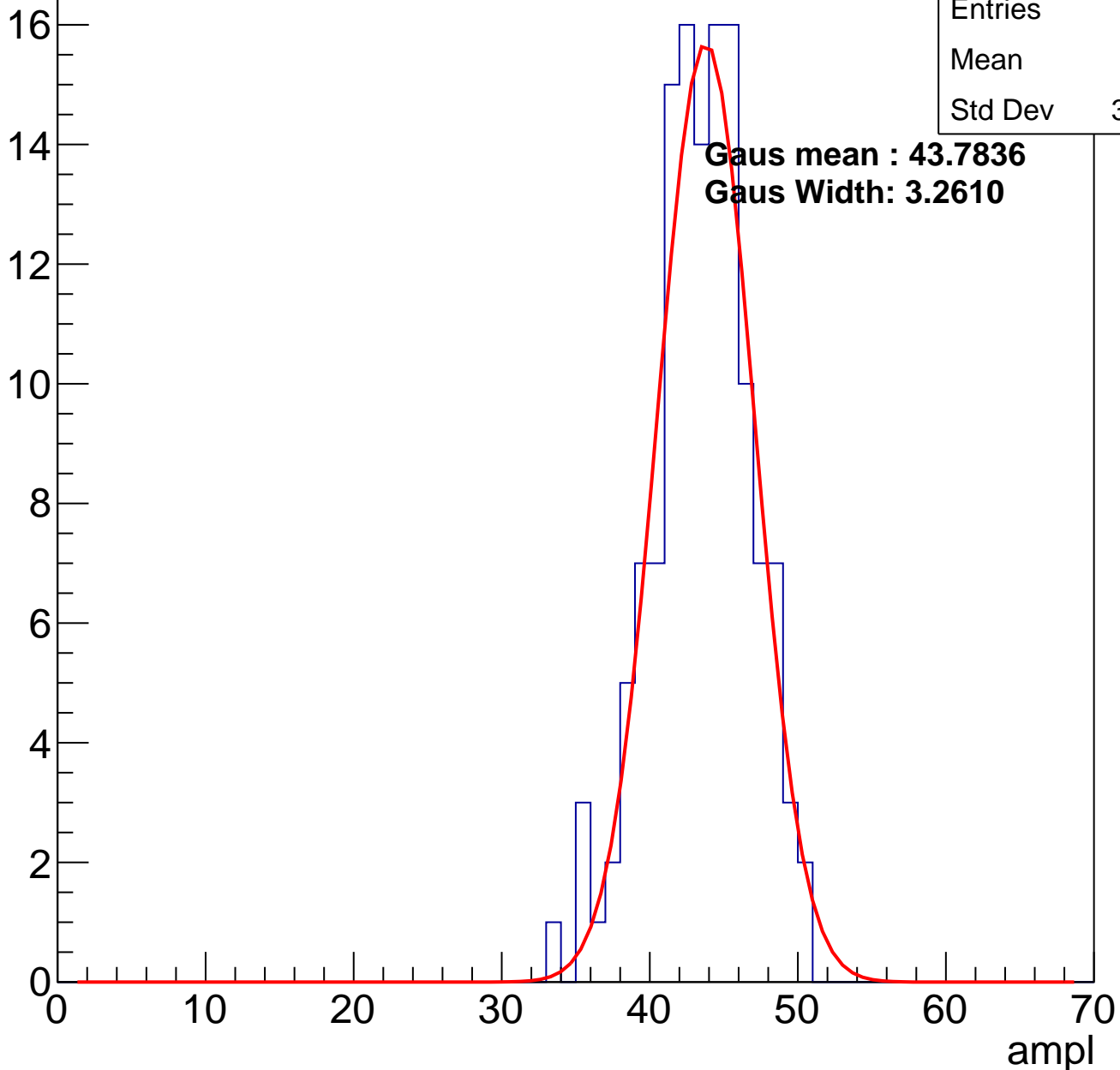
0 10 20 30 40 50 60 70



B1L001S, U21-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	132
Mean	43
Std Dev	3.317

B1L001S, U21-ch107, adc3

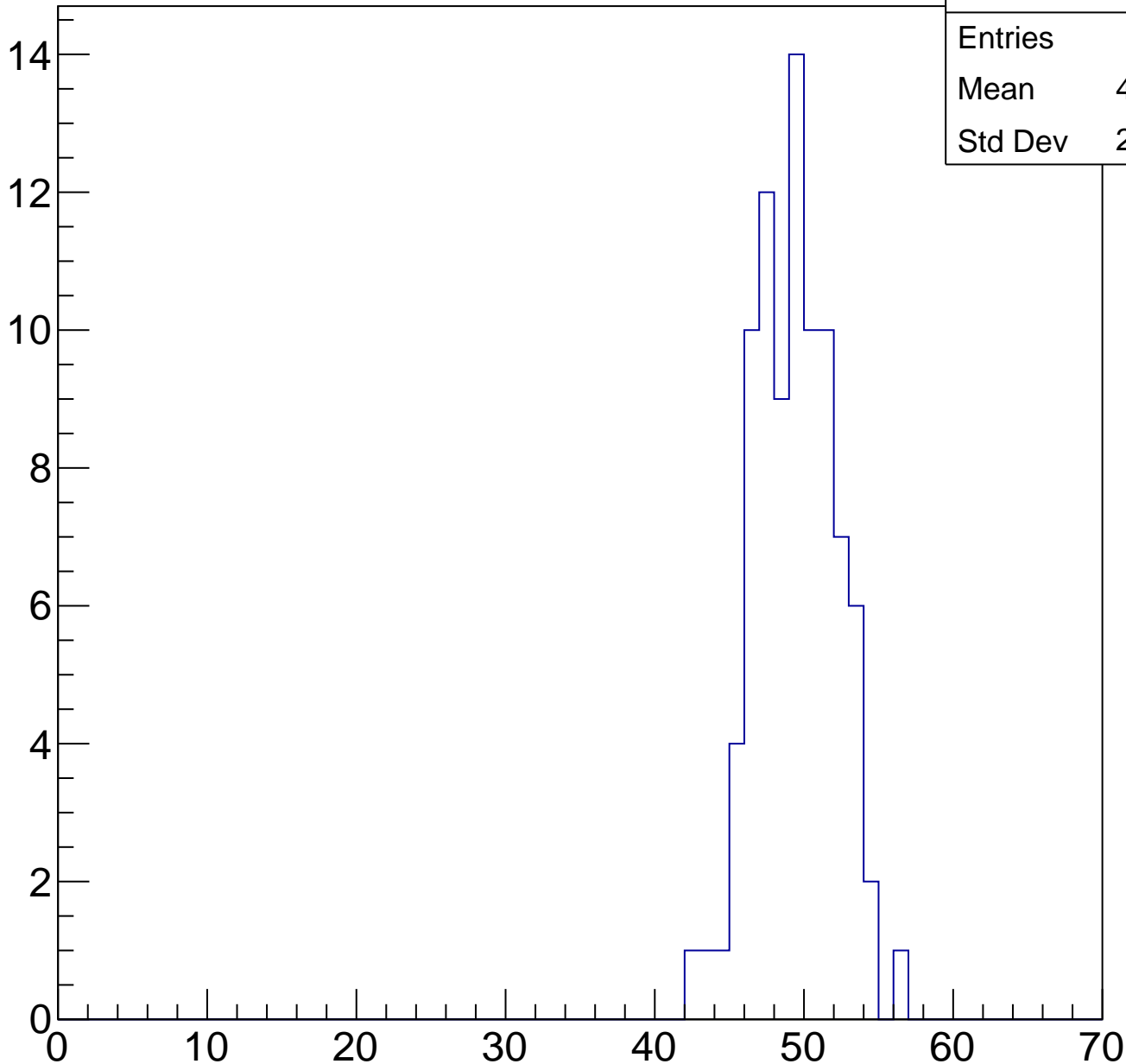
calib_packv5_042523_0143.root, FC#2, port C2

Entries	88
Mean	48.94
Std Dev	2.673

Entry

14
12
10
8
6
4
2
0

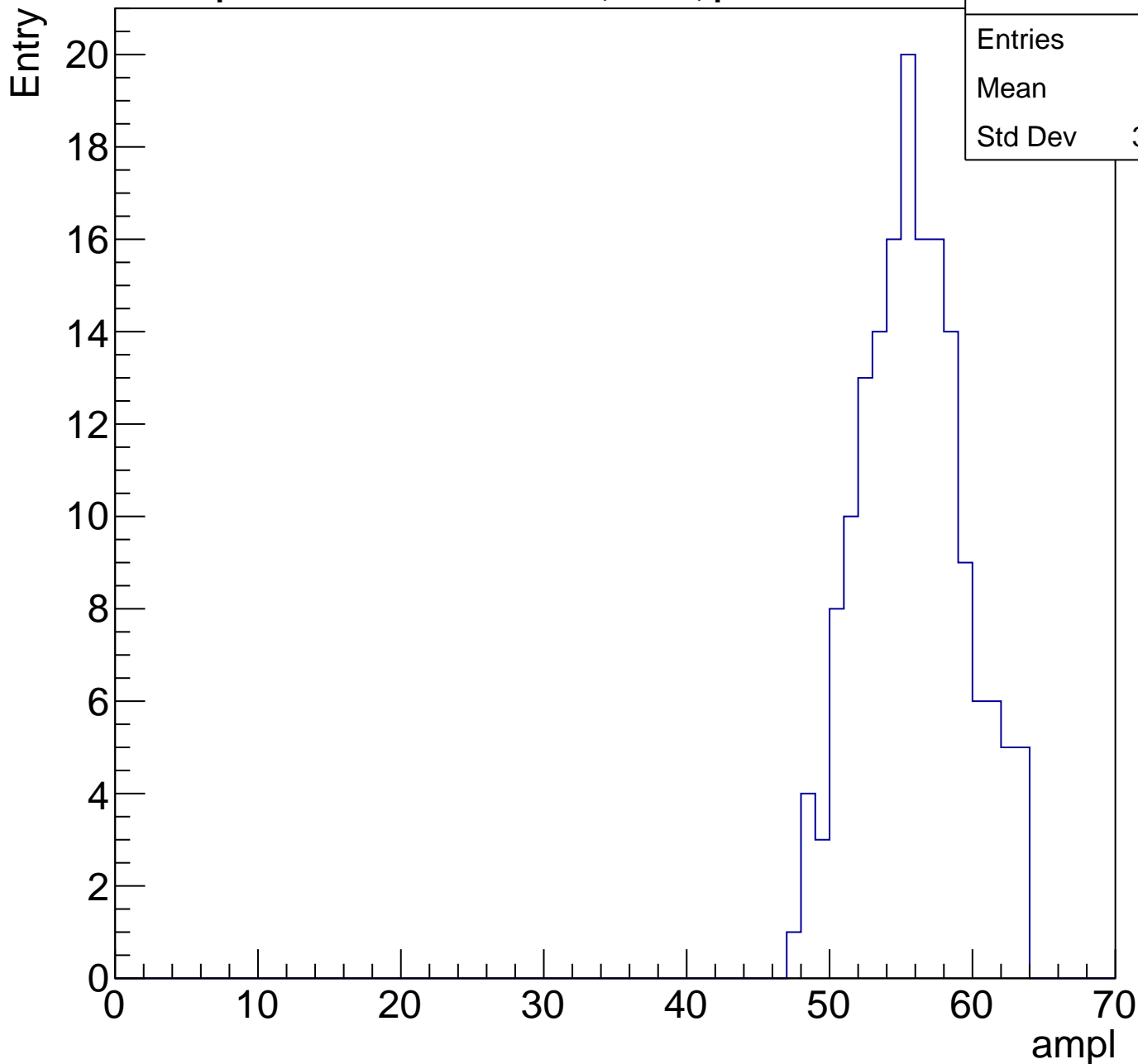
ampl



B1L001S, U21-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	166
Mean	55.3
Std Dev	3.626



B1L001S, U21-ch107, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

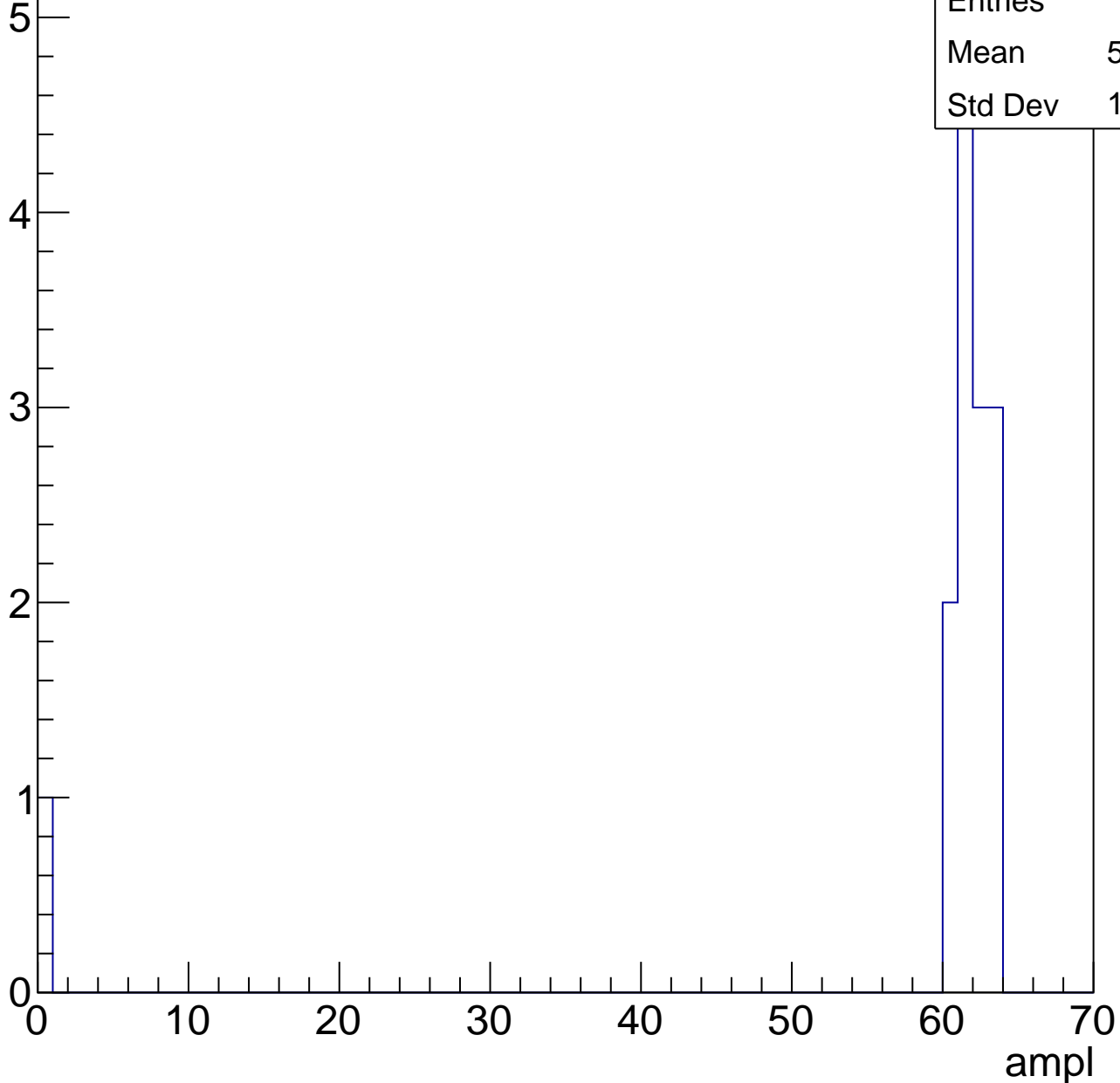
Entries	71
Mean	59.27
Std Dev	7.425

B1L001S, U21-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

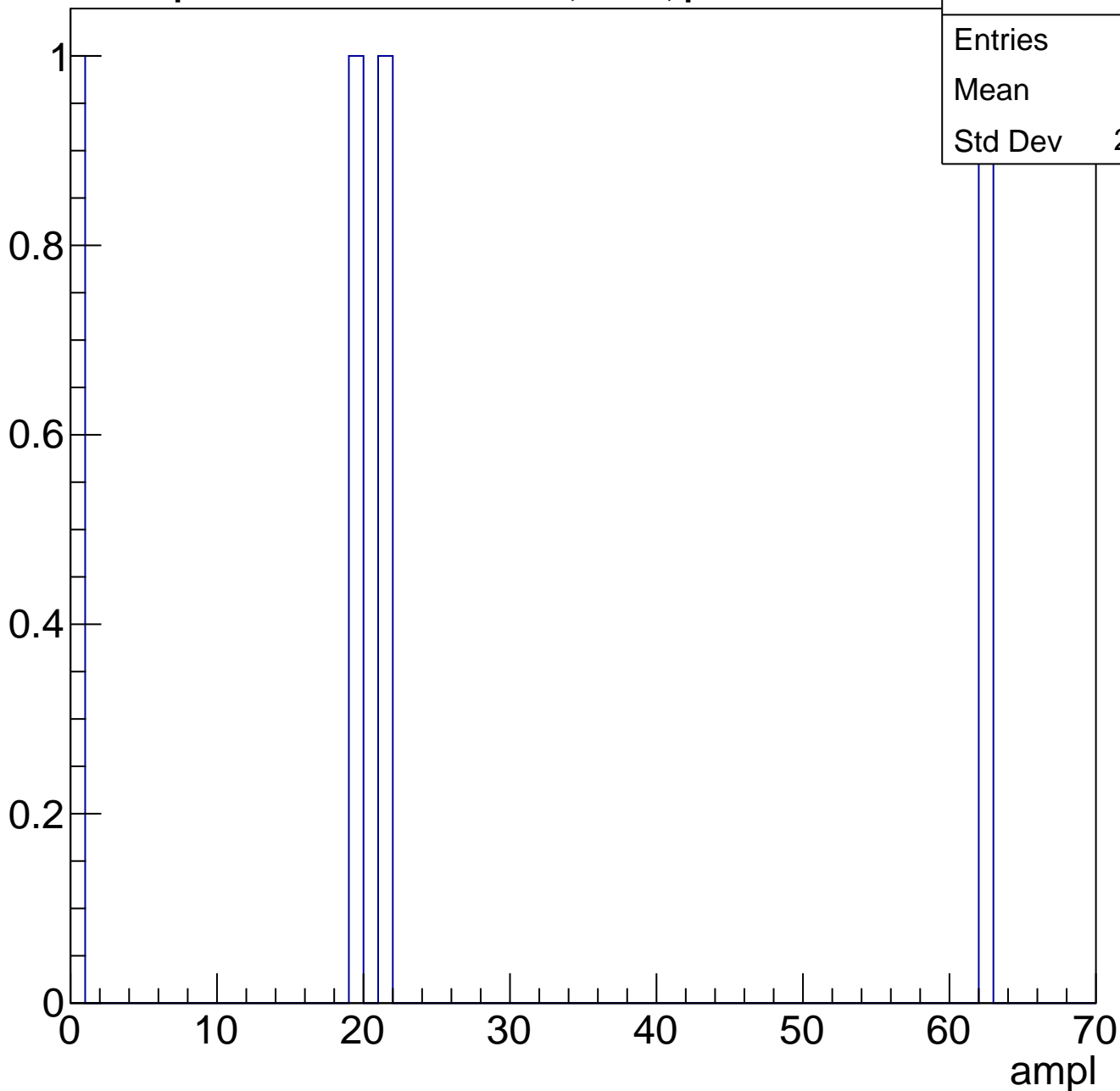
Entries	14
Mean	57.14
Std Dev	15.88



B1L001S, U21-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch108, adc0

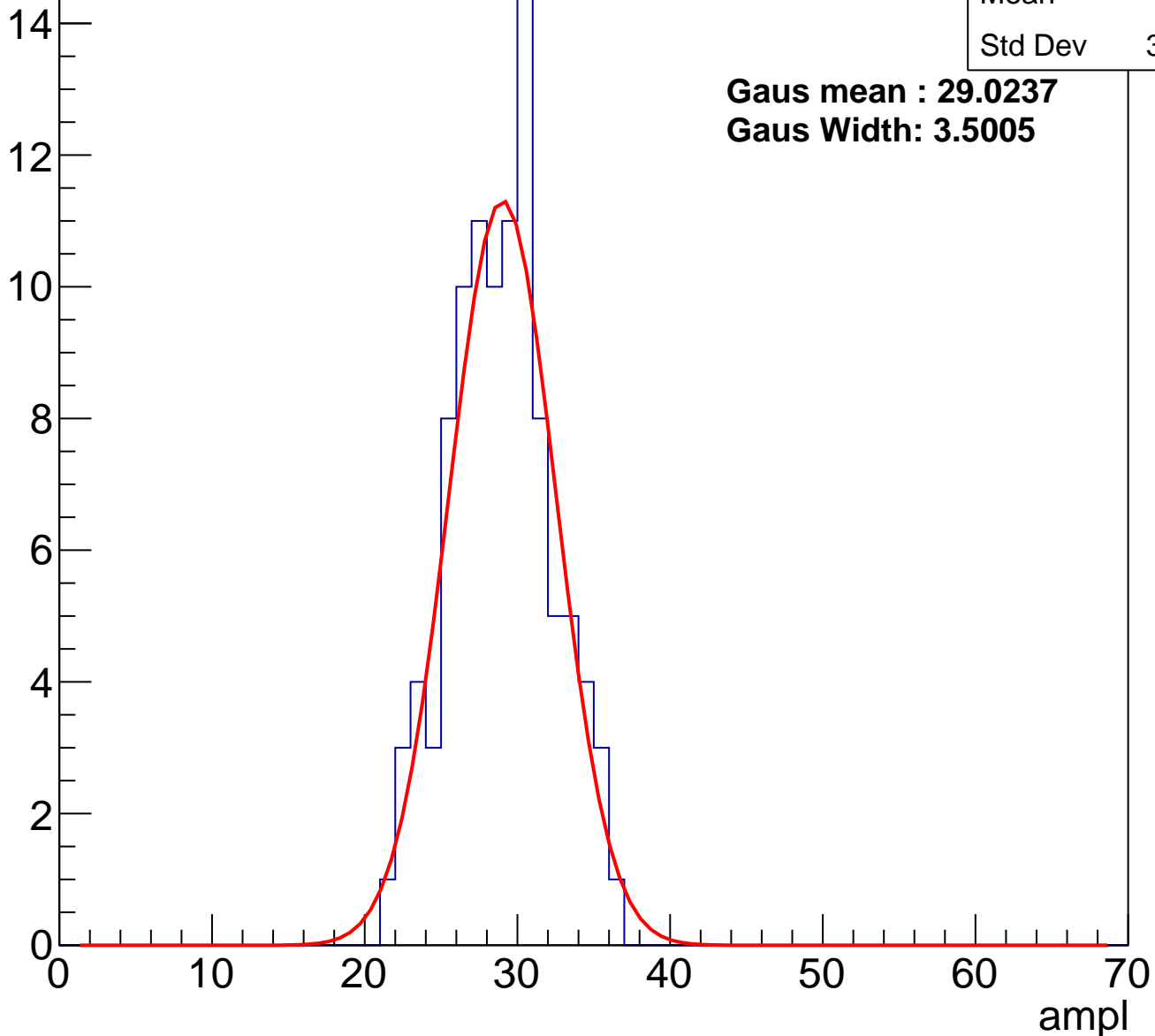
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	28.5
Std Dev	3.292

Gaus mean : 29.0237

Gaus Width: 3.5005

Entry



B1L001S, U21-ch108, adc1

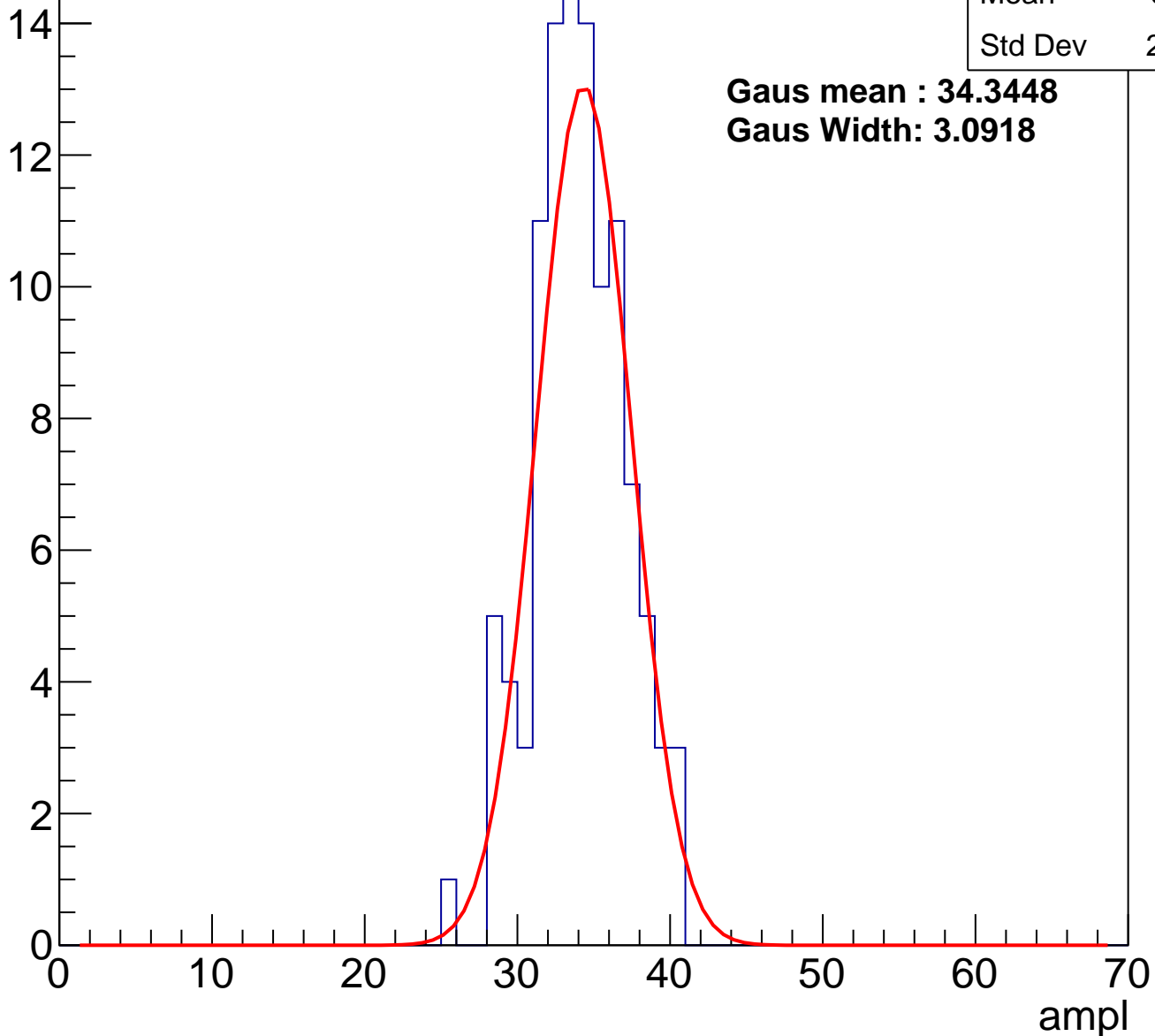
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	33.61
Std Dev	2.983

Gaus mean : 34.3448

Gaus Width: 3.0918

Entry



B1L001S, U21-ch108, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

148

Mean

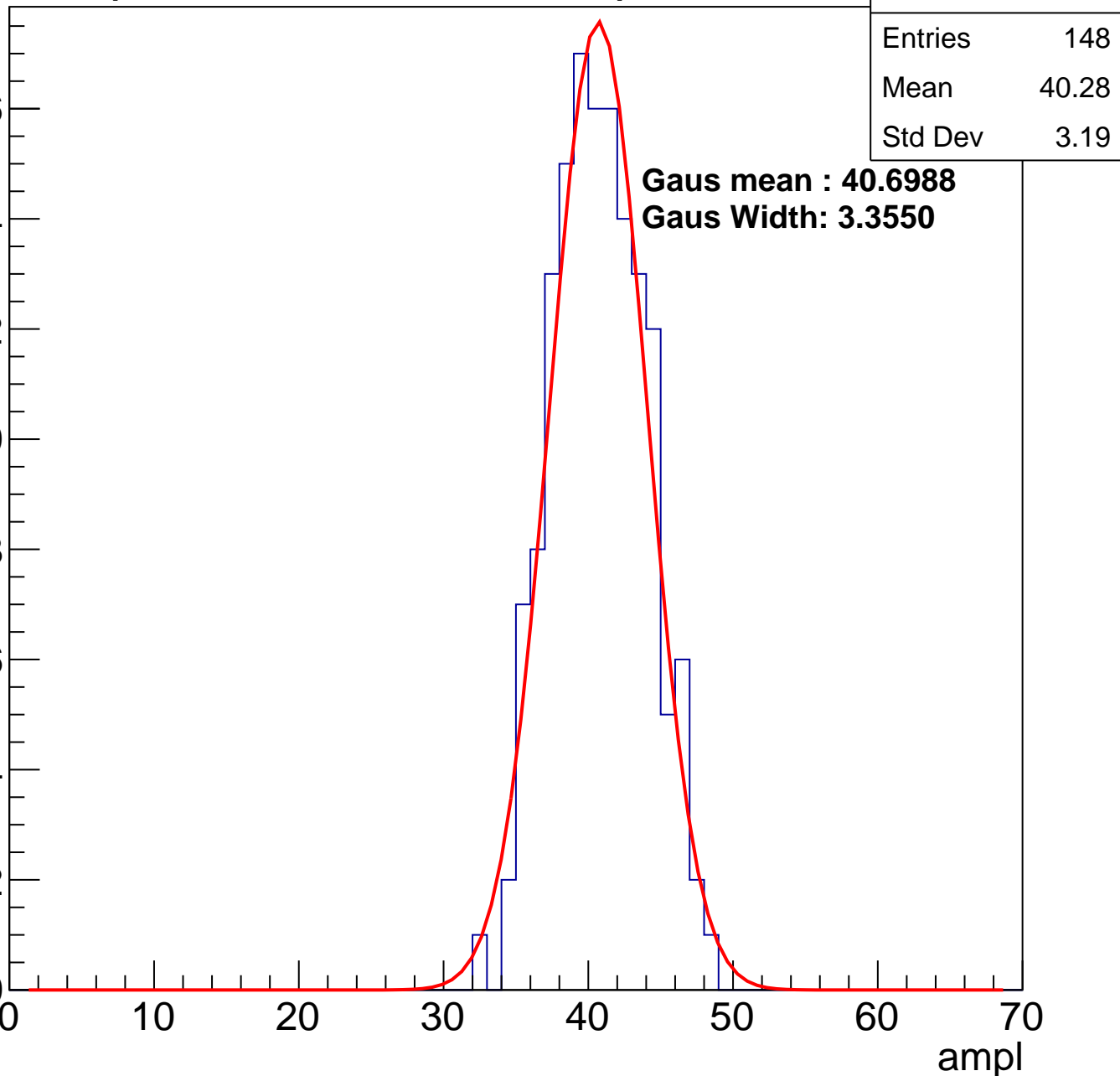
40.28

Std Dev

3.19

Gaus mean : 40.6988

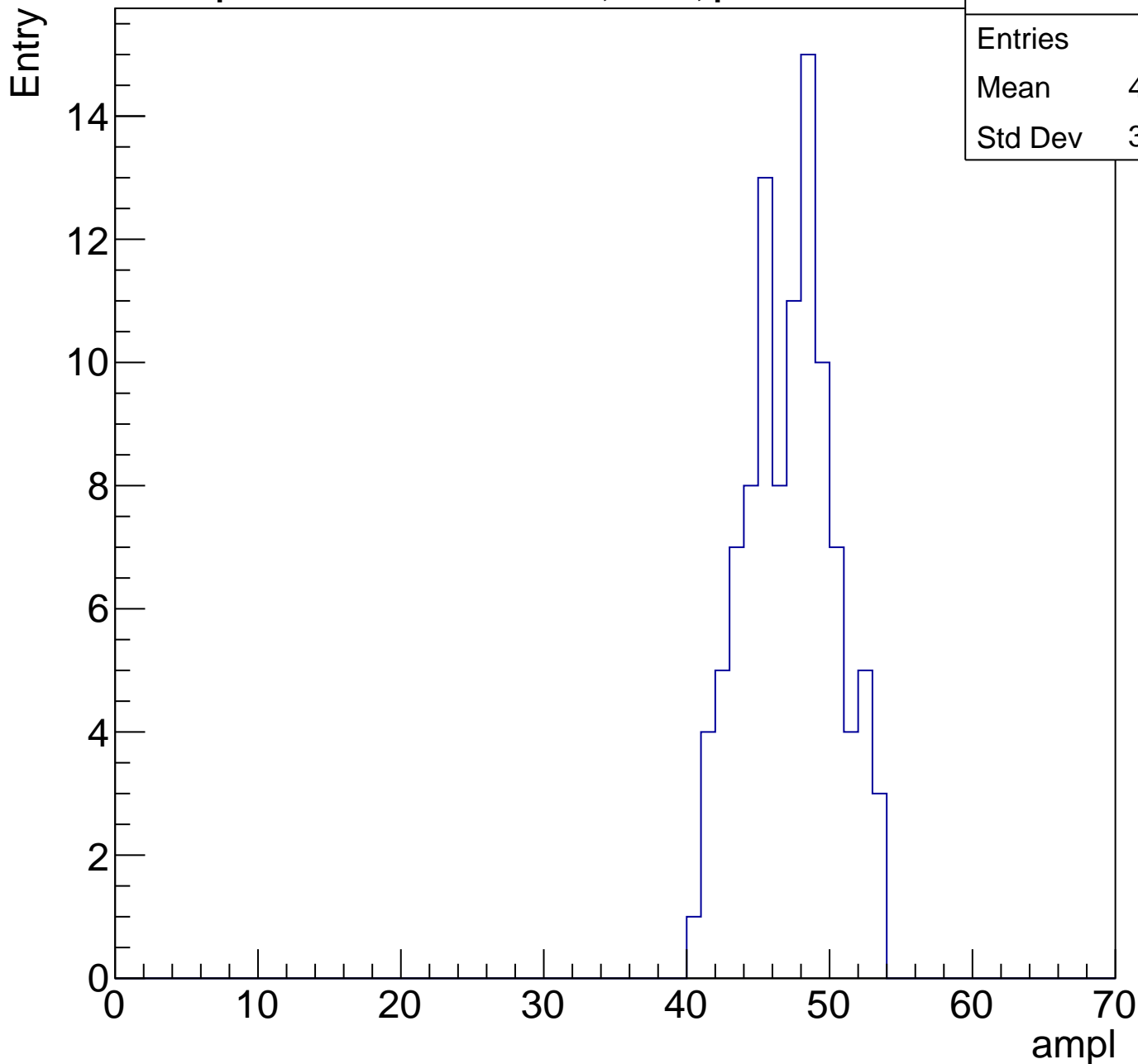
Gaus Width: 3.3550



B1L001S, U21-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

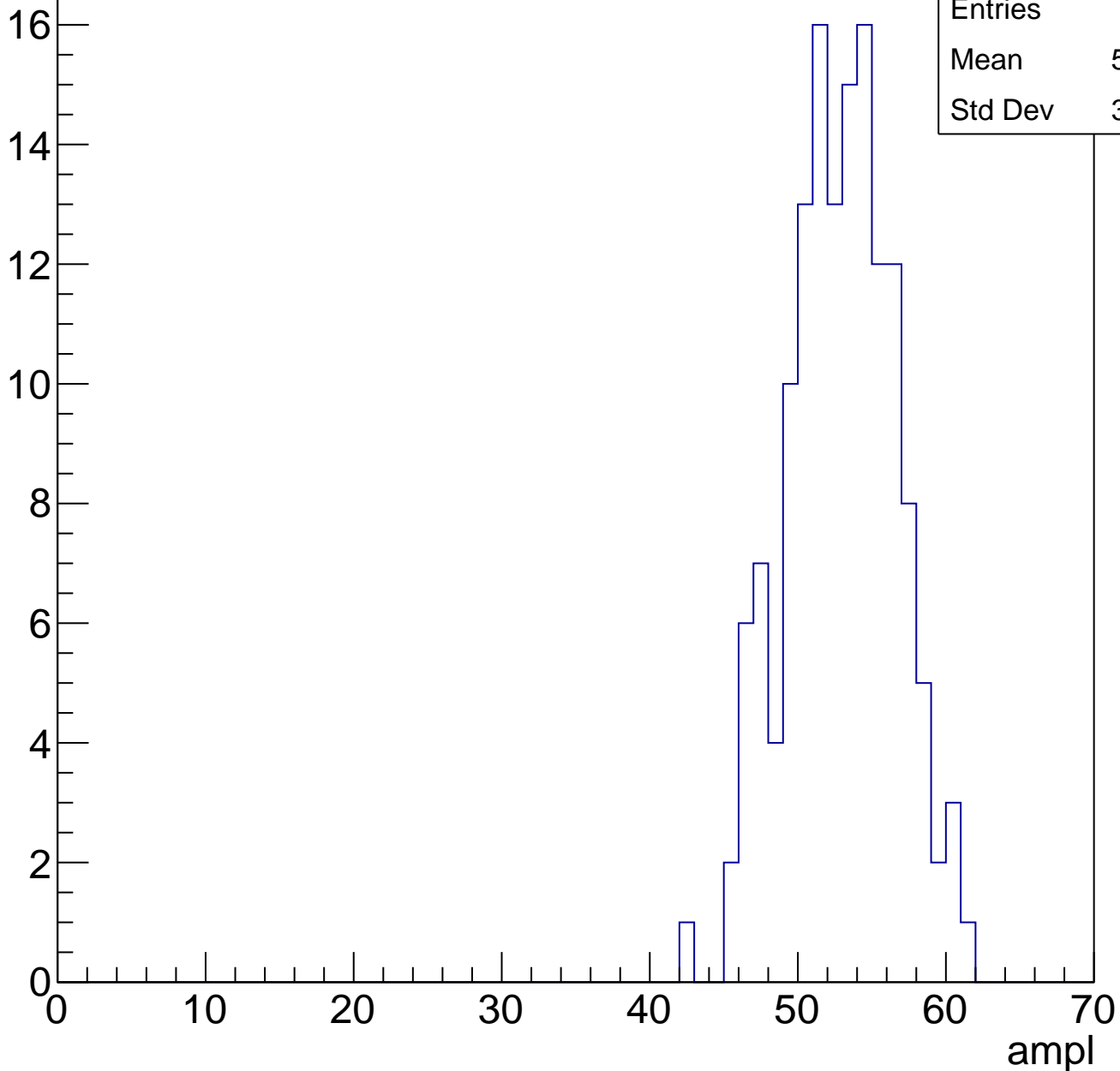
Entries	101
Mean	46.73
Std Dev	3.099



B1L001S, U21-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

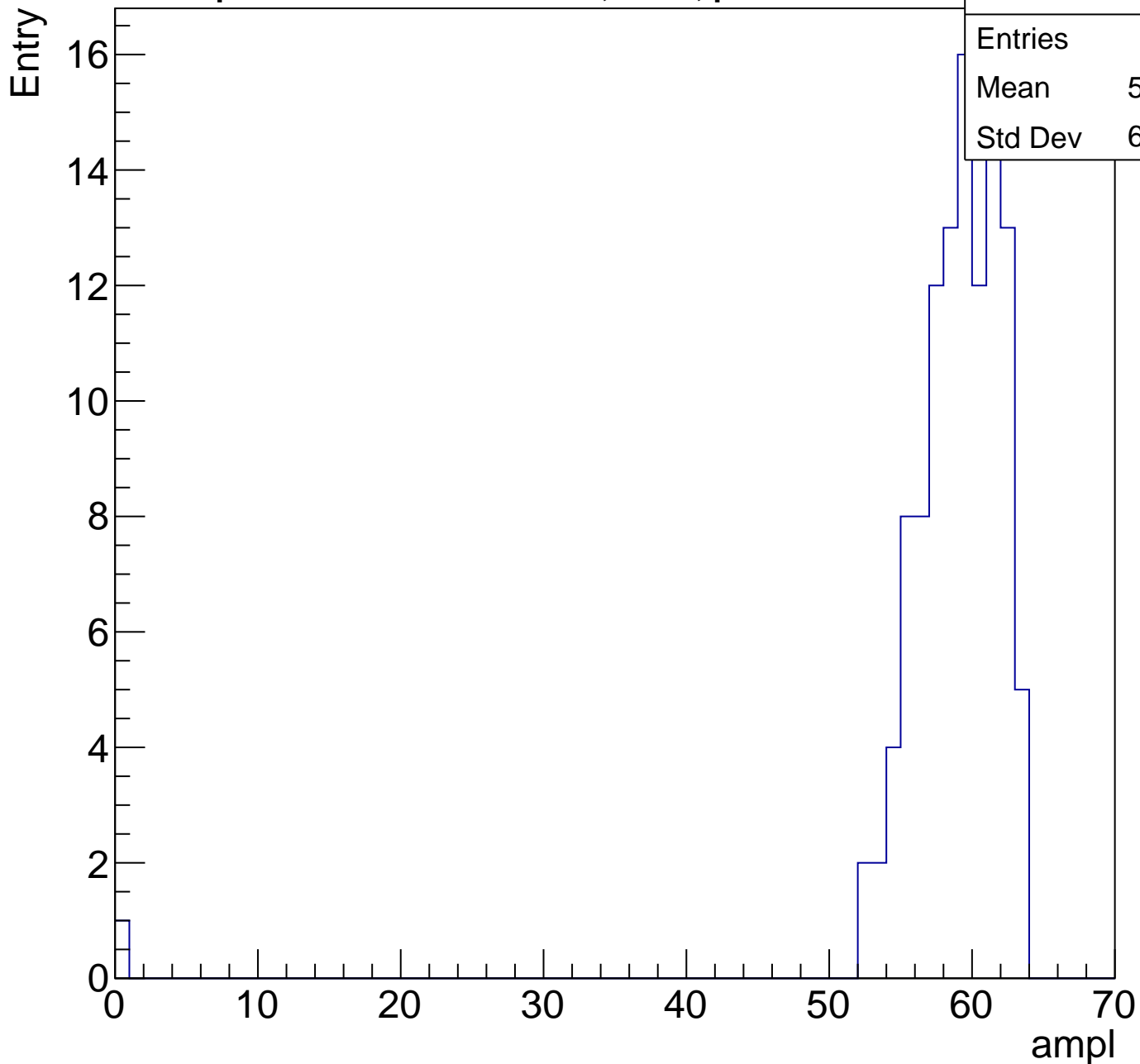
Entry



B1L001S, U21-ch108, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	58.13
Std Dev	6.148



B1L001S, U21-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

34

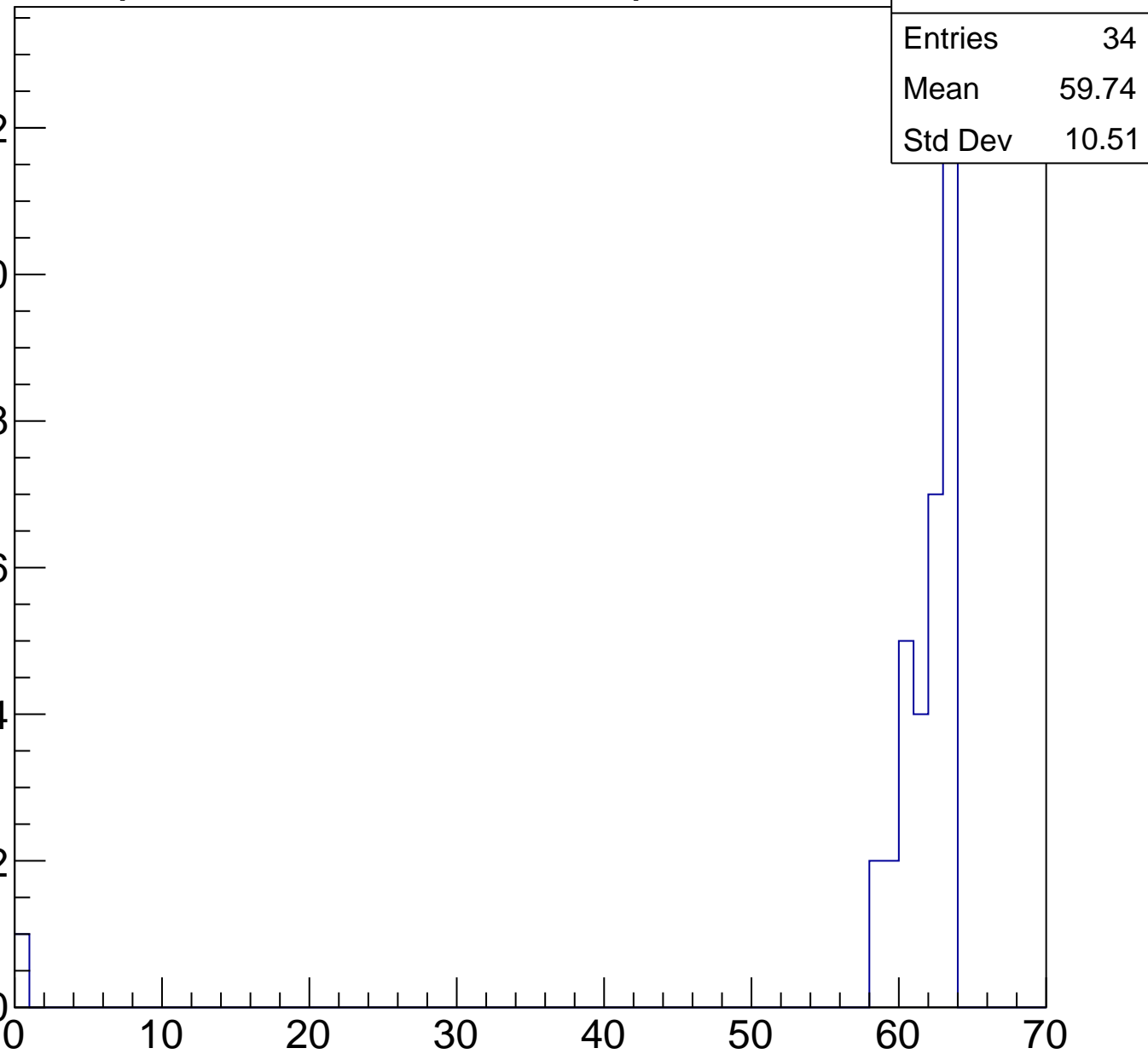
Mean

59.74

Std Dev

10.51

ampl



B1L001S, U21-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.4
Std Dev	4.879

Gaus mean : 30.3122

Gaus Width: 3.5459

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

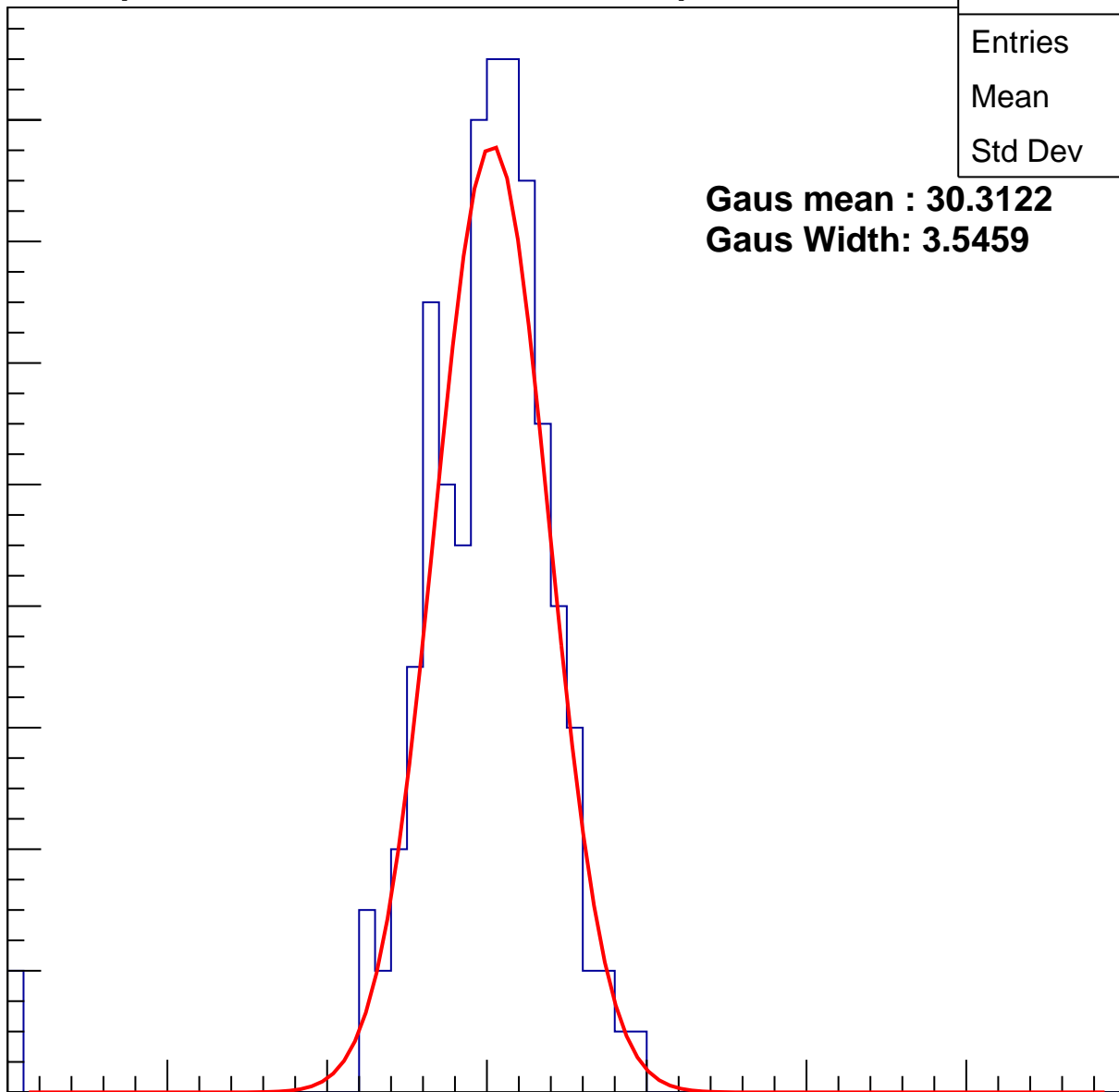
40

50

60

70

ampl



B1L001S, U21-ch109, adc1

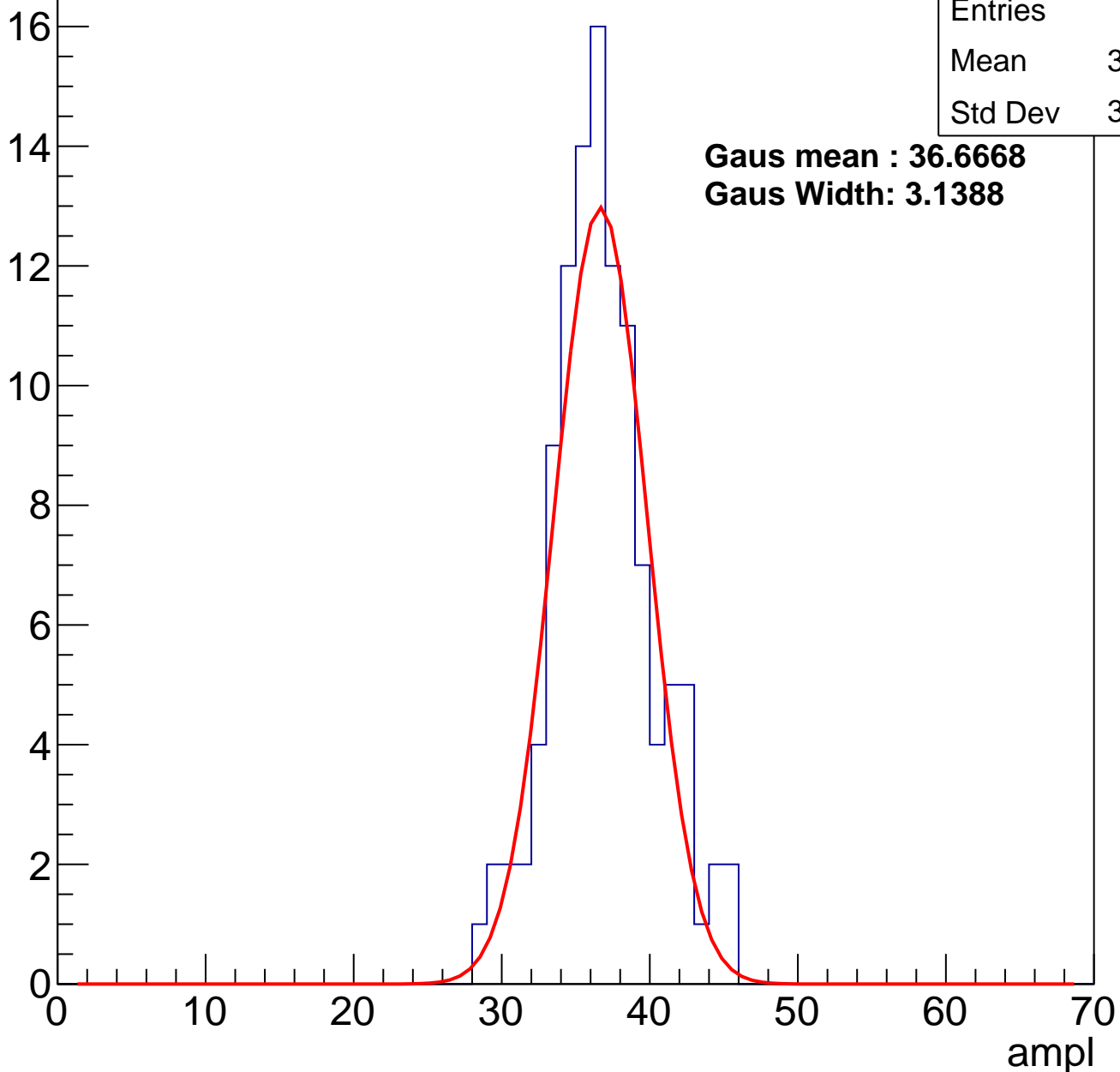
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.38
Std Dev	3.425

Gaus mean : 36.6668

Gaus Width: 3.1388

Entry



B1L001S, U21-ch109, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

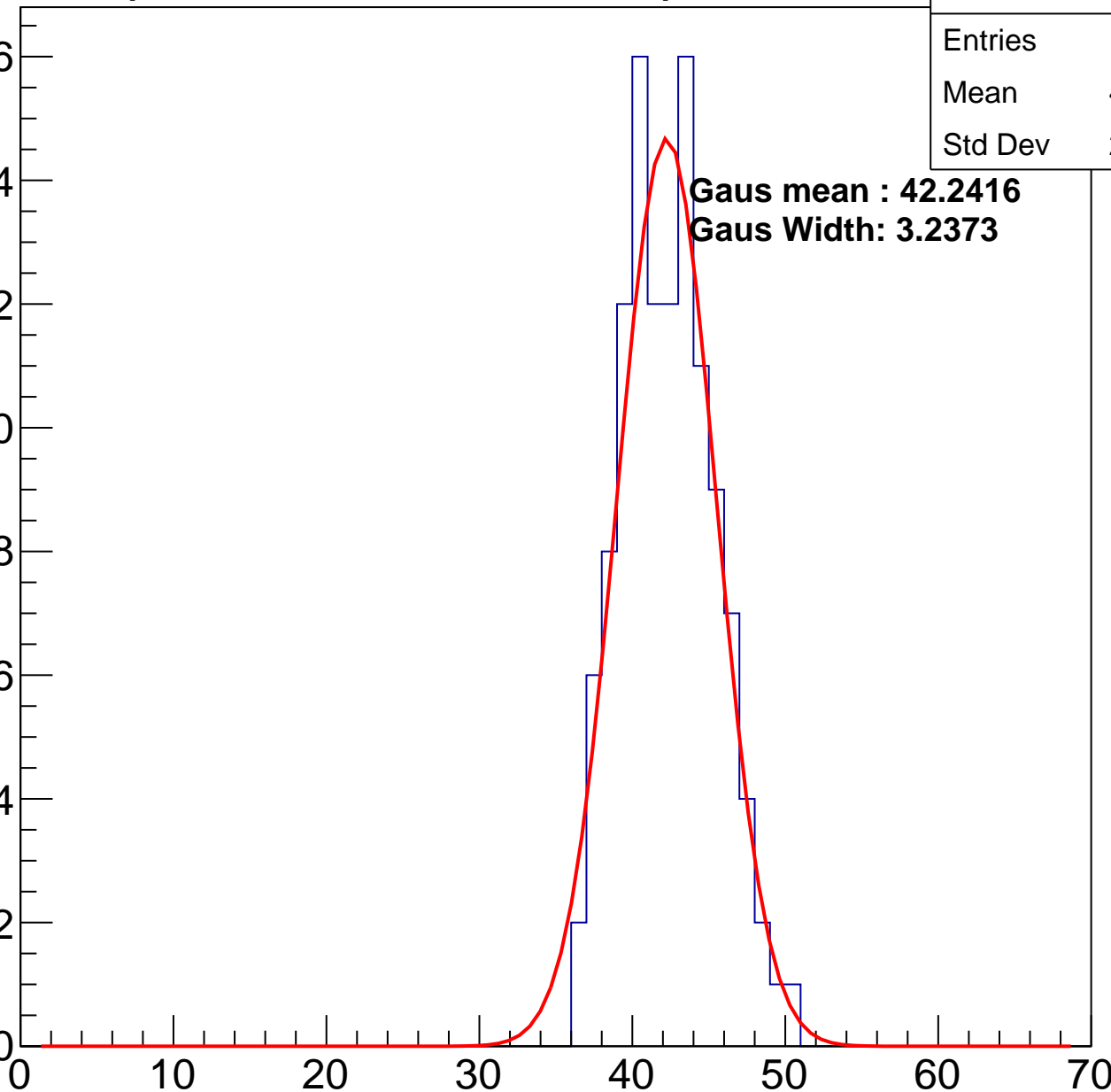
16
14
12
10
8
6
4
2
0

Entries	119
Mean	41.88
Std Dev	2.999

Gaus mean : 42.2416

Gaus Width: 3.2373

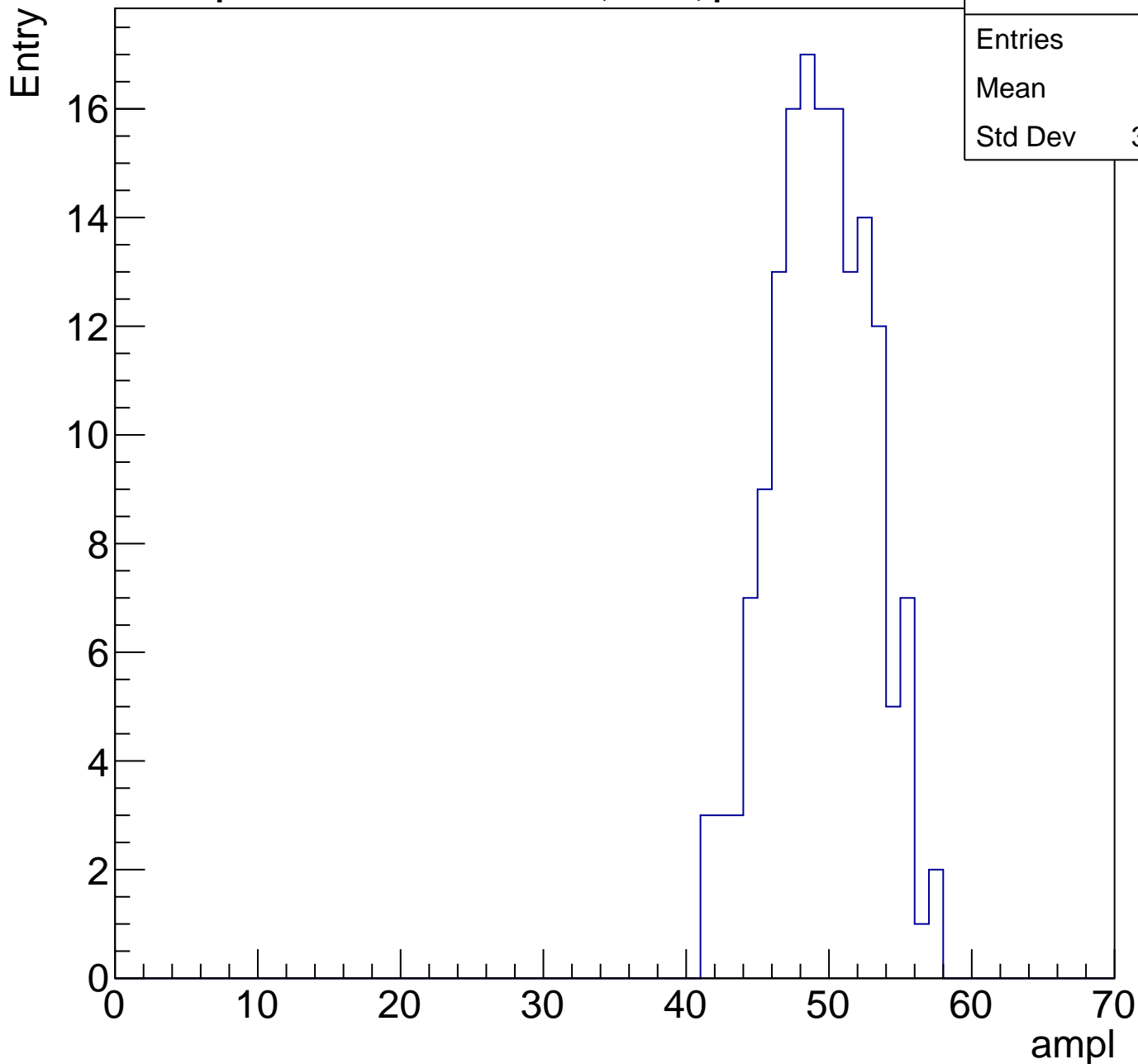
ampl



B1L001S, U21-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

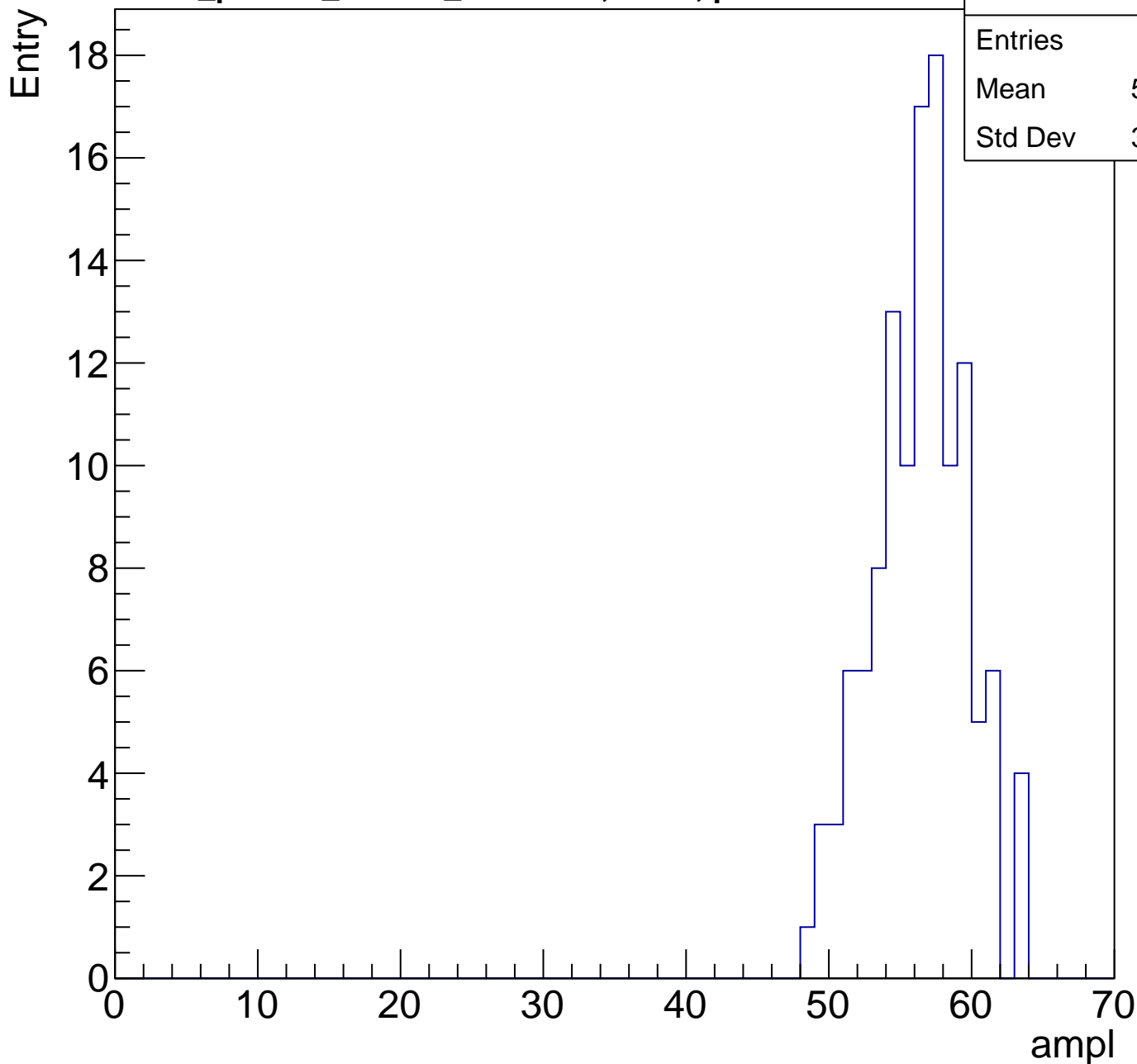
Entries	157
Mean	49
Std Dev	3.482



B1L001S, U21-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	55.93
Std Dev	3.252



B1L001S, U21-ch109, adc5

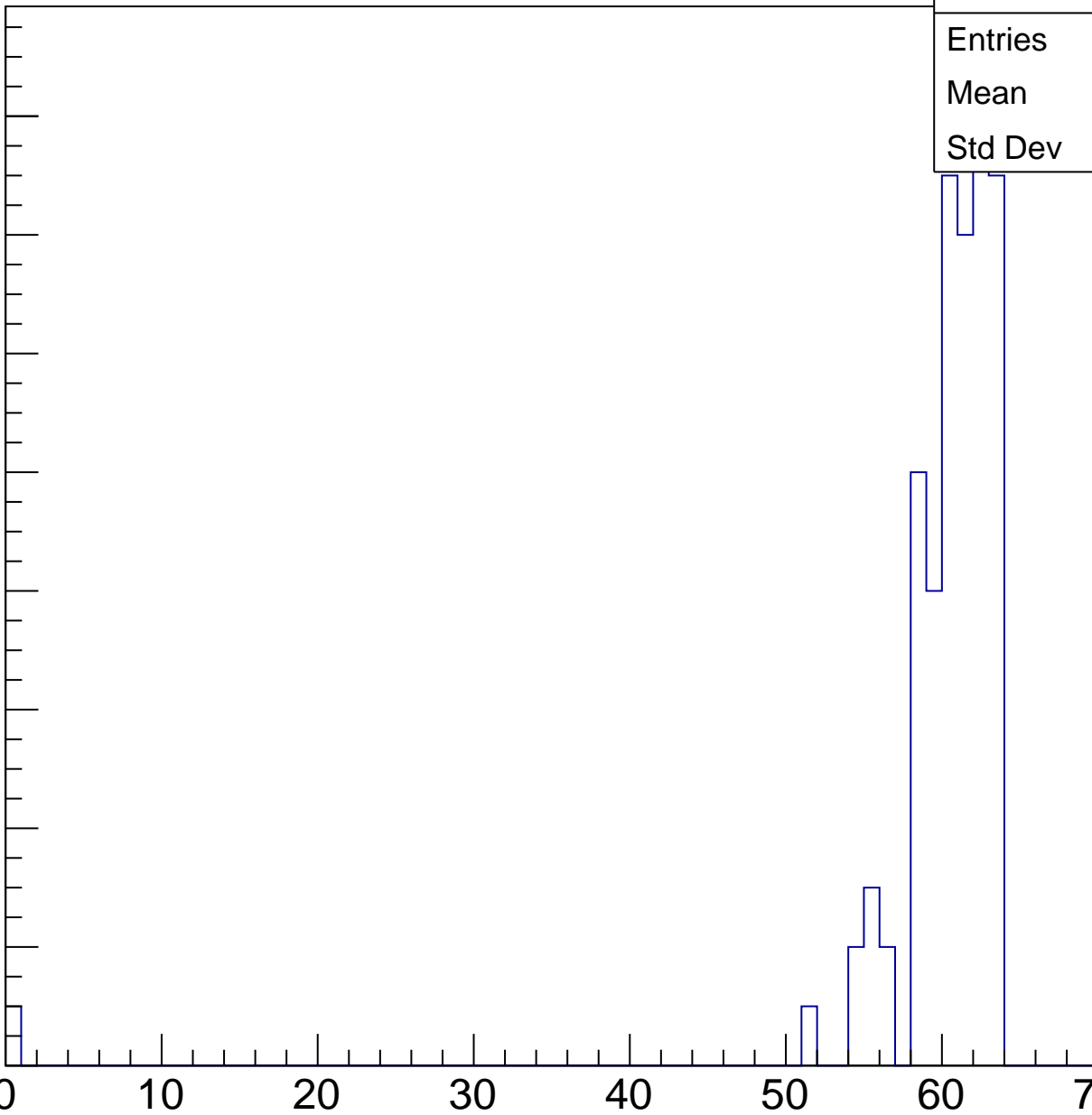
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.56
Std Dev	6.832

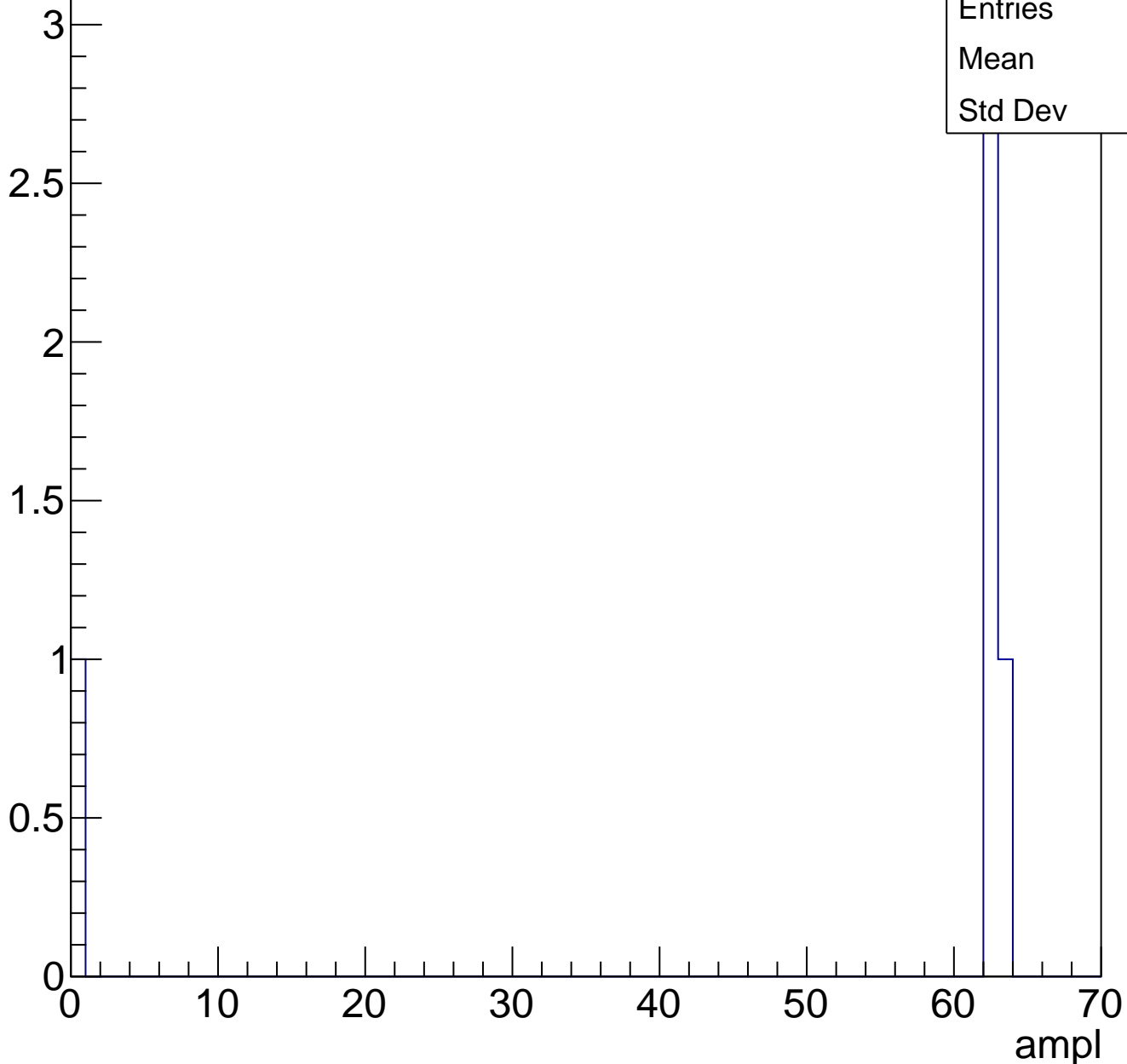
ampl



B1L001S, U21-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

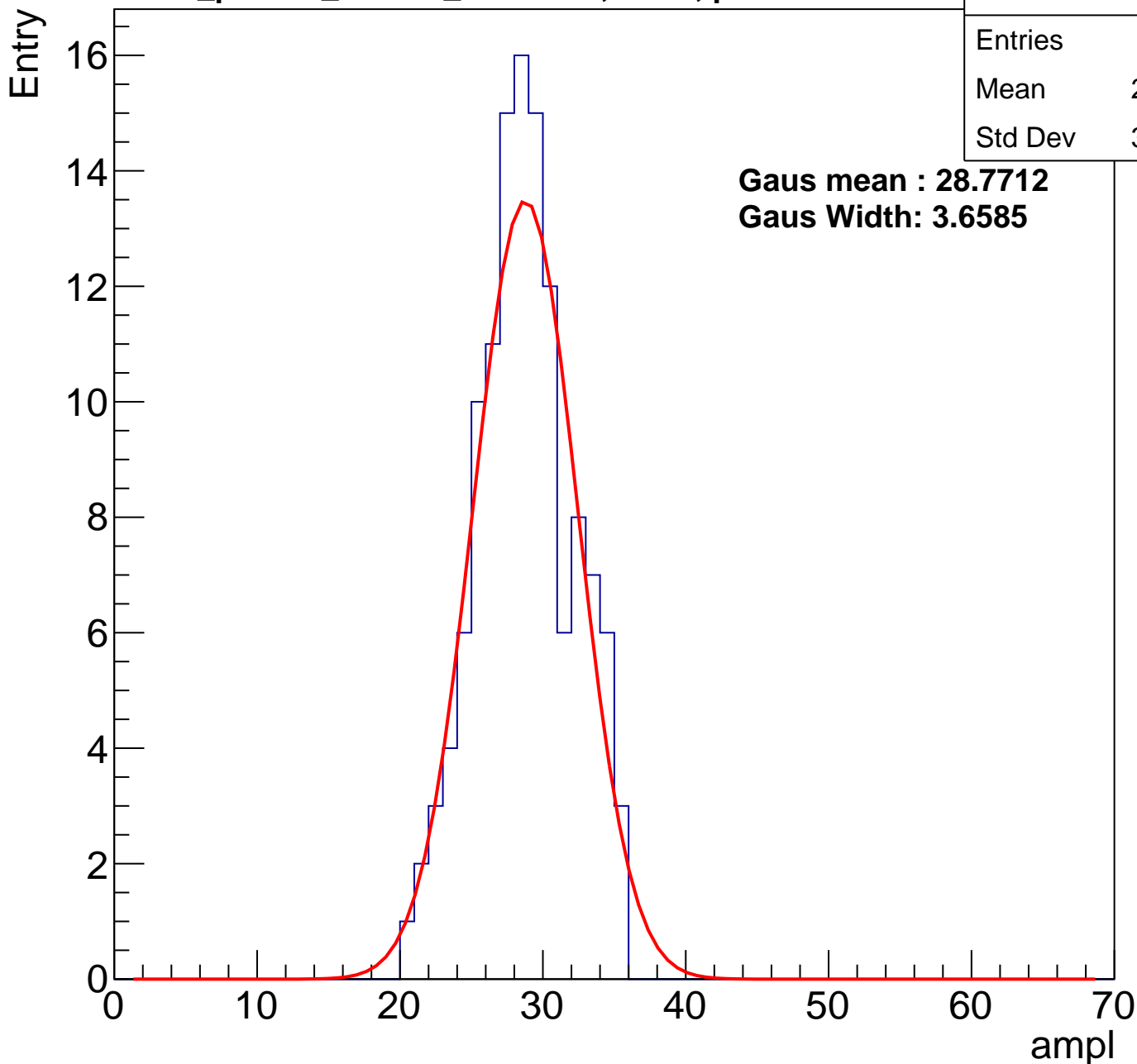
B1L001S, U21-ch110, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	28.24
Std Dev	3.336

Gaus mean : 28.7712

Gaus Width: 3.6585



B1L001S, U21-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	35.03
Std Dev	3.779

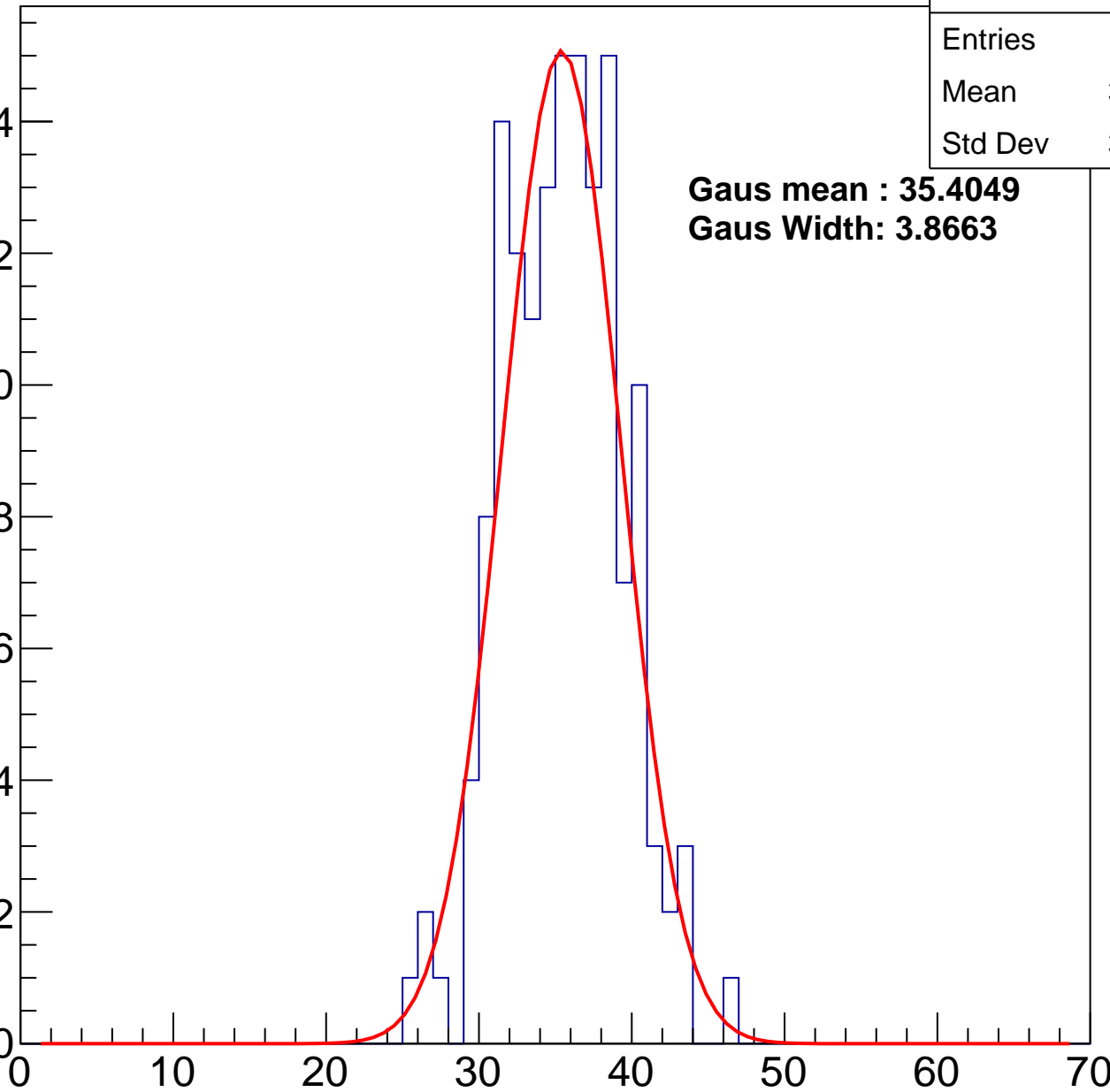
Gaus mean : 35.4049

Gaus Width: 3.8663

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch110, adc2

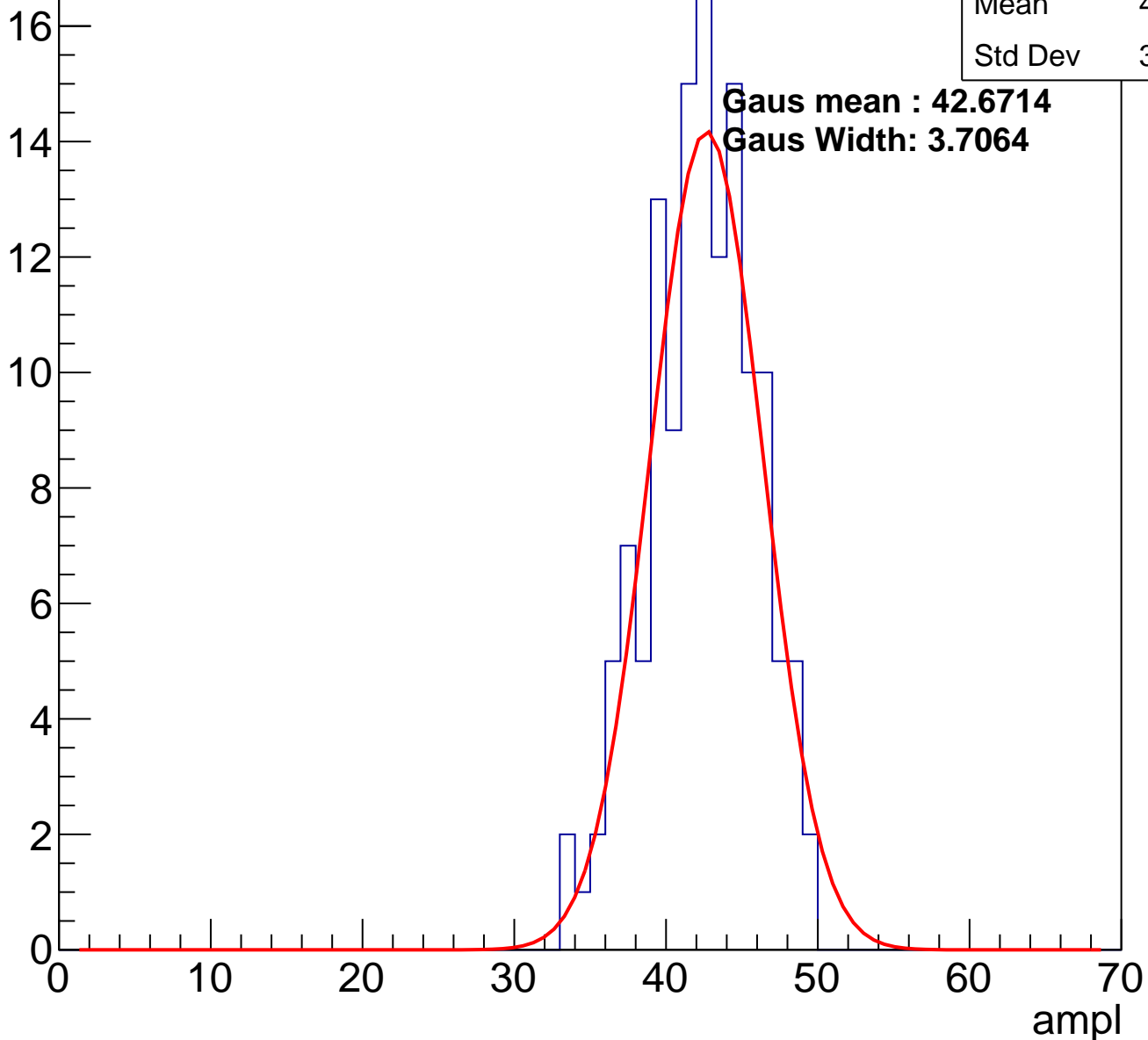
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	135
Mean	41.88
Std Dev	3.496

Gaus mean : 42.6714

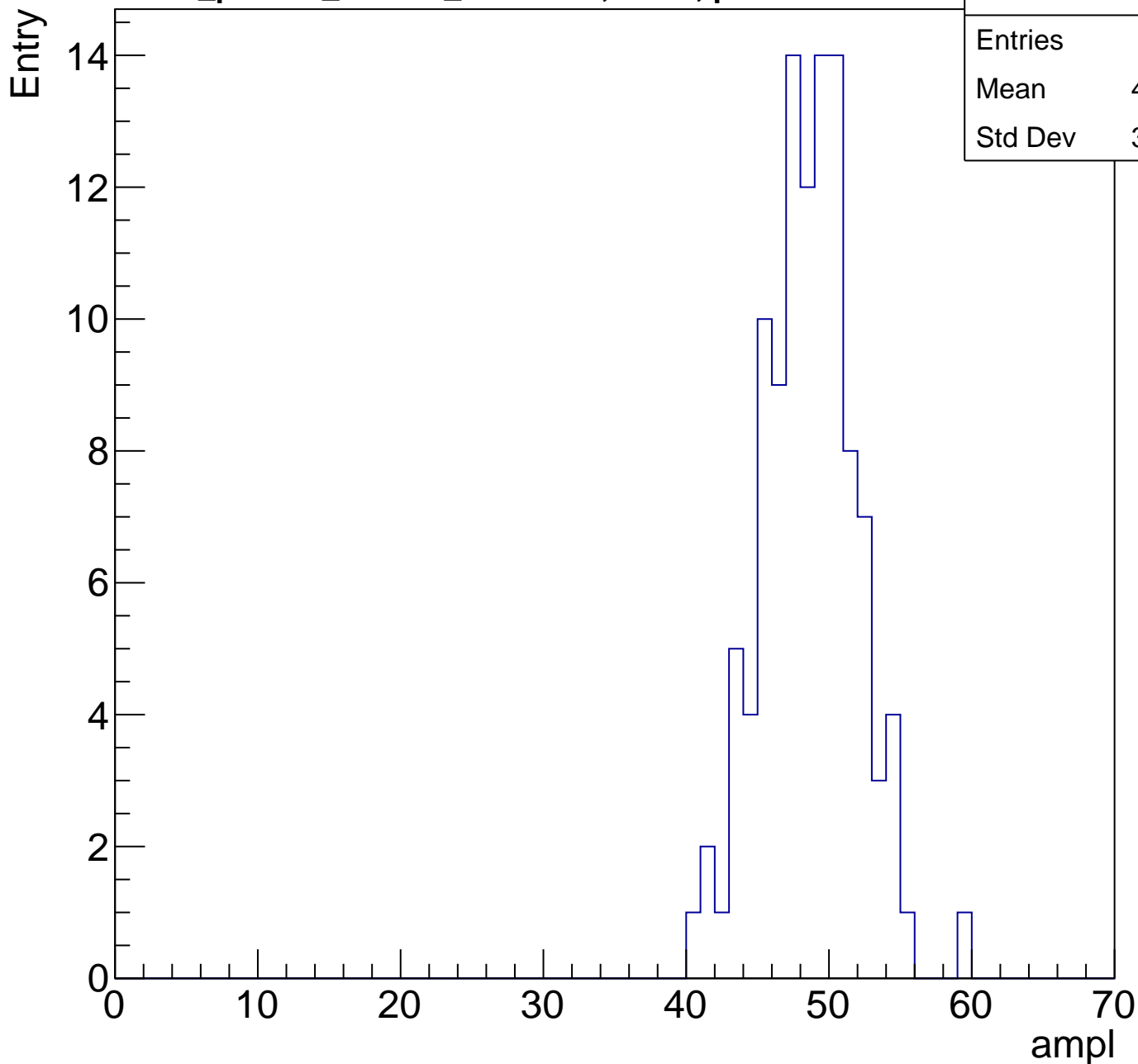
Gaus Width: 3.7064



B1L001S, U21-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

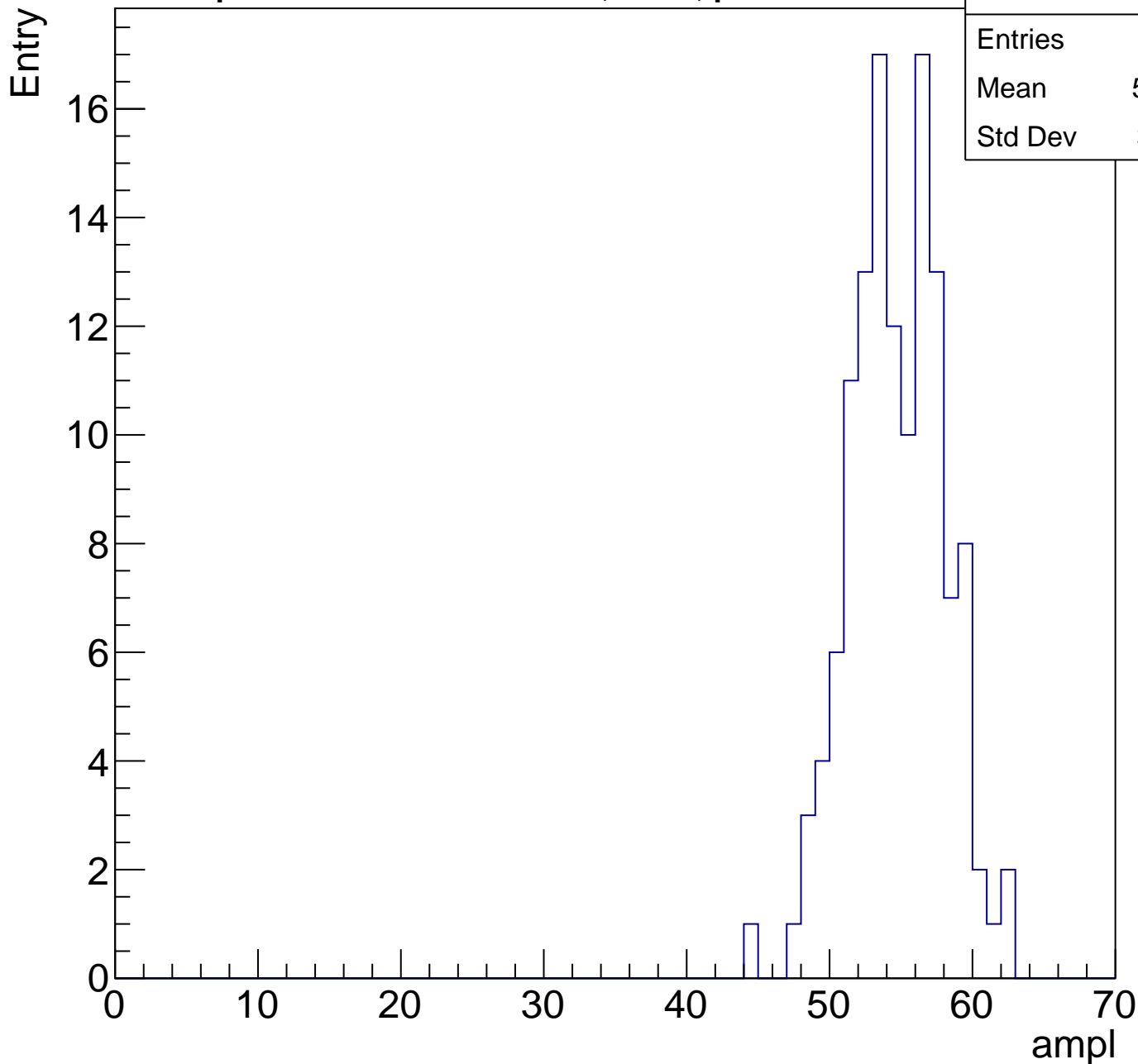
Entries	110
Mean	48.18
Std Dev	3.256



B1L001S, U21-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

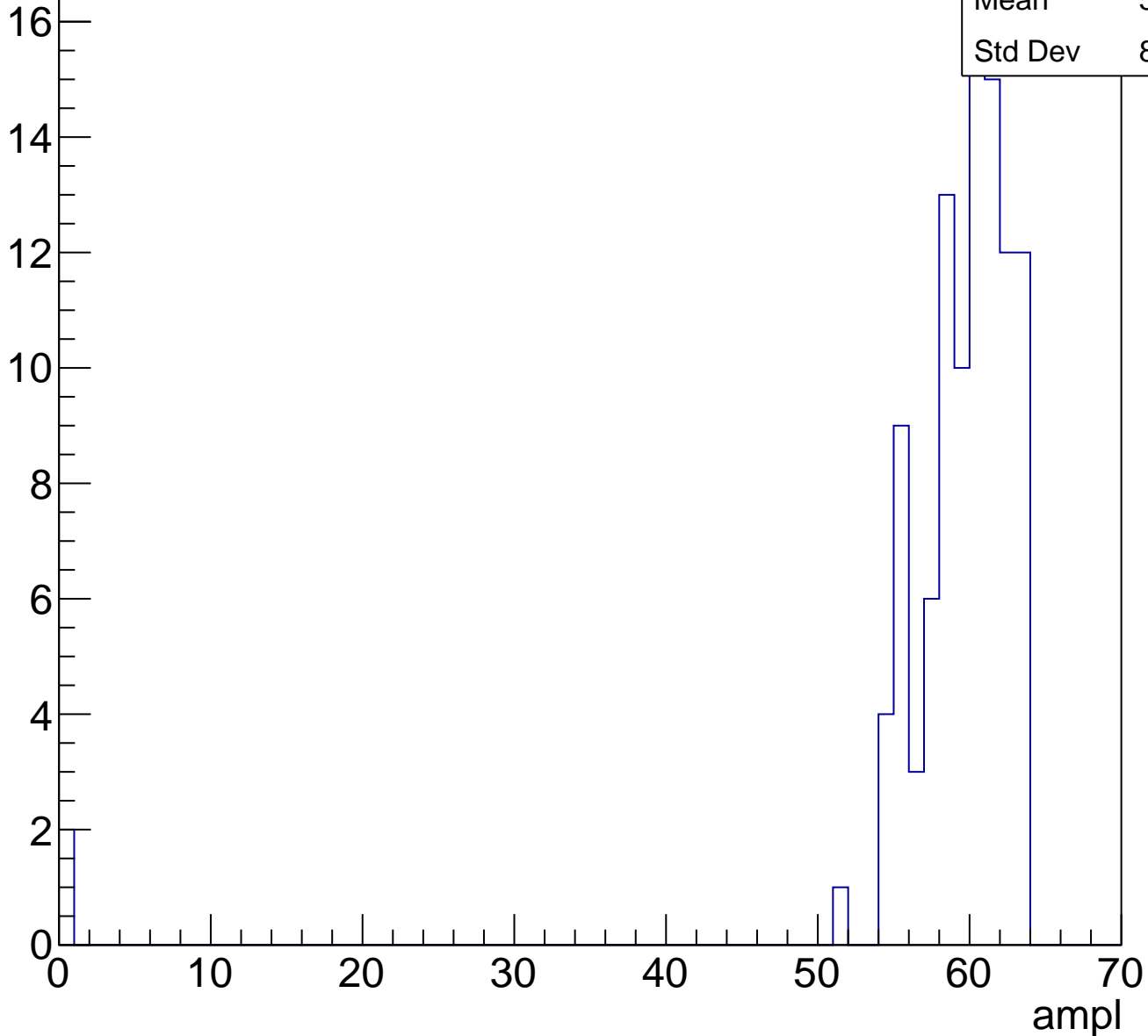
Entries	128
Mean	54.24
Std Dev	3.271



B1L001S, U21-ch110, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

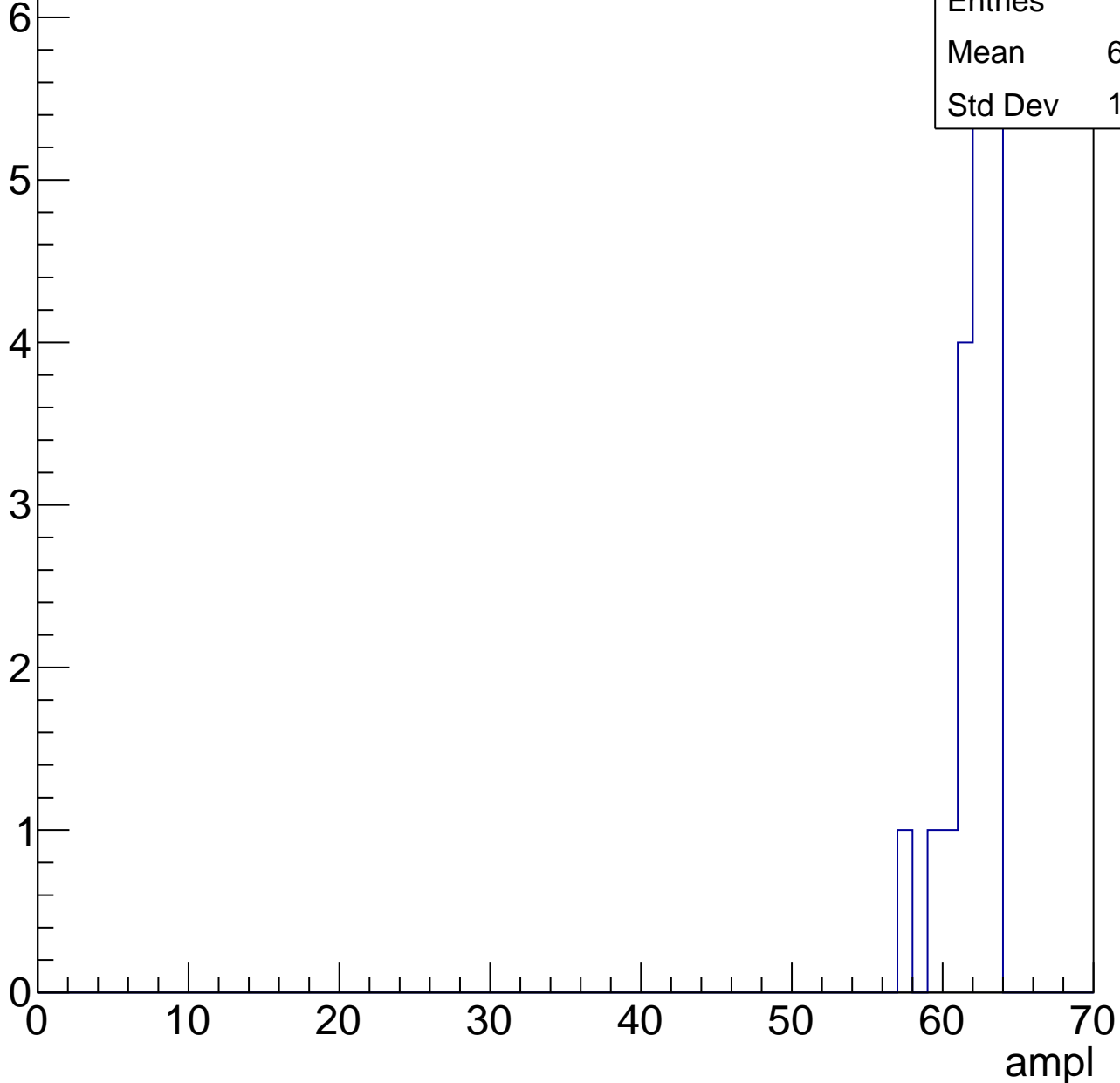


B1L001S, U21-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.58
Std Dev	1.533



B1L001S, U21-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch111, adc0

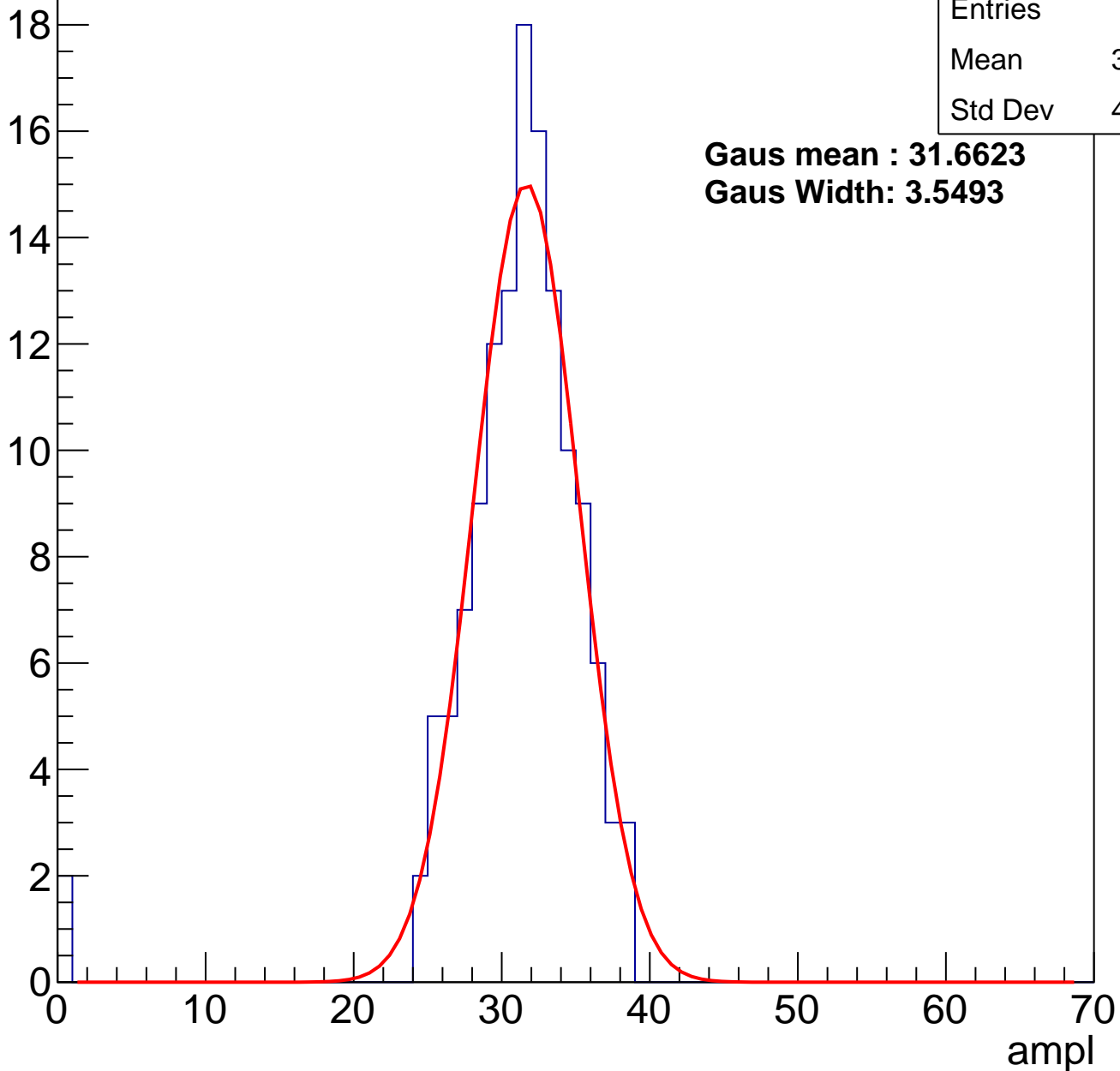
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.65
Std Dev	4.959

Gaus mean : 31.6623

Gaus Width: 3.5493

Entry



B1L001S, U21-ch111, adc1

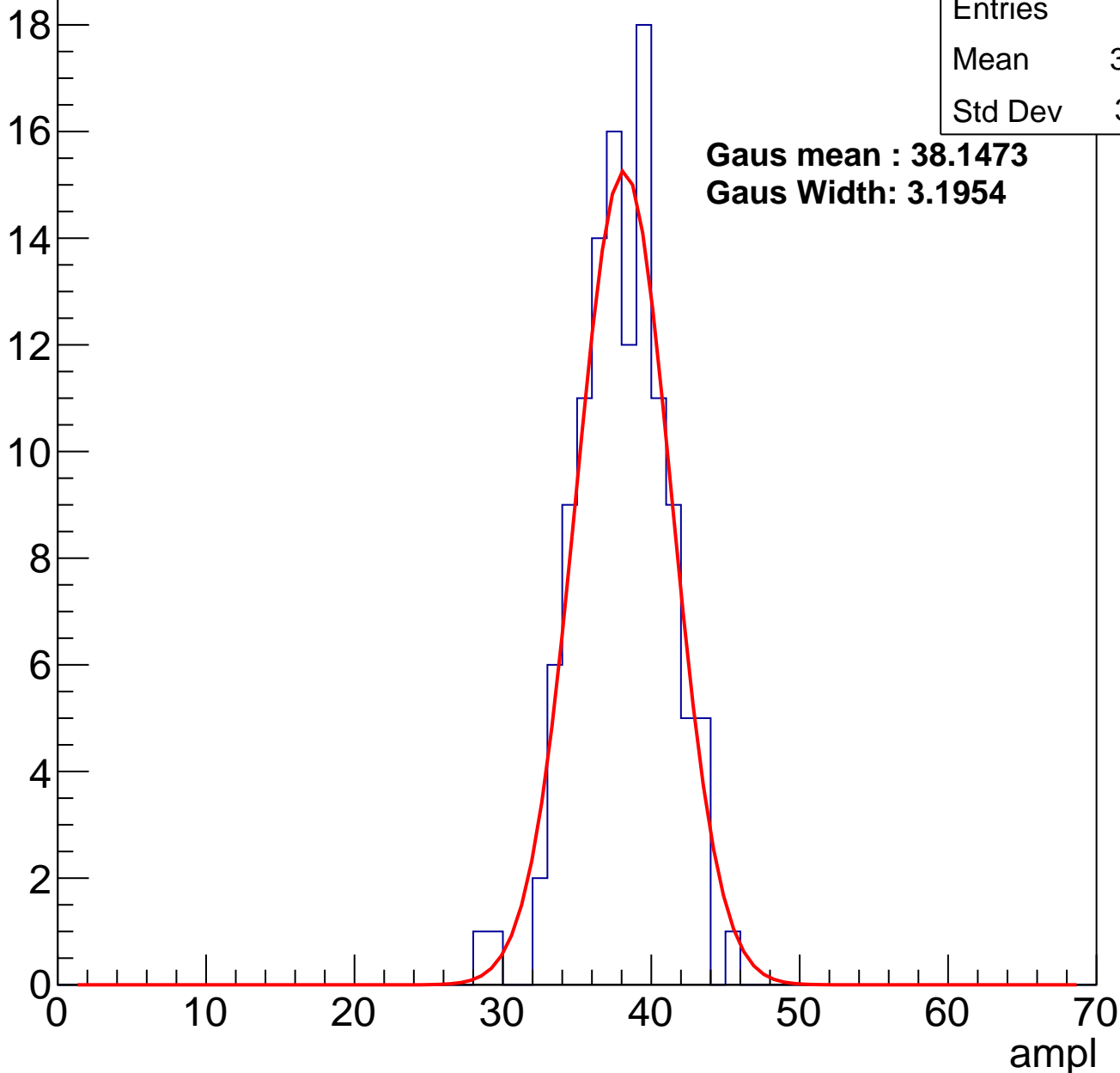
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	37.55
Std Dev	3.001

Gaus mean : 38.1473

Gaus Width: 3.1954

Entry



B1L001S, U21-ch111, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	43.96
Std Dev	3.421

Gaus mean : 44.5580

Gaus Width: 3.1934

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

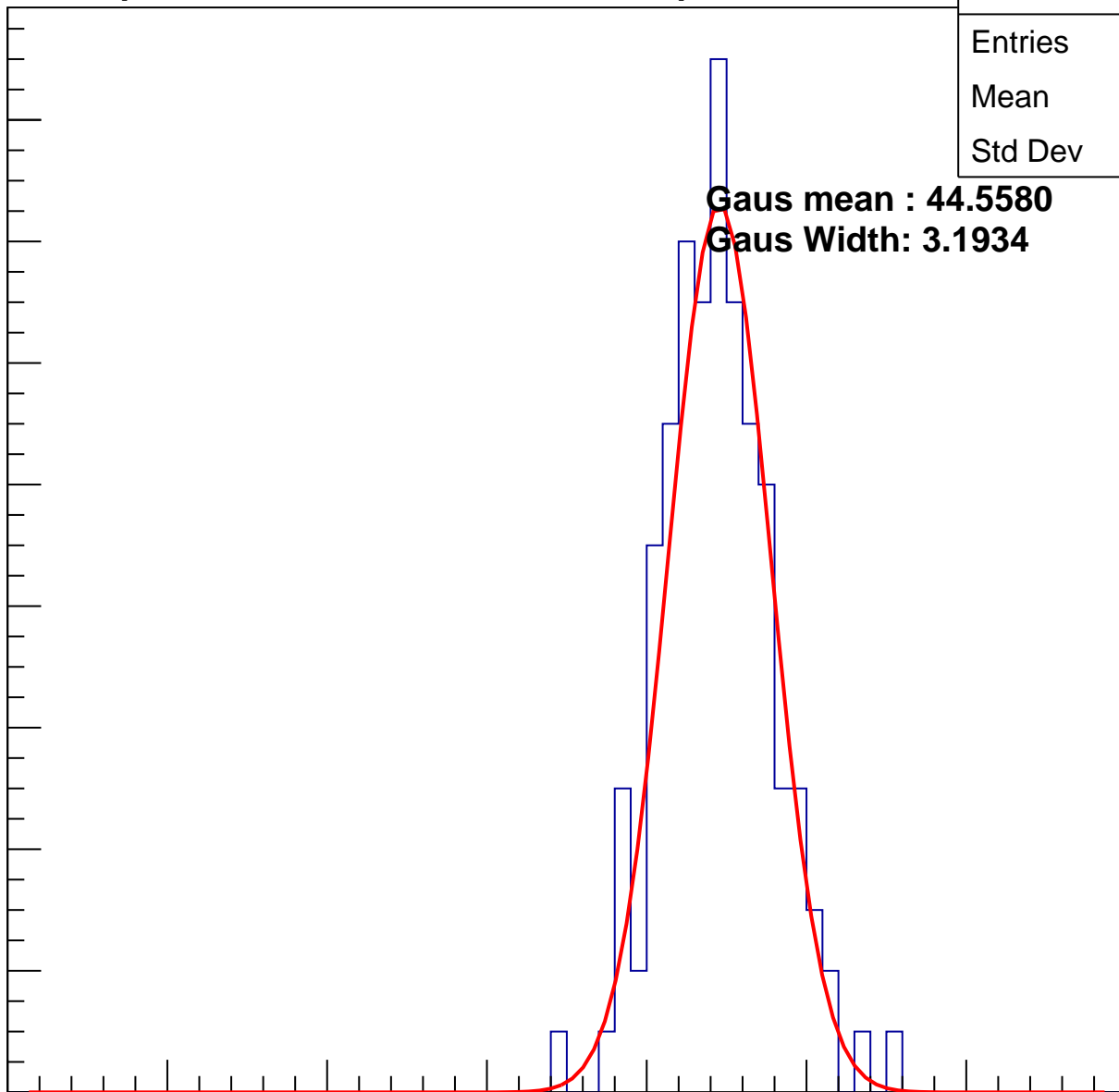
40

50

60

70

ampl



B1L001S, U21-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

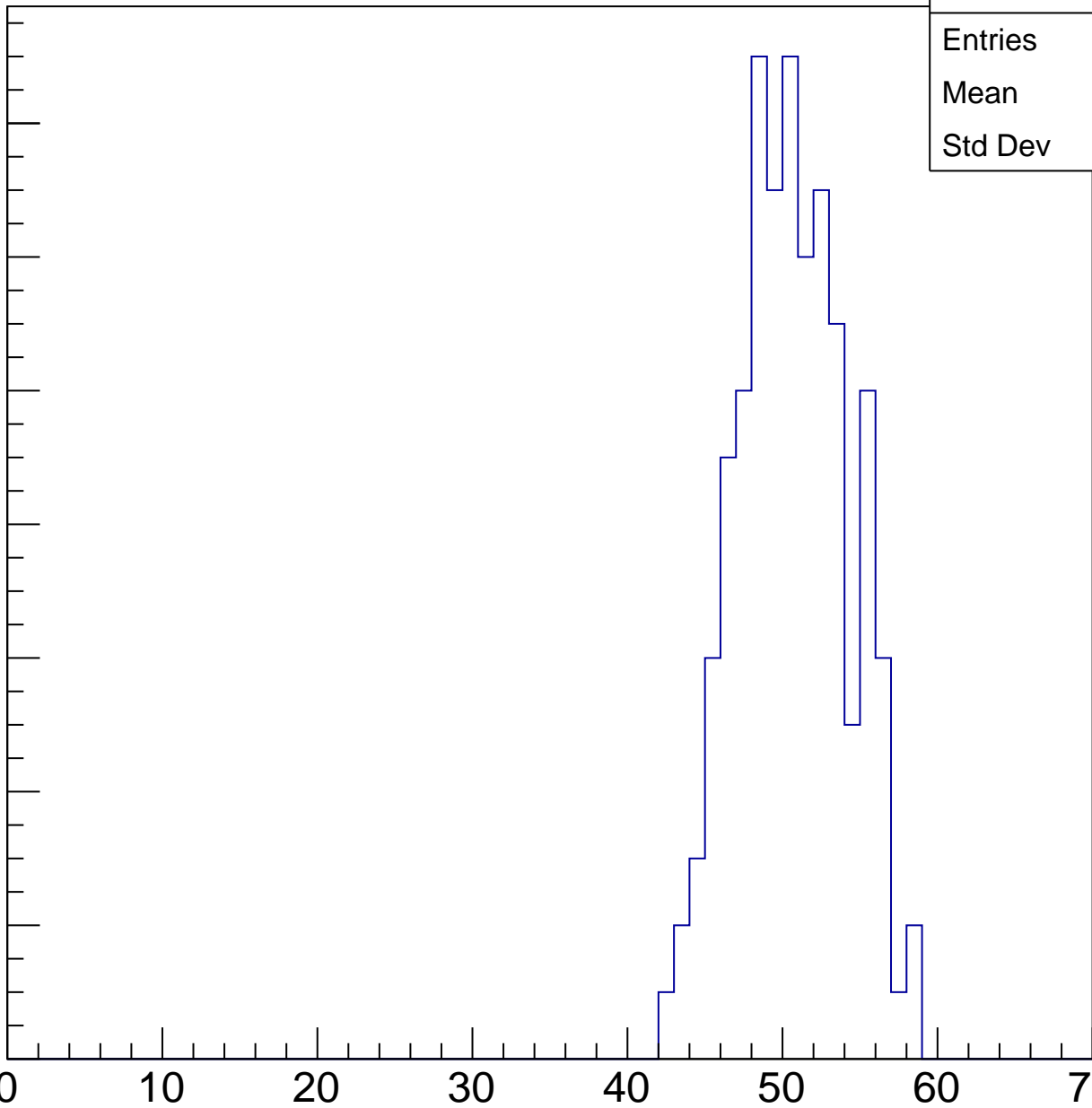
Entries	134
Mean	50.16
Std Dev	3.466

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

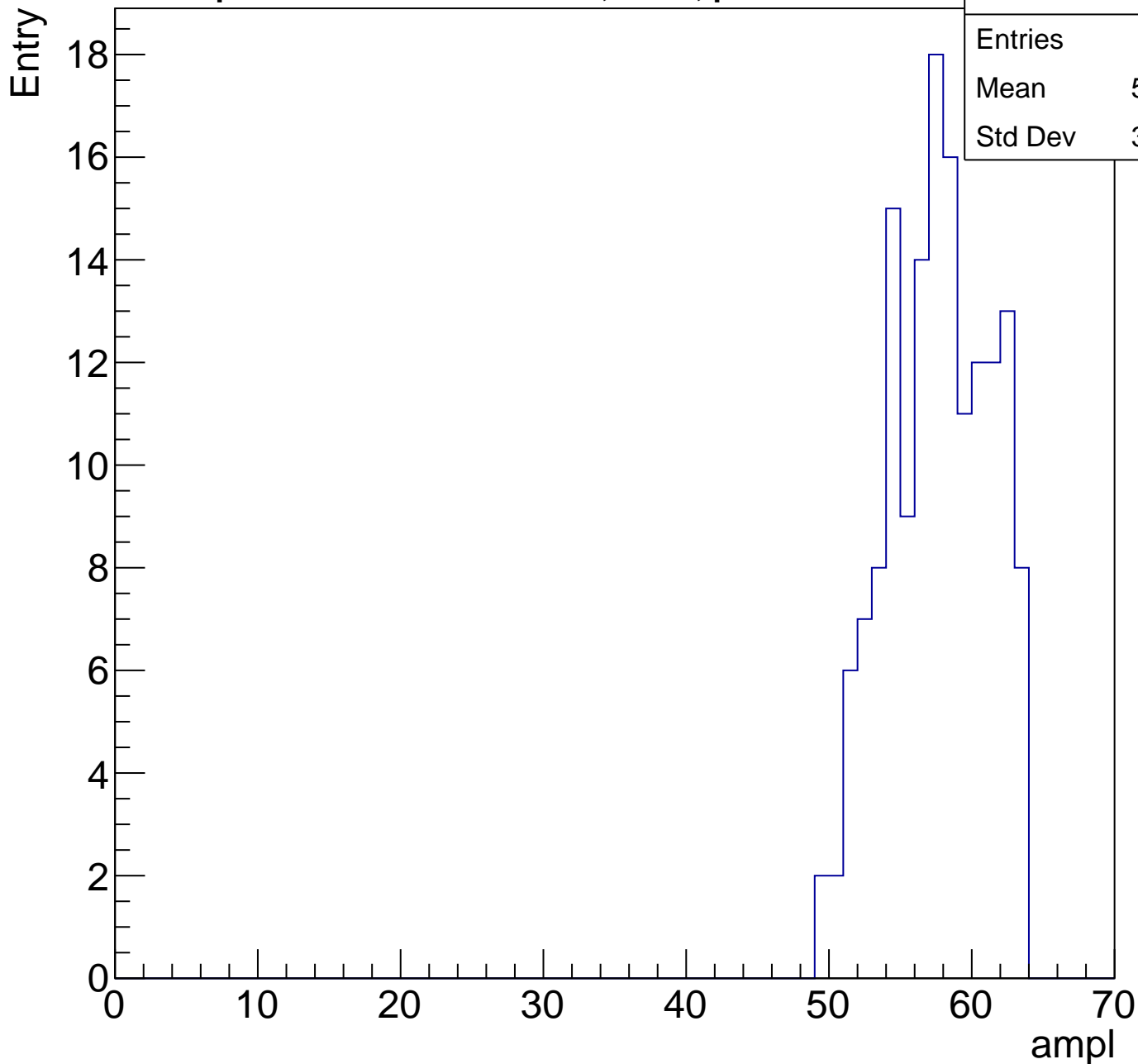
ampl



B1L001S, U21-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

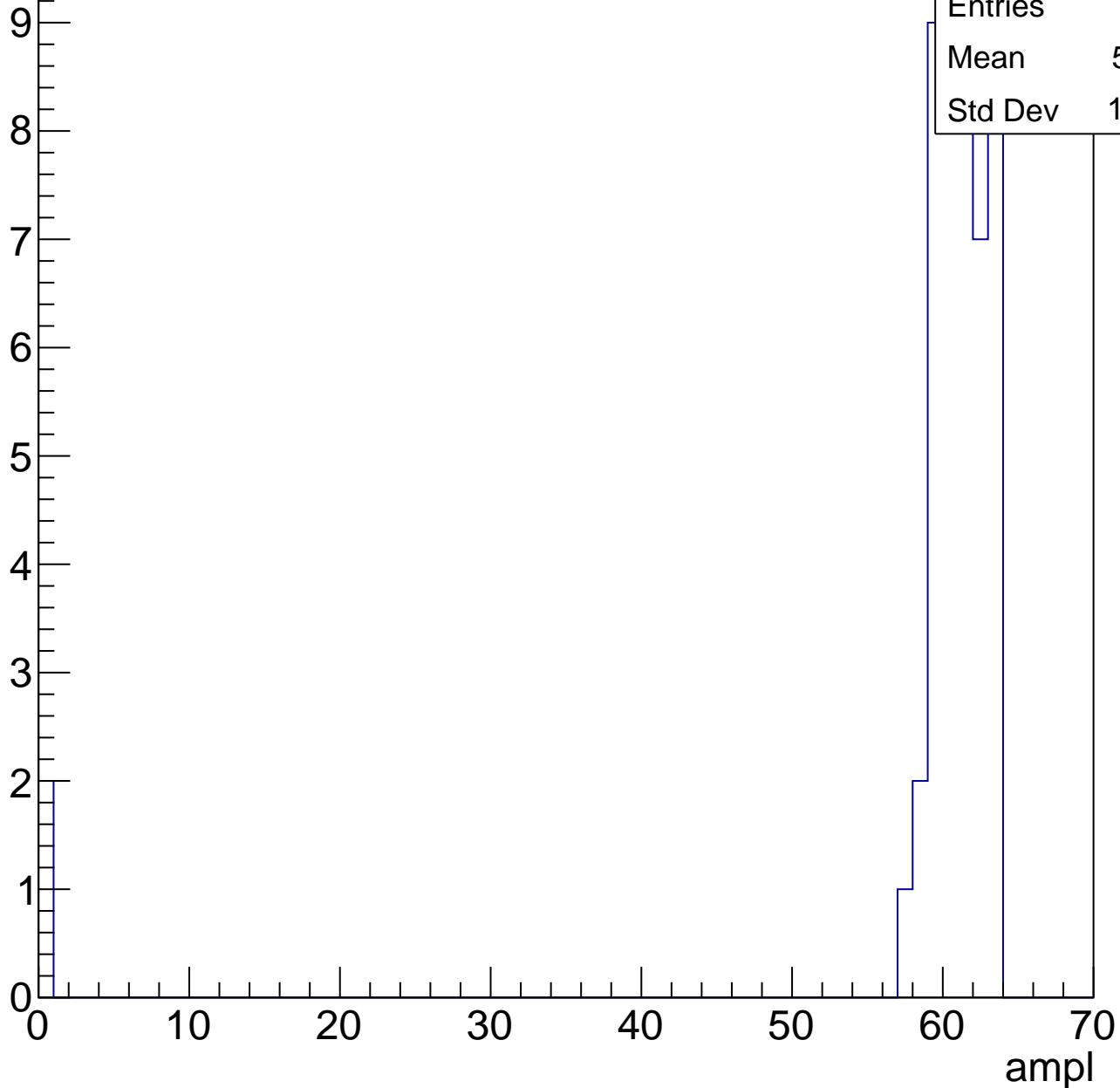
Entries	153
Mean	57.16
Std Dev	3.523



B1L001S, U21-ch111, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch112, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	29.18
Std Dev	2.949

Gaus mean : 29.7088

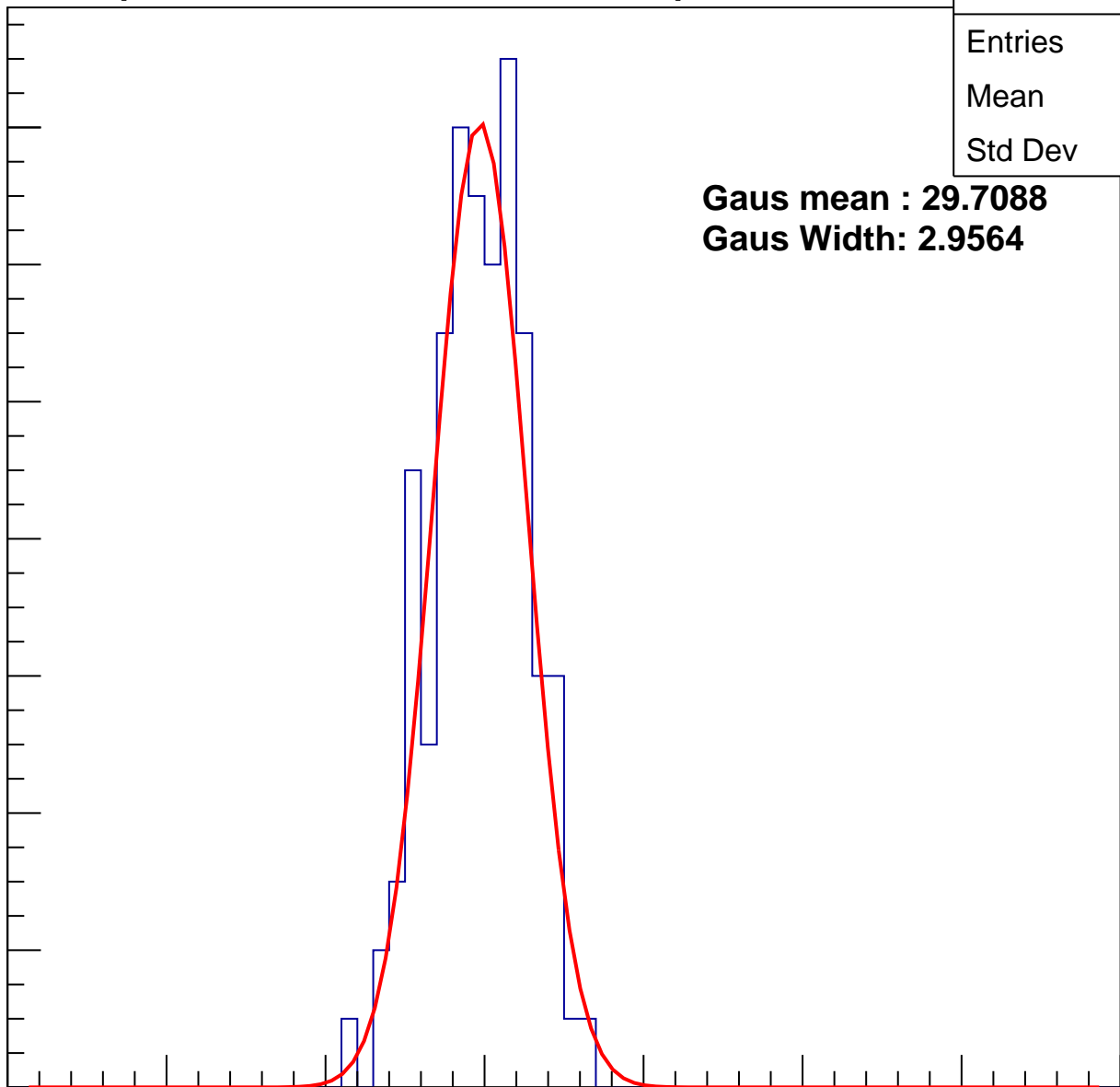
Gaus Width: 2.9564

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch112, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	35.26
Std Dev	3.187

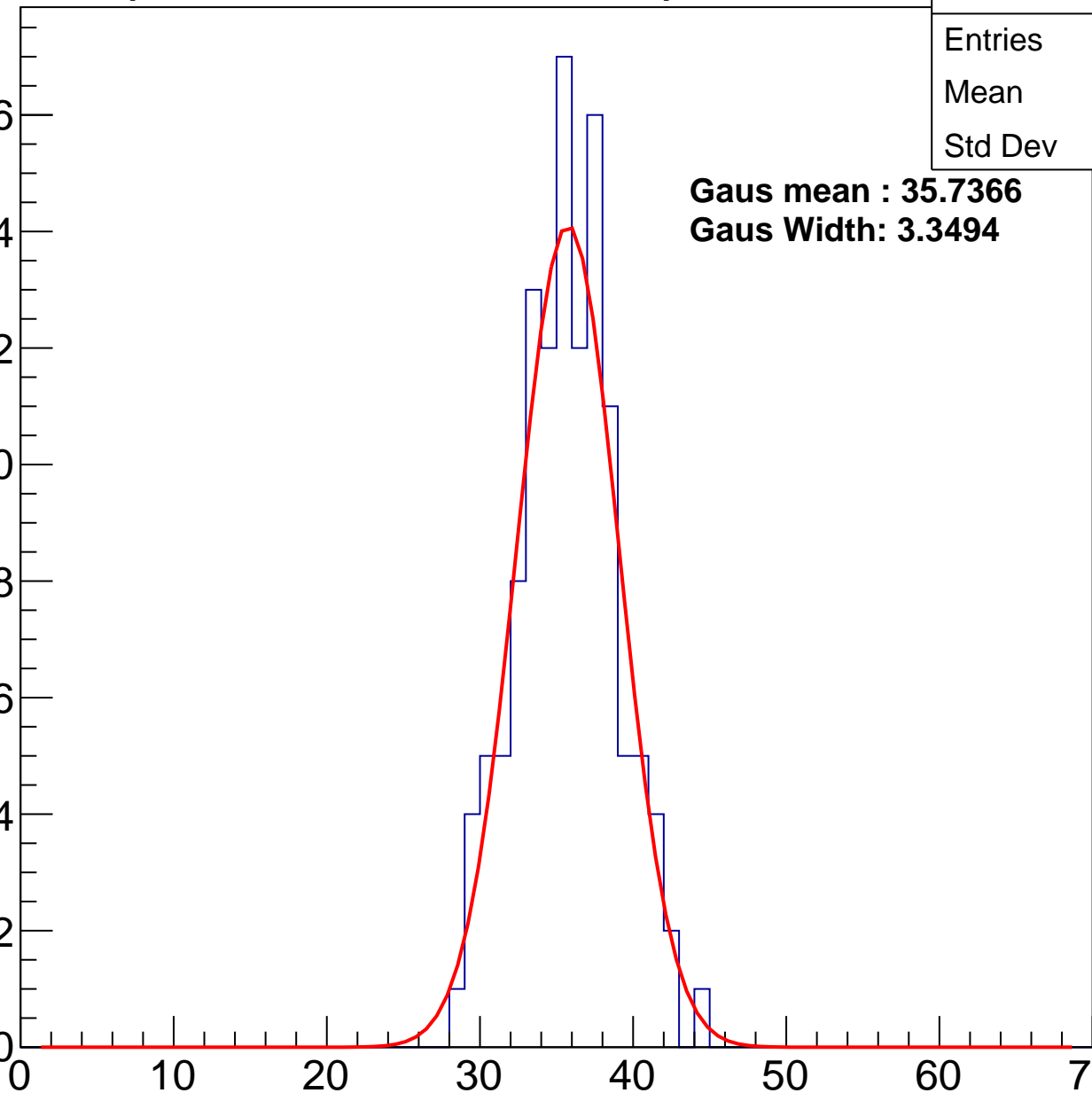
Gaus mean : 35.7366

Gaus Width: 3.3494

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

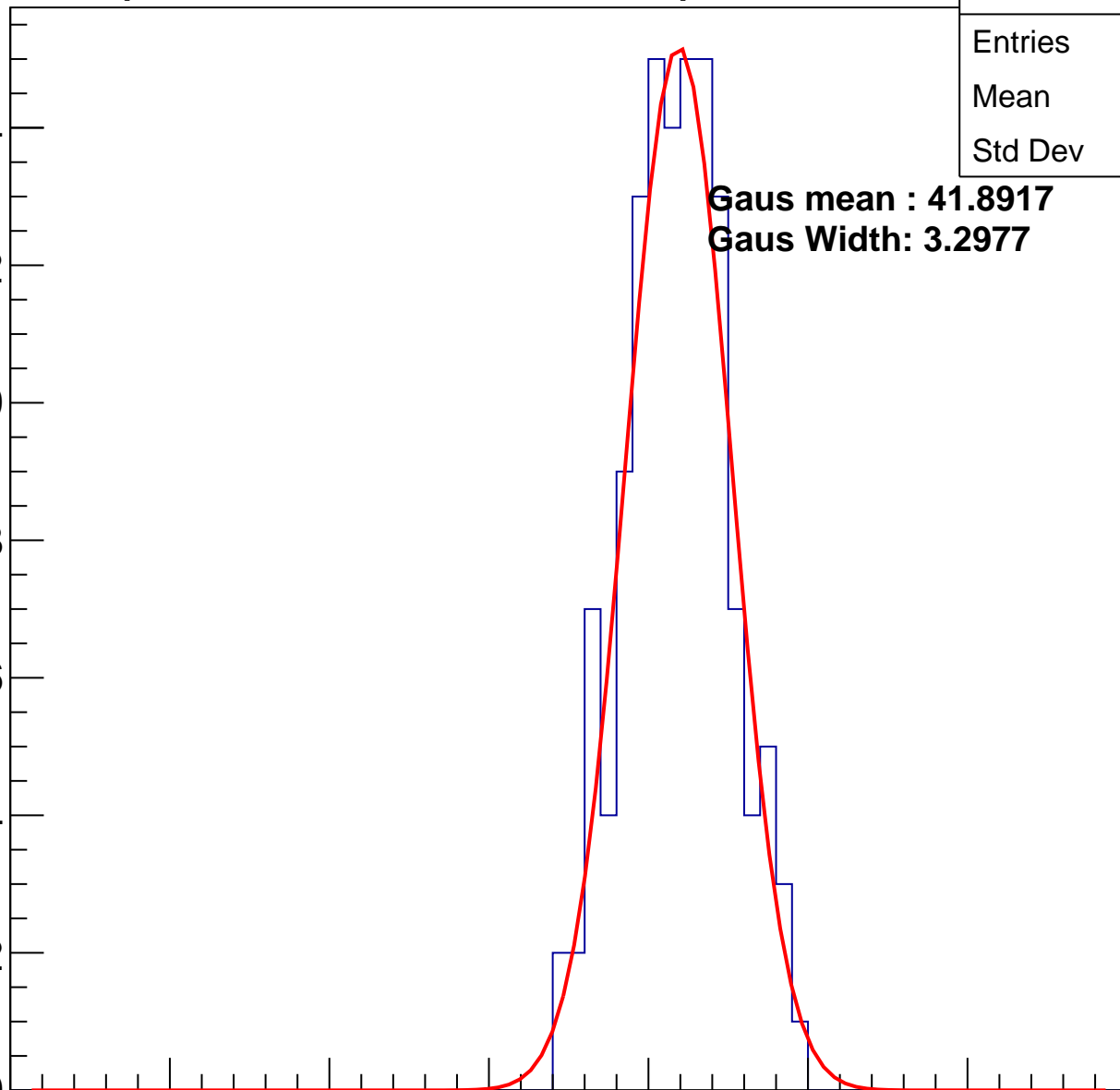
70

ampl

Entries	129
Mean	41.36
Std Dev	3.218

Gaus mean : 41.8917

Gaus Width: 3.2977

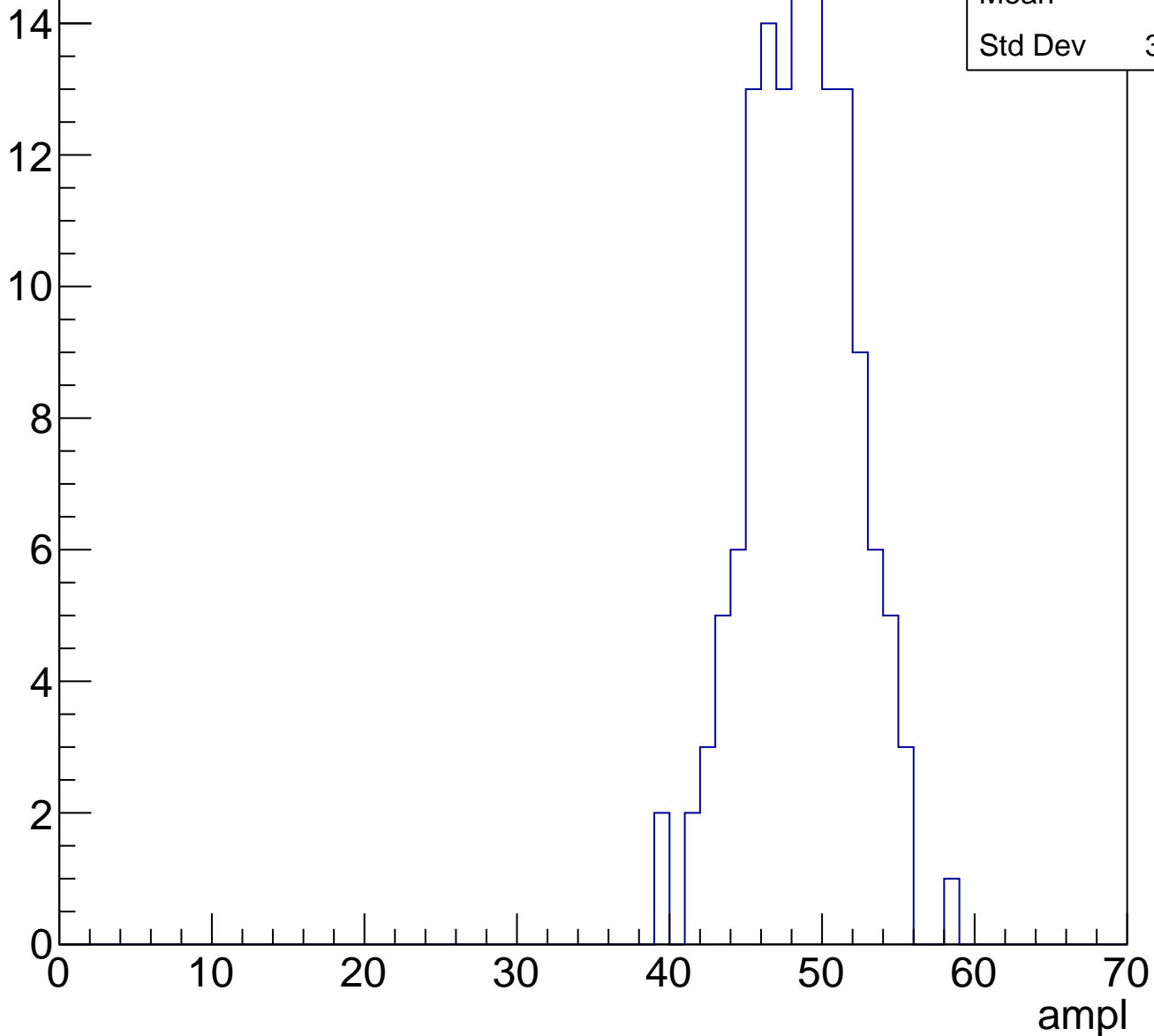


B1L001S, U21-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	48.2
Std Dev	3.483

Entry



B1L001S, U21-ch112, adc4

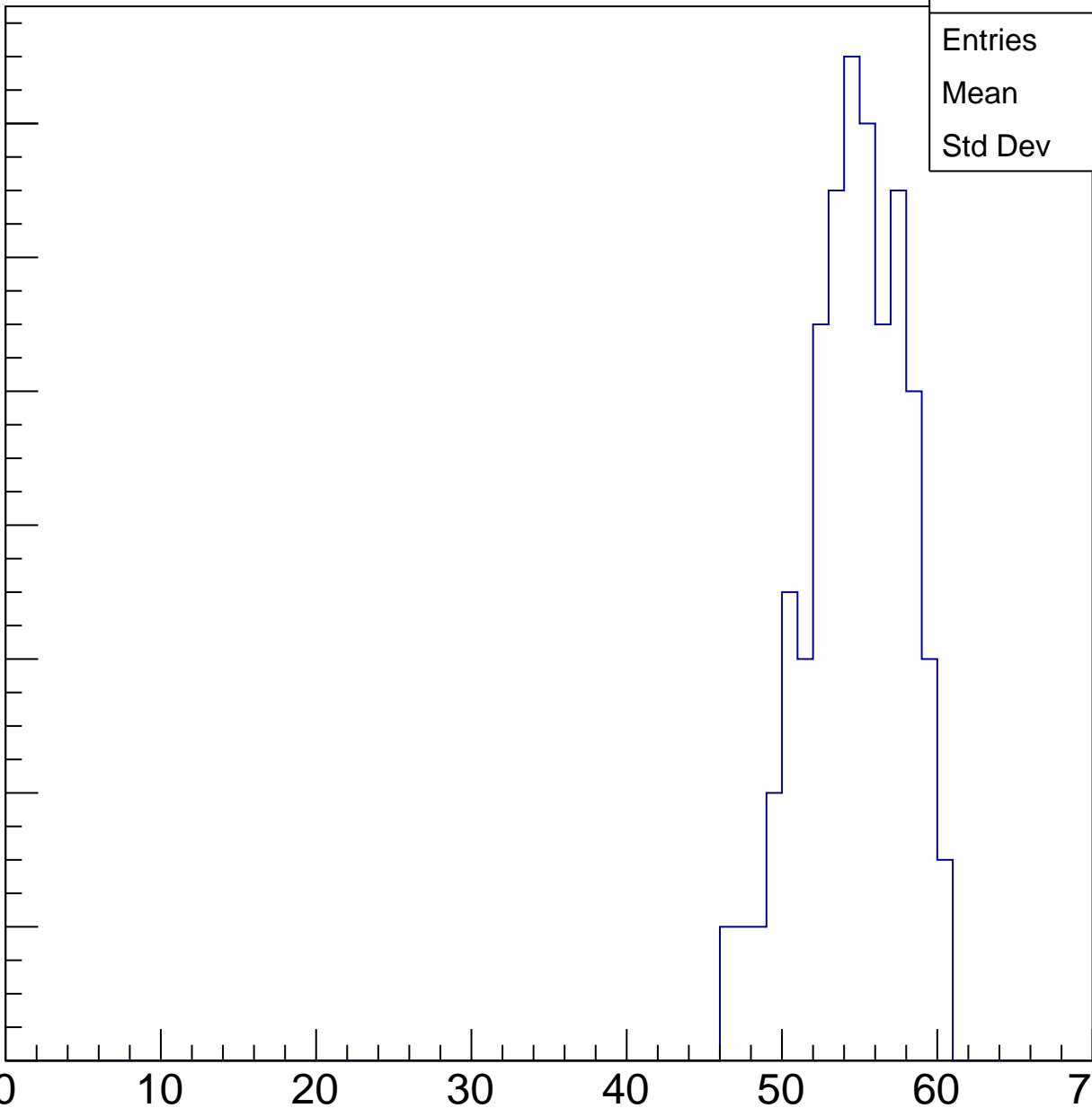
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	54.17
Std Dev	3.19

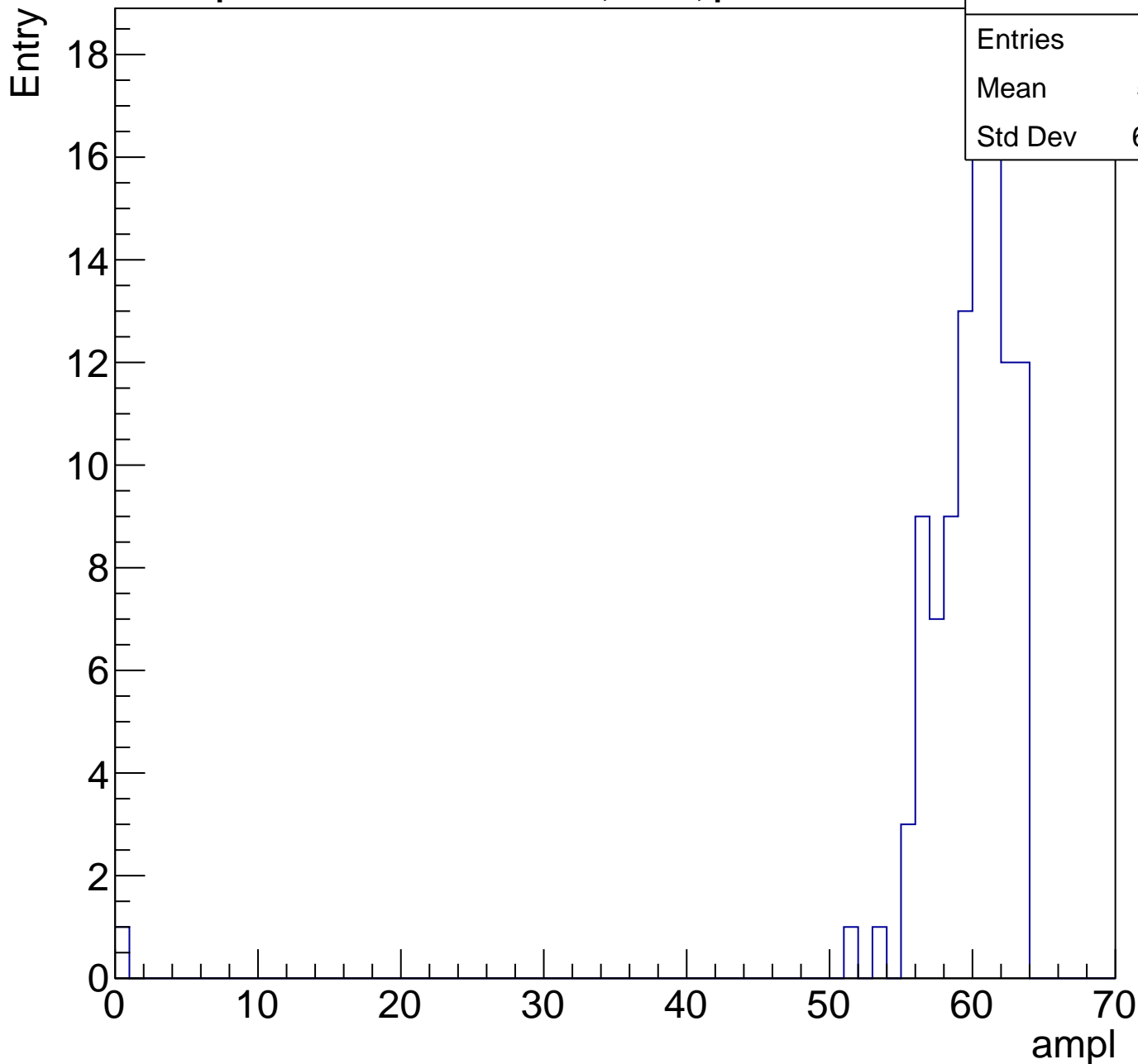
ampl



B1L001S, U21-ch112, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	59.01
Std Dev	6.367

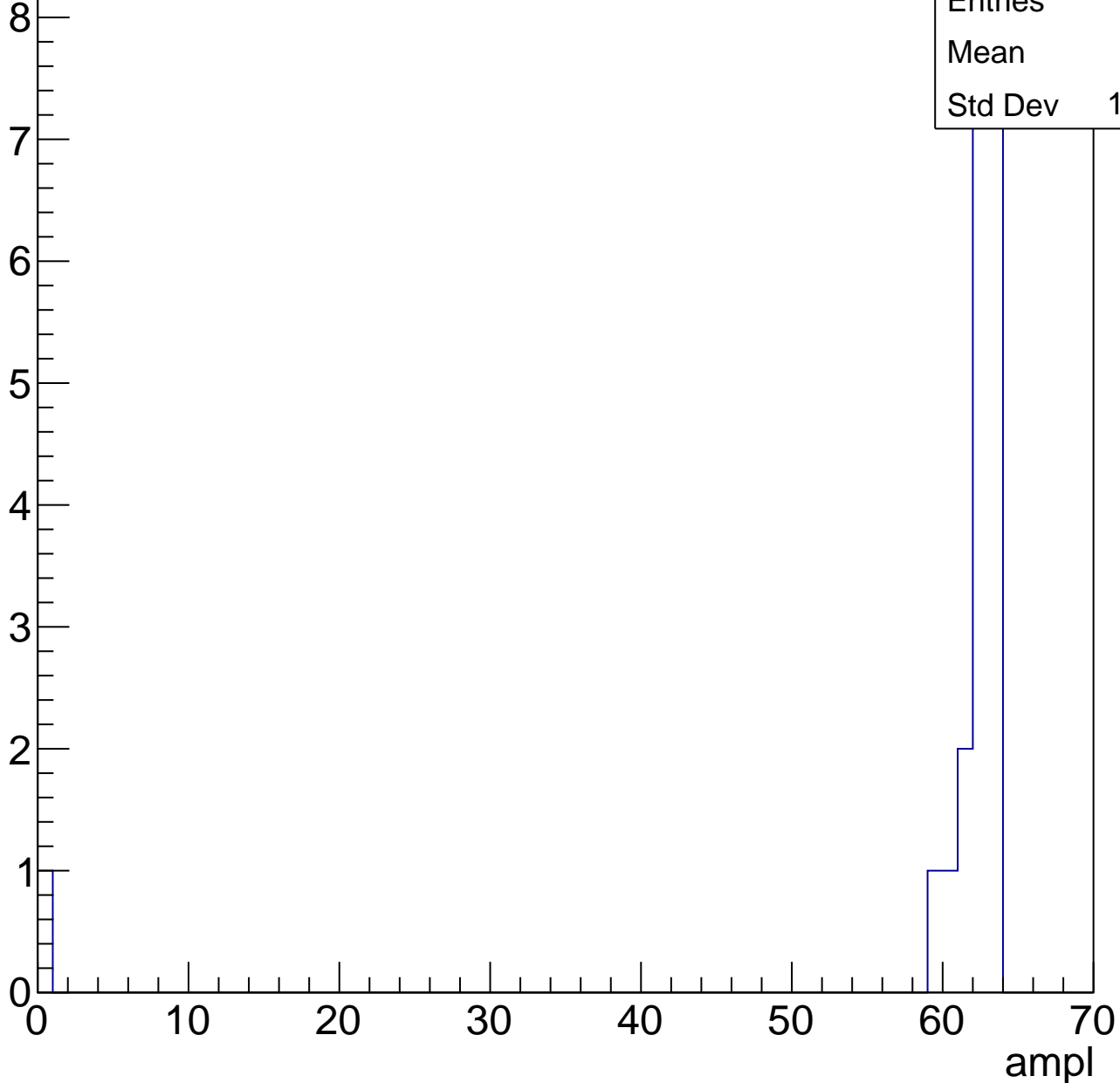


B1L001S, U21-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	59.1
Std Dev	13.26



B1L001S, U21-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

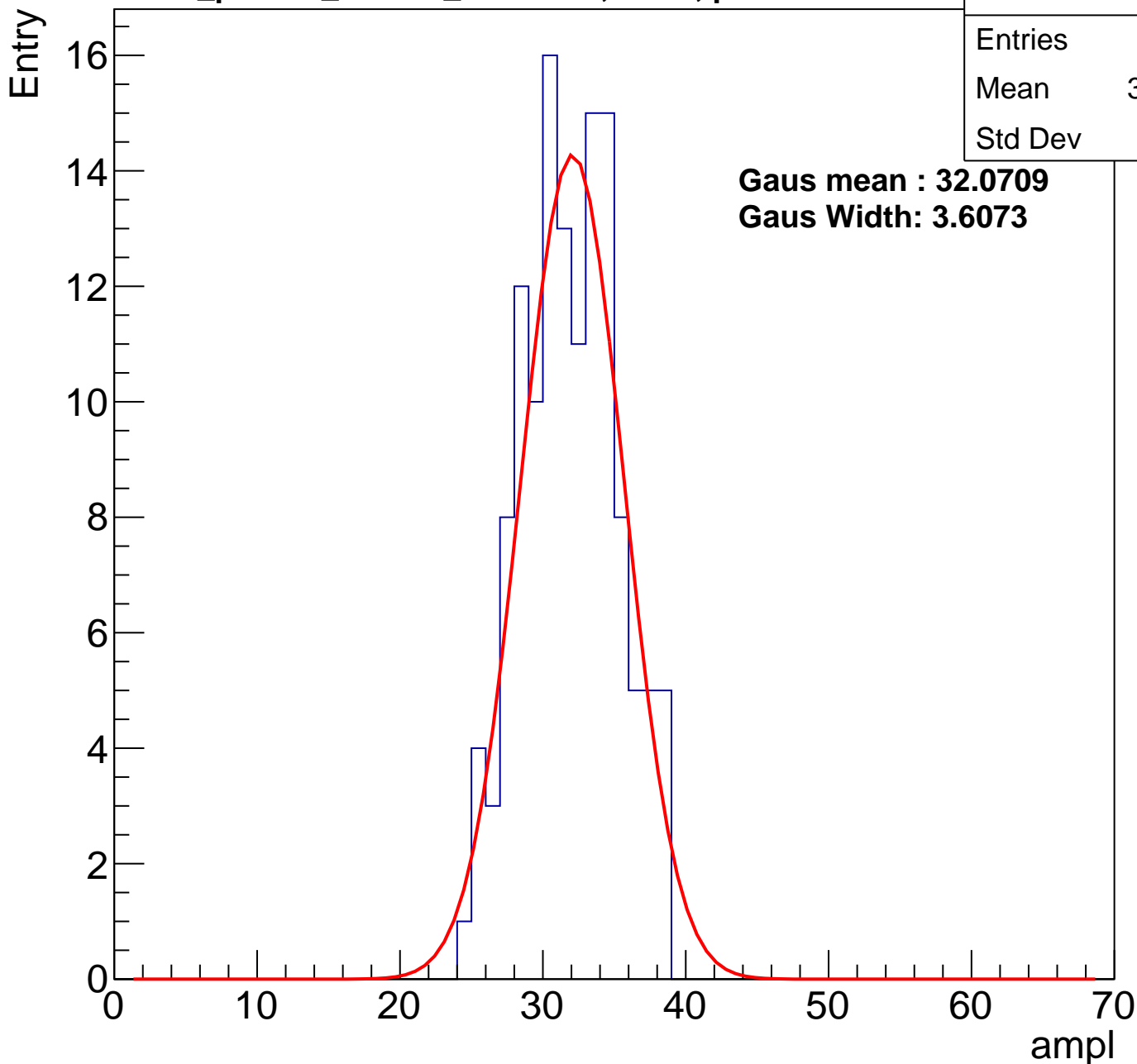
B1L001S, U21-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	31.44
Std Dev	3.29

Gaus mean : 32.0709

Gaus Width: 3.6073



B1L001S, U21-ch113, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	38.13
Std Dev	3.428

Gaus mean : 38.7756

Gaus Width: 3.3879

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

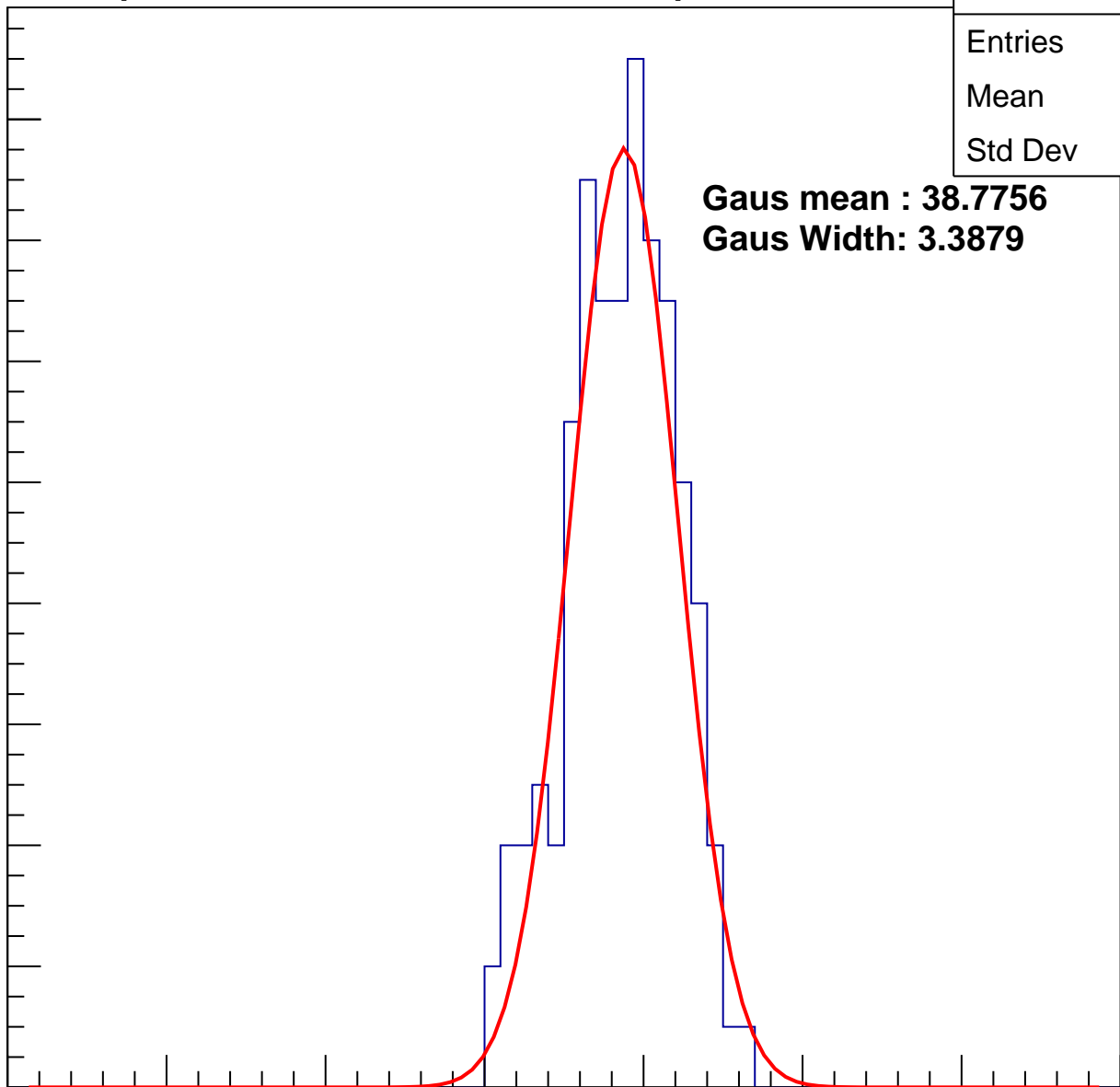
40

50

60

70

ampl



B1L001S, U21-ch113, adc2

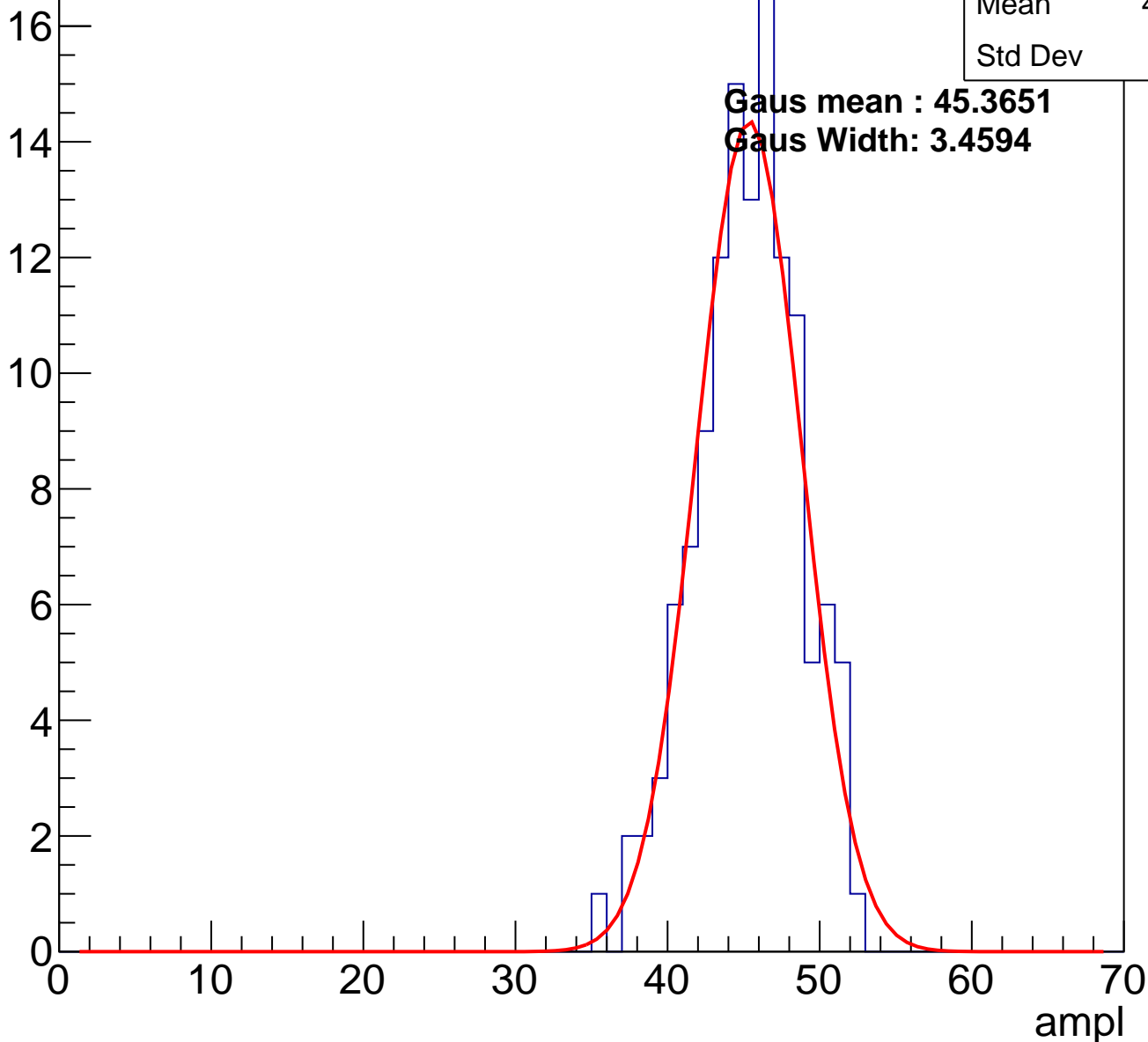
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	127
Mean	44.83
Std Dev	3.39

Gaus mean : 45.3651

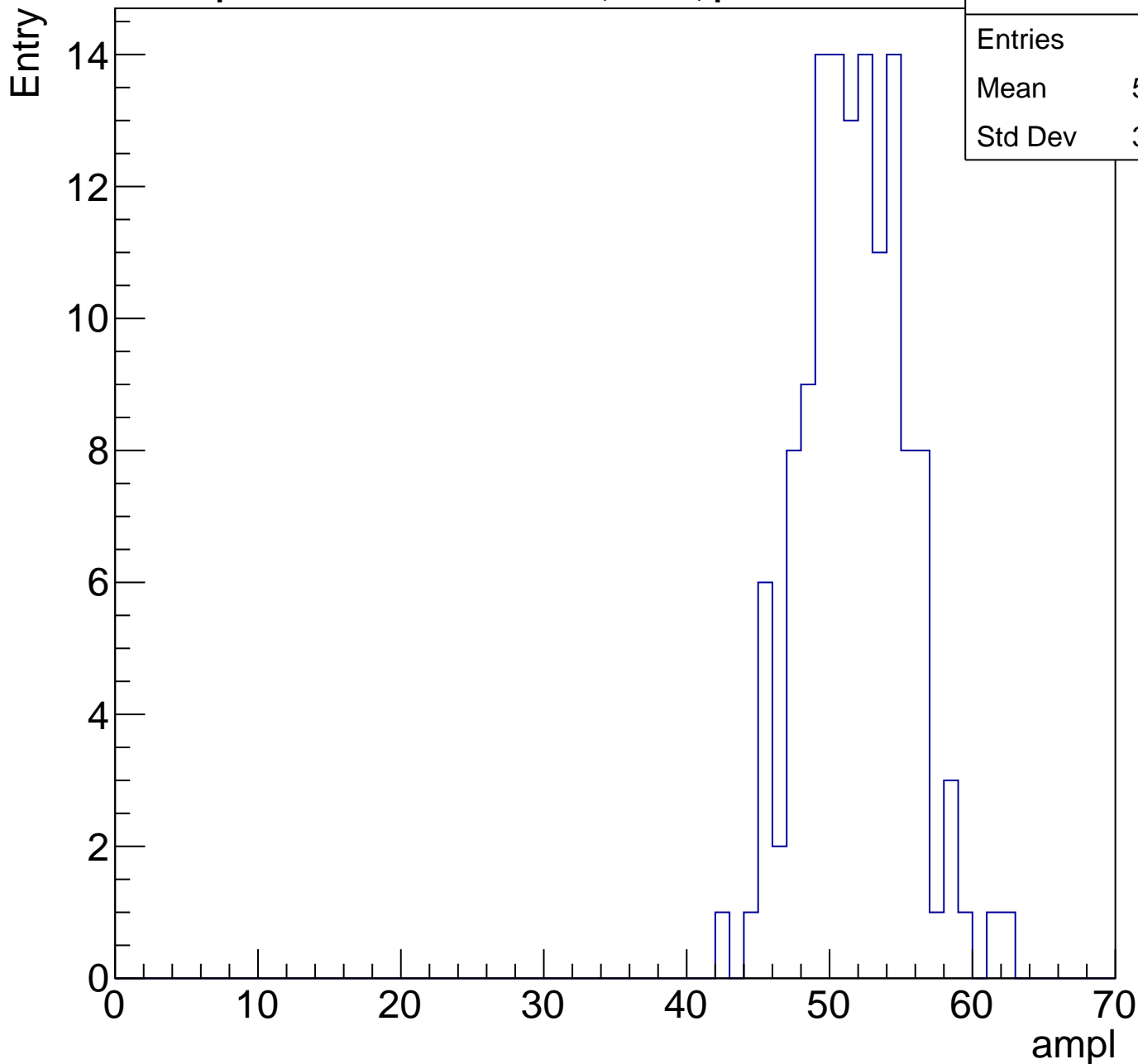
Gaus Width: 3.4594



B1L001S, U21-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	51.33
Std Dev	3.553

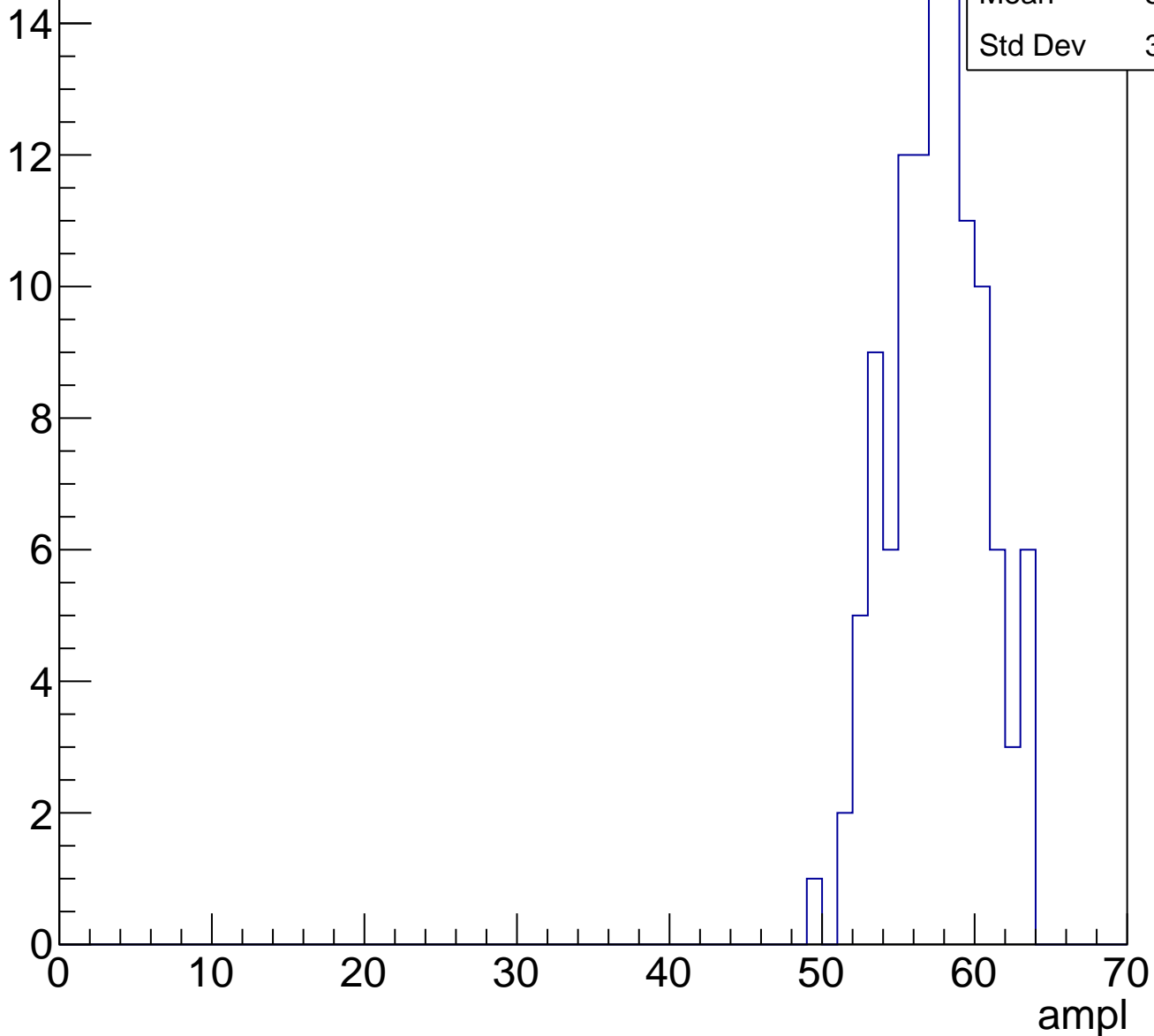


B1L001S, U21-ch113, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	57.06
Std Dev	3.055

Entry



B1L001S, U21-ch113, adc5

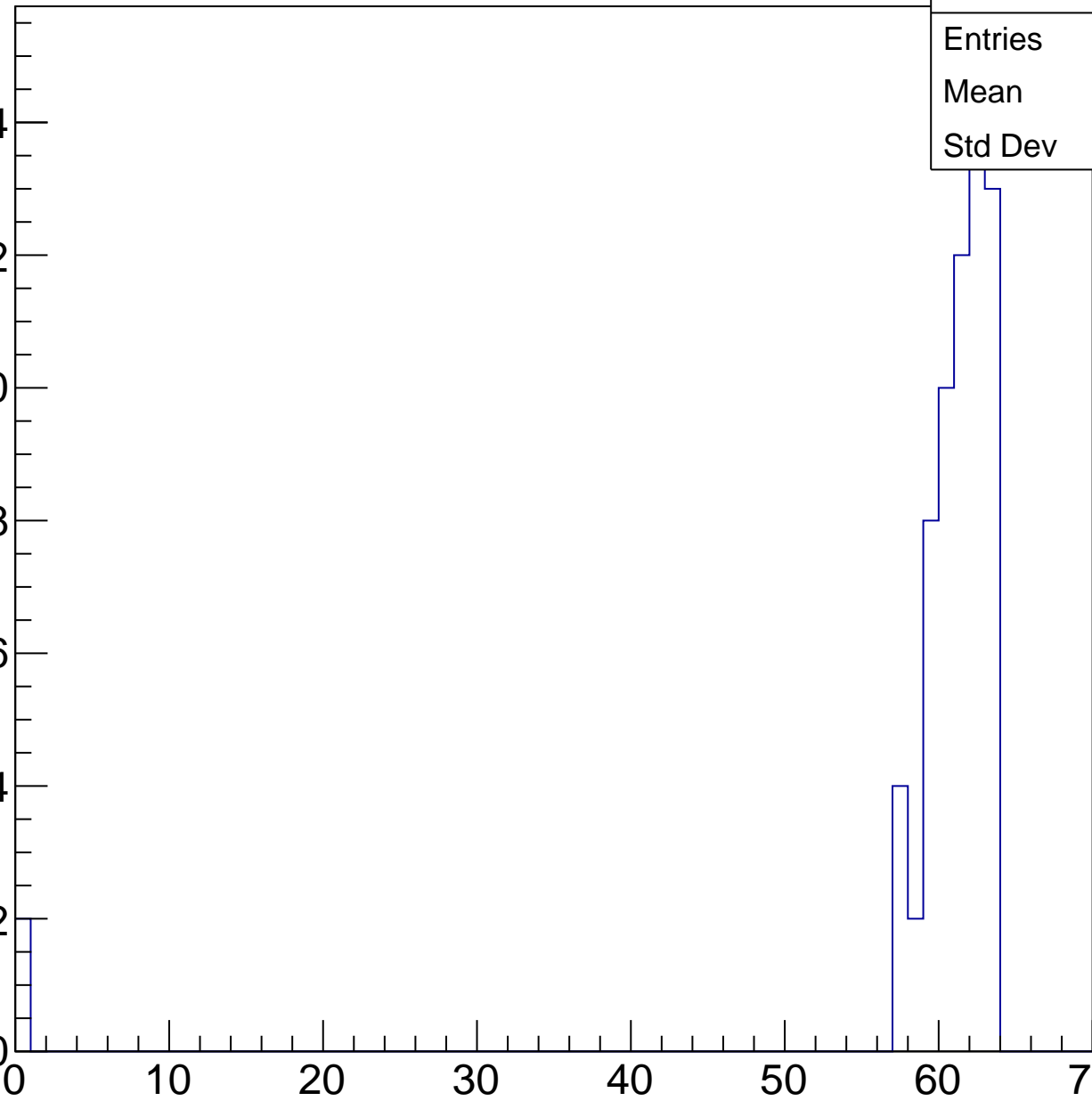
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	66
Mean	59.05
Std Dev	10.58

ampl



B1L001S, U21-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch114, adc0

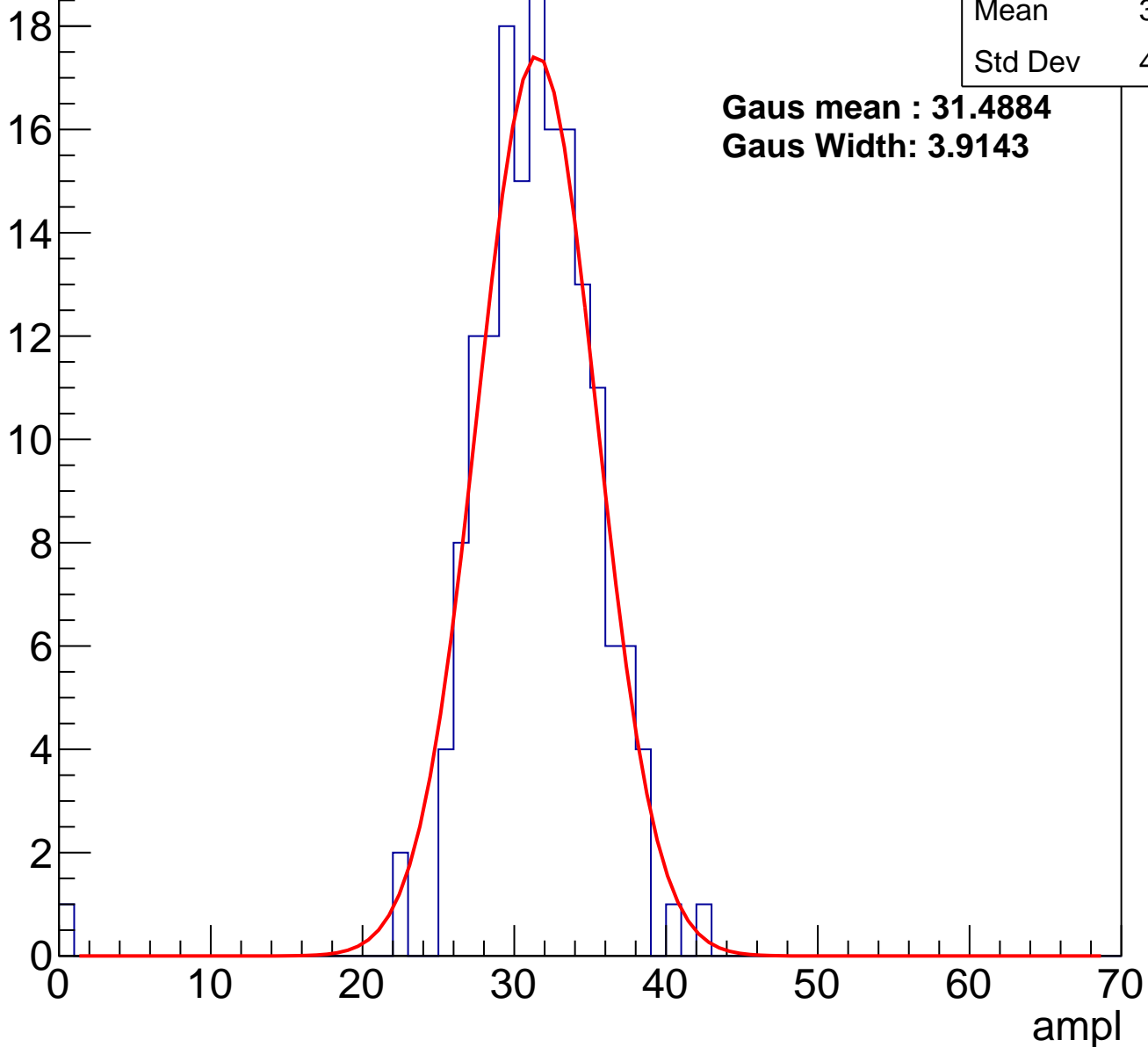
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	30.98
Std Dev	4.258

Gaus mean : 31.4884

Gaus Width: 3.9143

Entry



B1L001S, U21-ch114, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

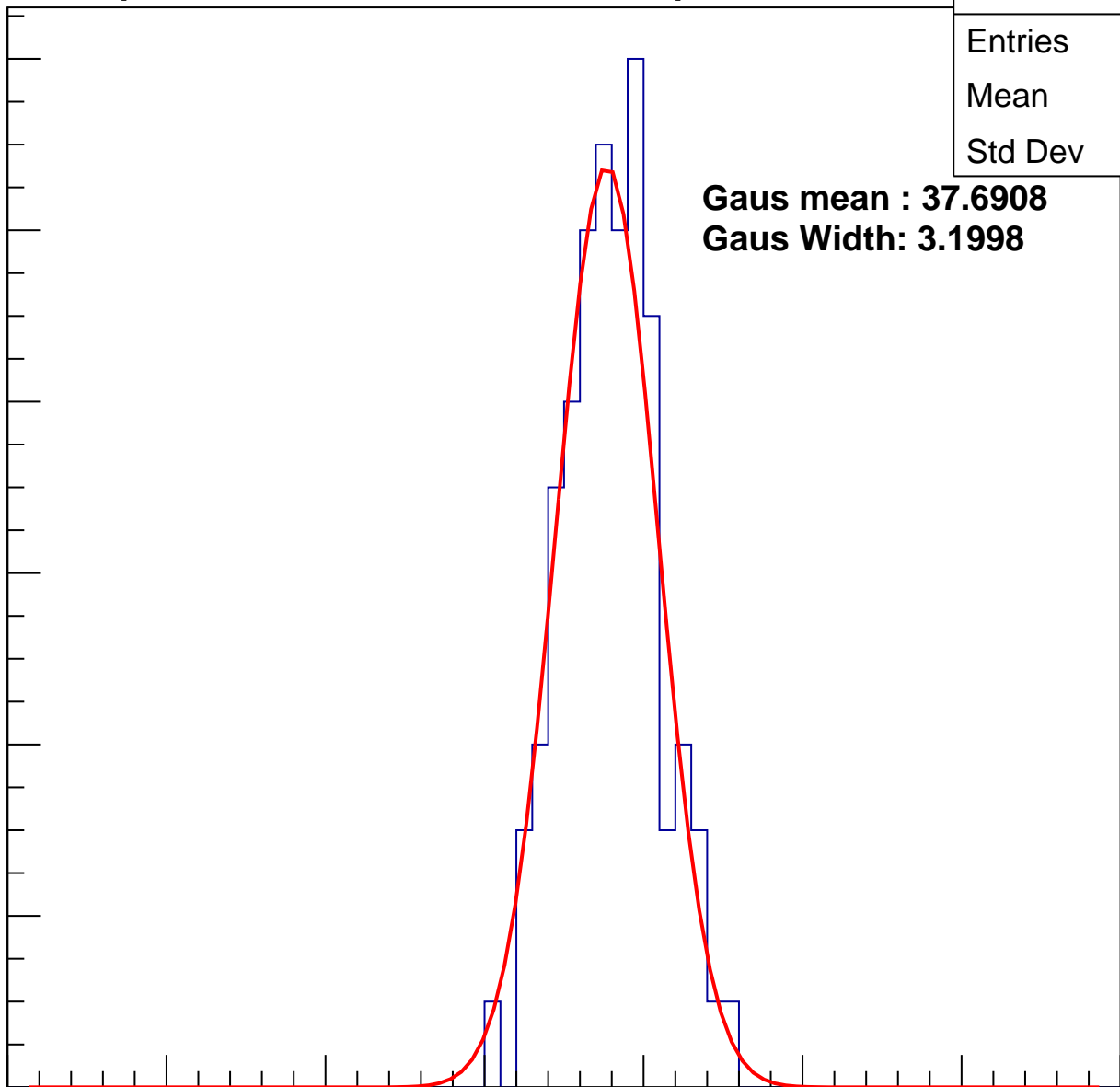
Entries	87
Mean	37.47
Std Dev	2.997

Gaus mean : 37.6908

Gaus Width: 3.1998

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	43.24
Std Dev	3.382

Gaus mean : 43.6572

Gaus Width: 3.5368

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

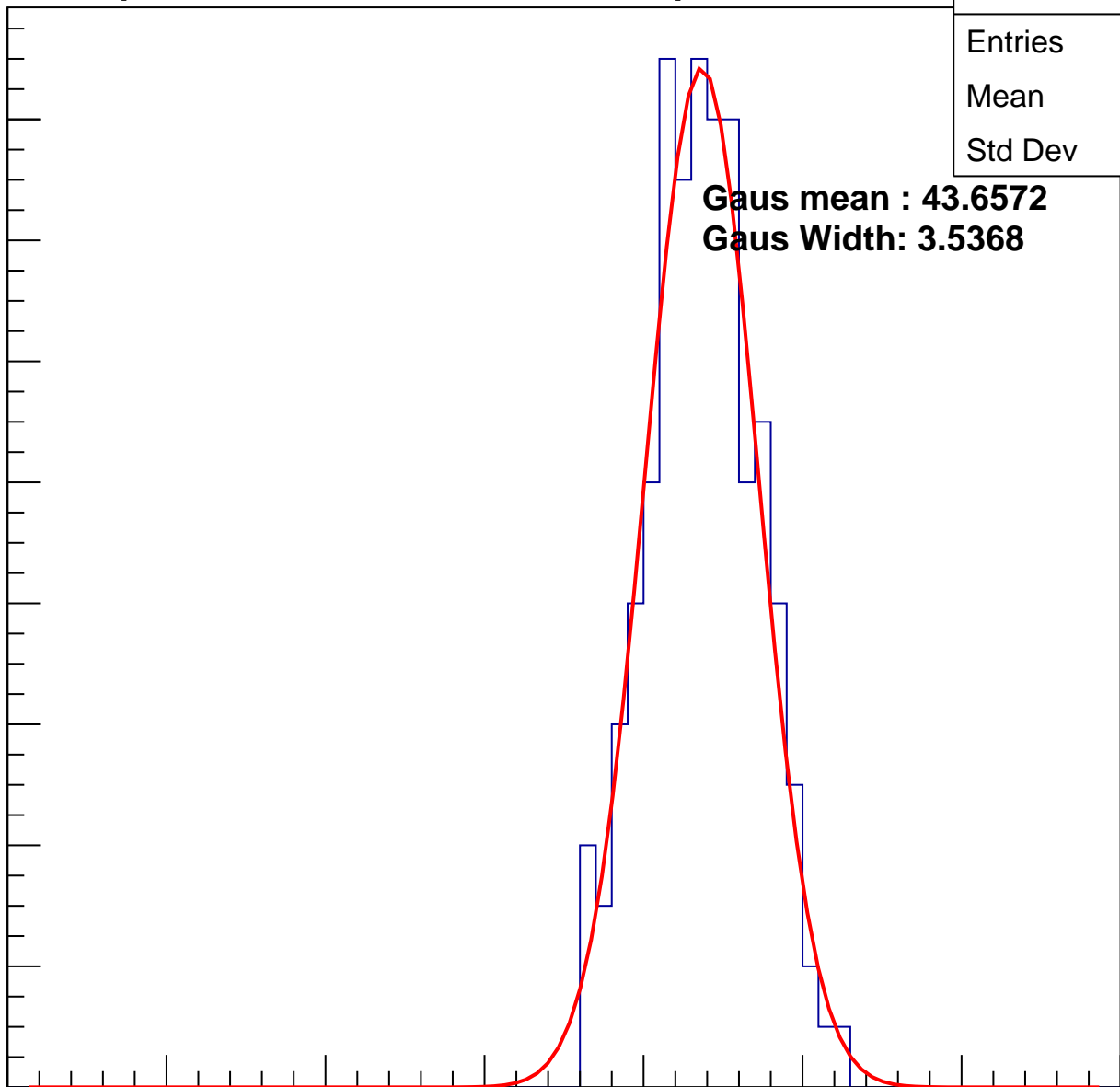
40

50

60

70

ampl



B1L001S, U21-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

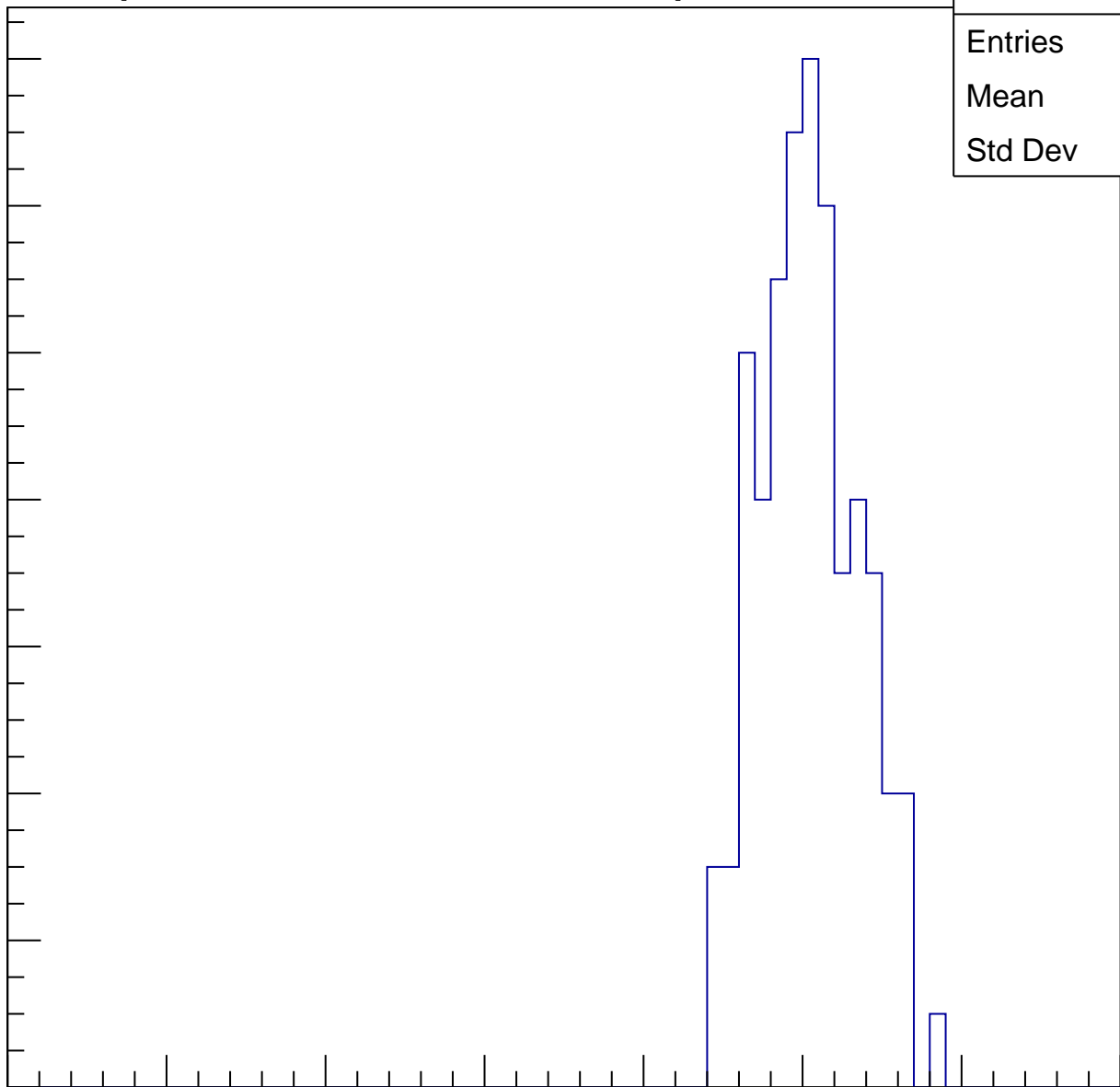
Entries	105
Mean	49.98
Std Dev	3.092

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

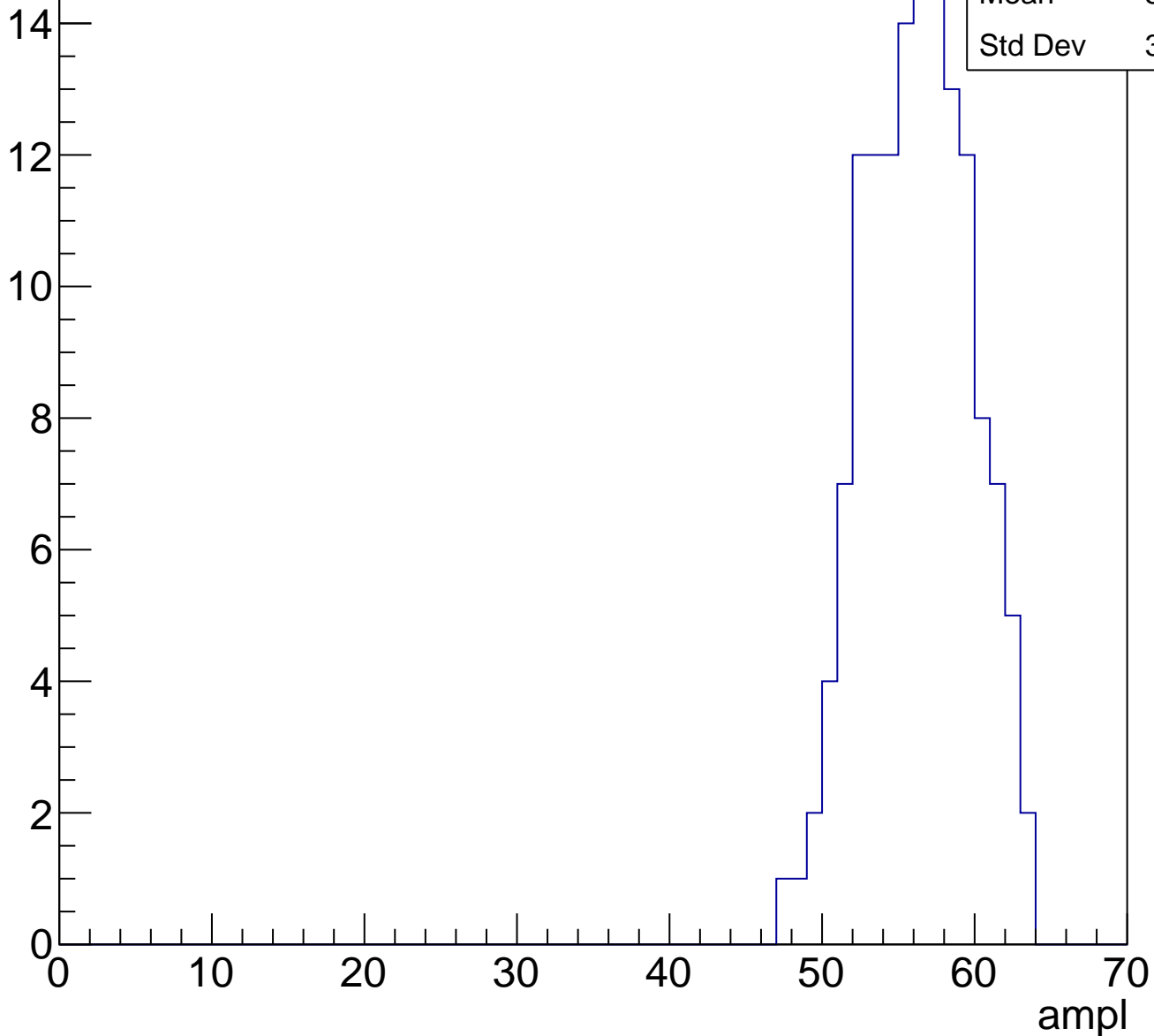


B1L001S, U21-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	55.83
Std Dev	3.435

Entry



B1L001S, U21-ch114, adc5

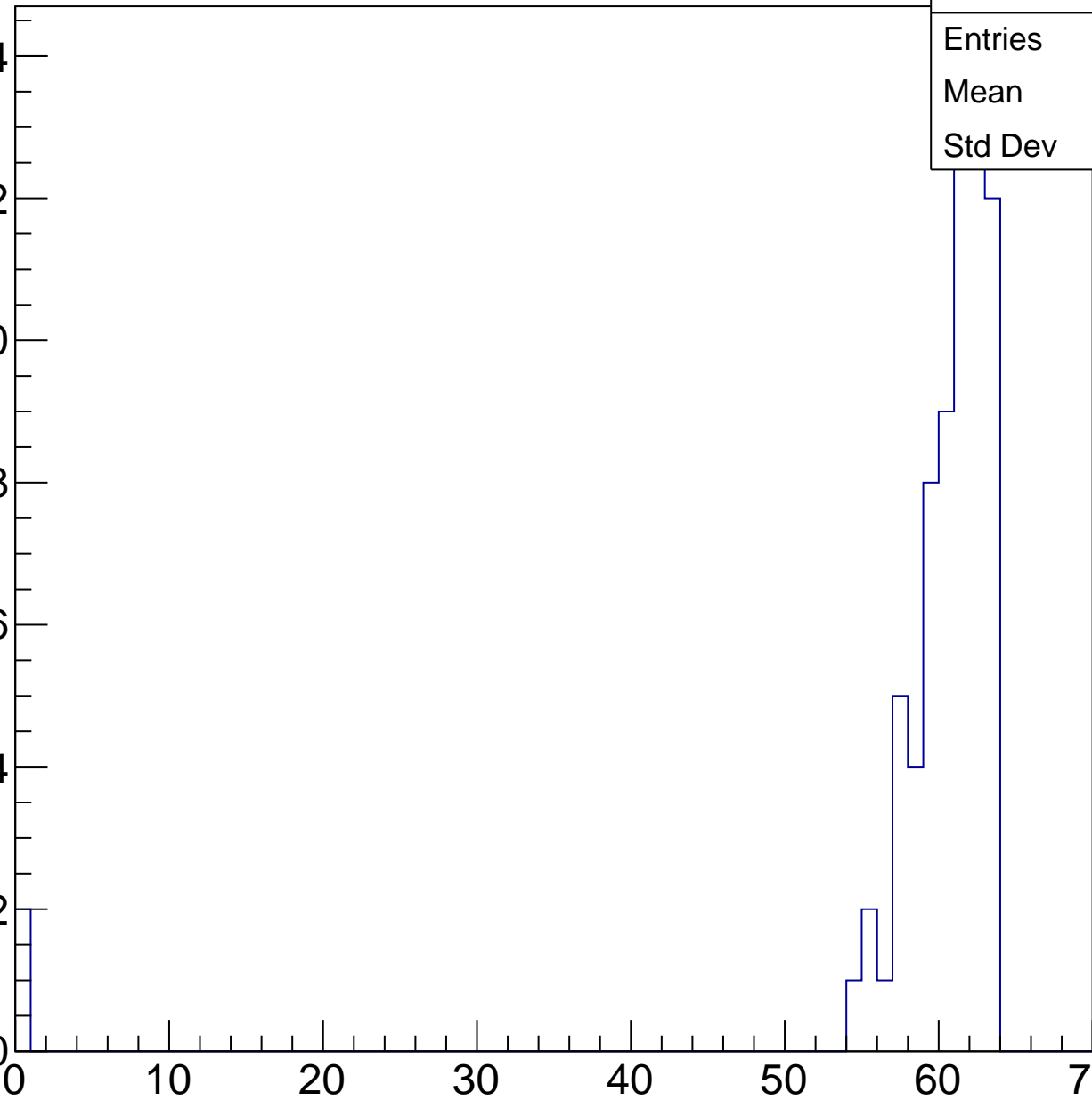
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.68
Std Dev	10.23

ampl

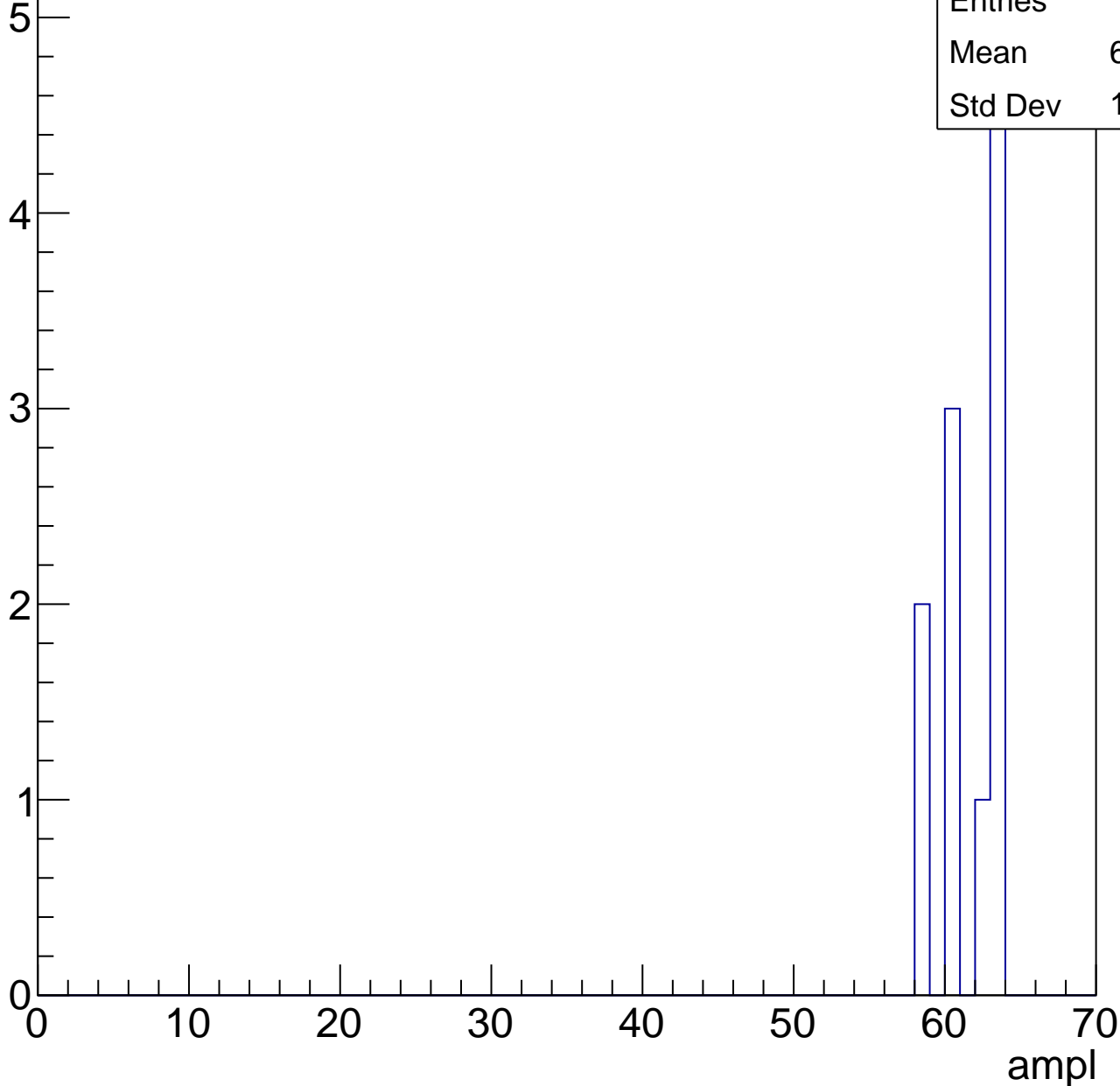


B1L001S, U21-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

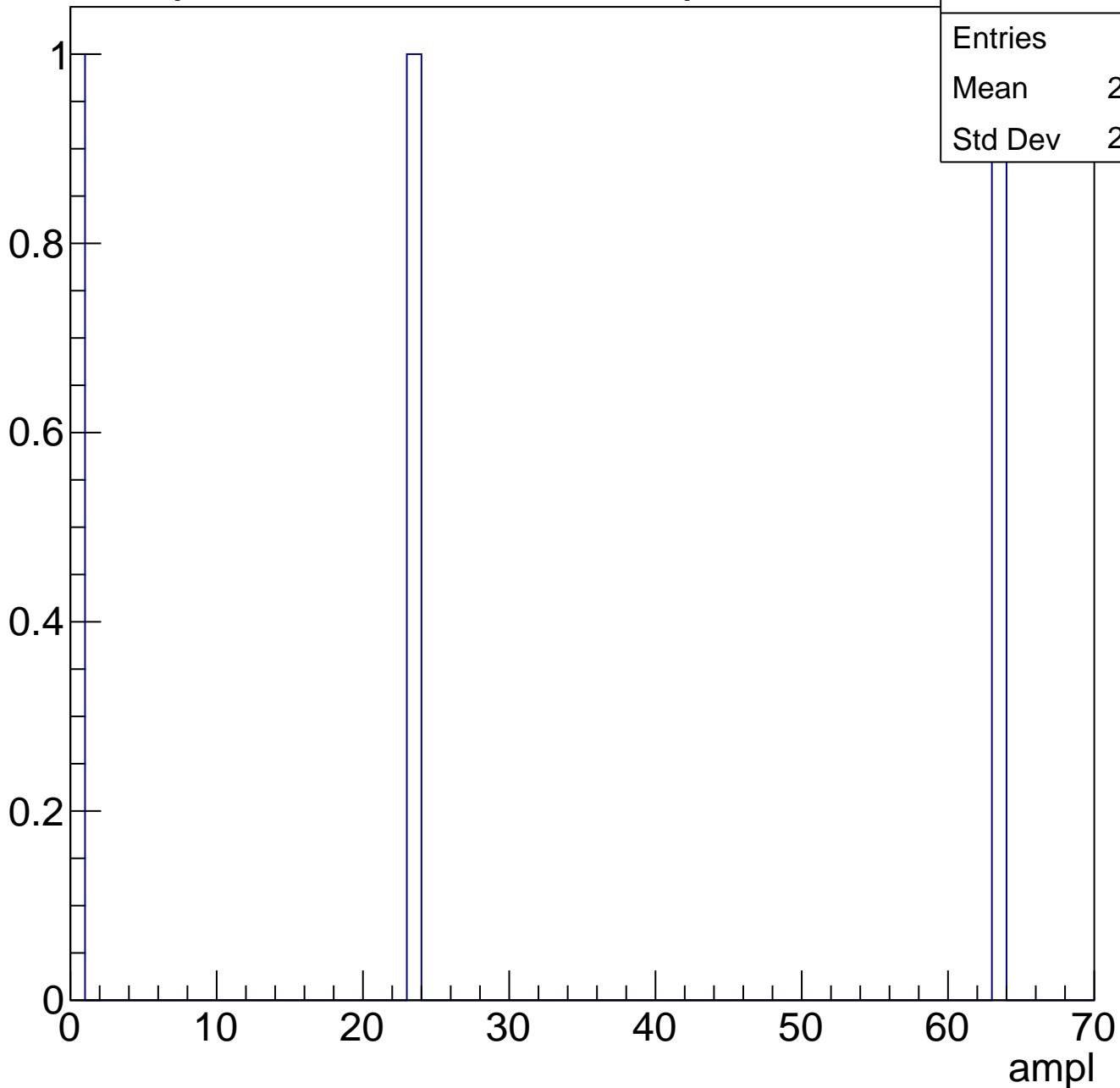
Entries	11
Mean	61.18
Std Dev	1.946



B1L001S, U21-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B1L001S, U21-ch115, adc0

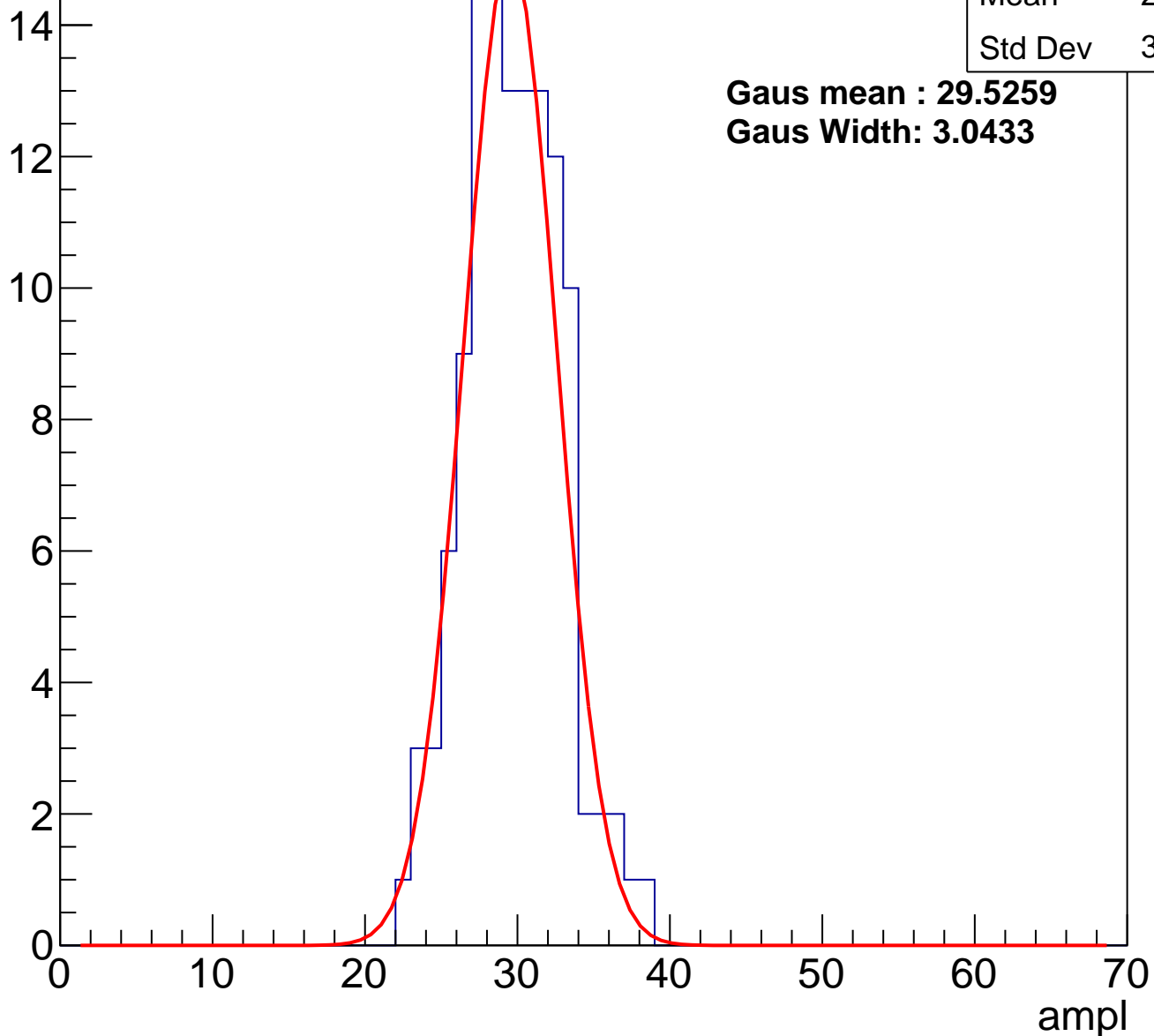
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	29.26
Std Dev	3.114

Gaus mean : 29.5259

Gaus Width: 3.0433

Entry



B1L001S, U21-ch115, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	35.44
Std Dev	3.518

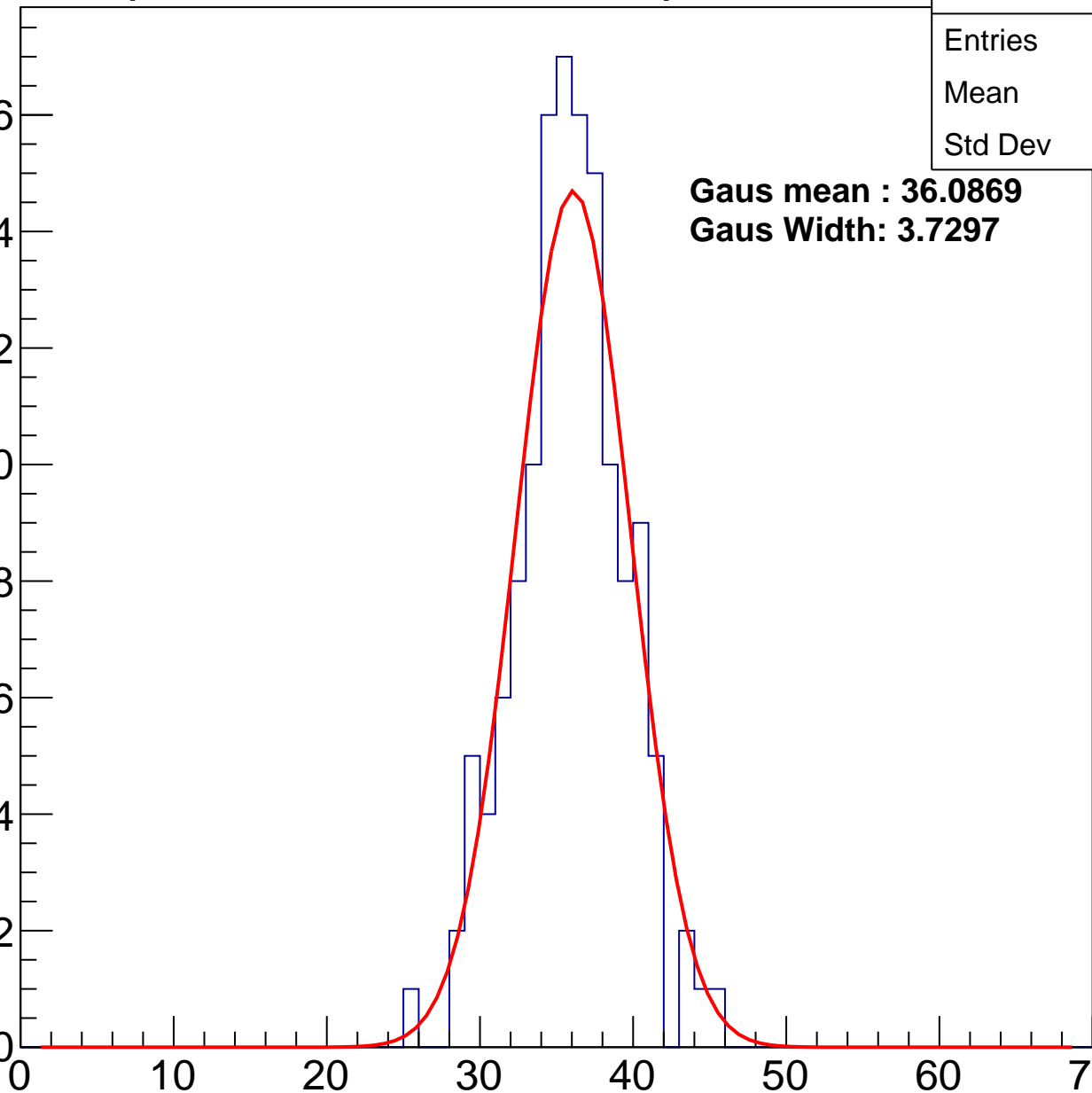
Gaus mean : 36.0869

Gaus Width: 3.7297

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch115, adc2

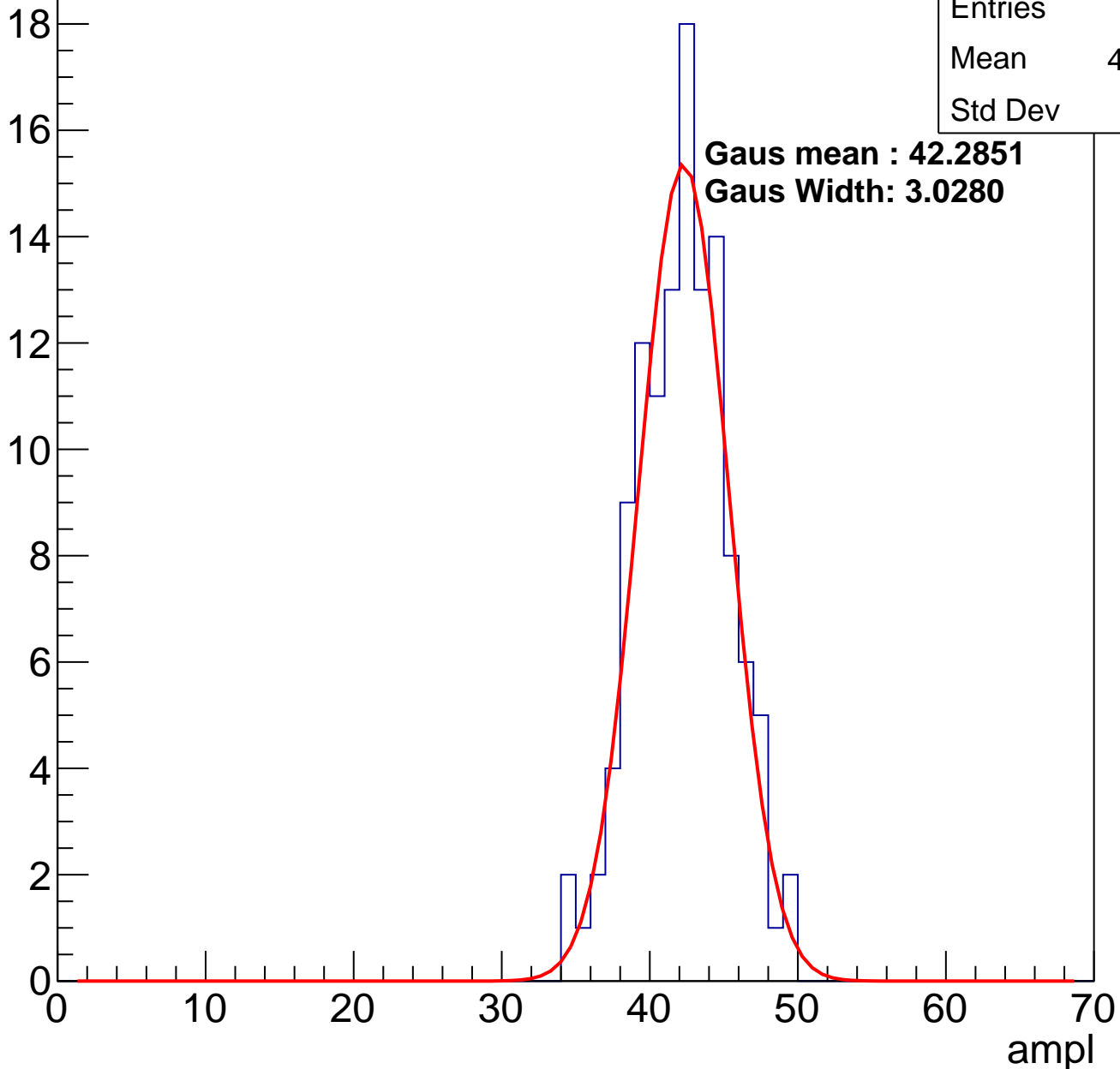
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	41.77
Std Dev	3.08

Gaus mean : 42.2851

Gaus Width: 3.0280

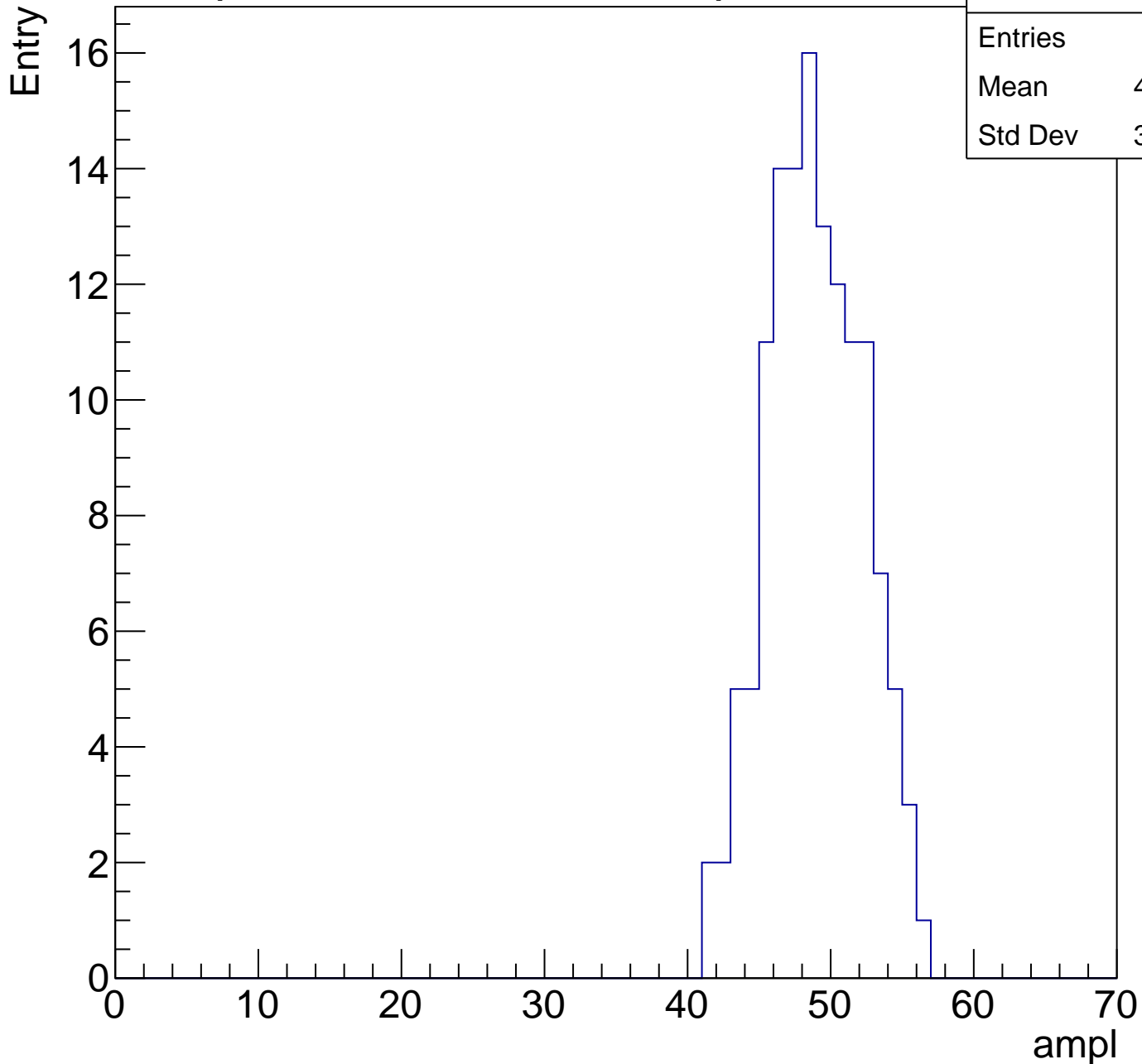
Entry



B1L001S, U21-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

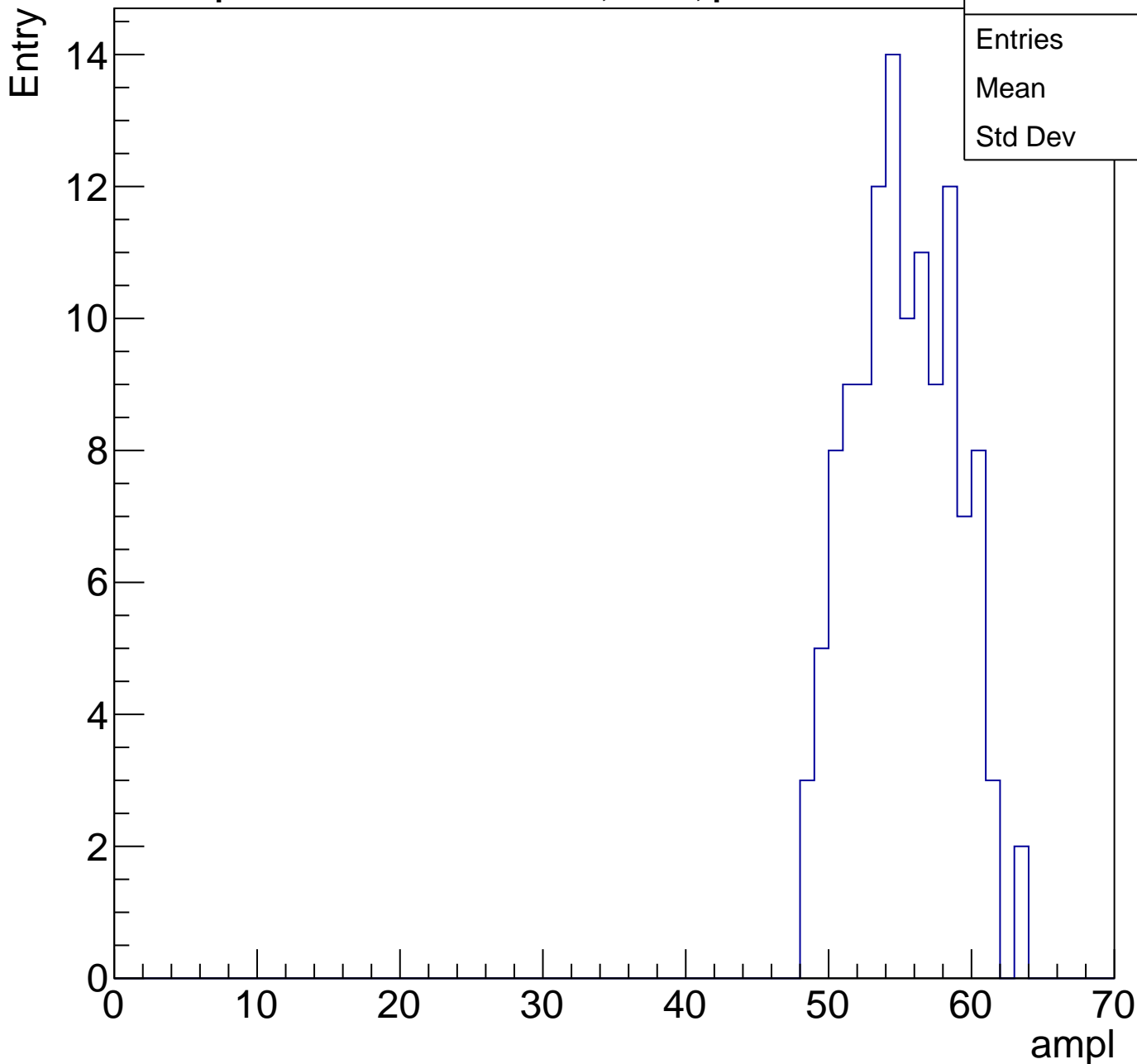
Entries	132
Mean	48.47
Std Dev	3.276



B1L001S, U21-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	54.8
Std Dev	3.52

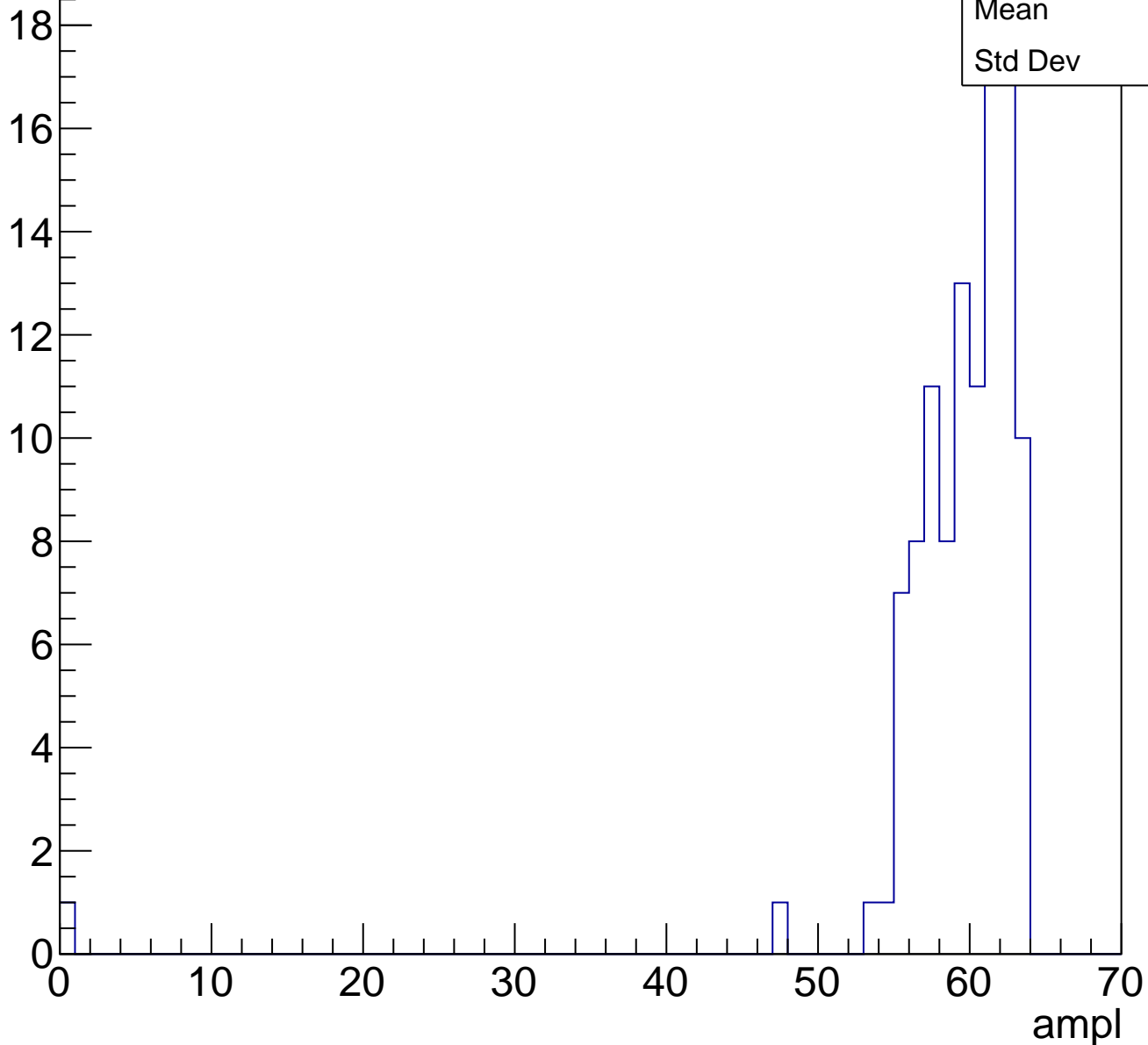


B1L001S, U21-ch115, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	58.8
Std Dev	6.33

Entry

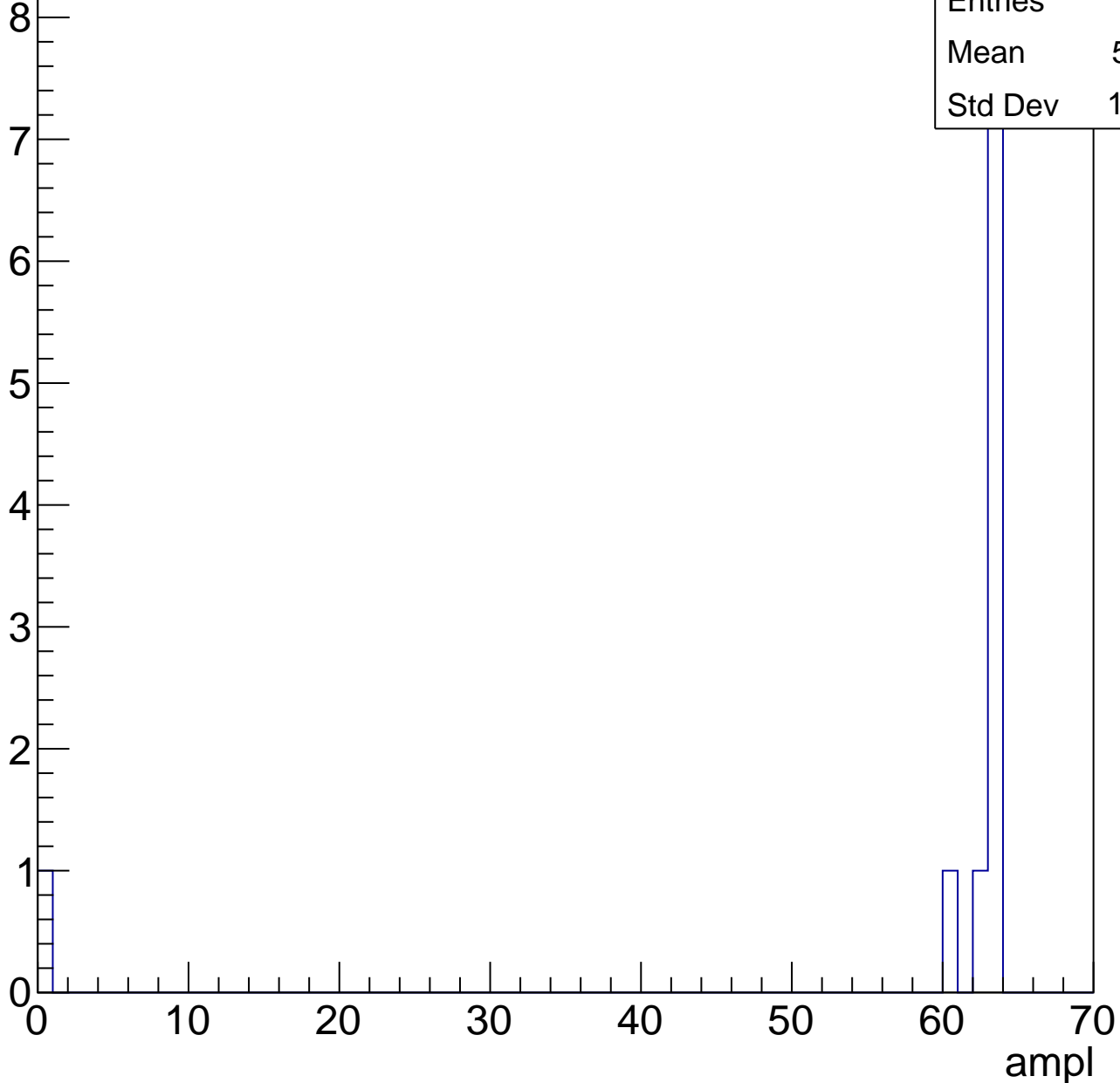


B1L001S, U21-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.91
Std Dev	18.02



B1L001S, U21-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch116, adc0

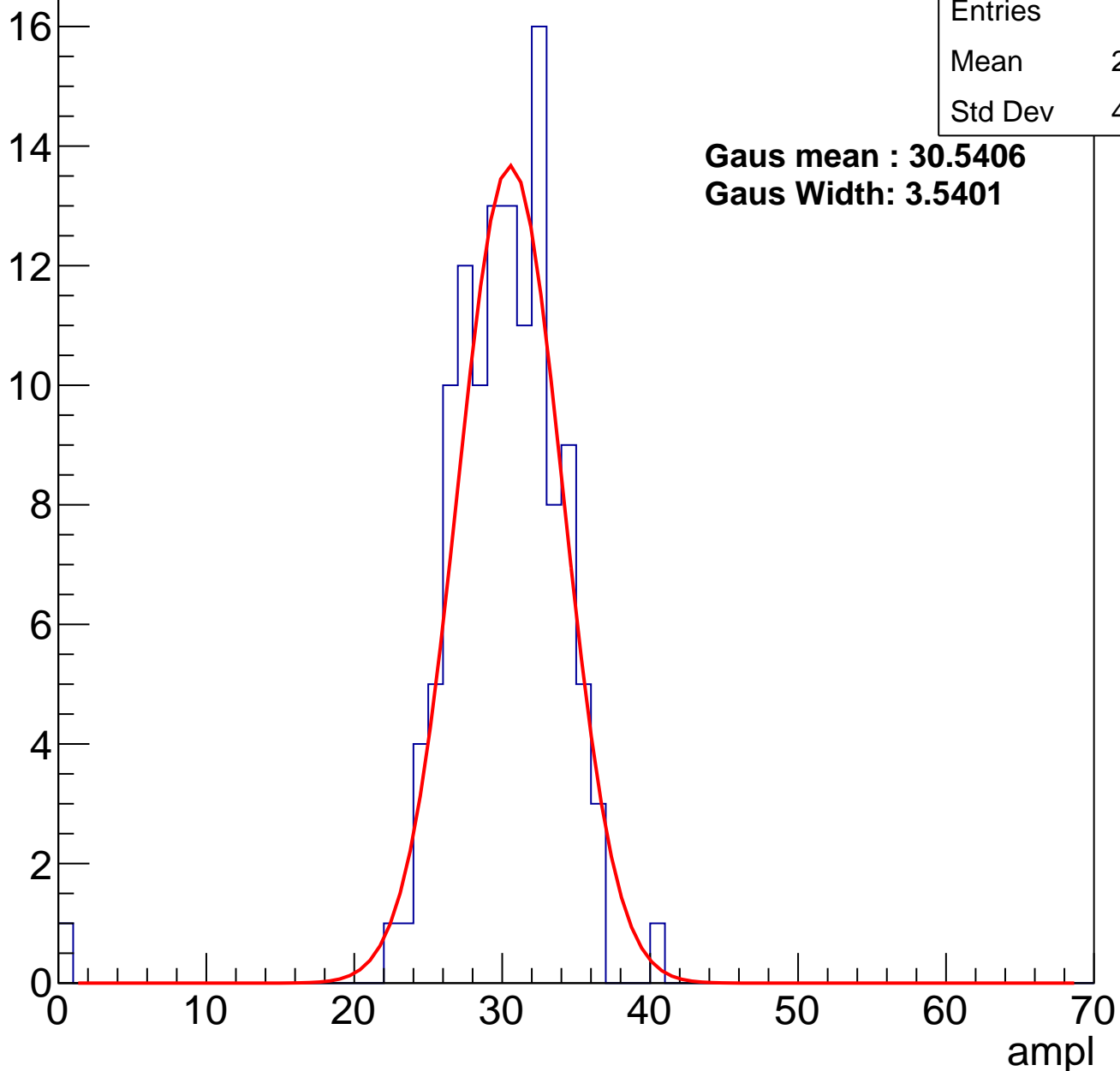
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	29.62
Std Dev	4.238

Gaus mean : 30.5406

Gaus Width: 3.5401

Entry



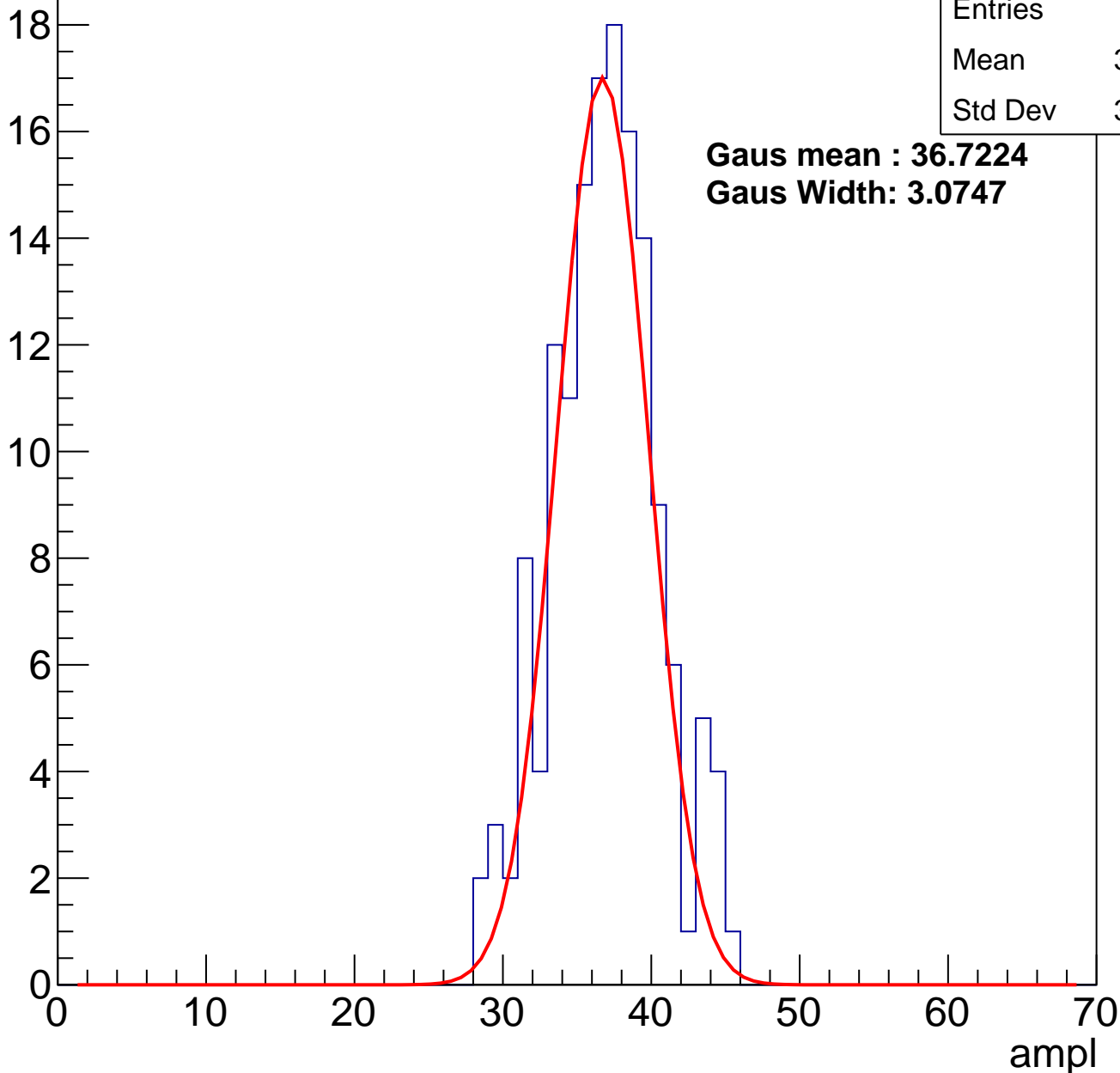
B1L001S, U21-ch116, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.42
Std Dev	3.557

Gaus mean : 36.7224
Gaus Width: 3.0747

Entry



B1L001S, U21-ch116, adc2

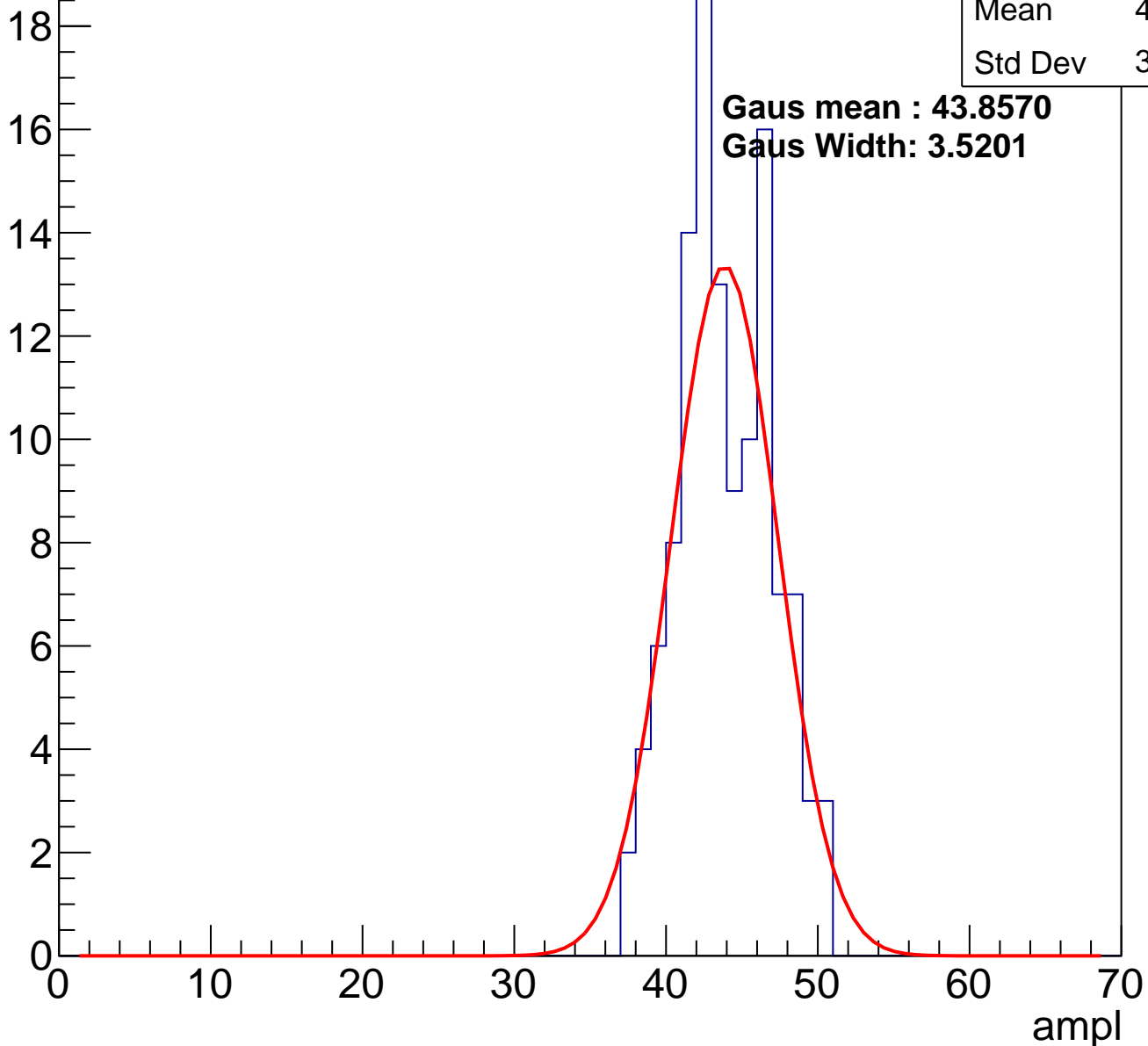
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	43.43
Std Dev	3.053

Gaus mean : 43.8570

Gaus Width: 3.5201

Entry

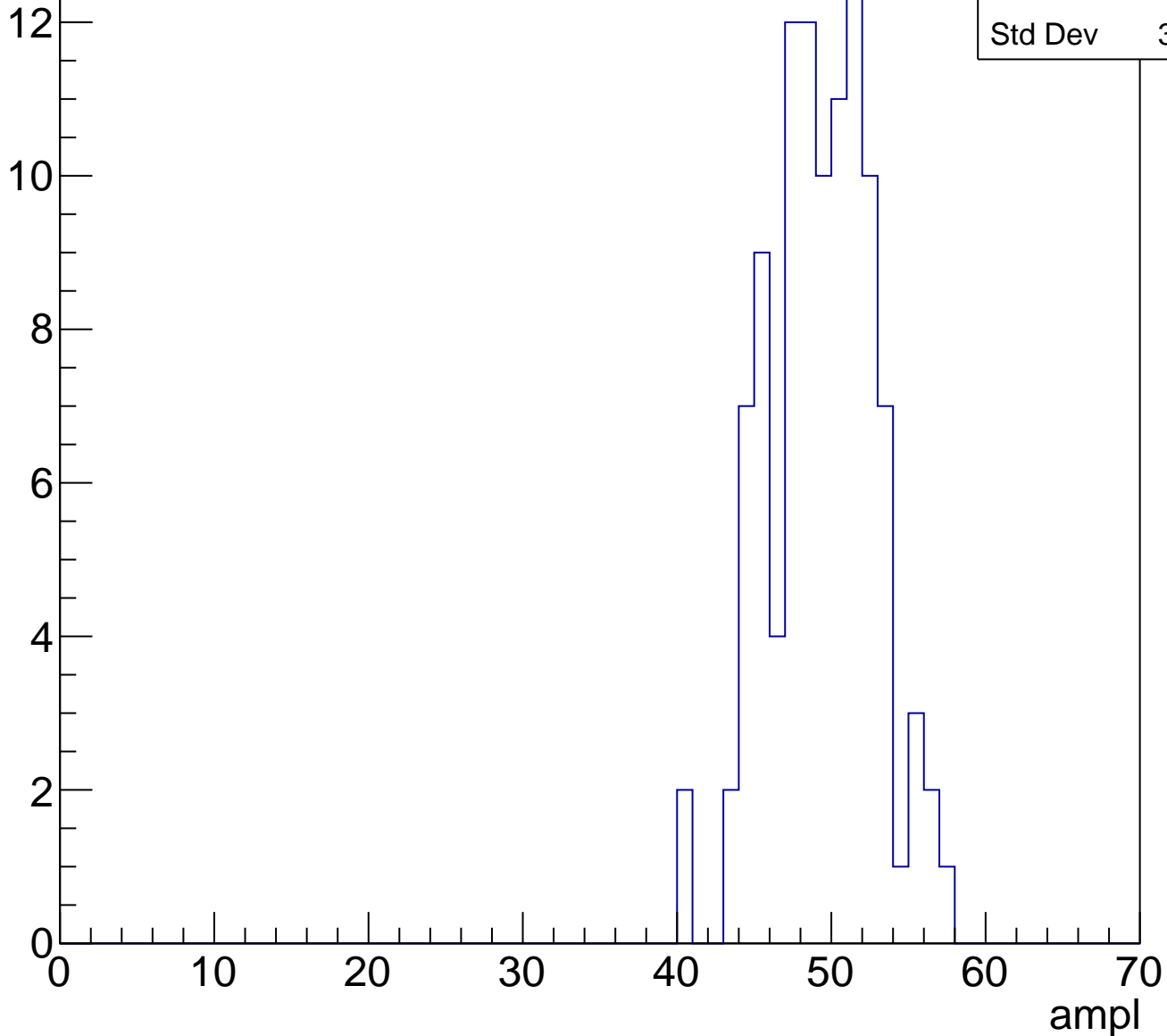


B1L001S, U21-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	48.92
Std Dev	3.376

Entry

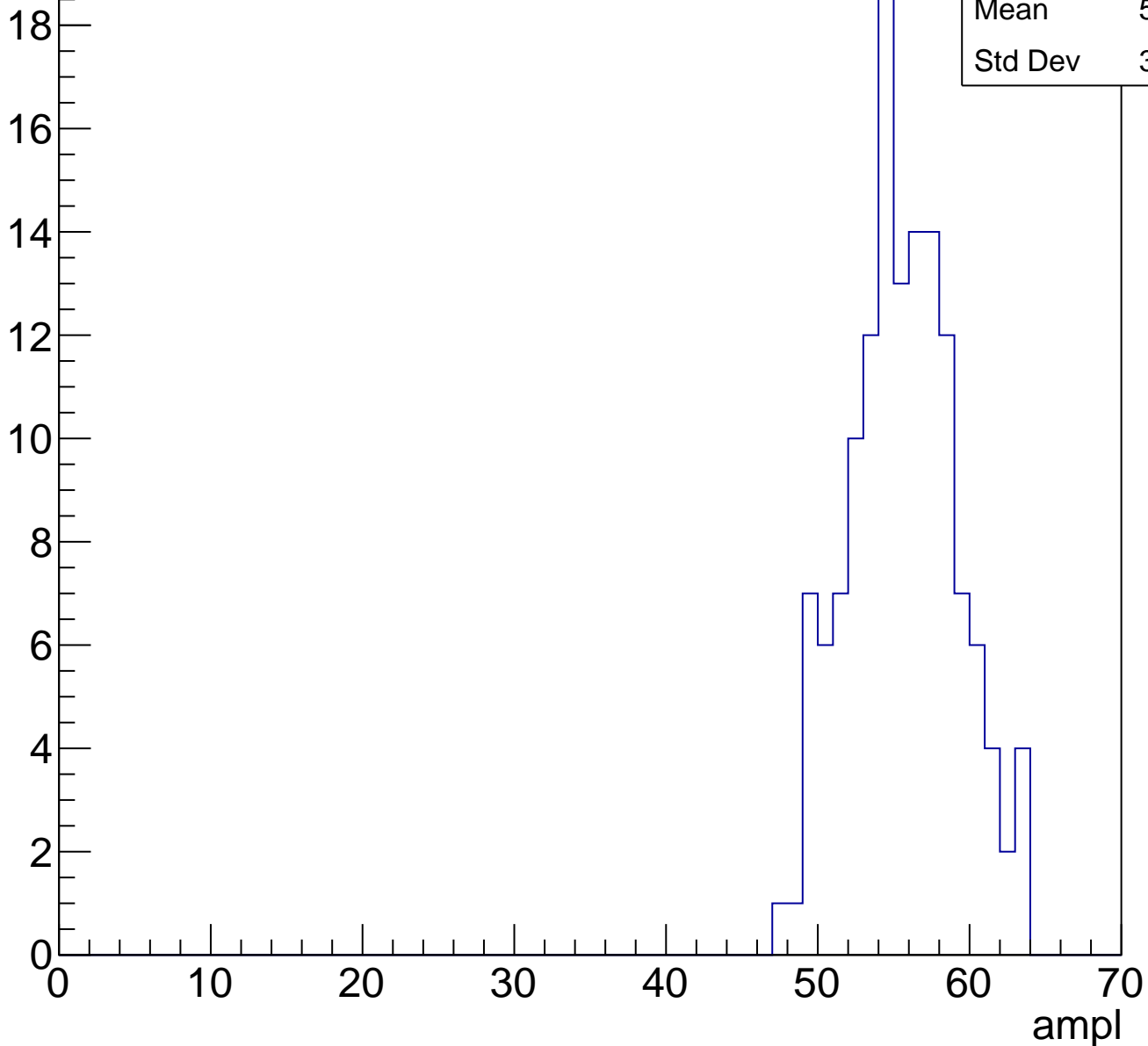


B1L001S, U21-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	55.13
Std Dev	3.517

Entry



B1L001S, U21-ch116, adc5

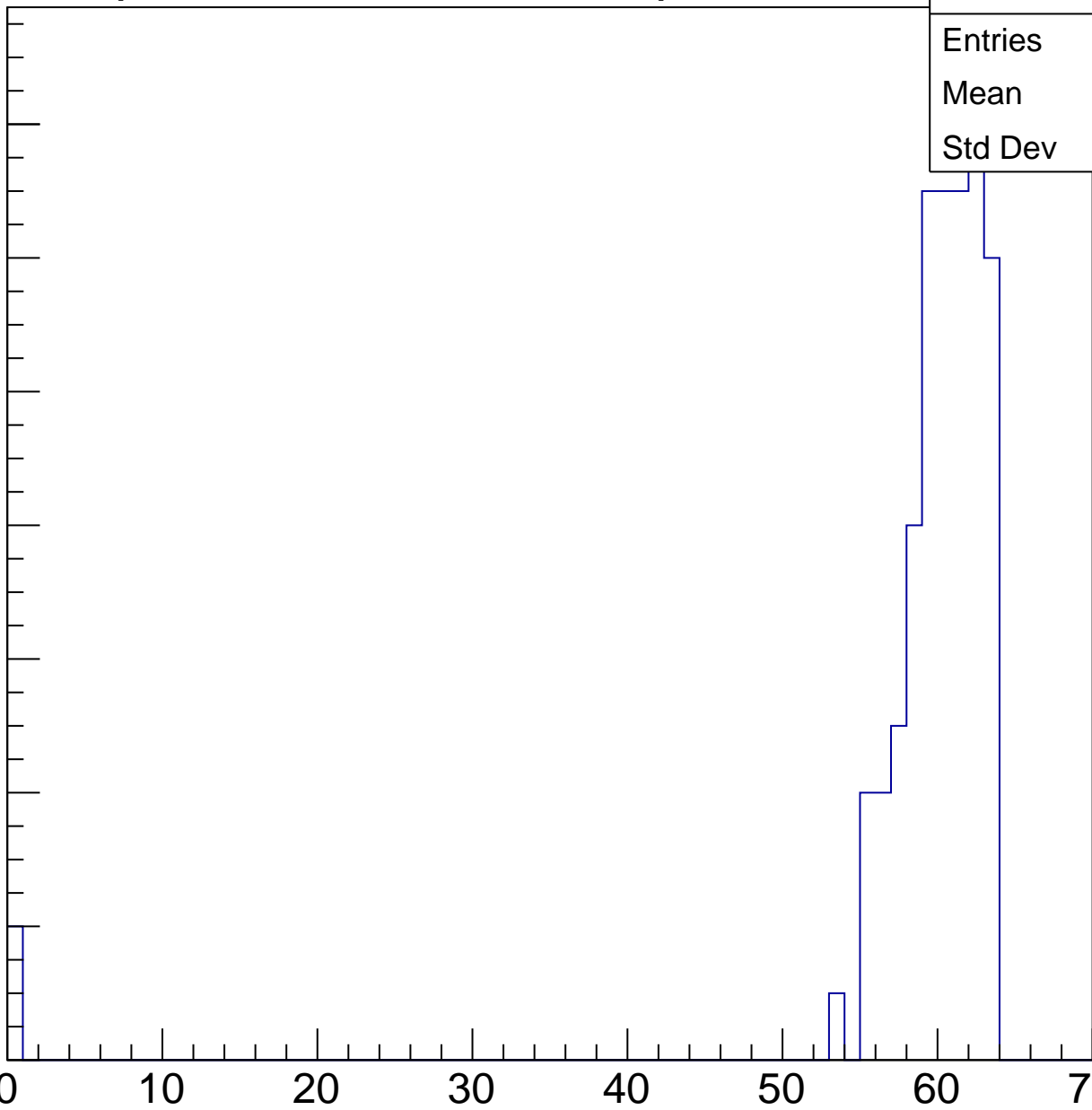
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.58
Std Dev	9.13

ampl



B1L001S, U21-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

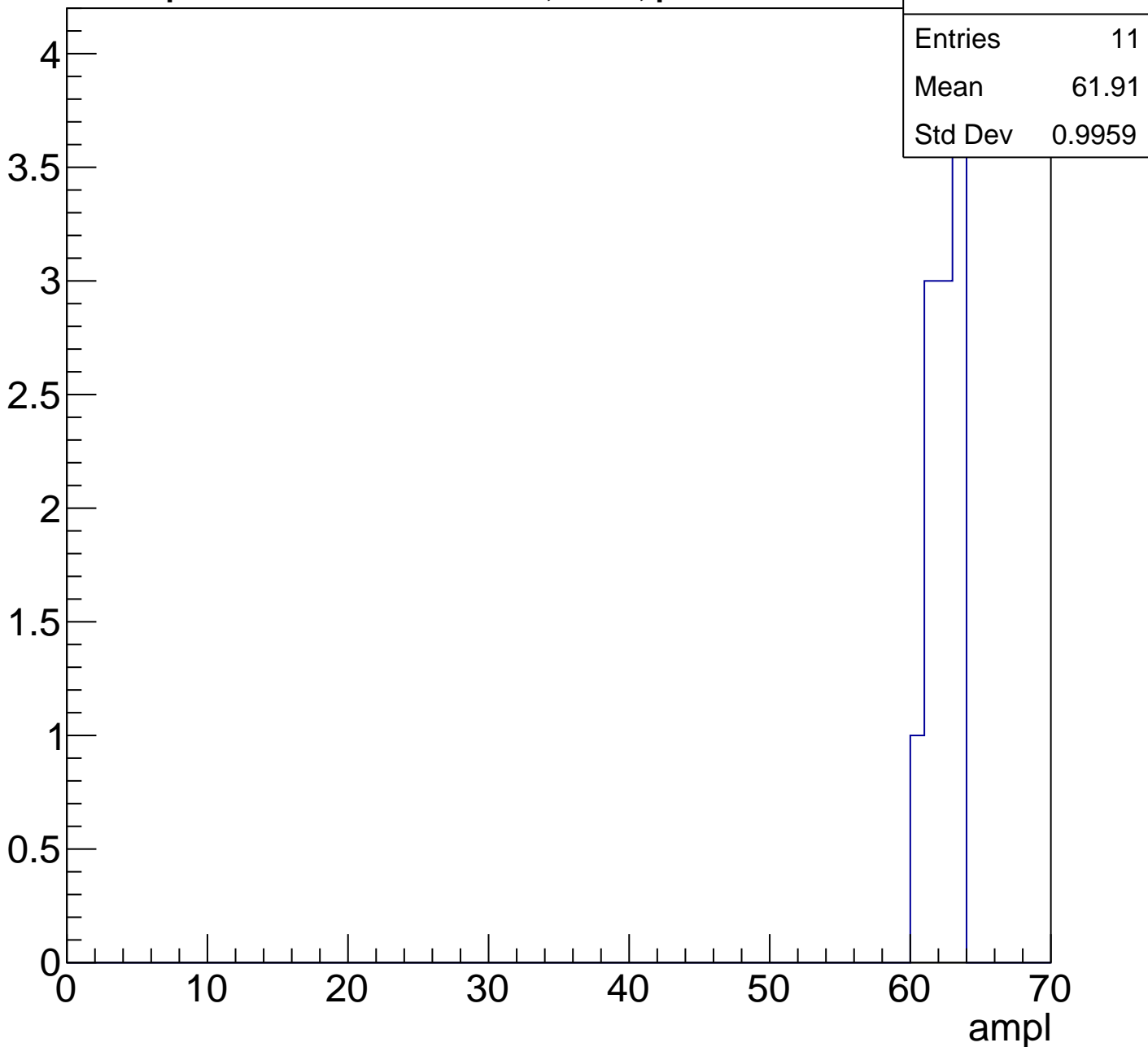
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	61.91
Std Dev	0.9959

0 10 20 30 40 50 60 70

ampl



B1L001S, U21-ch116, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U21-ch117, adc0

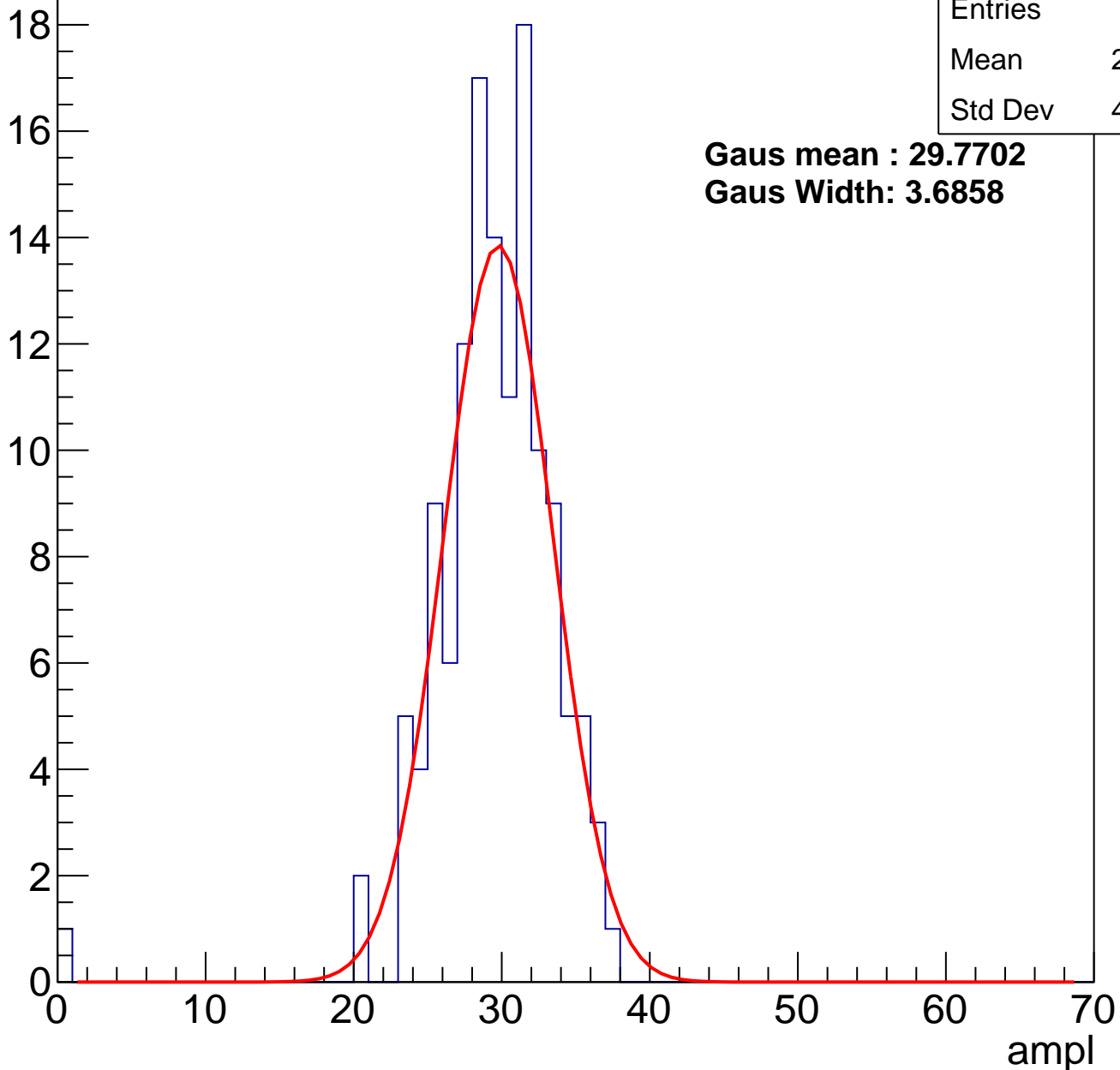
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	29.04
Std Dev	4.245

Gaus mean : 29.7702

Gaus Width: 3.6858

Entry



B1L001S, U21-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	35.87
Std Dev	3.402

Gaus mean : 36.6963

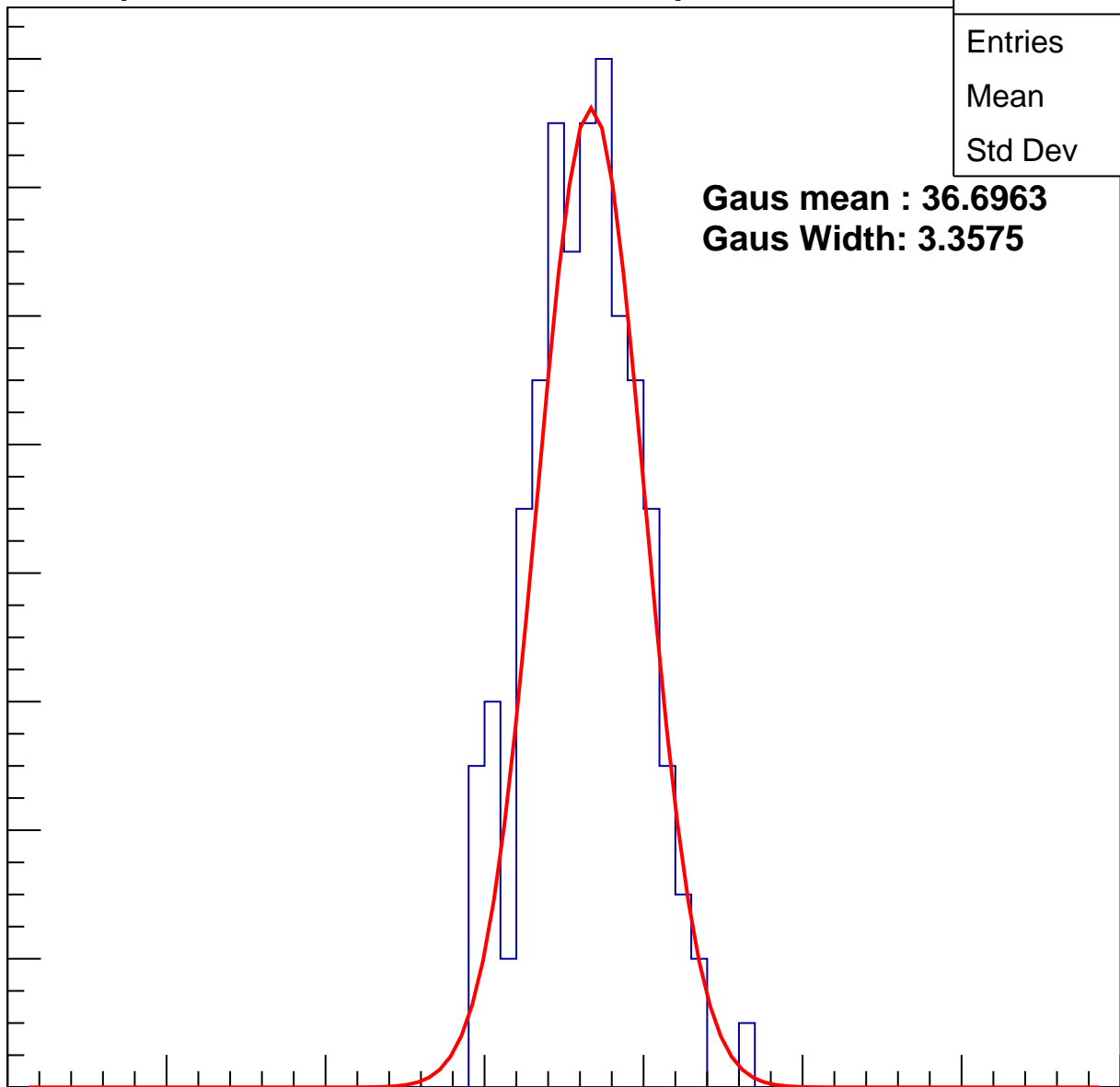
Gaus Width: 3.3575

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



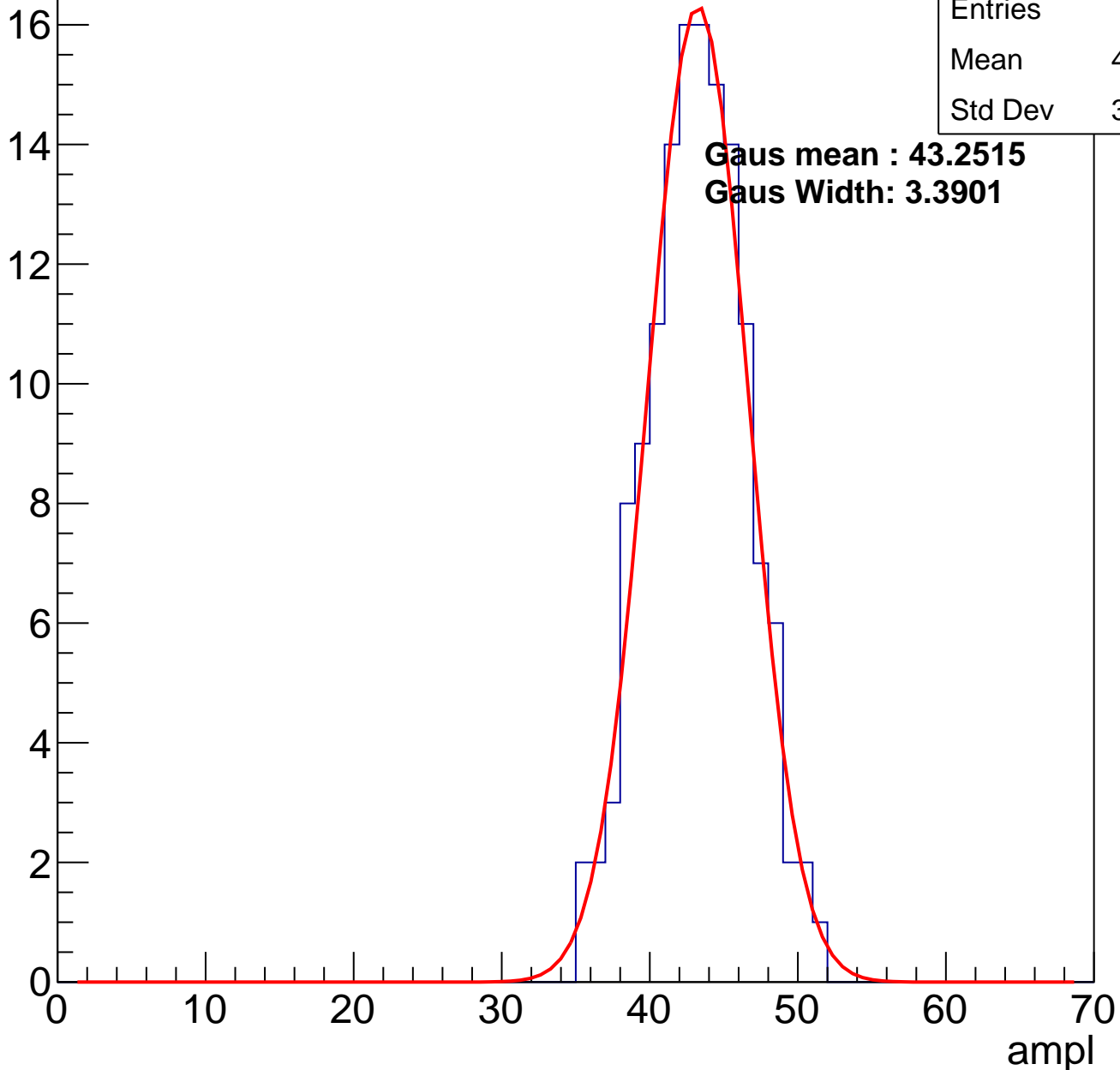
B1L001S, U21-ch117, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	42.76
Std Dev	3.293

Gaus mean : 43.2515
Gaus Width: 3.3901

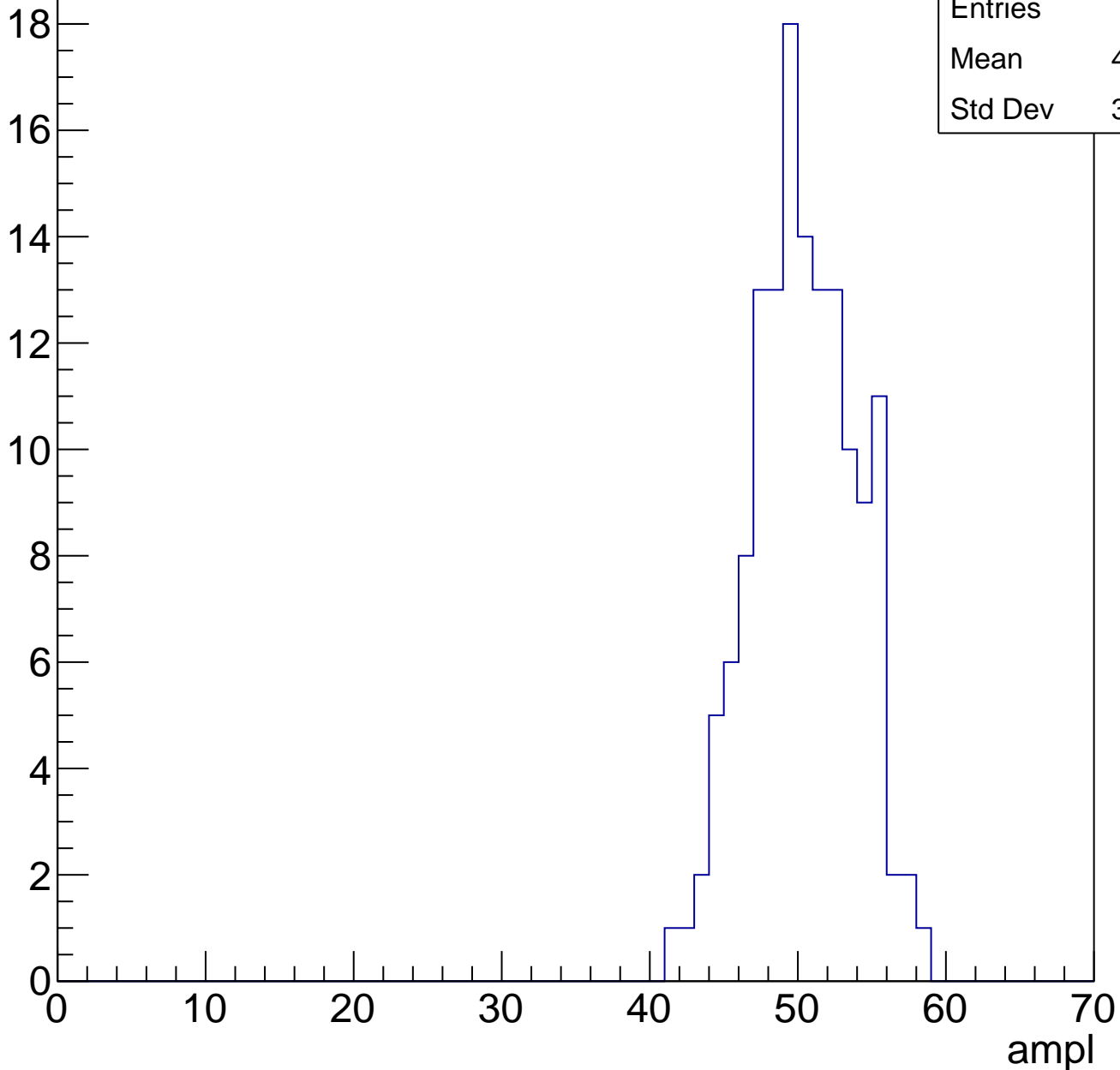
Entry



B1L001S, U21-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	55.39
Std Dev	6.27

Entry

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L001S, U21-ch117, adc5

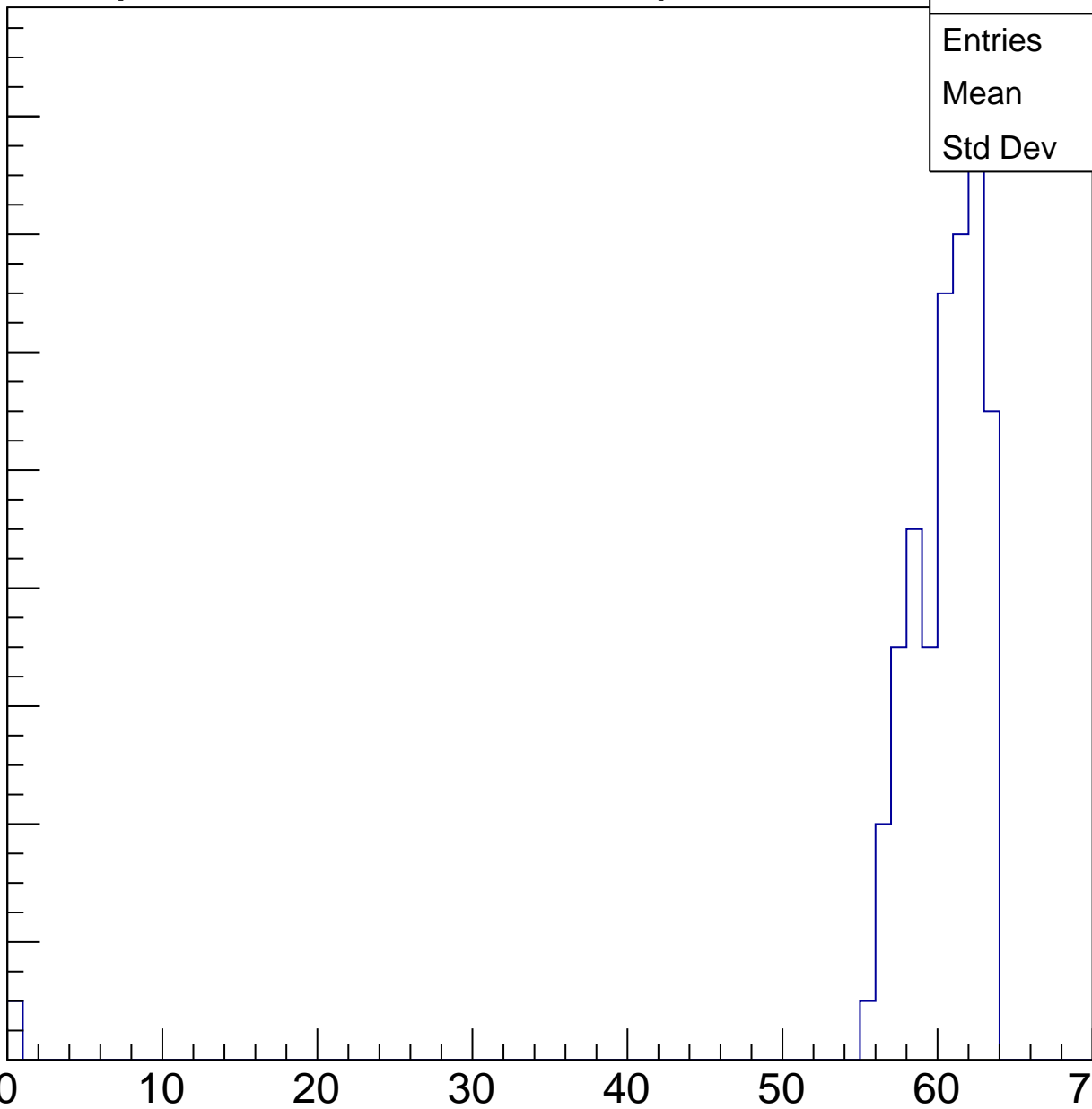
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.45
Std Dev	6.858

ampl

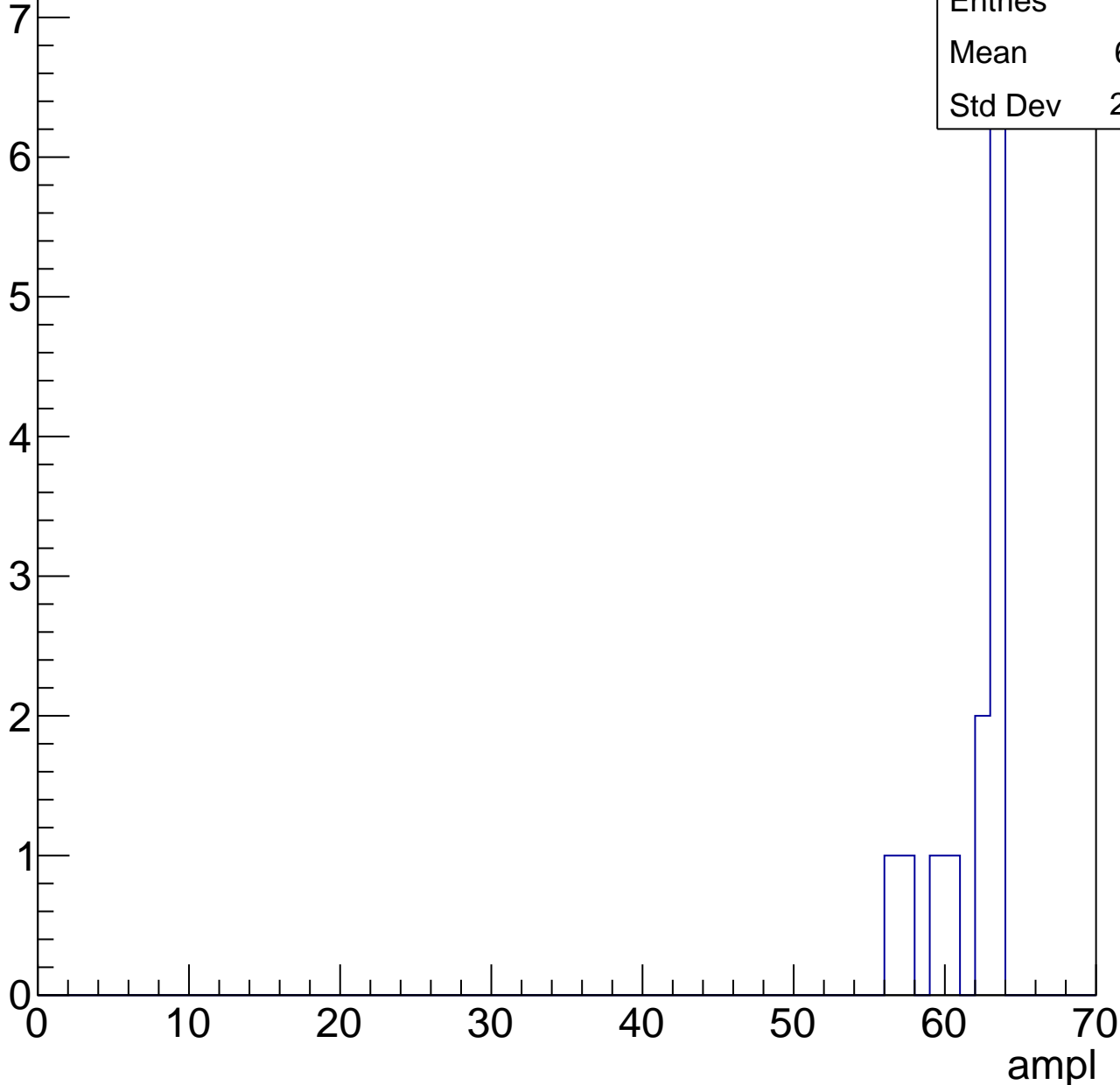


B1L001S, U21-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	61.31
Std Dev	2.398



B1L001S, U21-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch118, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.1
Std Dev	3.414

Gaus mean : 30.8417

Gaus Width: 2.8090

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

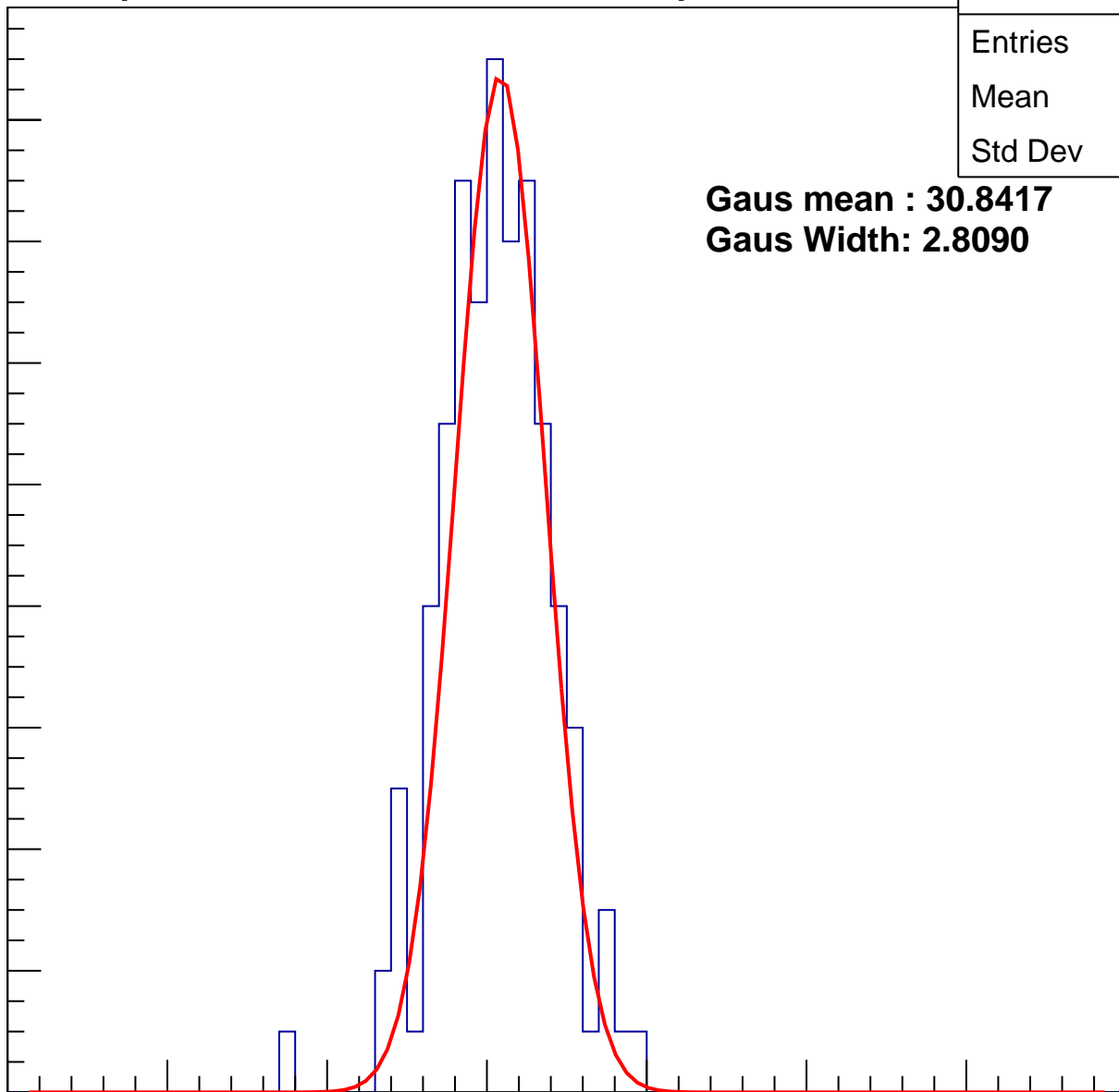
40

50

60

70

ampl



B1L001S, U21-ch118, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	36.4
Std Dev	3.029

Gaus mean : 37.0855

Gaus Width: 2.9967

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

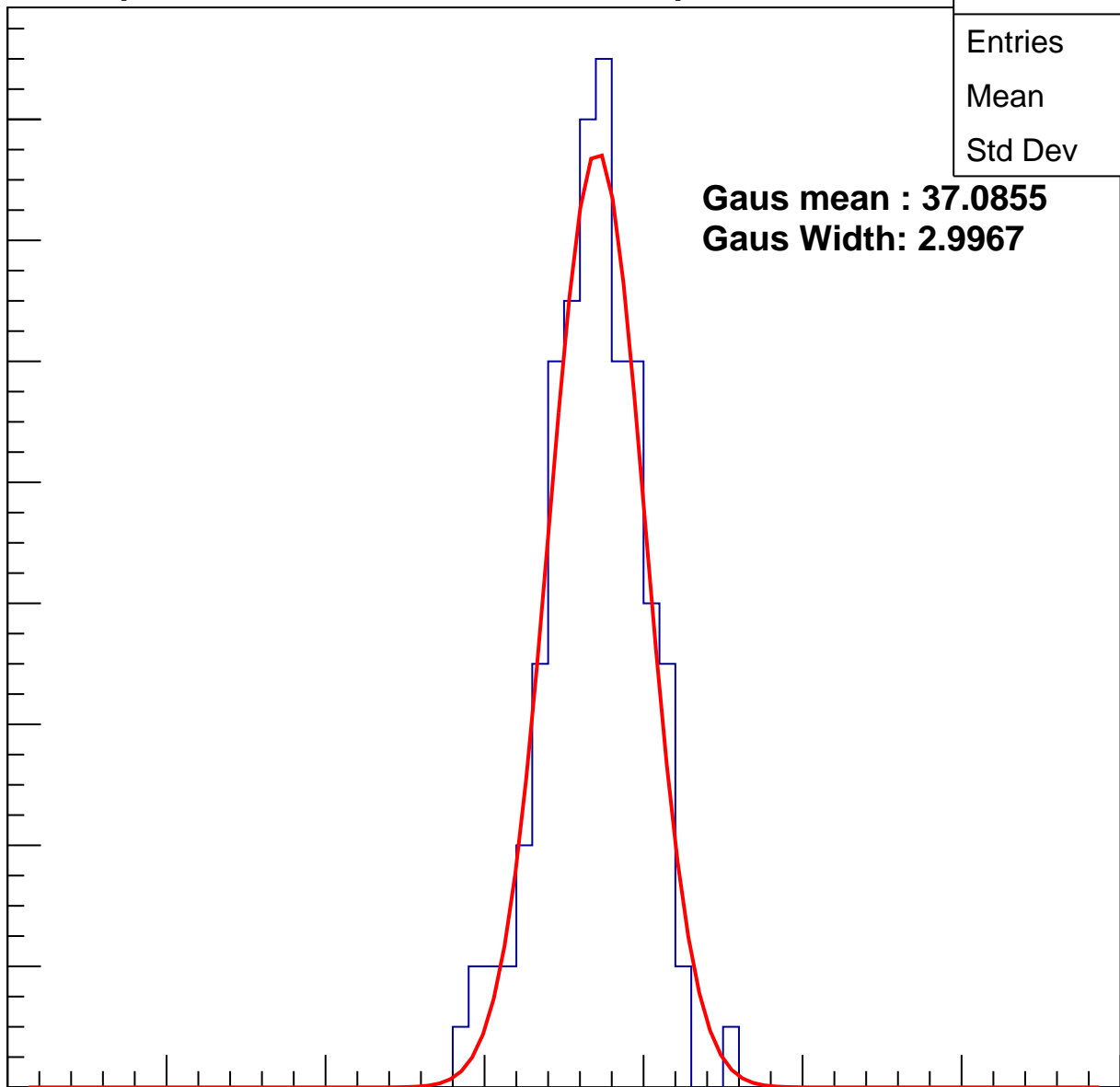
40

50

60

70

ampl



B1L001S, U21-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.63
Std Dev	3.482

Gaus mean : 43.0586

Gaus Width: 3.4380

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

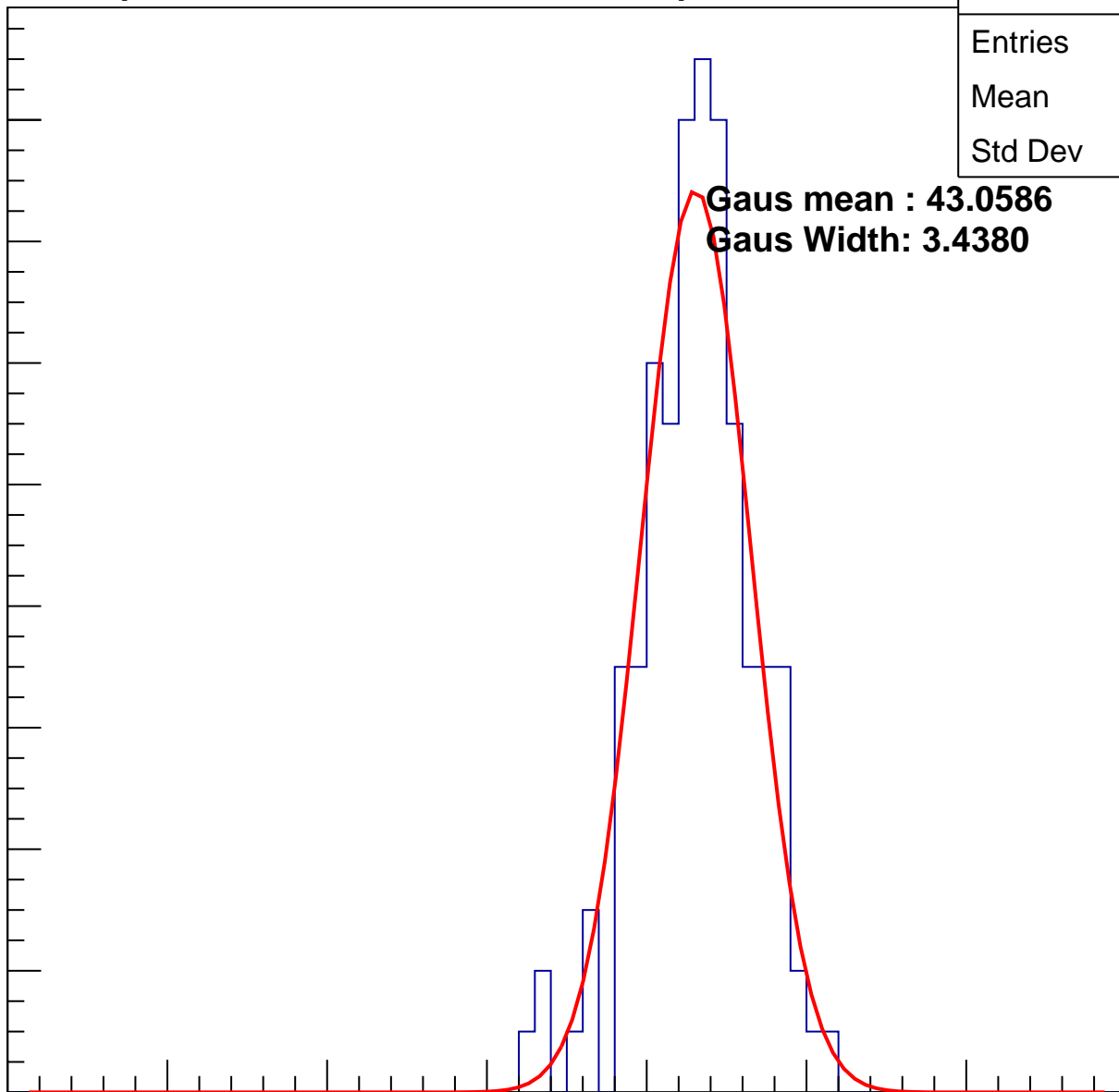
40

50

60

70

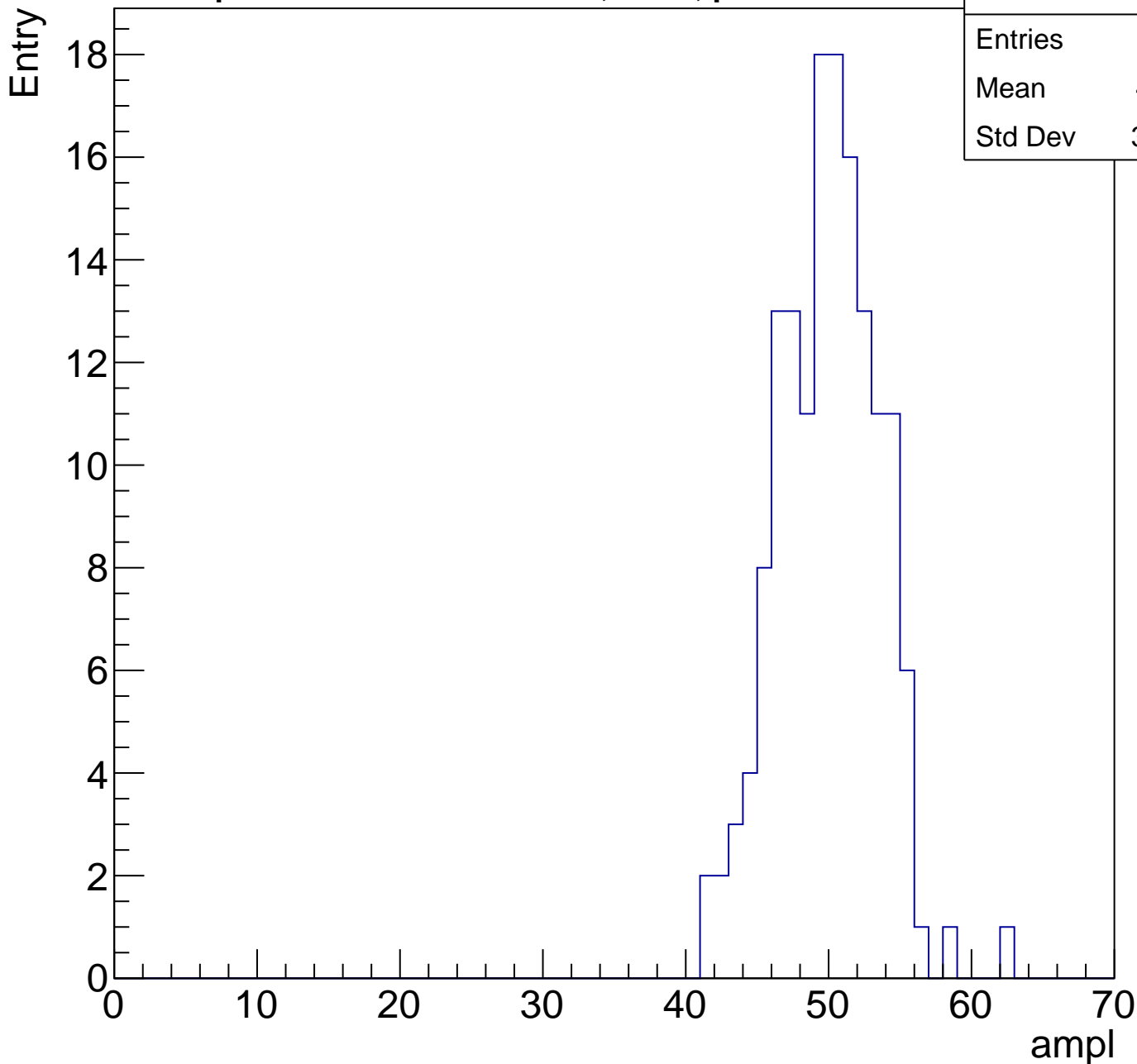
ampl



B1L001S, U21-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

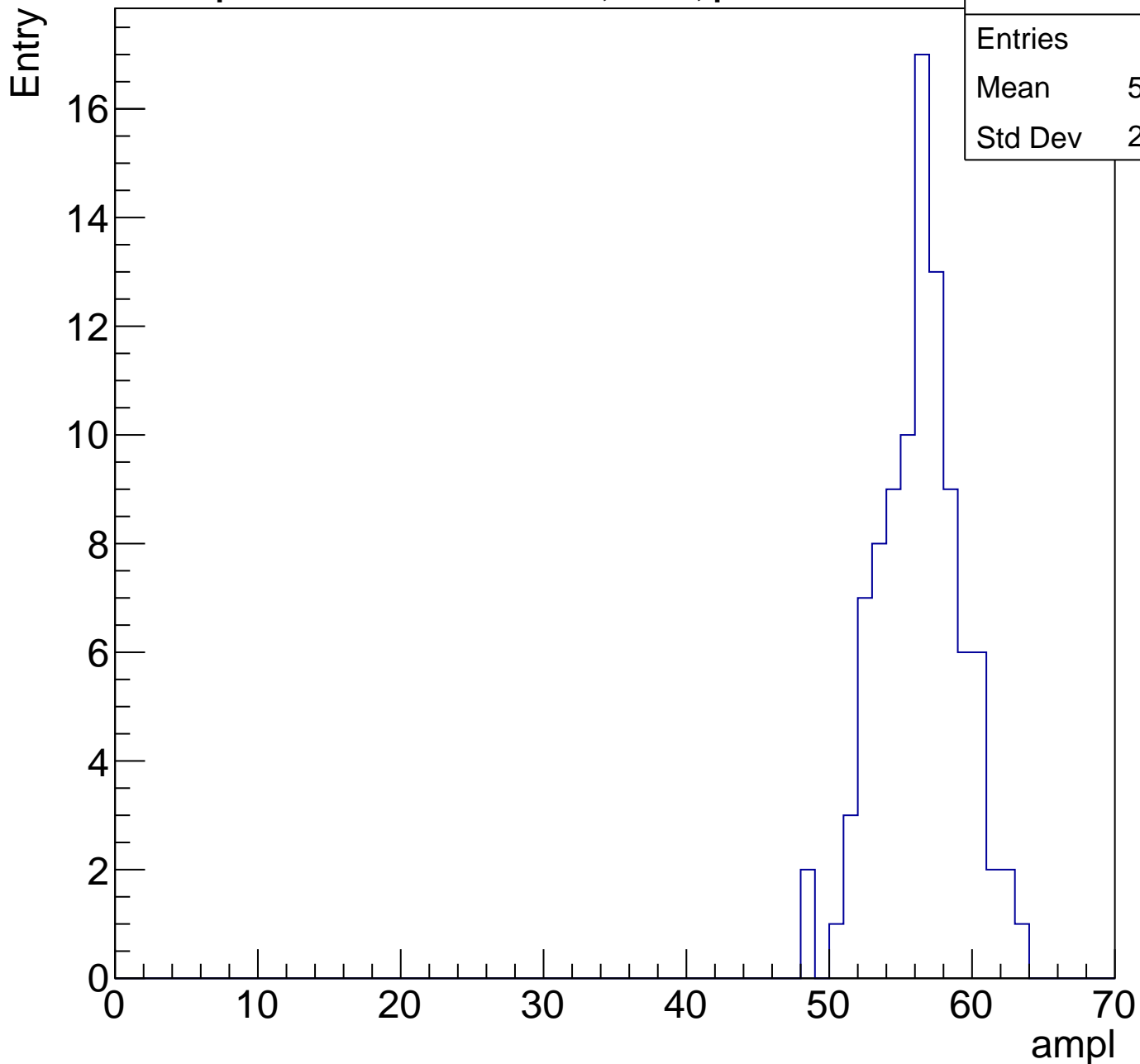
Entries	152
Mean	49.51
Std Dev	3.506



B1L001S, U21-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	55.84
Std Dev	2.952



B1L001S, U21-ch118, adc5

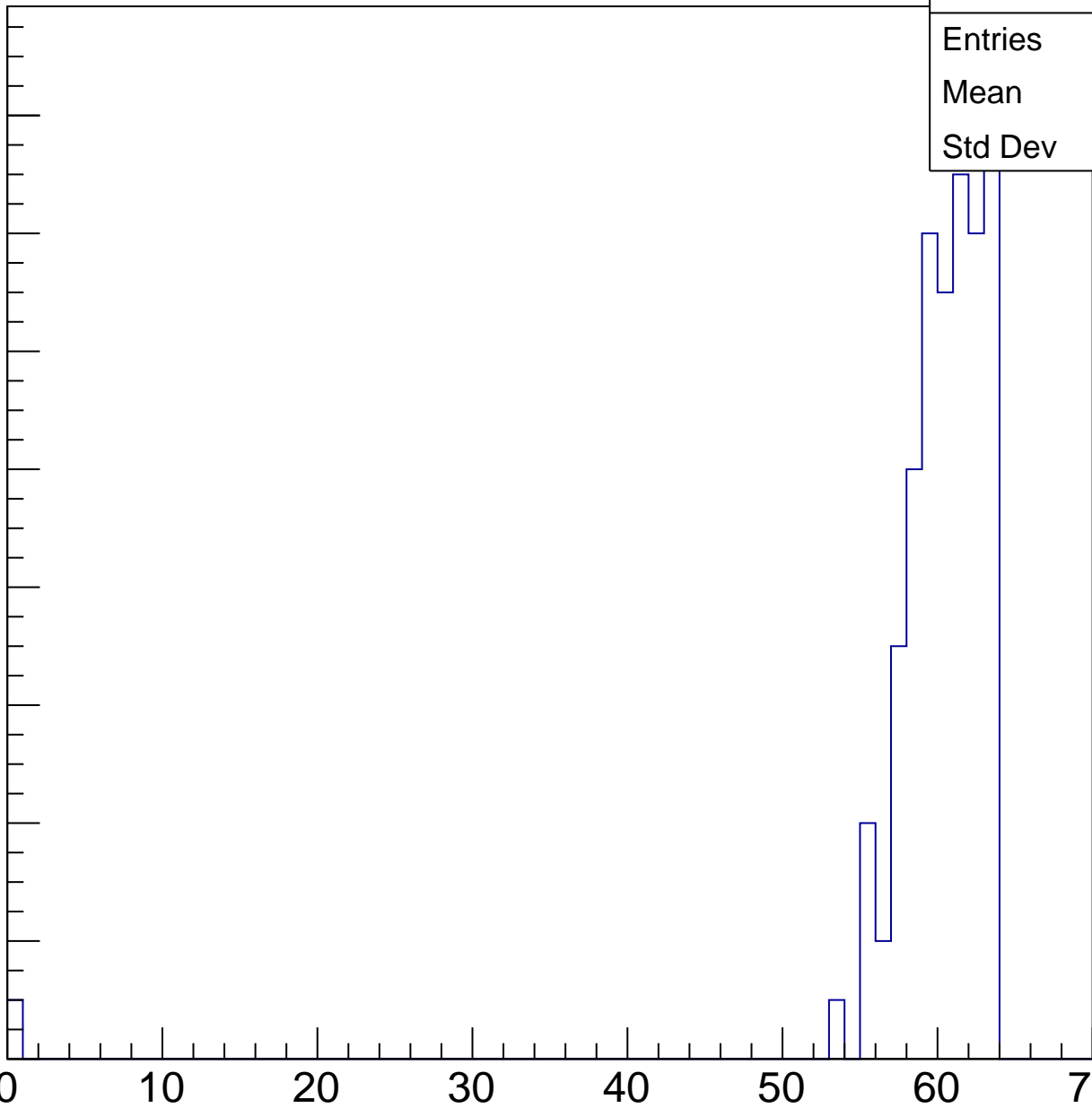
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.43
Std Dev	6.46

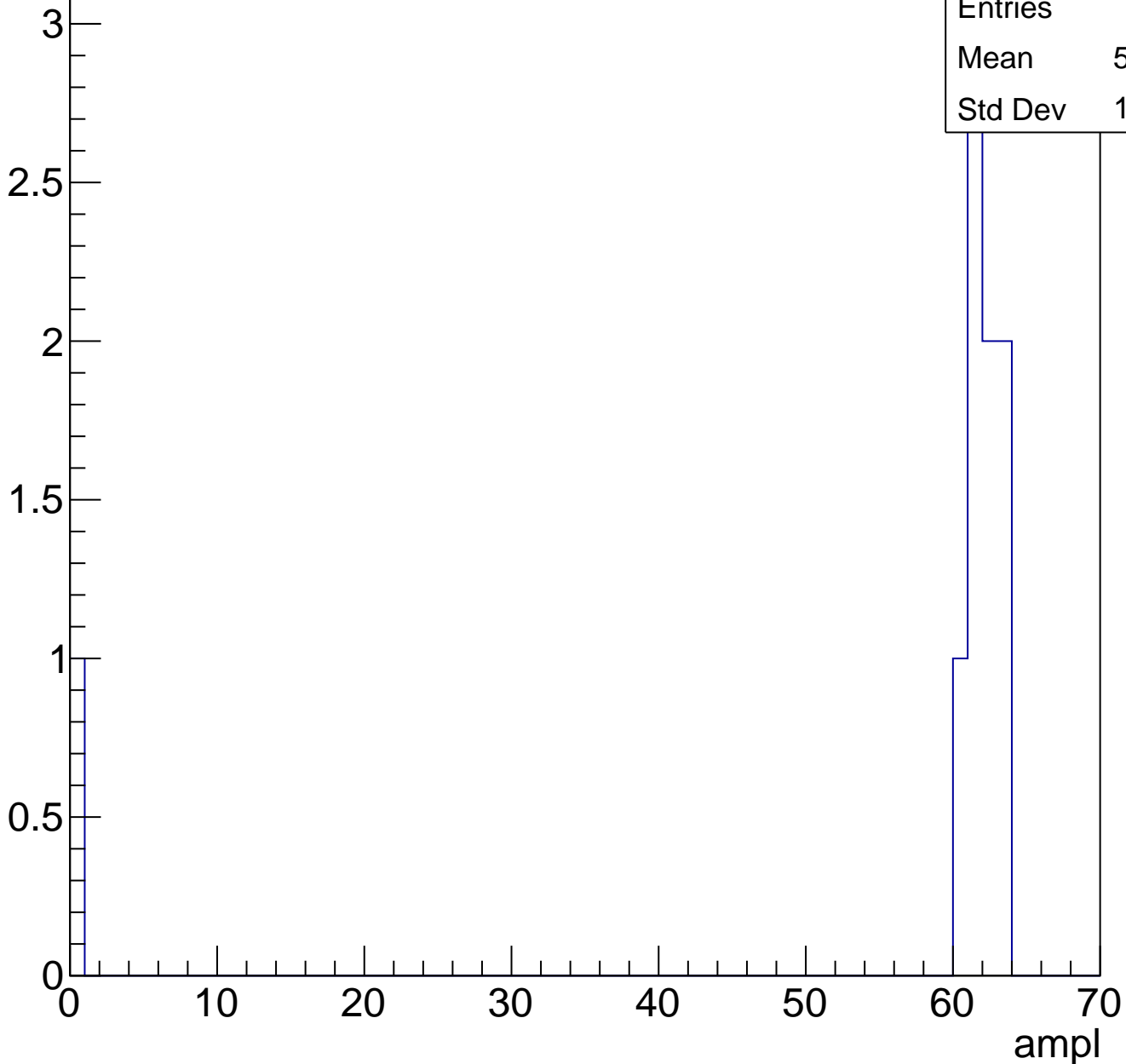
ampl



B1L001S, U21-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

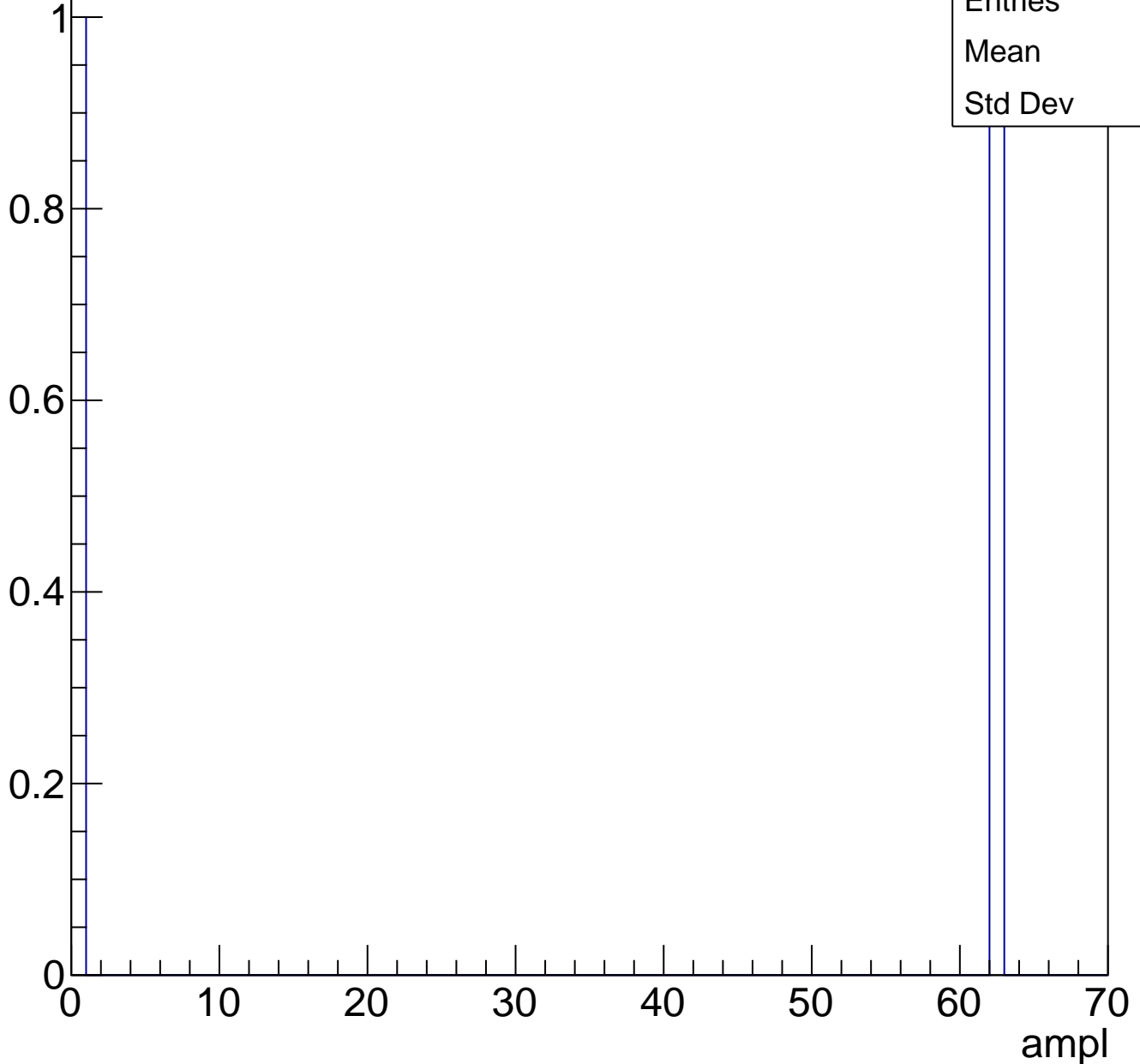
Entry



B1L001S, U21-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch119, adc0

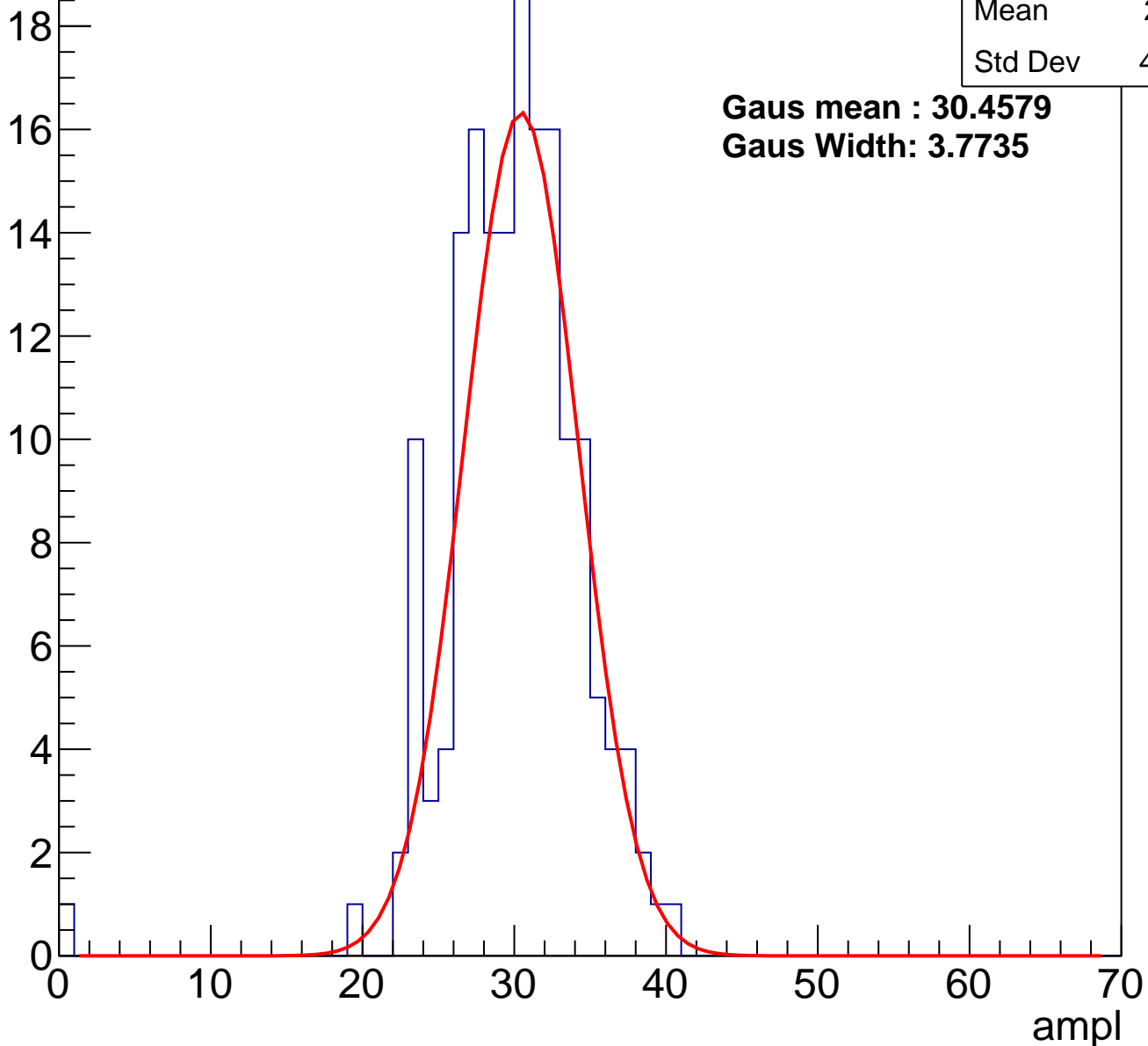
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	29.51
Std Dev	4.479

Gaus mean : 30.4579

Gaus Width: 3.7735

Entry



B1L001S, U21-ch119, adc1

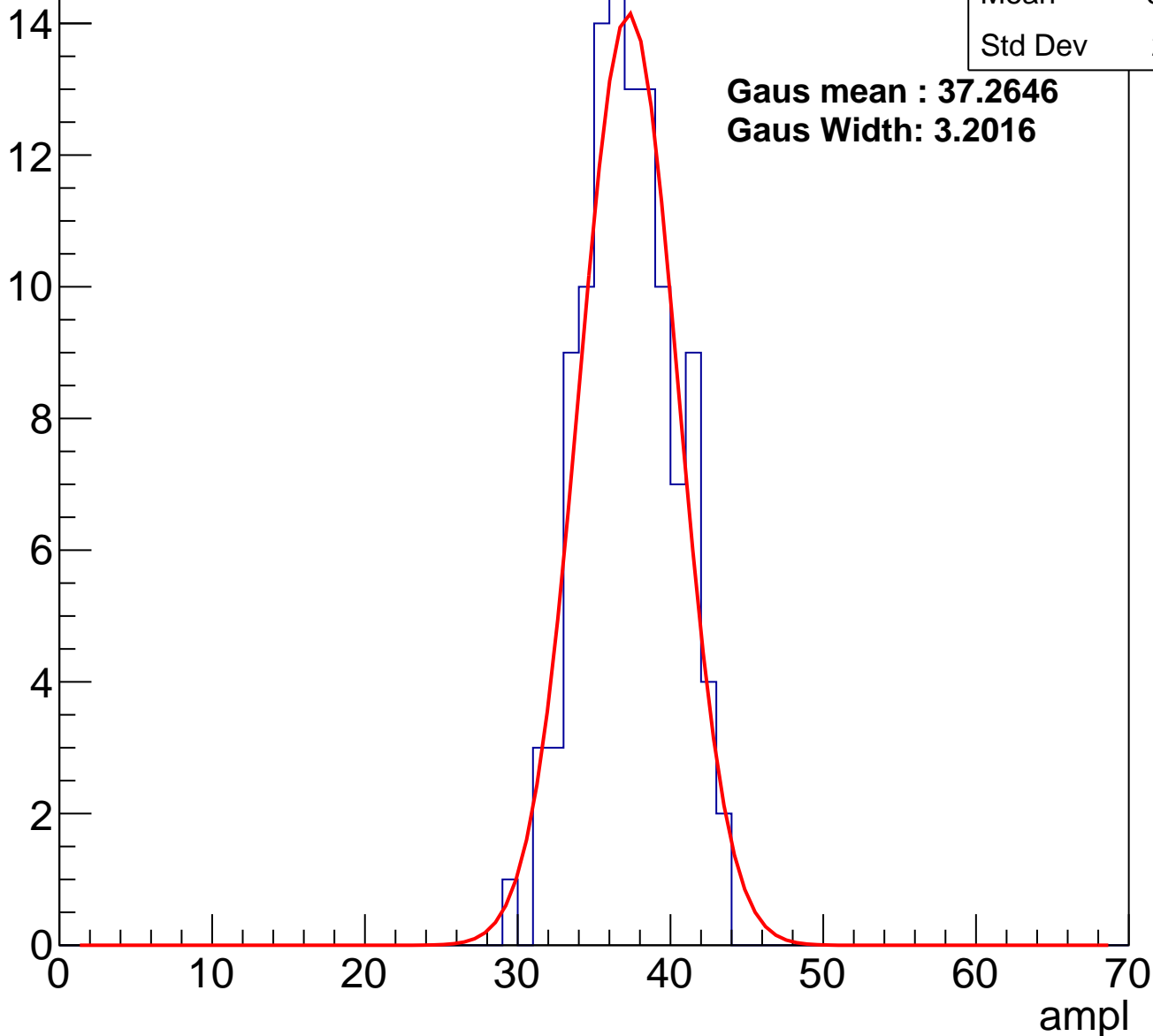
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	36.75
Std Dev	2.941

Gaus mean : 37.2646

Gaus Width: 3.2016

Entry



B1L001S, U21-ch119, adc2

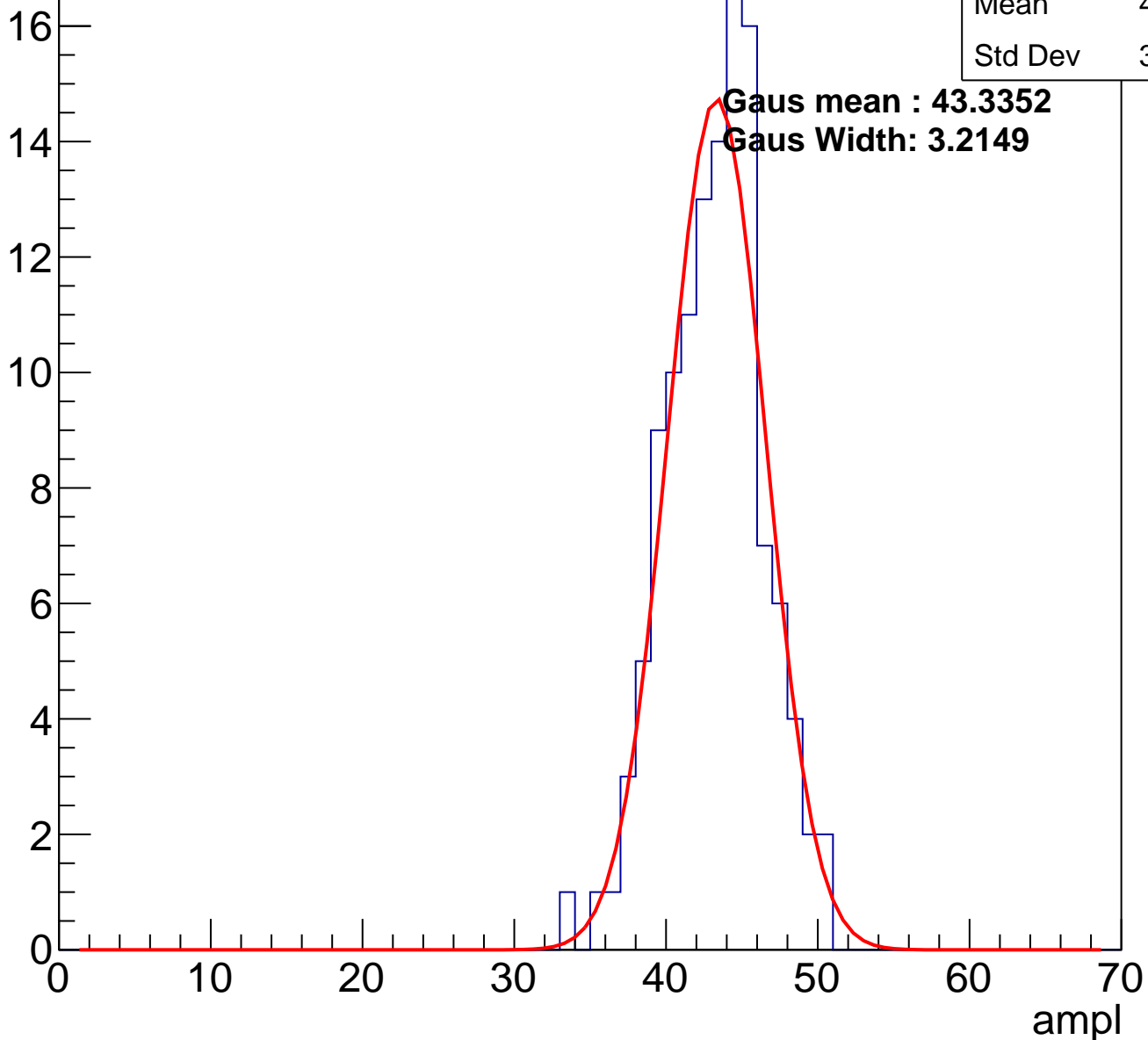
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	122
Mean	42.76
Std Dev	3.188

Gaus mean : 43.3352

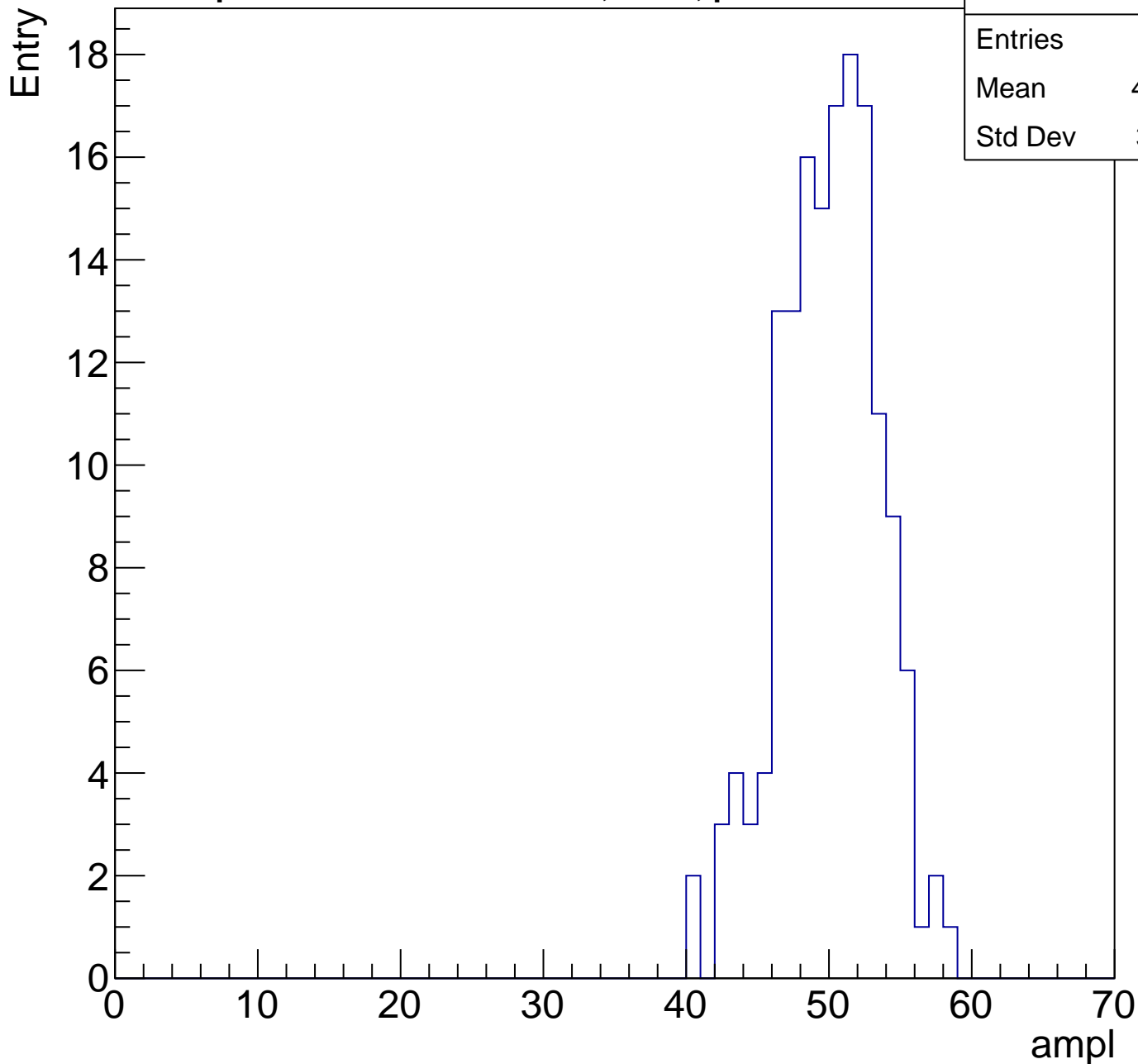
Gaus Width: 3.2149



B1L001S, U21-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	49.55
Std Dev	3.451

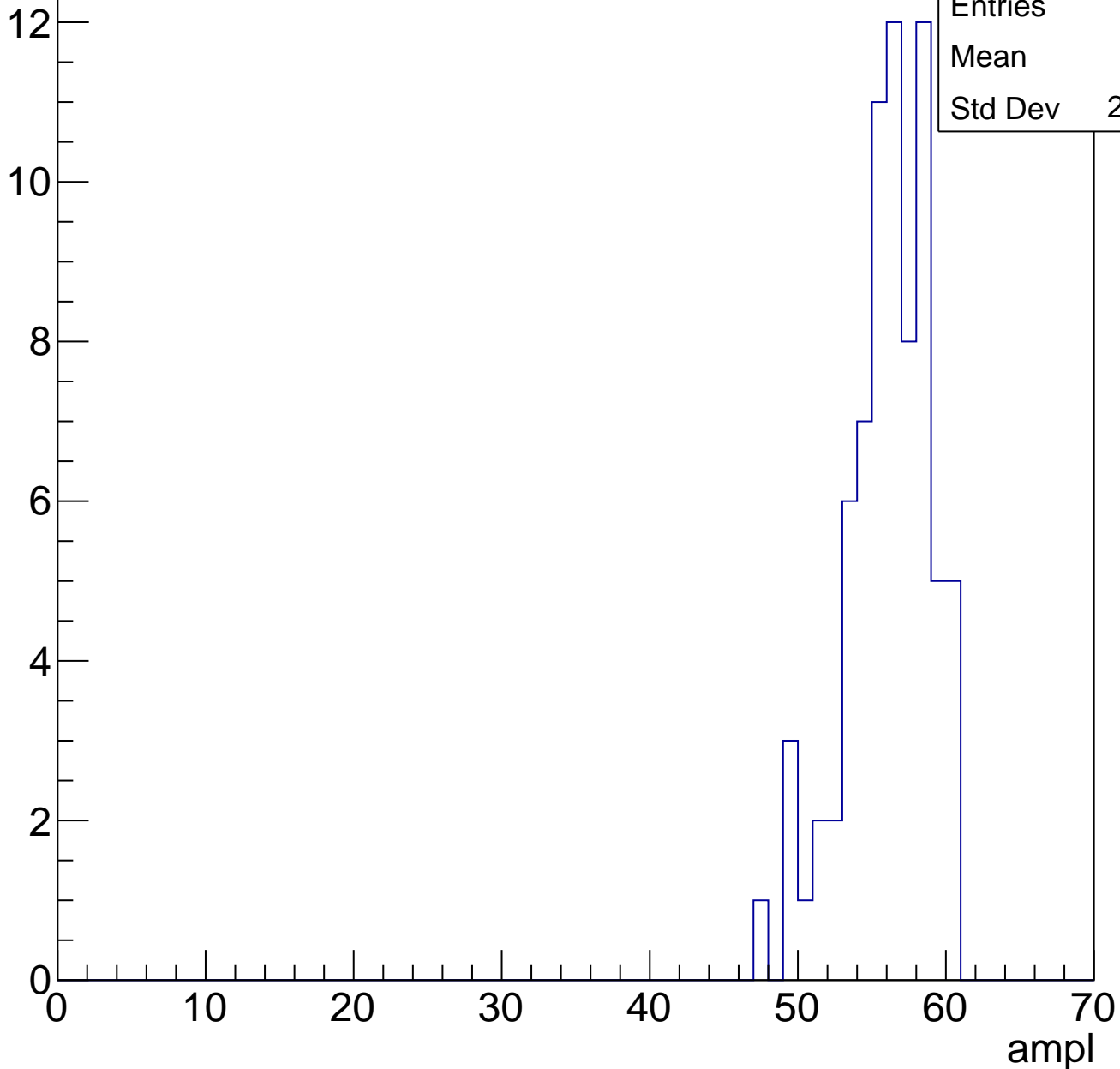


B1L001S, U21-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	75
Mean	55.6
Std Dev	2.847

Entry

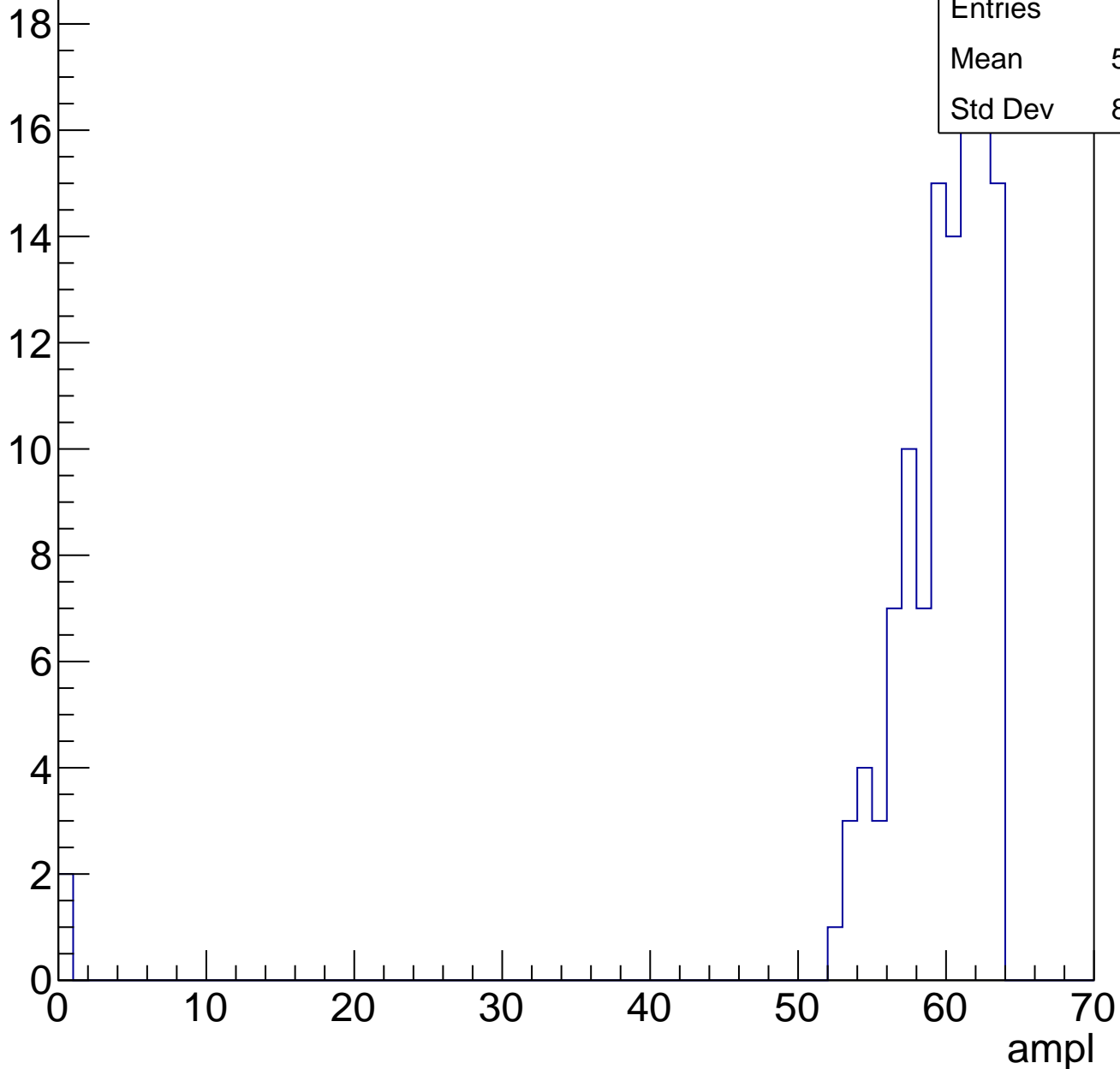


B1L001S, U21-ch119, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	58.47
Std Dev	8.212

Entry

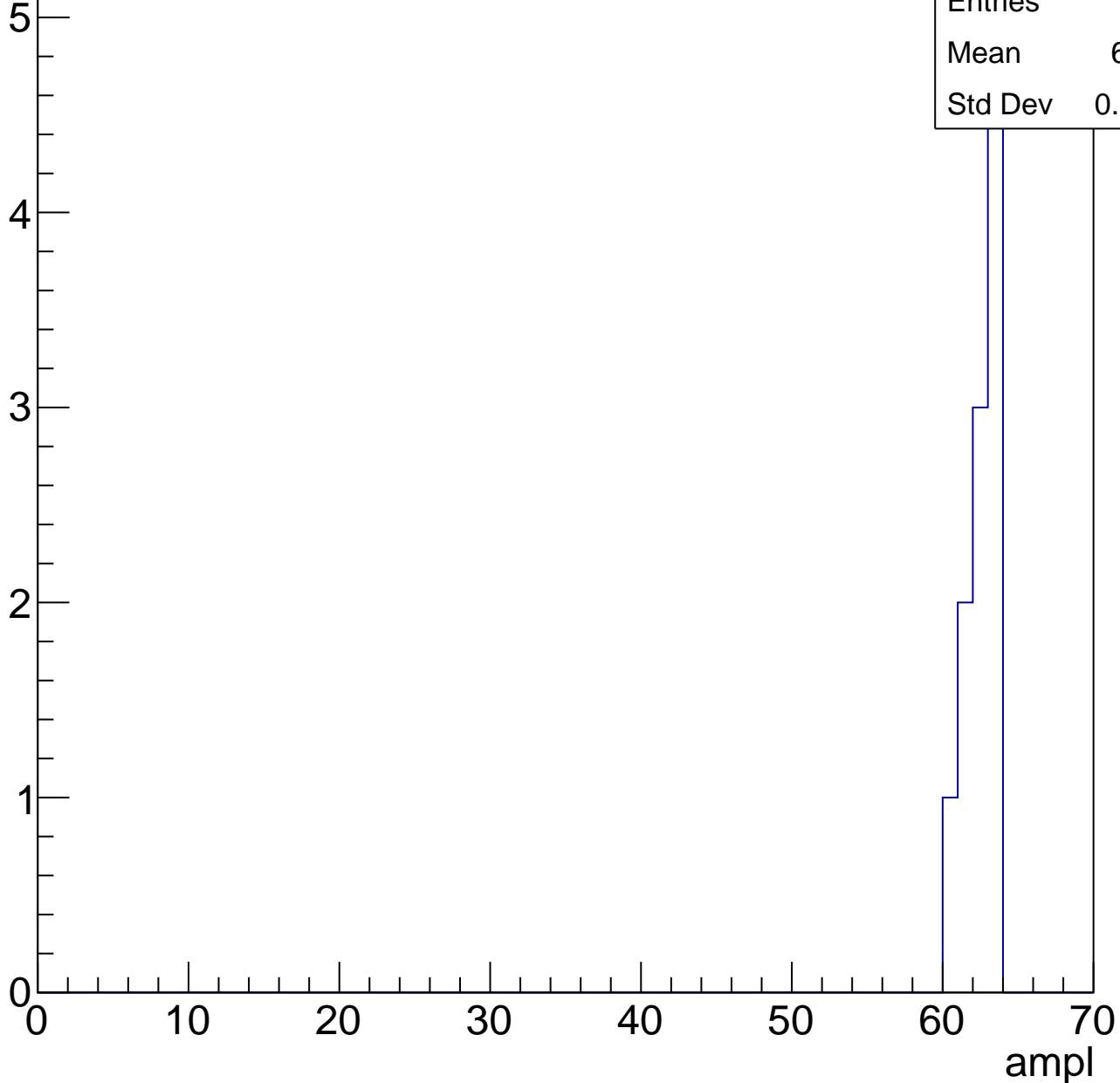


B1L001S, U21-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.09
Std Dev	0.9959



B1L001S, U21-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L001S, U21-ch120, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.87
Std Dev	3.316

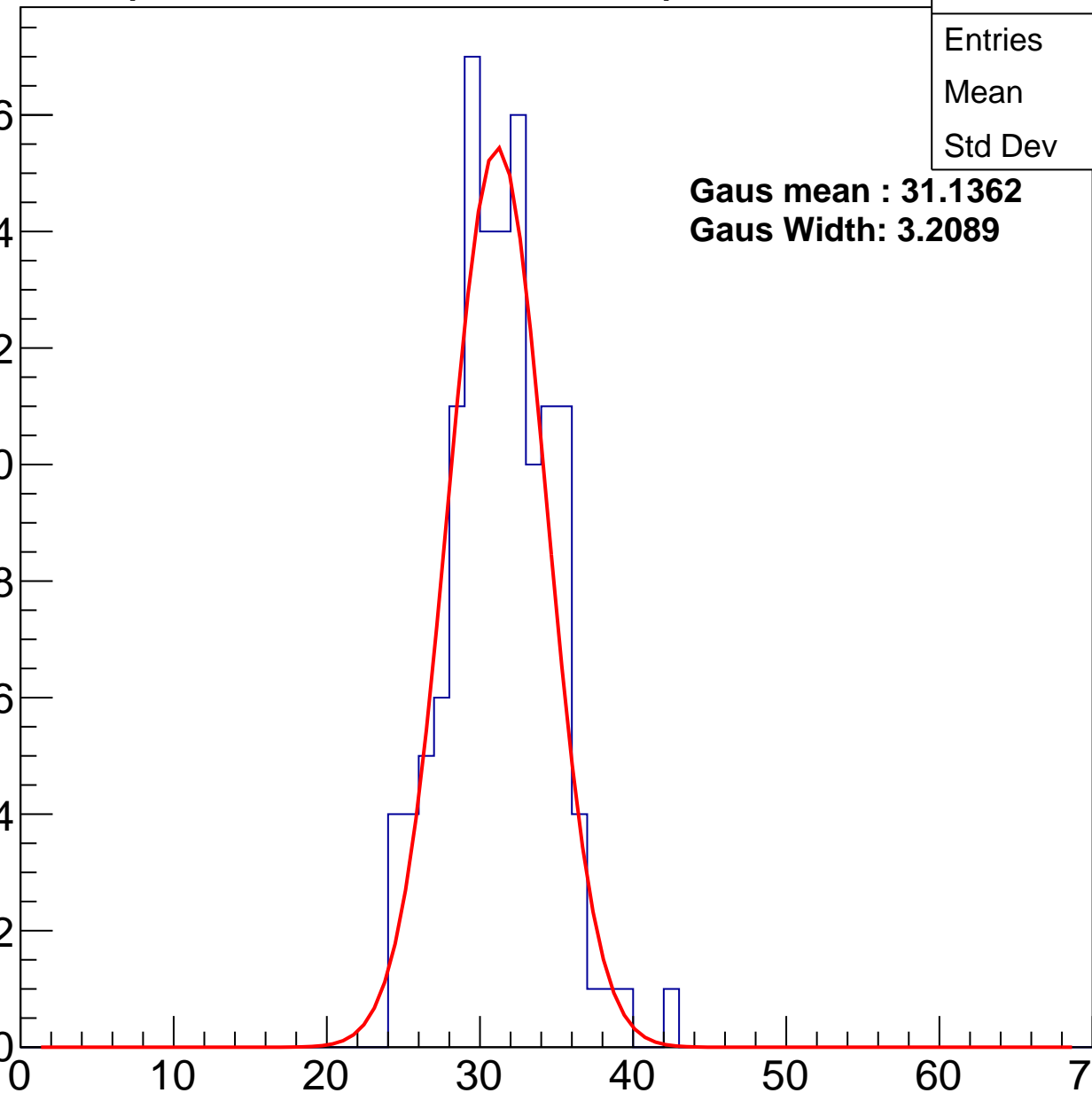
Gaus mean : 31.1362

Gaus Width: 3.2089

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch120, adc1

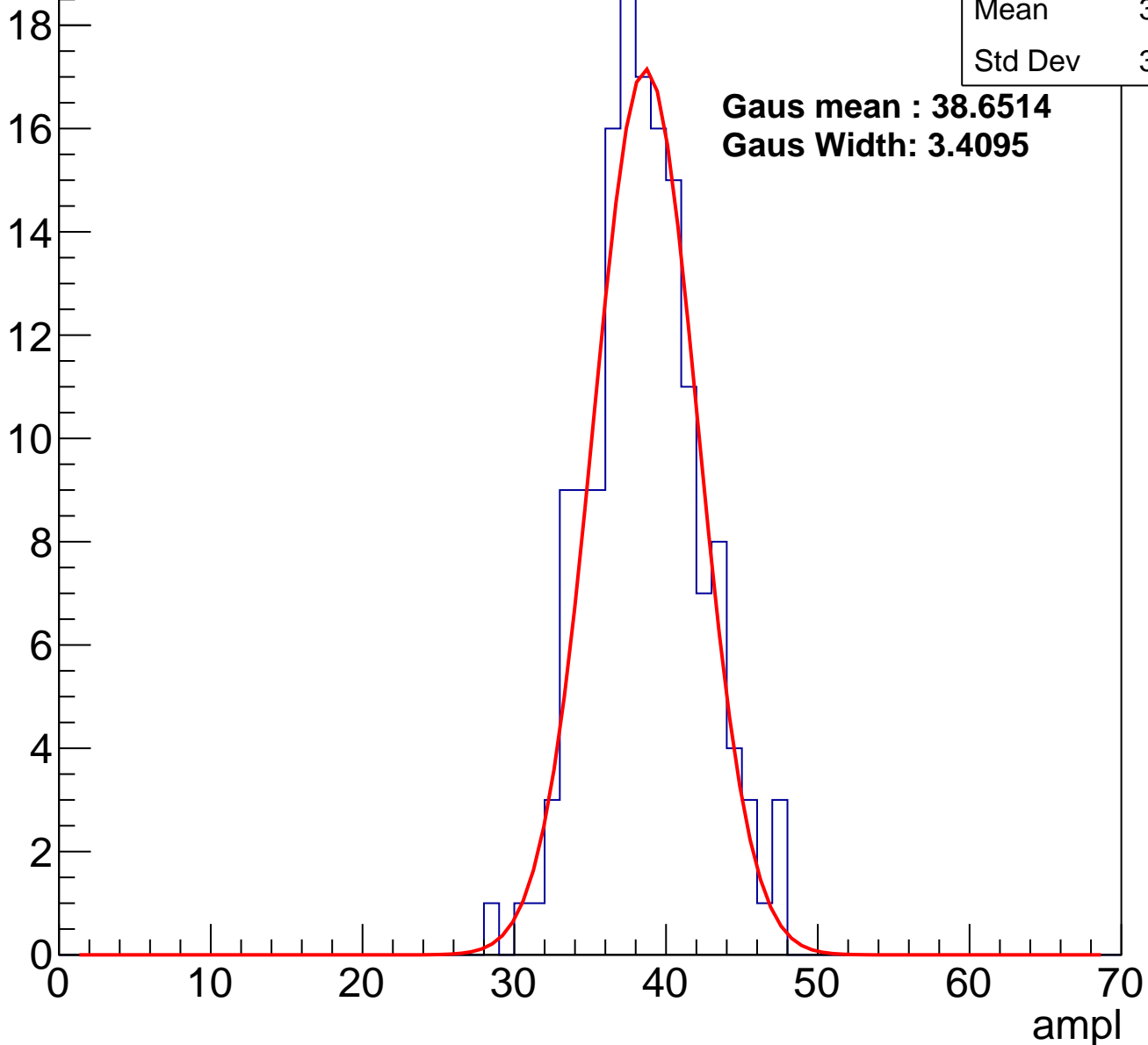
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	38.16
Std Dev	3.556

Gaus mean : 38.6514

Gaus Width: 3.4095

Entry



B1L001S, U21-ch120, adc2

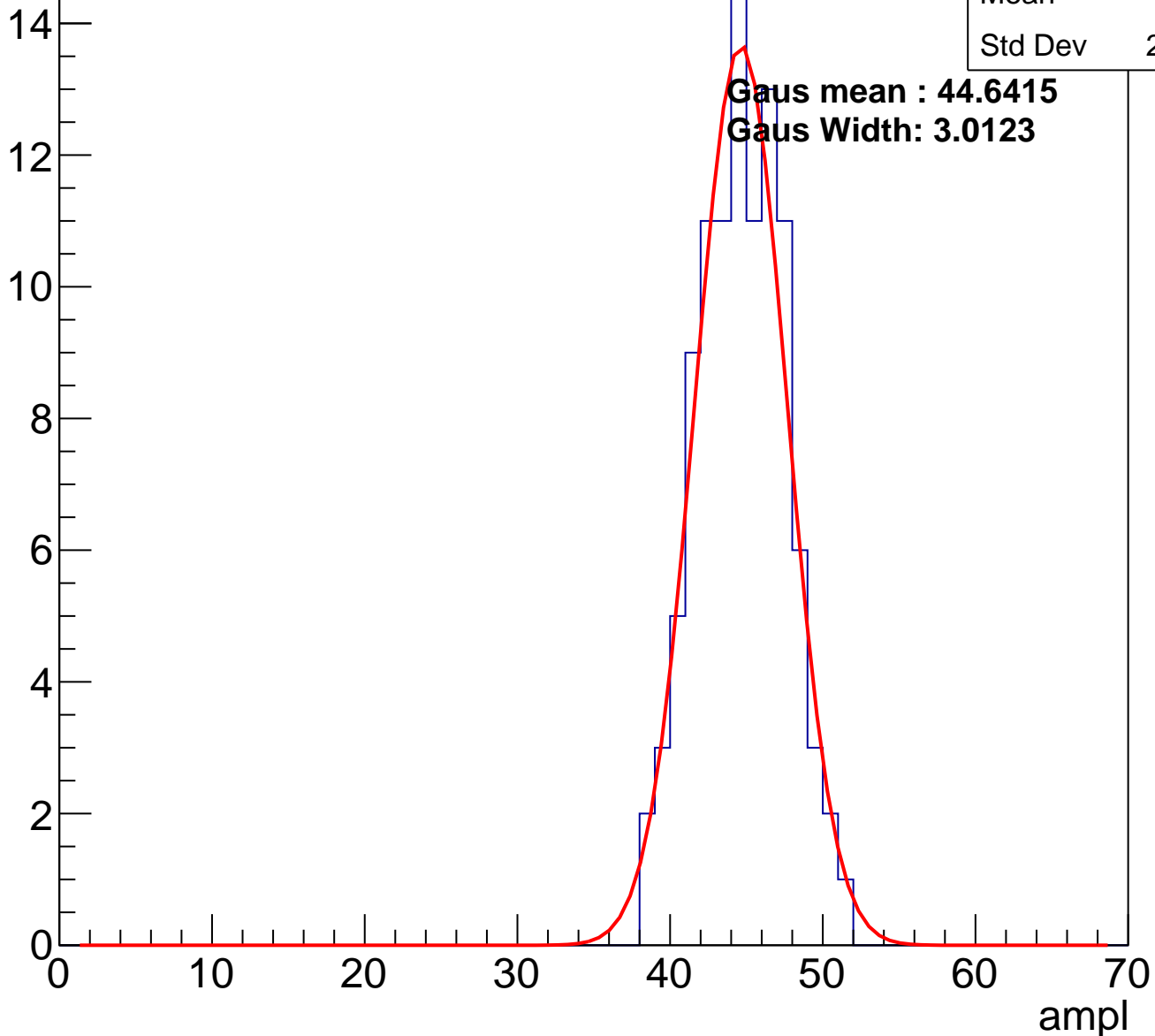
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	44.2
Std Dev	2.809

Gaus mean : 44.6415

Gaus Width: 3.0123

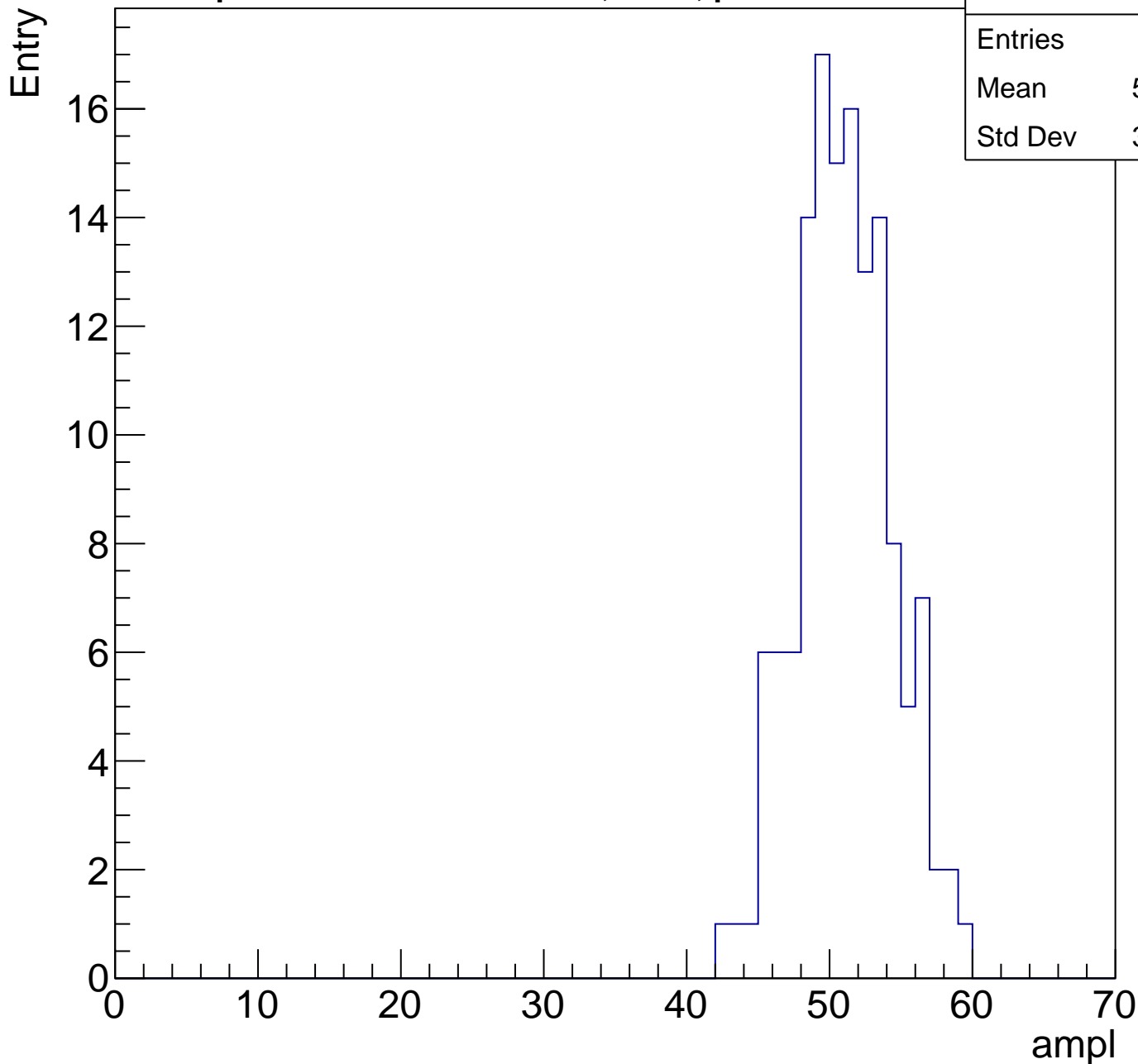
Entry



B1L001S, U21-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

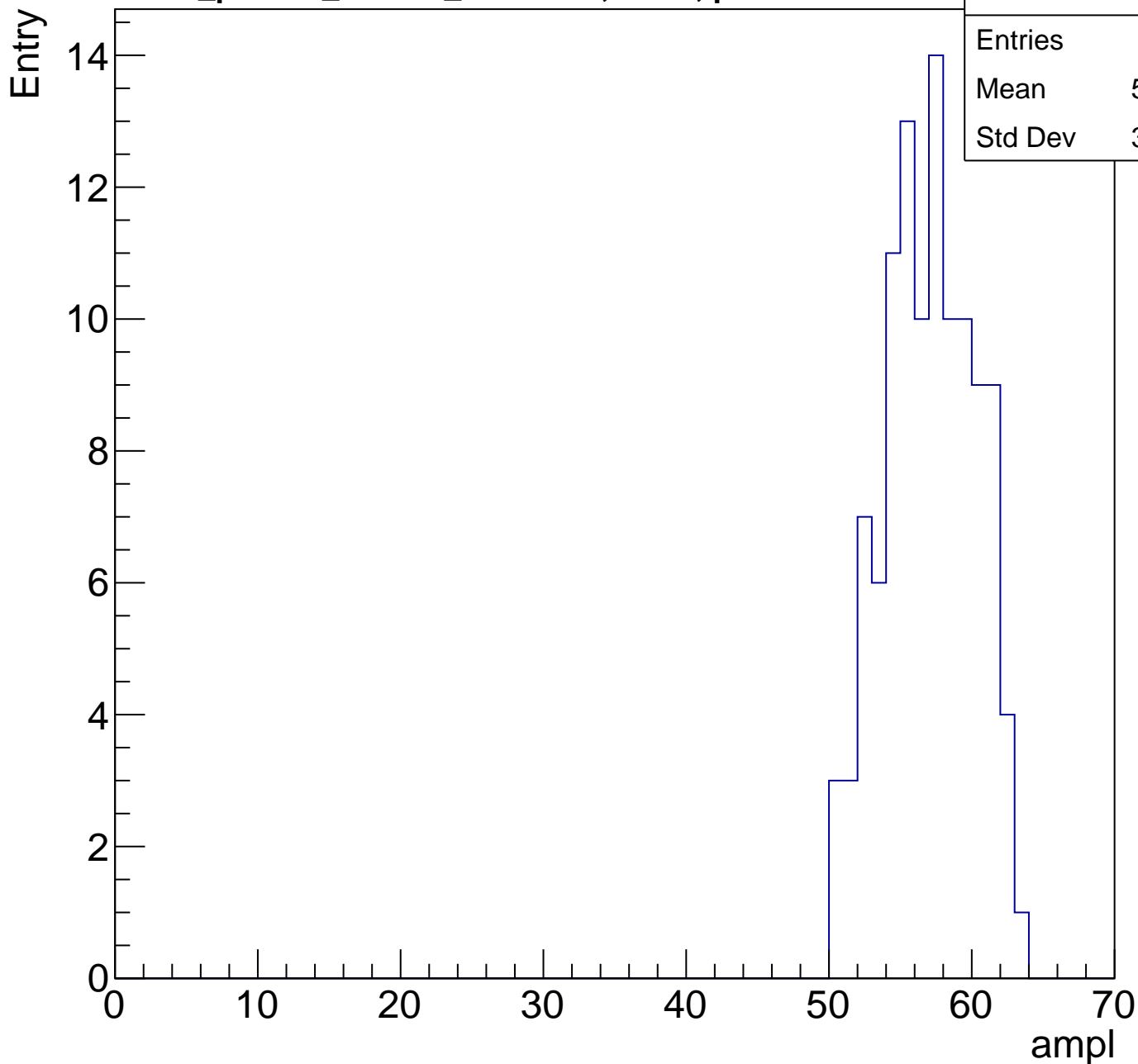
Entries	135
Mean	50.62
Std Dev	3.306



B1L001S, U21-ch120, adc4

calib_packv5_042523_0143.root, FC#2, port C2

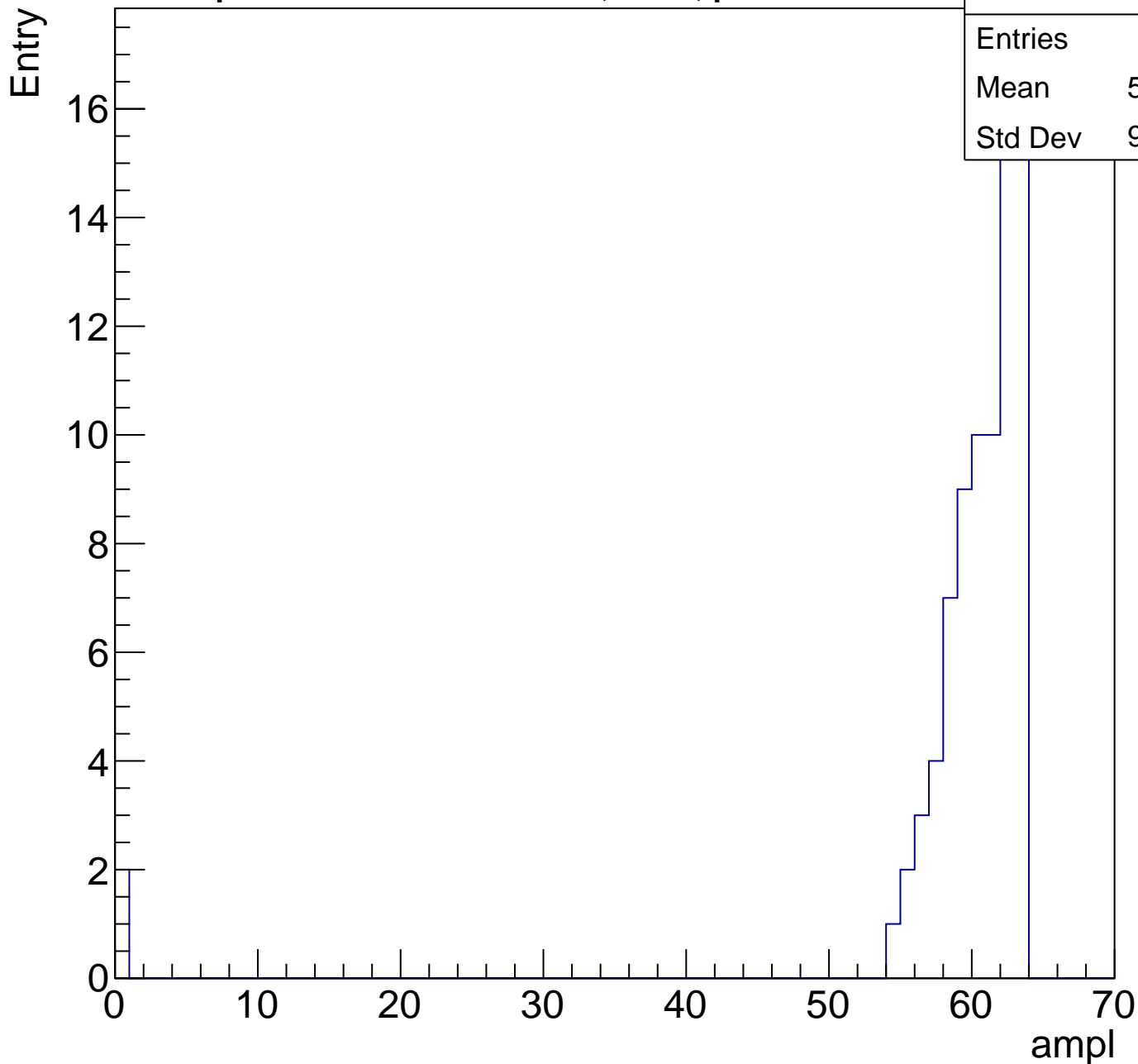
Entries	110
Mean	56.56
Std Dev	3.129



B1L001S, U21-ch120, adc5

calib_packv5_042523_0143.root, FC#2, port C2

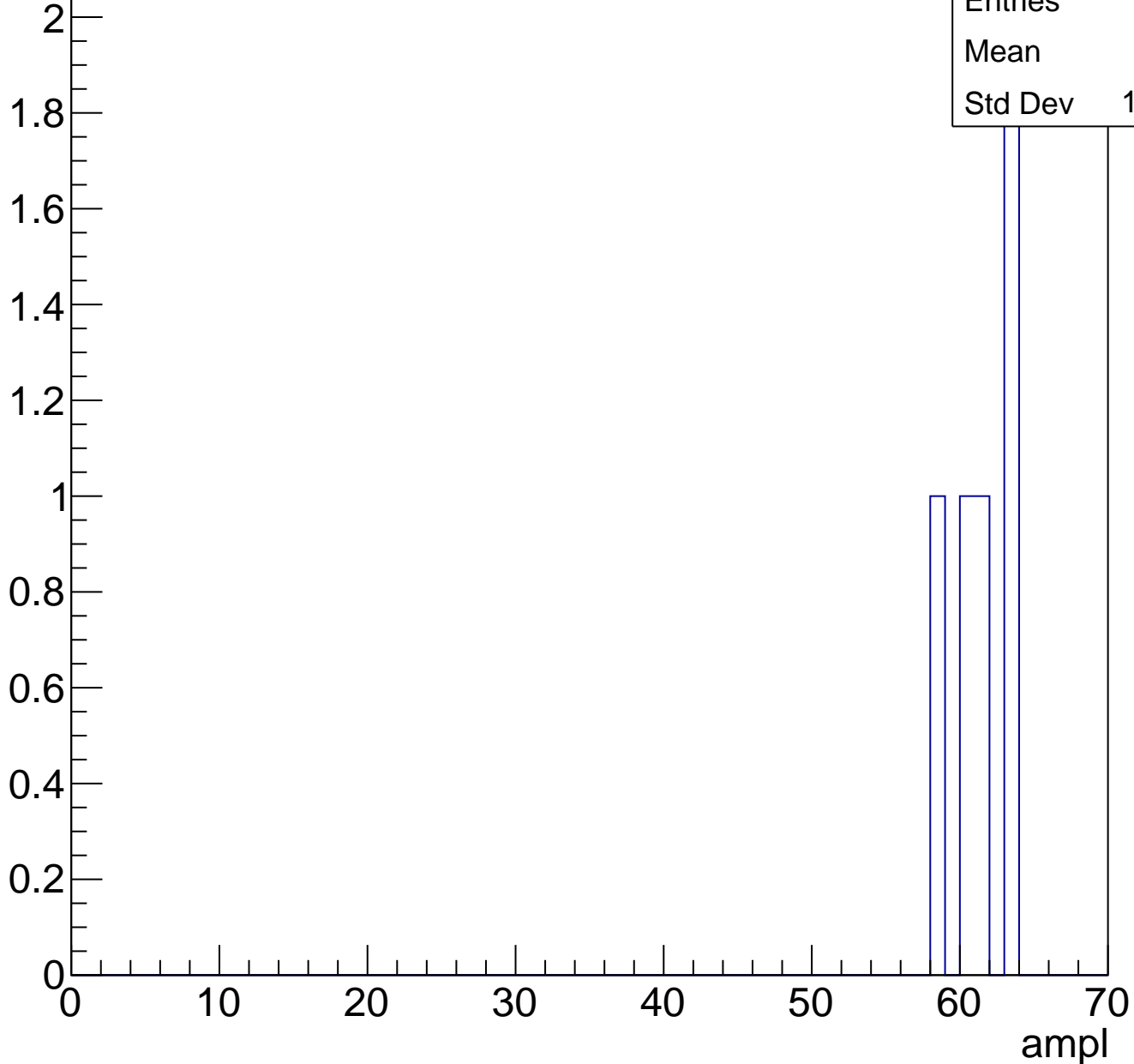
Entries	81
Mean	58.89
Std Dev	9.644



B1L001S, U21-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U21-ch121, adc0

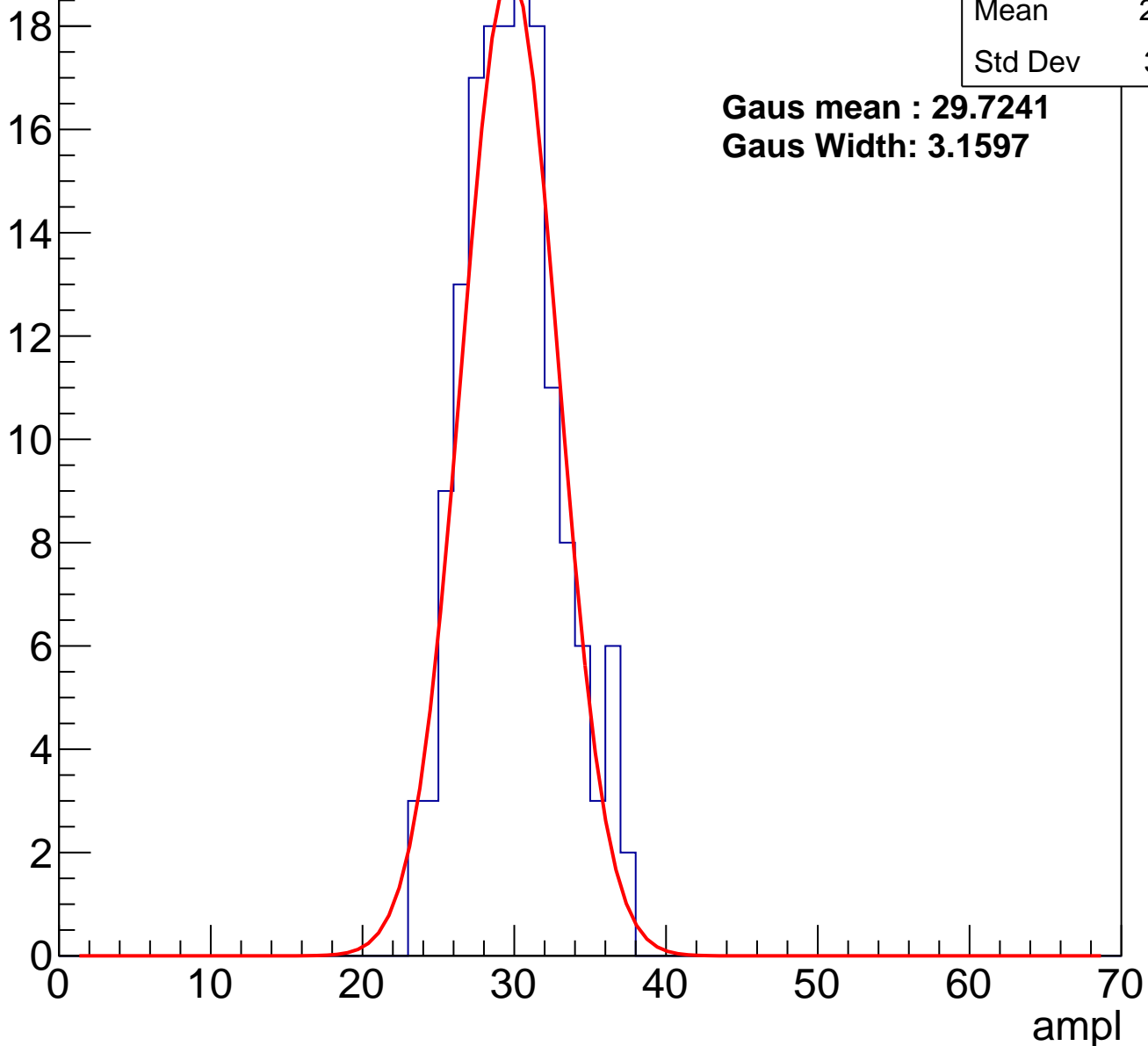
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	29.43
Std Dev	3.121

Gaus mean : 29.7241

Gaus Width: 3.1597

Entry



B1L001S, U21-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.3
Std Dev	3.153

Gaus mean : 37.1278

Gaus Width: 3.2288

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

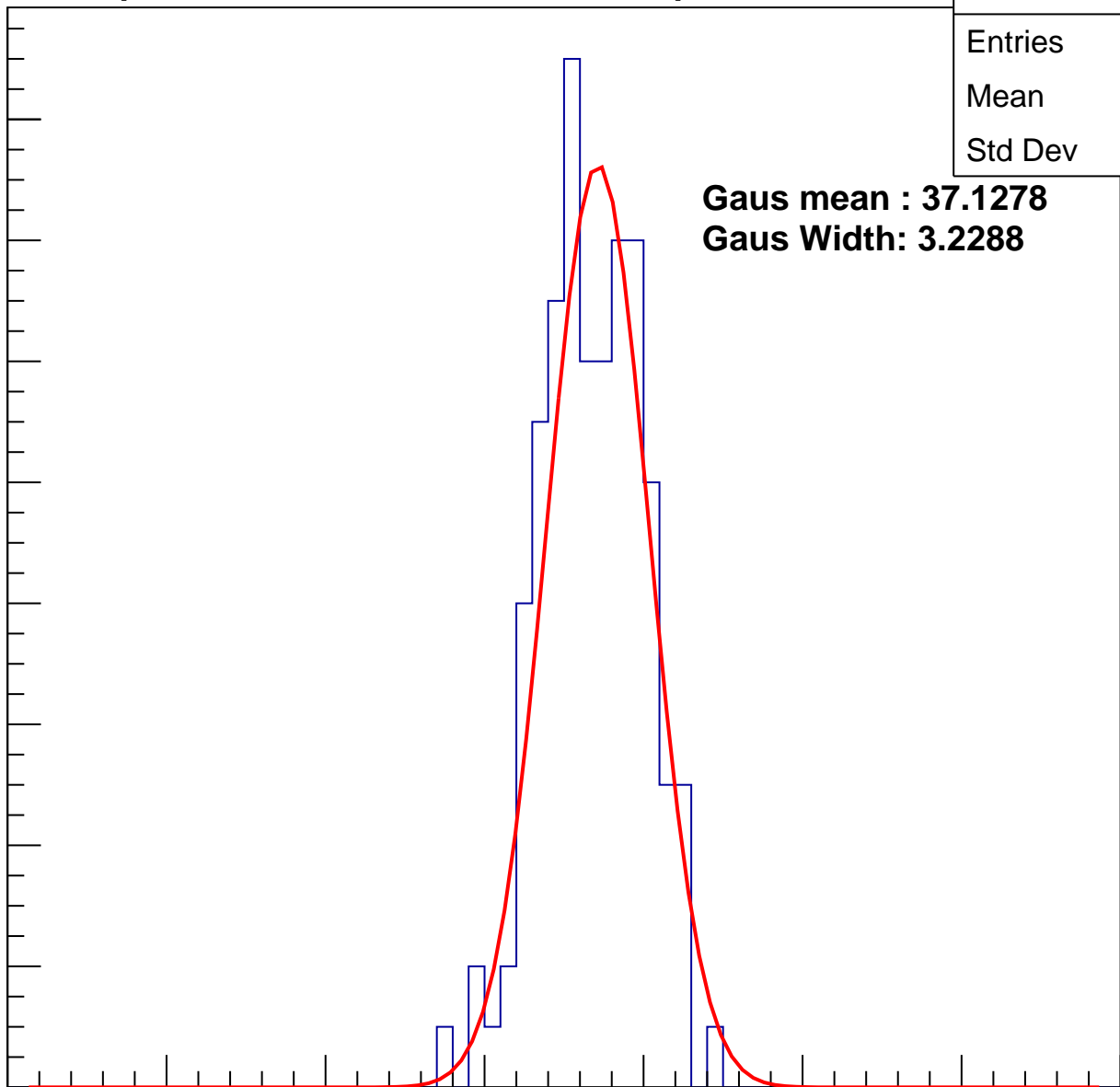
40

50

60

70

ampl



B1L001S, U21-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

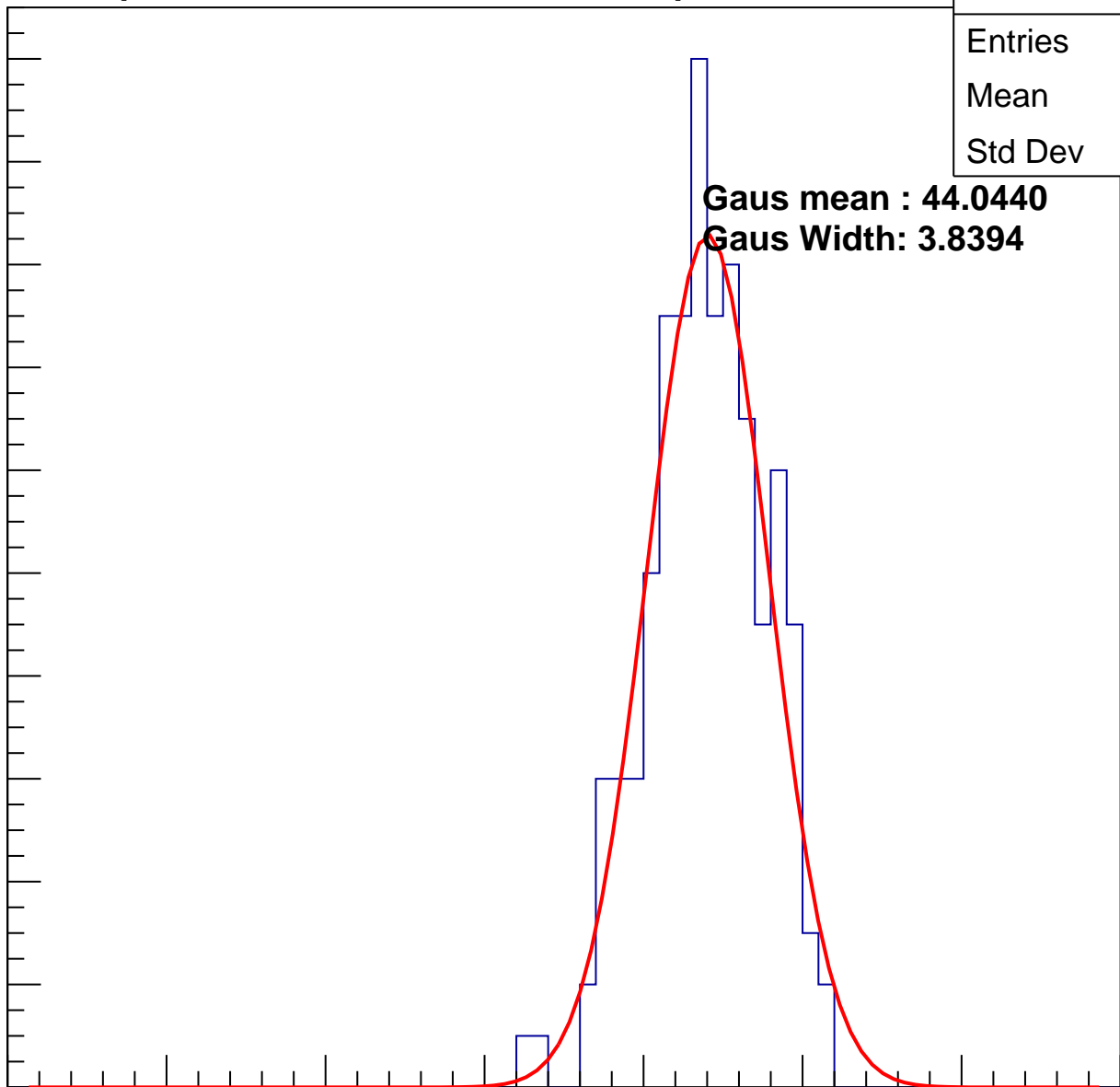
Entries	161
Mean	43.45
Std Dev	3.656

Gaus mean : 44.0440

Gaus Width: 3.8394

0 10 20 30 40 50 60 70

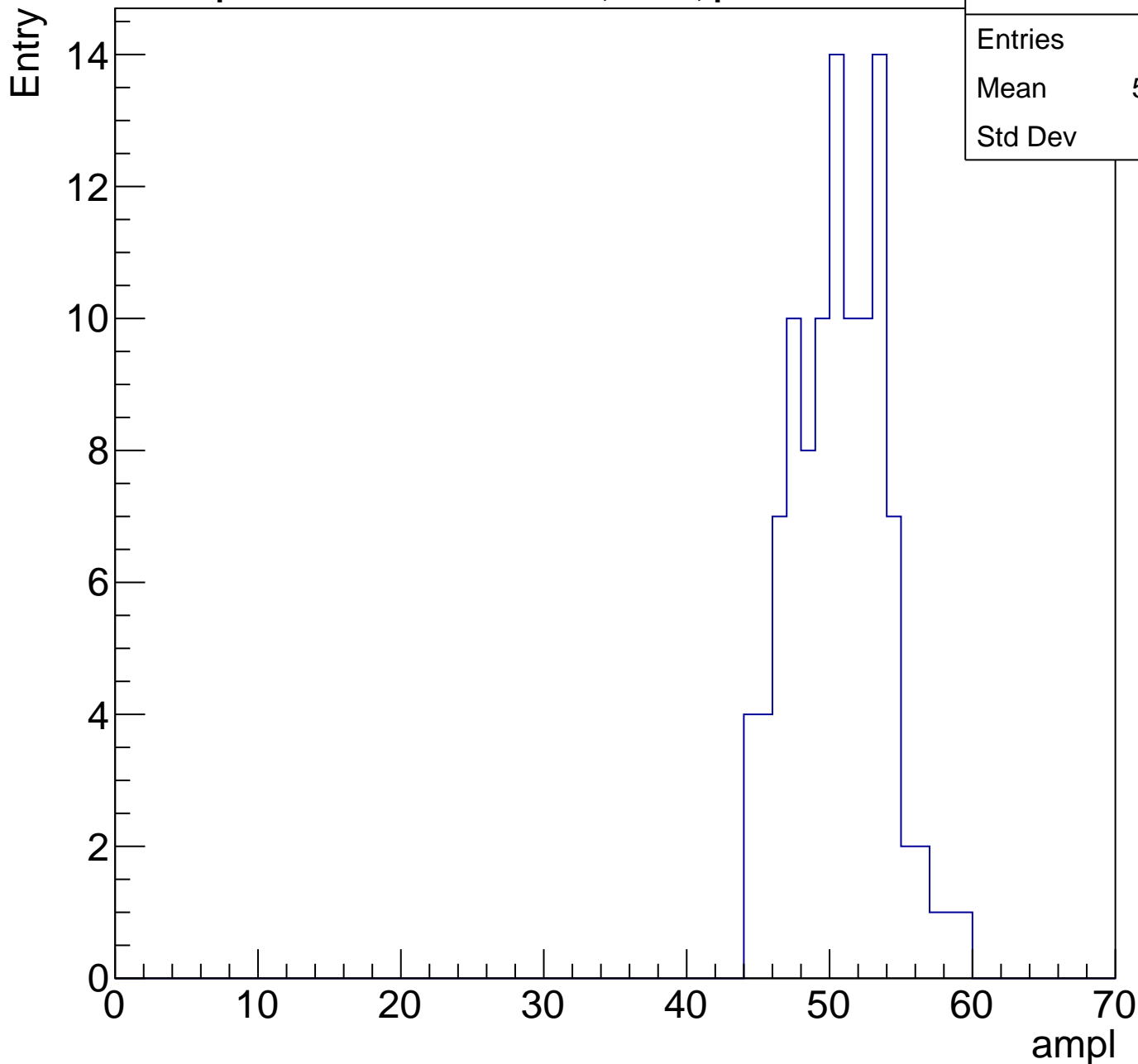
ampl



B1L001S, U21-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	50.17
Std Dev	3.22



B1L001S, U21-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	54.81
Std Dev	7.729

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

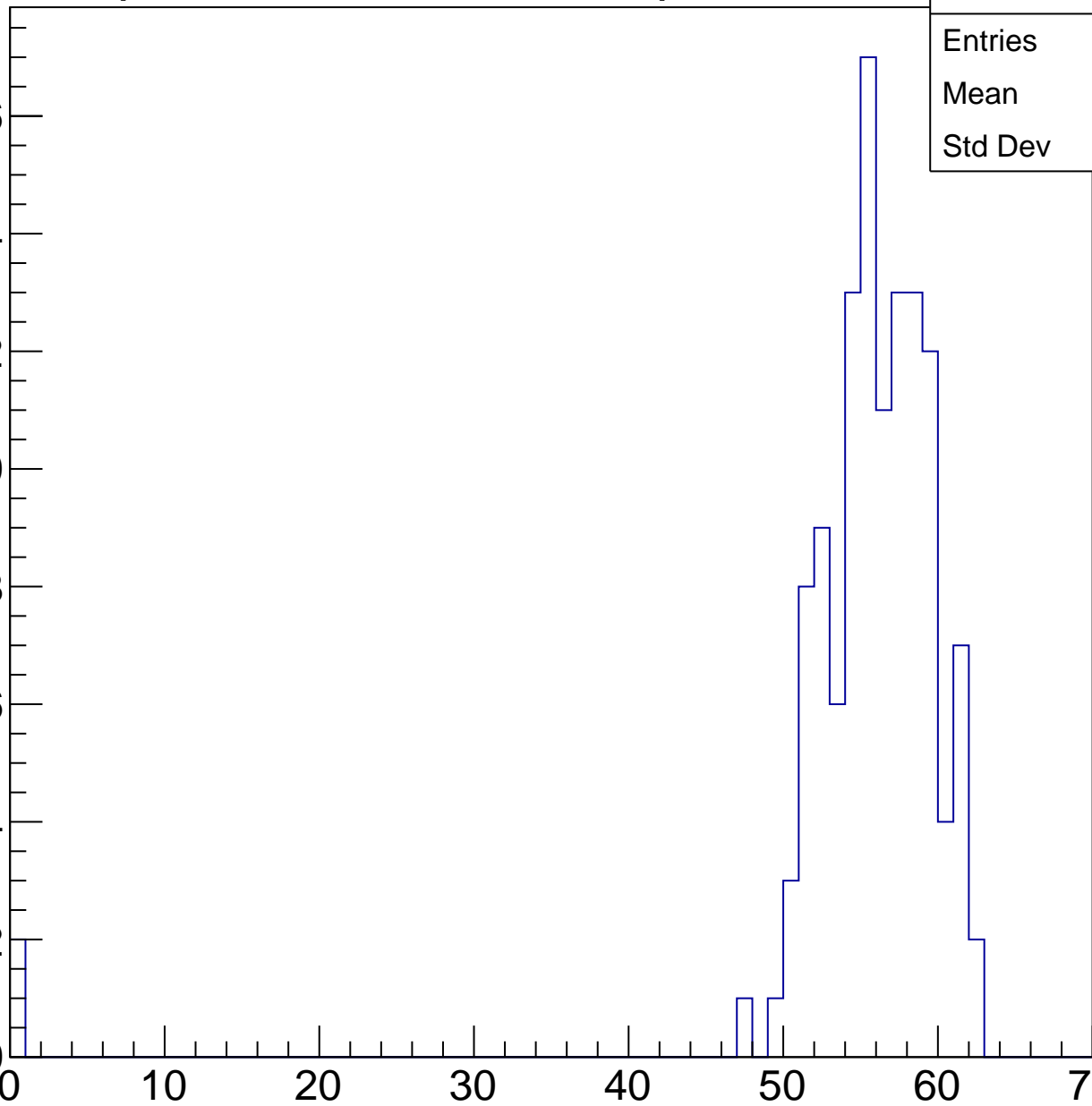
40

50

60

70

ampl



B1L001S, U21-ch121, adc5

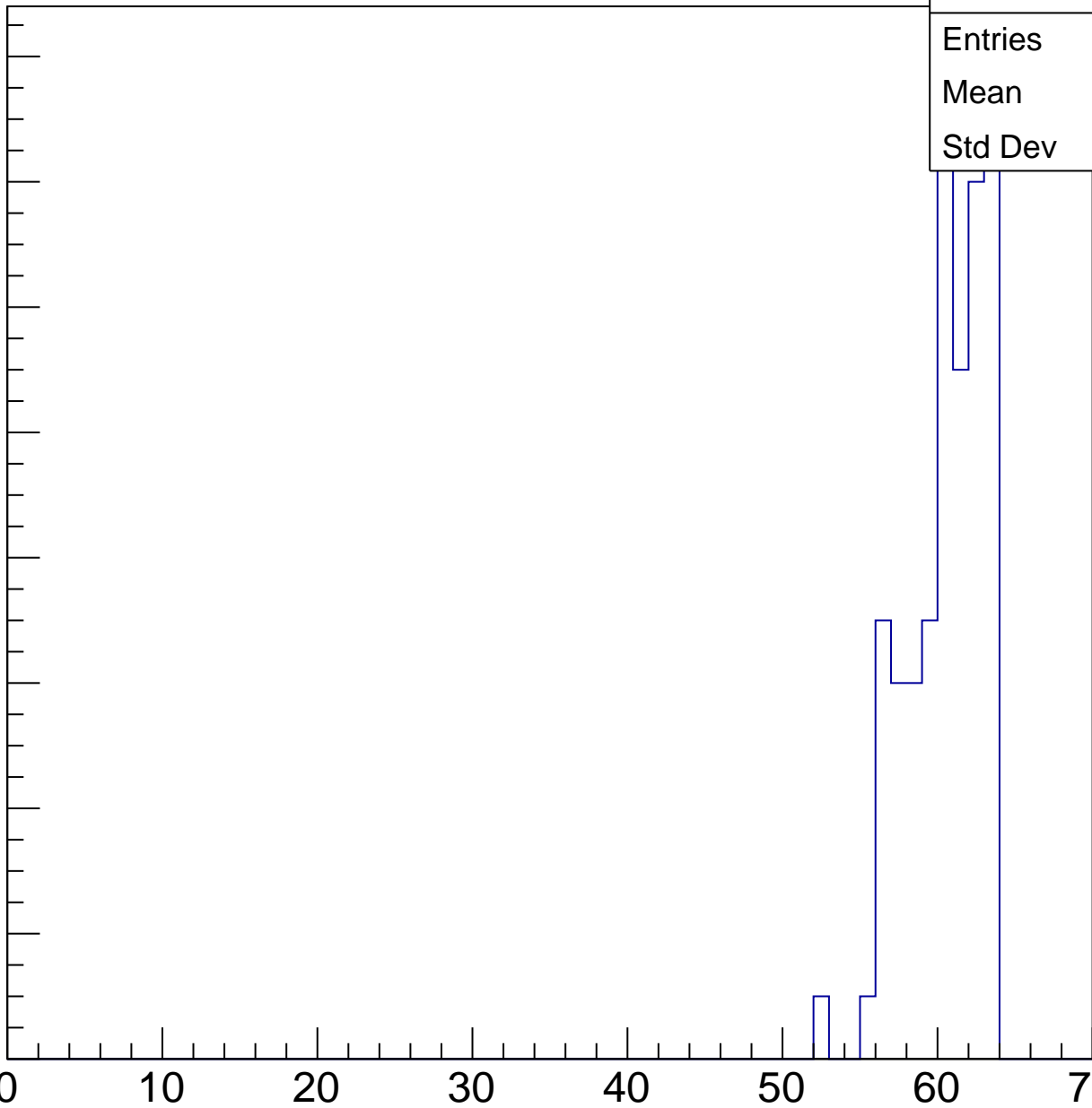
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	60.11
Std Dev	2.43

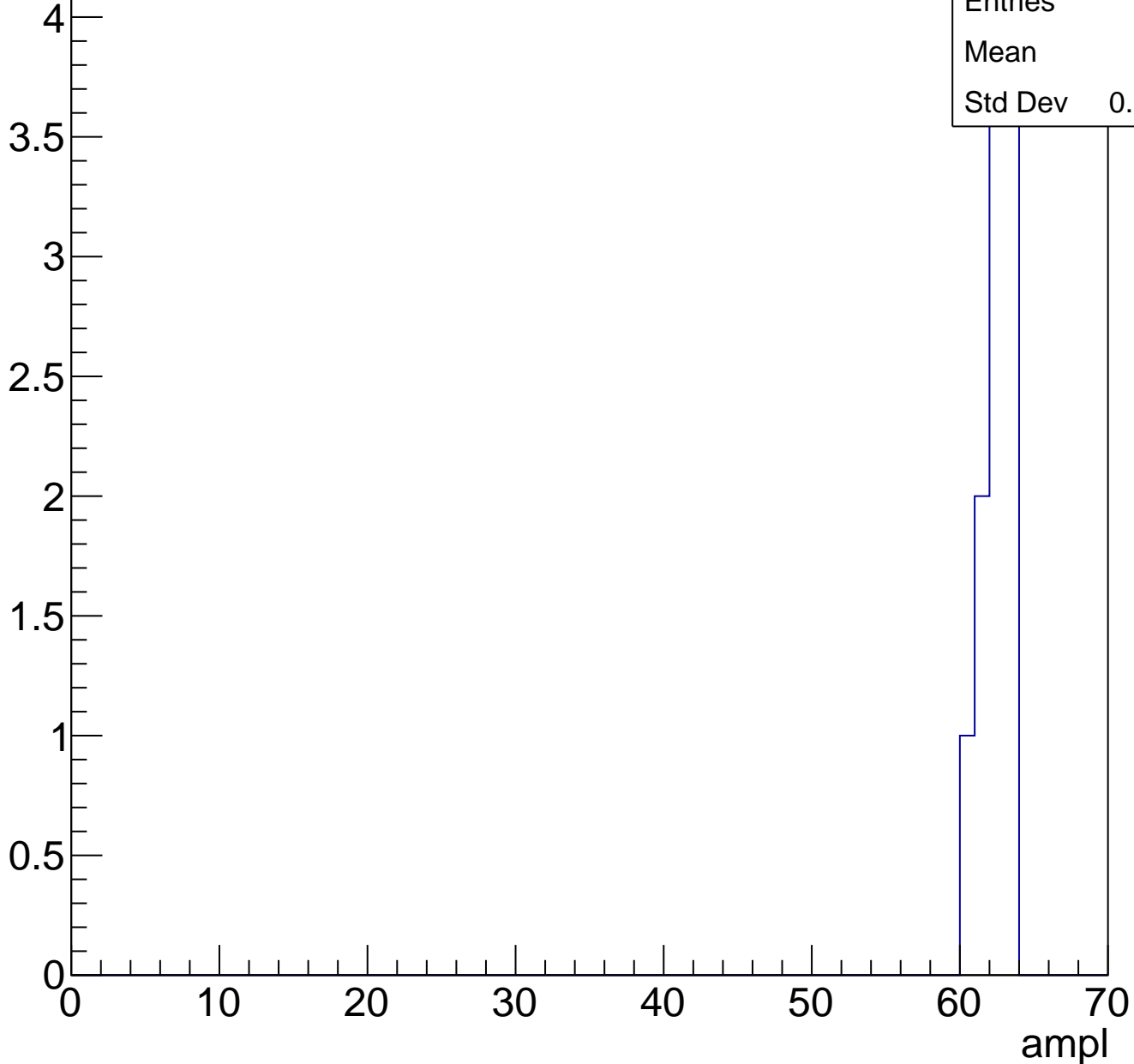
ampl



B1L001S, U21-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

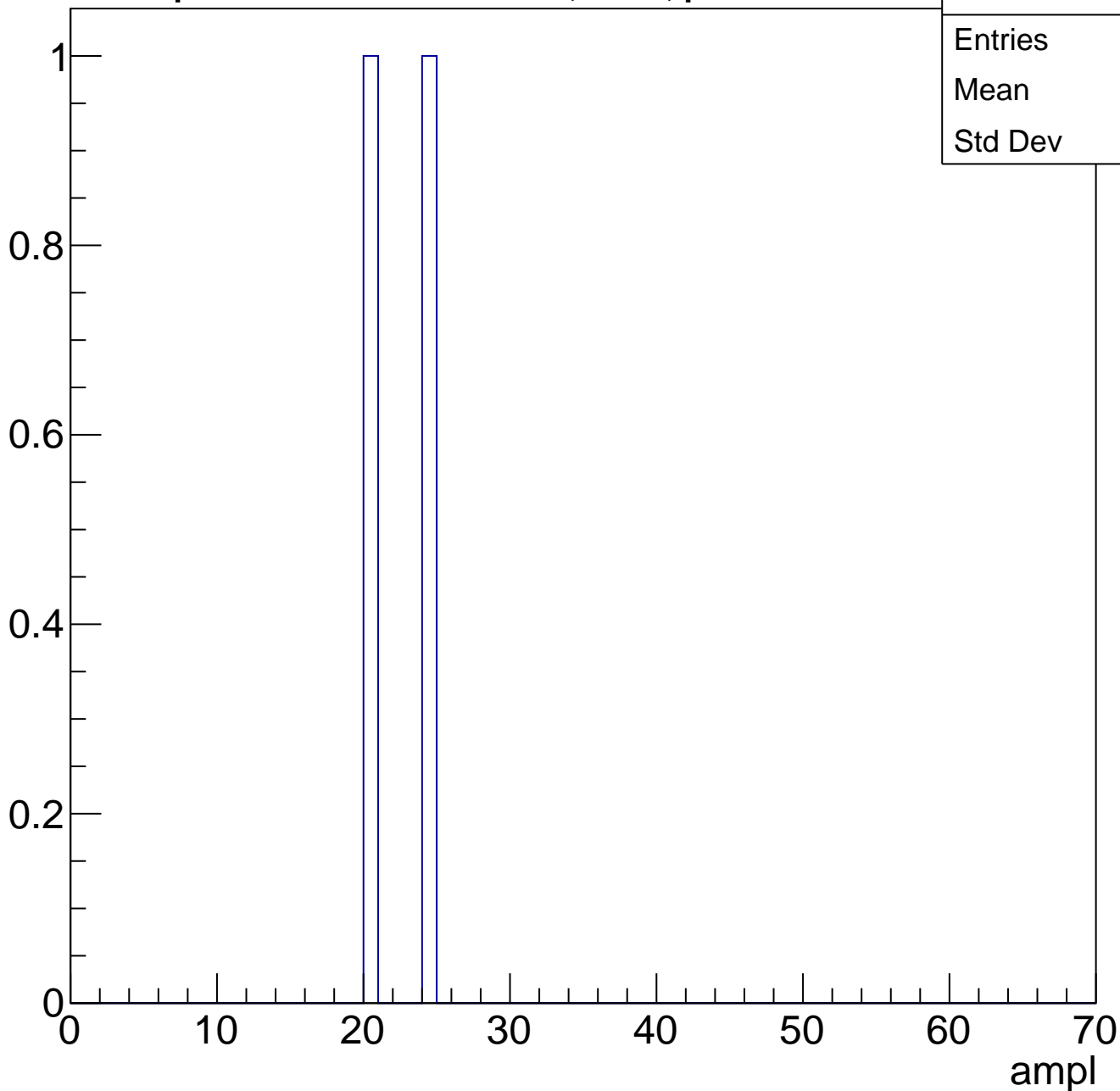
Entry



B1L001S, U21-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch122, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.13
Std Dev	4.307

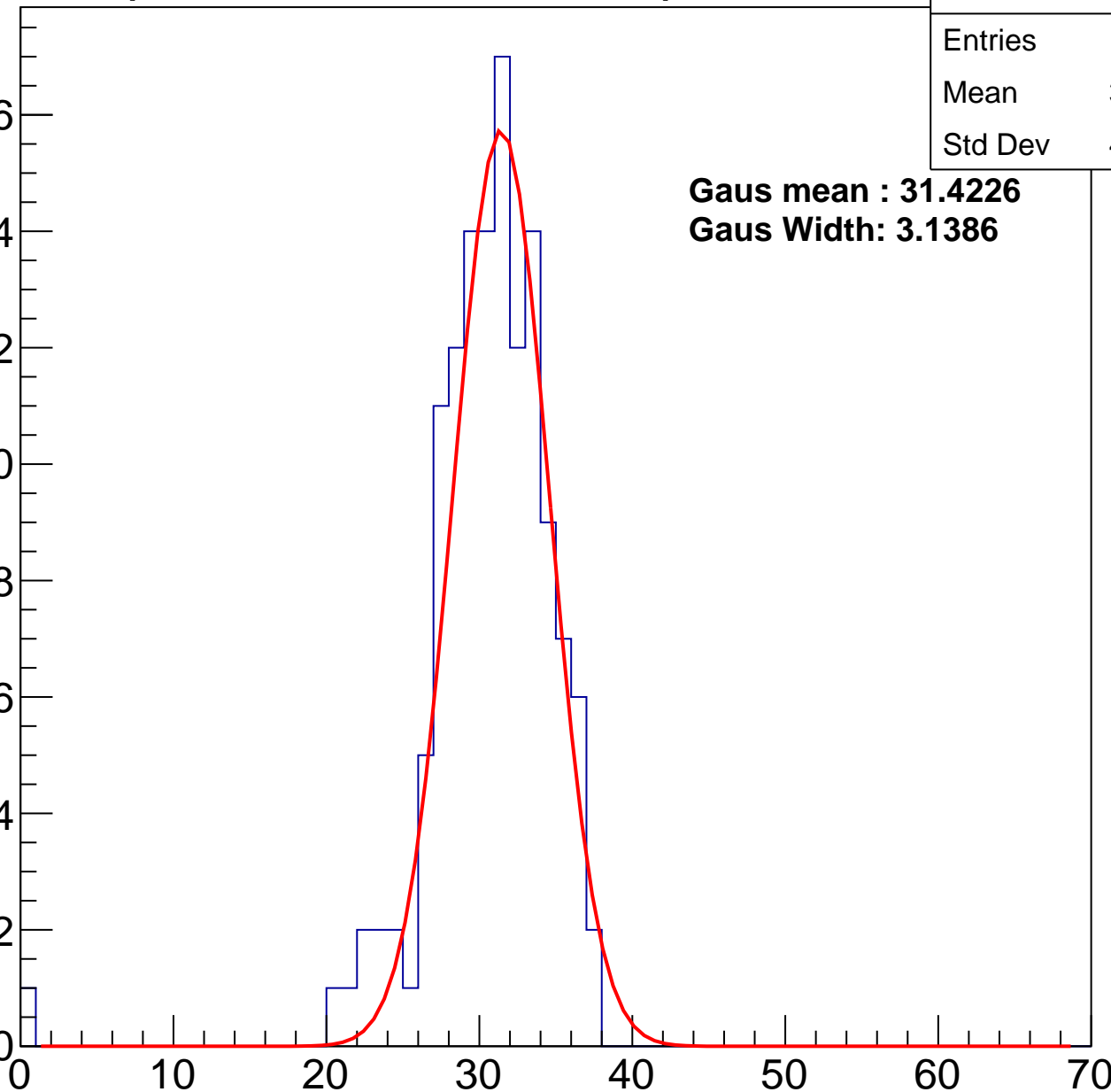
Gaus mean : 31.4226

Gaus Width: 3.1386

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U21-ch122, adc1

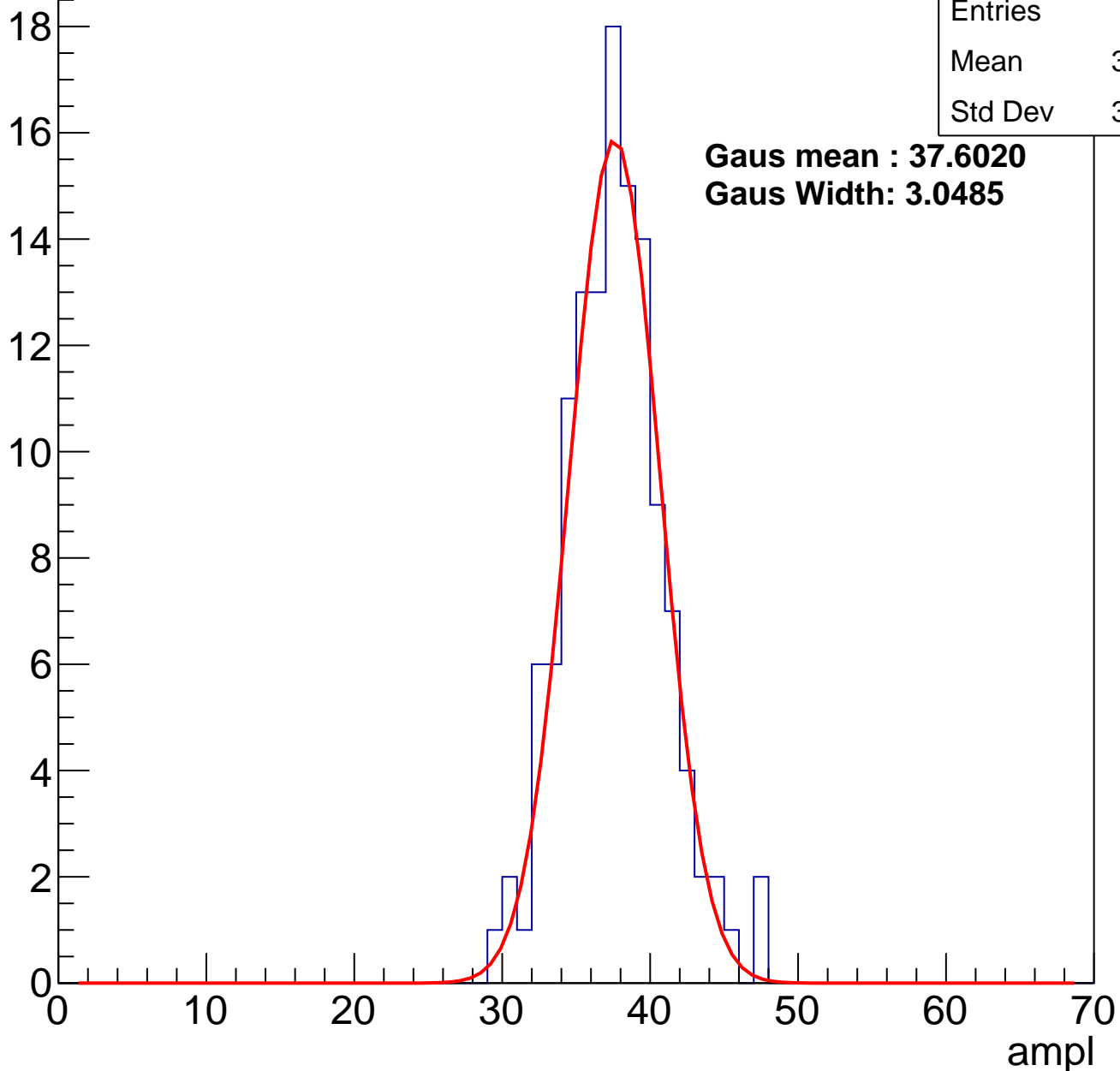
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	37.14
Std Dev	3.324

Gaus mean : 37.6020

Gaus Width: 3.0485

Entry



B1L001S, U21-ch122, adc2

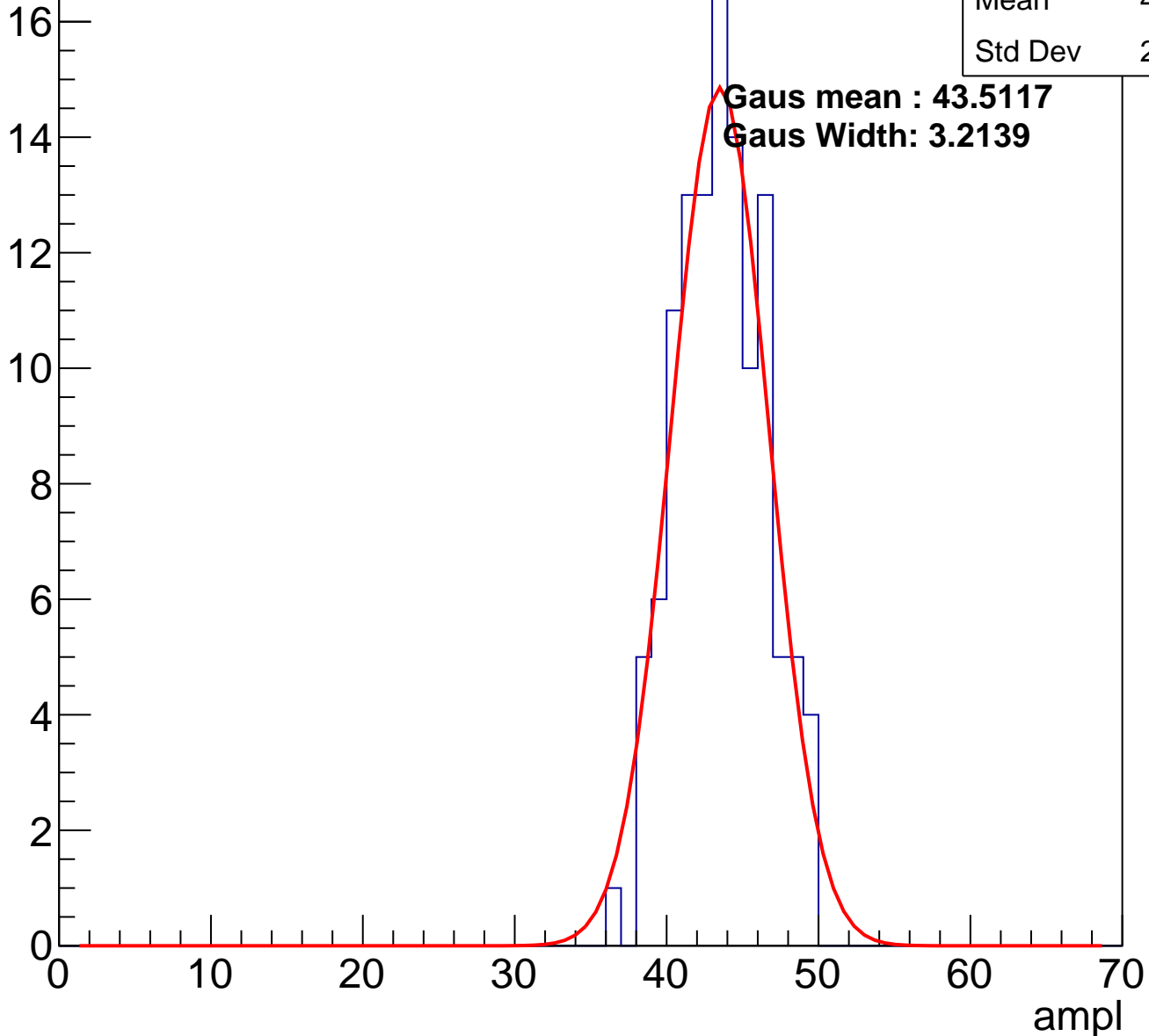
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	117
Mean	43.12
Std Dev	2.853

Gaus mean : 43.5117

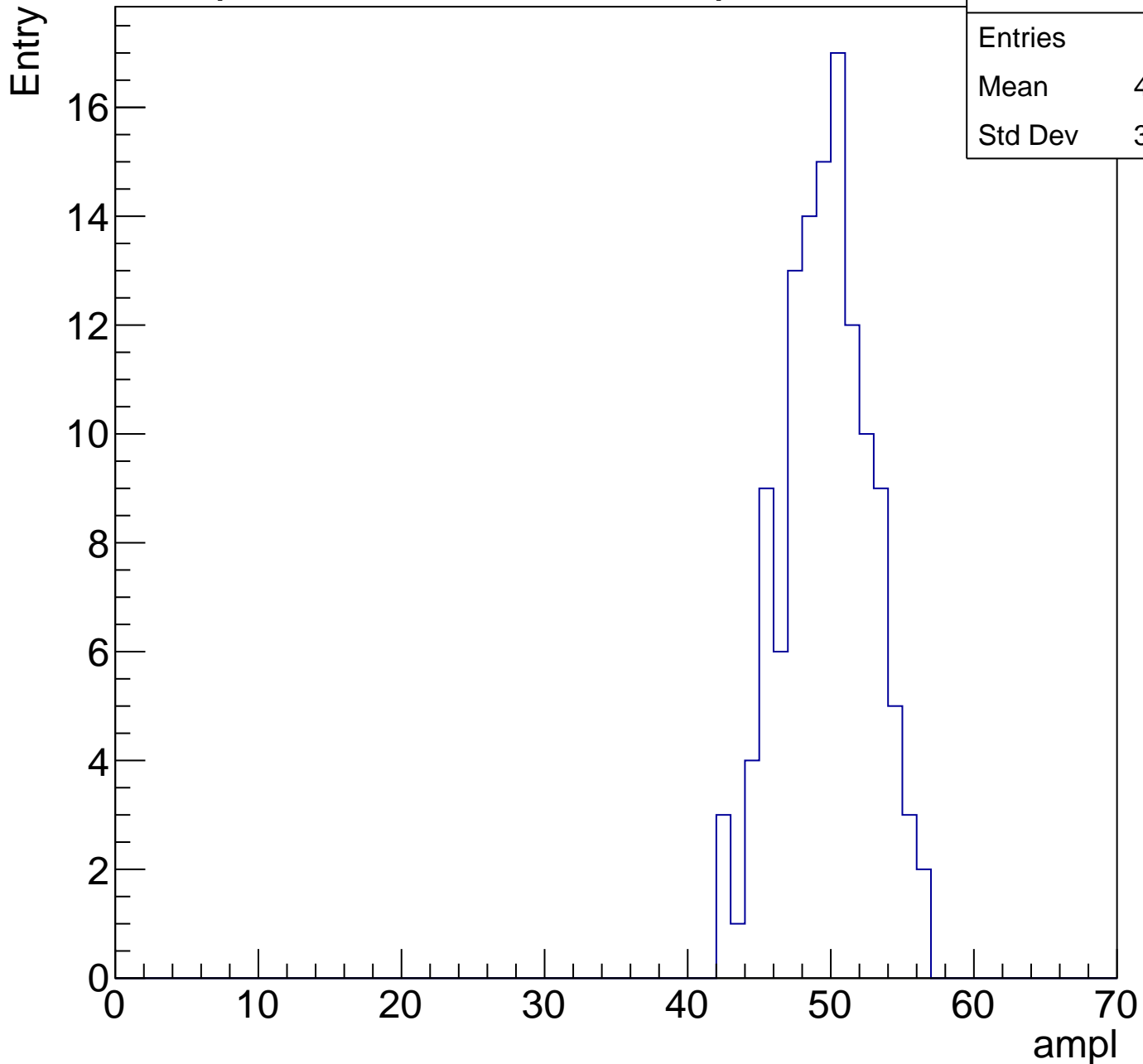
Gaus Width: 3.2139



B1L001S, U21-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

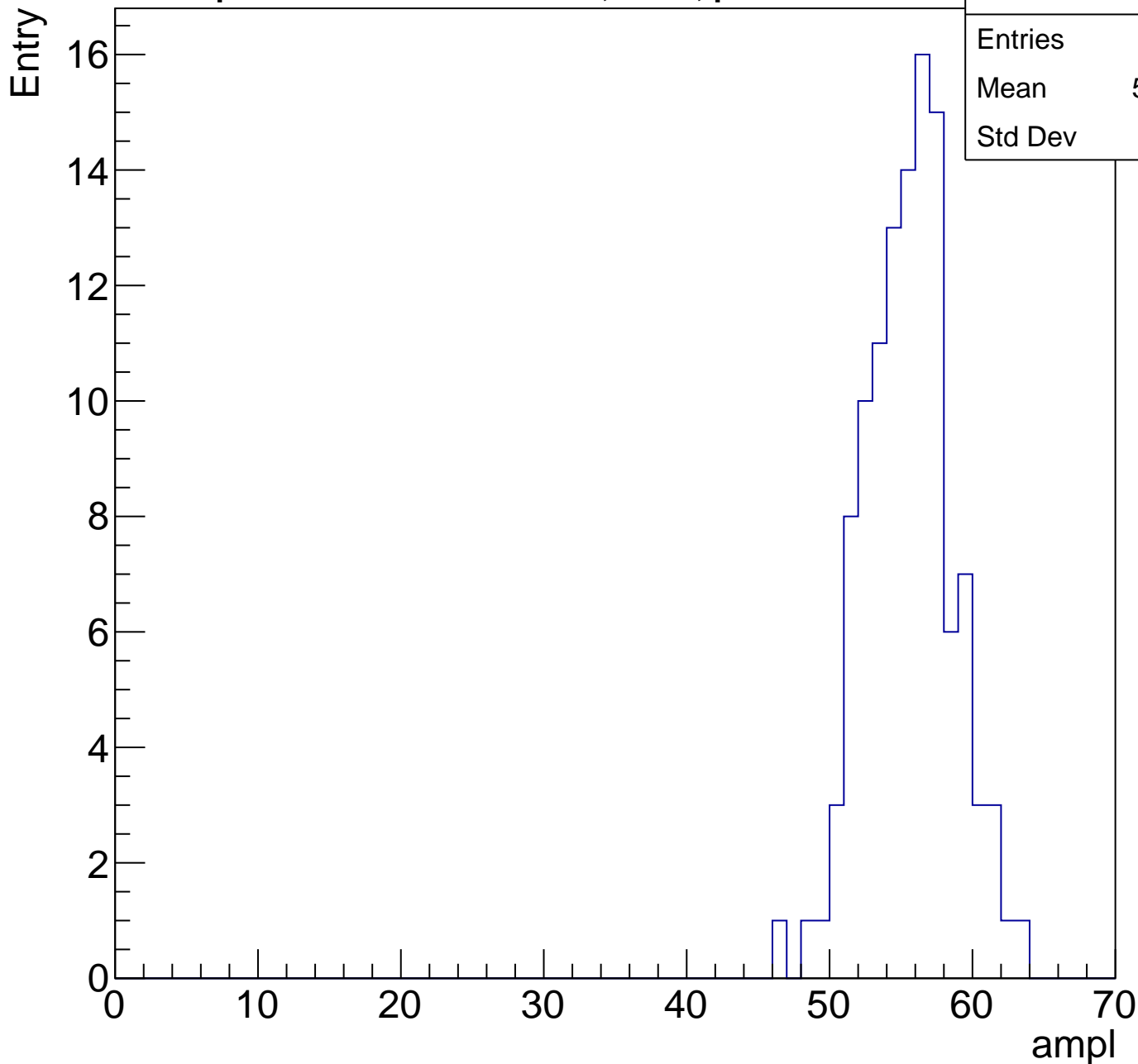
Entries	123
Mean	49.19
Std Dev	3.088



B1L001S, U21-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

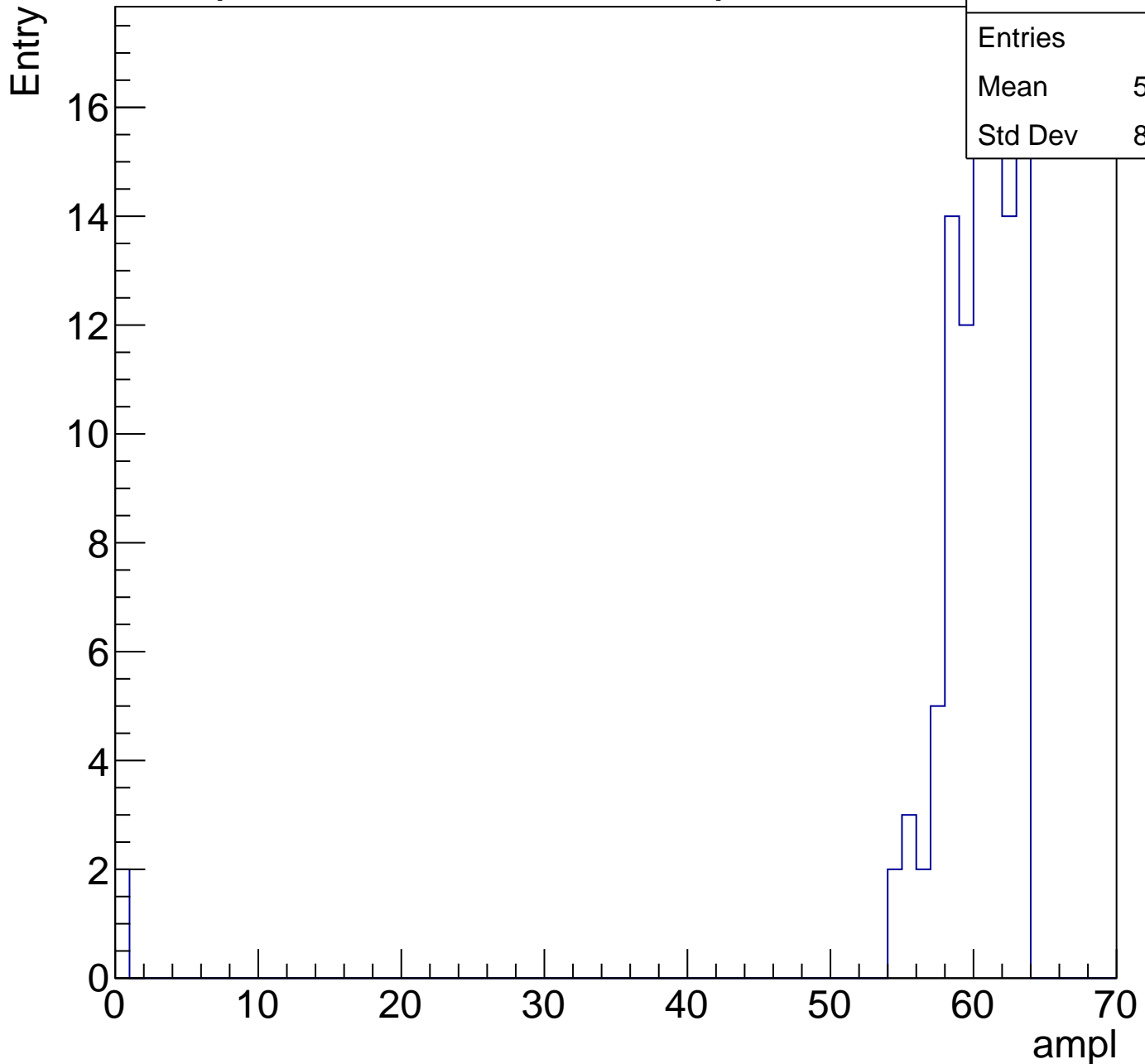
Entries	114
Mean	55.05
Std Dev	3.04



B1L001S, U21-ch122, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	58.86
Std Dev	8.576

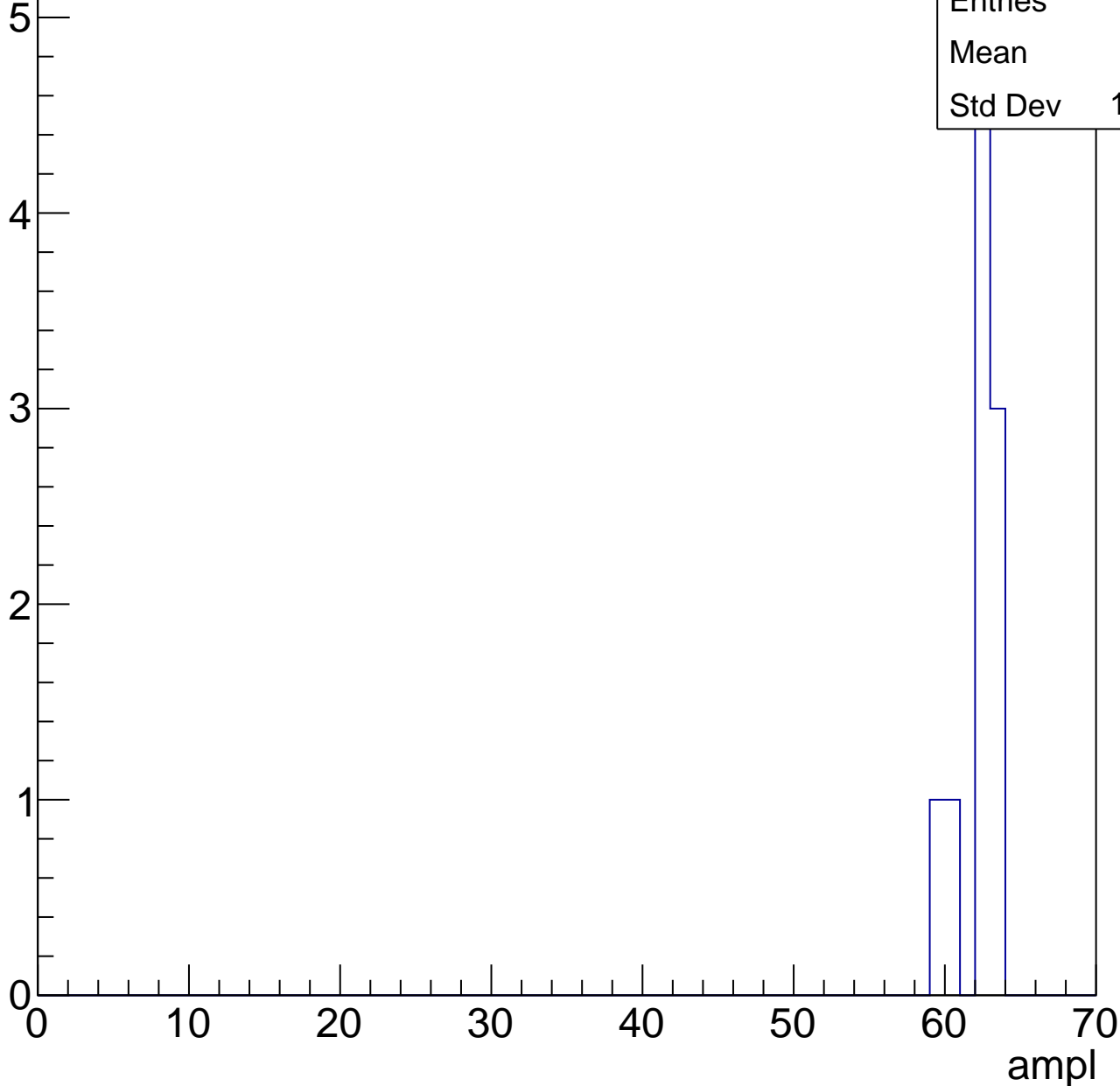


B1L001S, U21-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	61.8
Std Dev	1.249



B1L001S, U21-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch123, adc0

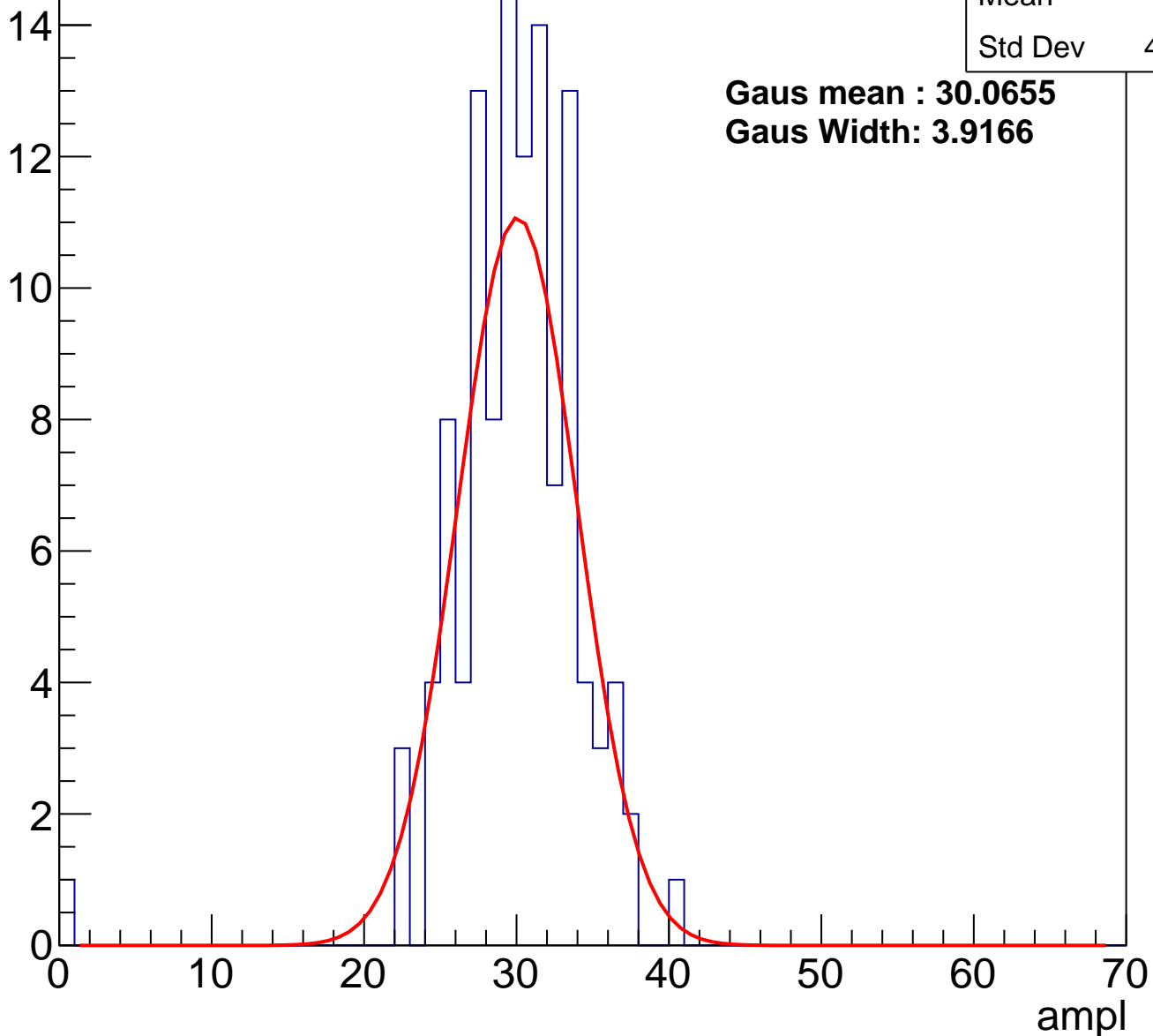
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.5
Std Dev	4.429

Gaus mean : 30.0655

Gaus Width: 3.9166

Entry



B1L001S, U21-ch123, adc1

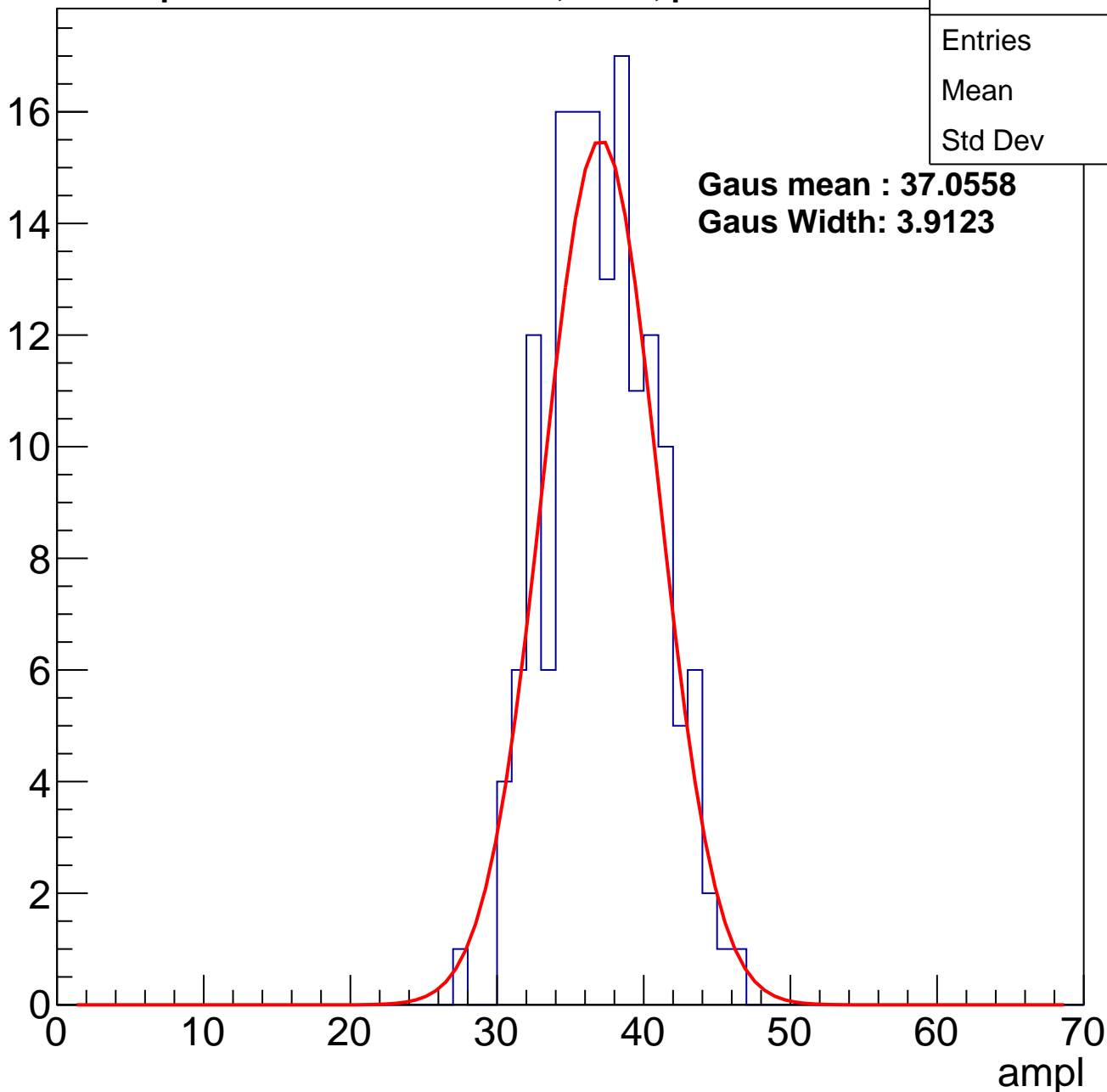
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.7
Std Dev	3.62

Gaus mean : 37.0558

Gaus Width: 3.9123

Entry



B1L001S, U21-ch123, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

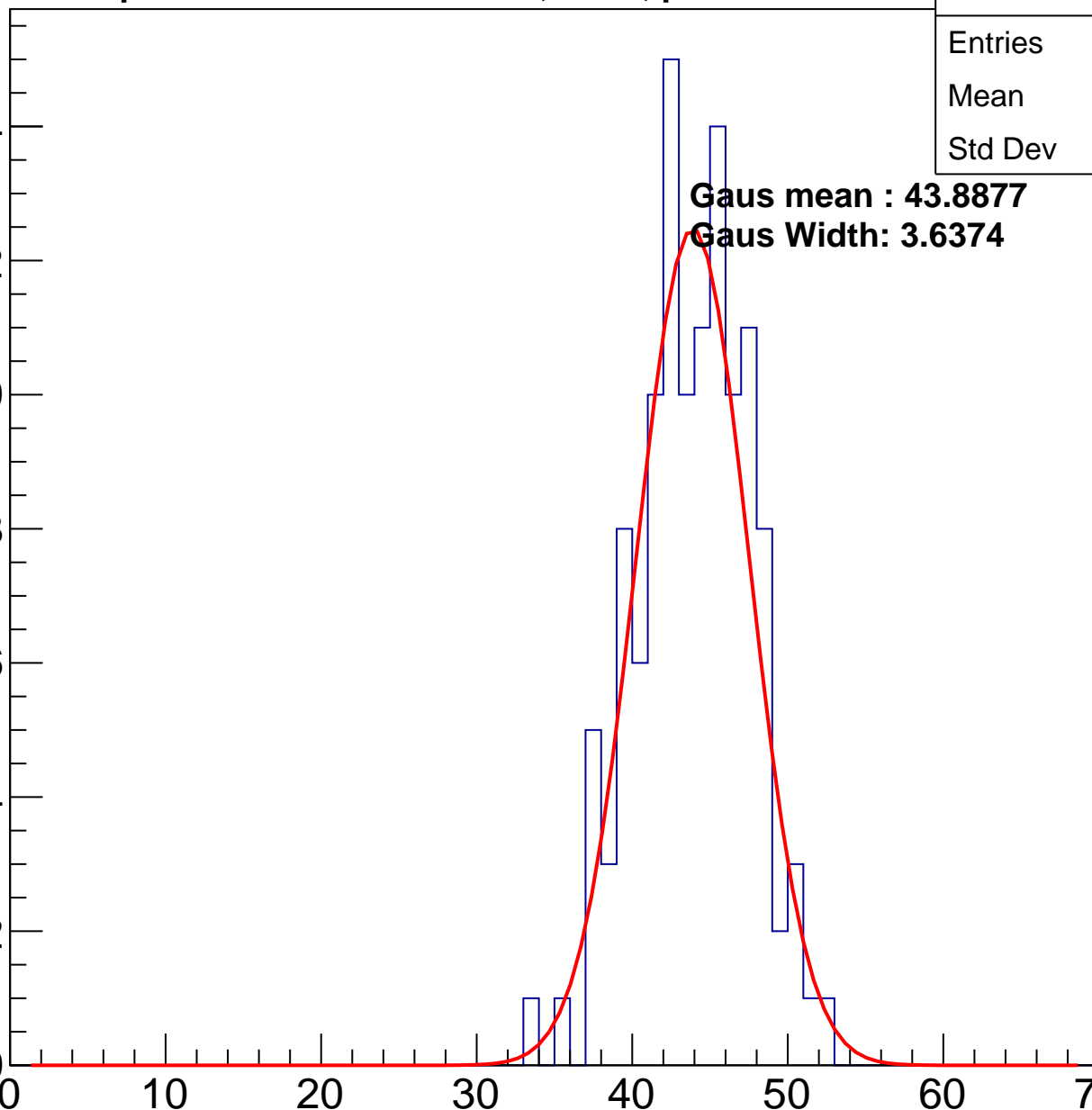
70

ampl

Entries	120
Mean	43.46
Std Dev	3.568

Gaus mean : 43.8877

Gaus Width: 3.6374



B1L001S, U21-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

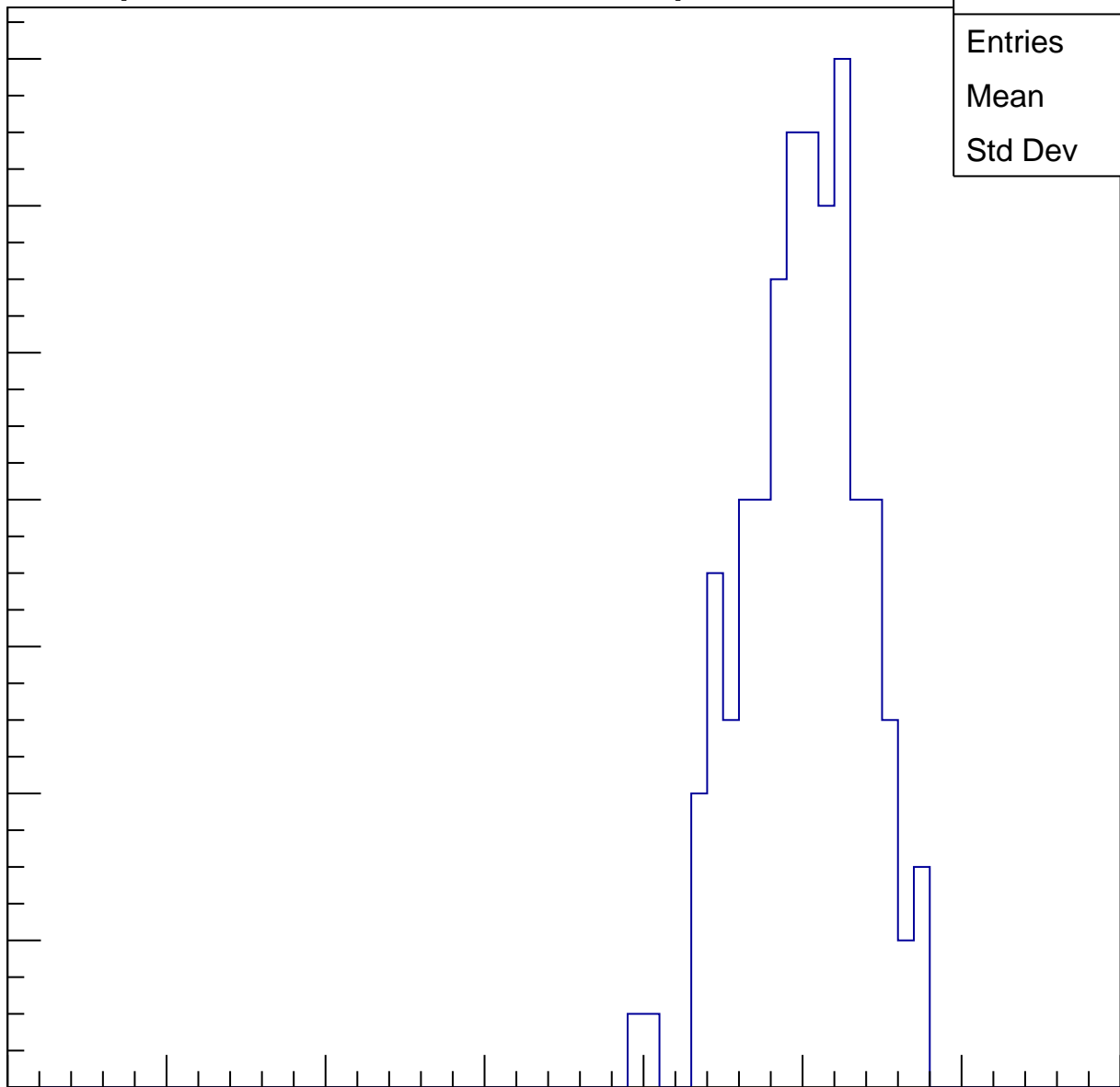
Entries	123
Mean	49.57
Std Dev	3.652

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

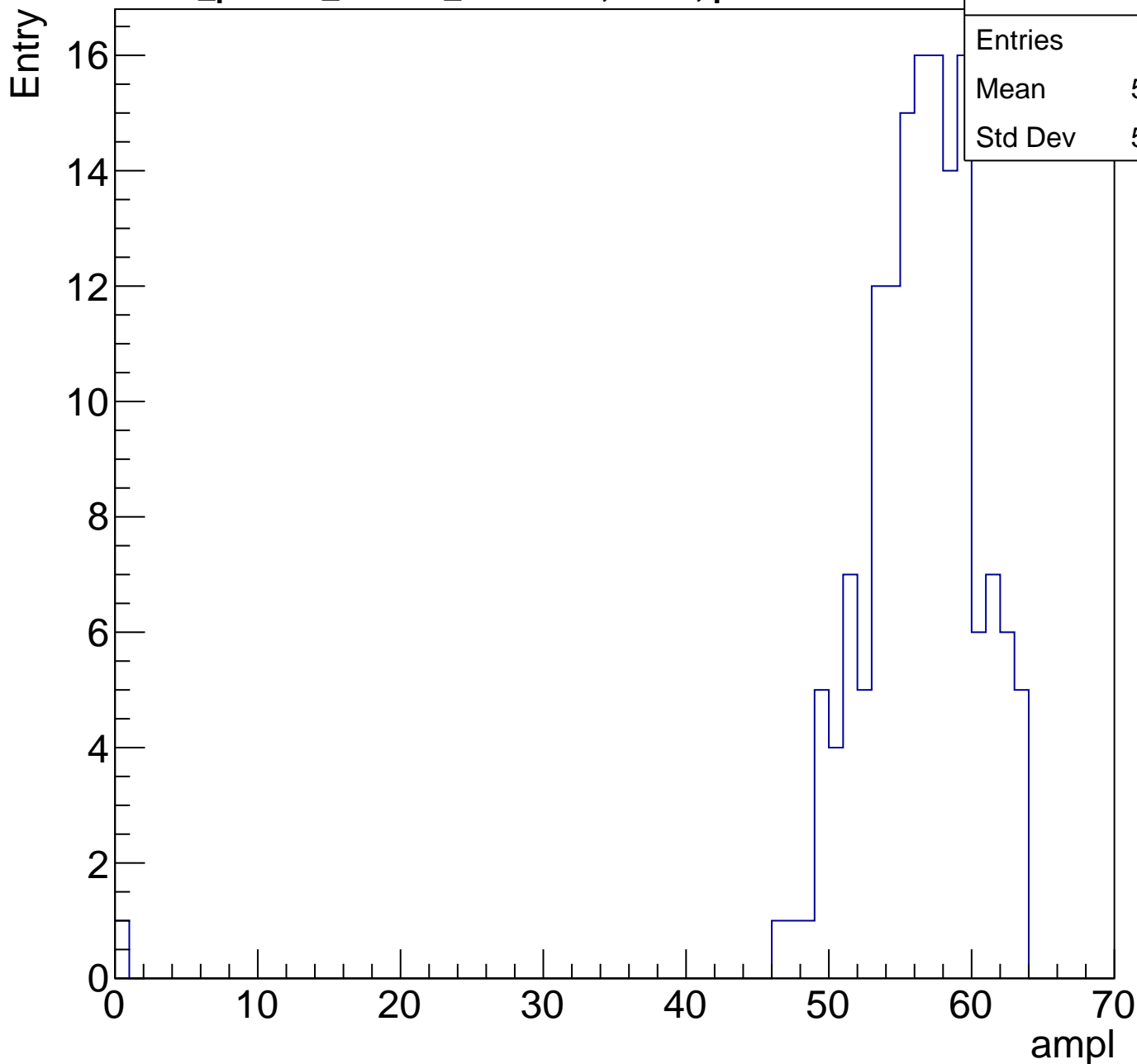
ampl



B1L001S, U21-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	55.67
Std Dev	5.847



B1L001S, U21-ch123, adc5

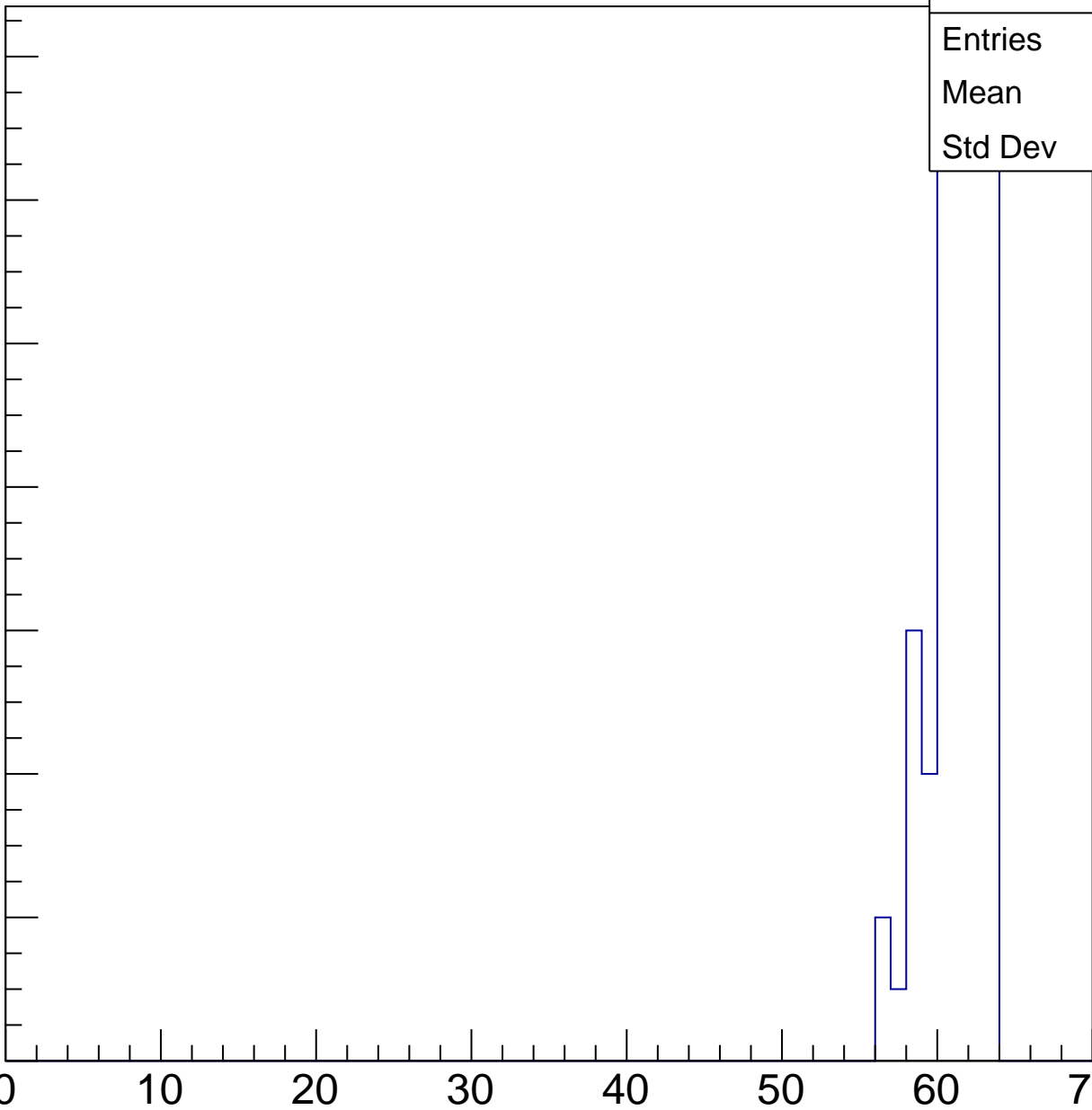
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	67
Mean	60.79
Std Dev	1.783

ampl



B1L001S, U21-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U21-ch124, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	31.42
Std Dev	3.413

Gaus mean : 32.1146

Gaus Width: 3.4652

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

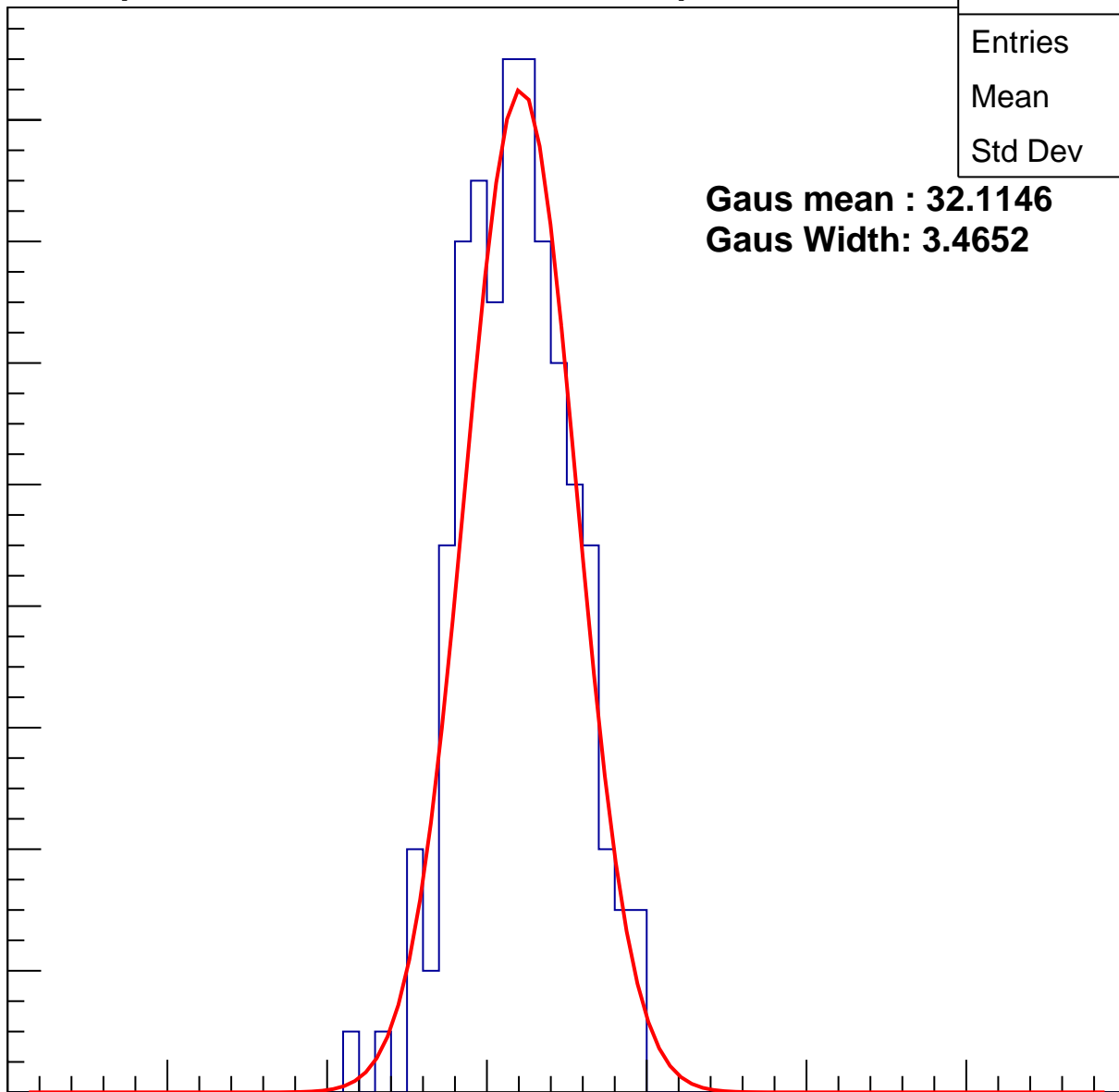
40

50

60

70

ampl



B1L001S, U21-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	98
Mean	37.43
Std Dev	3.188

Gaus mean : 37.8307

Gaus Width: 3.2765

0

B1L001S, U21-ch124, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

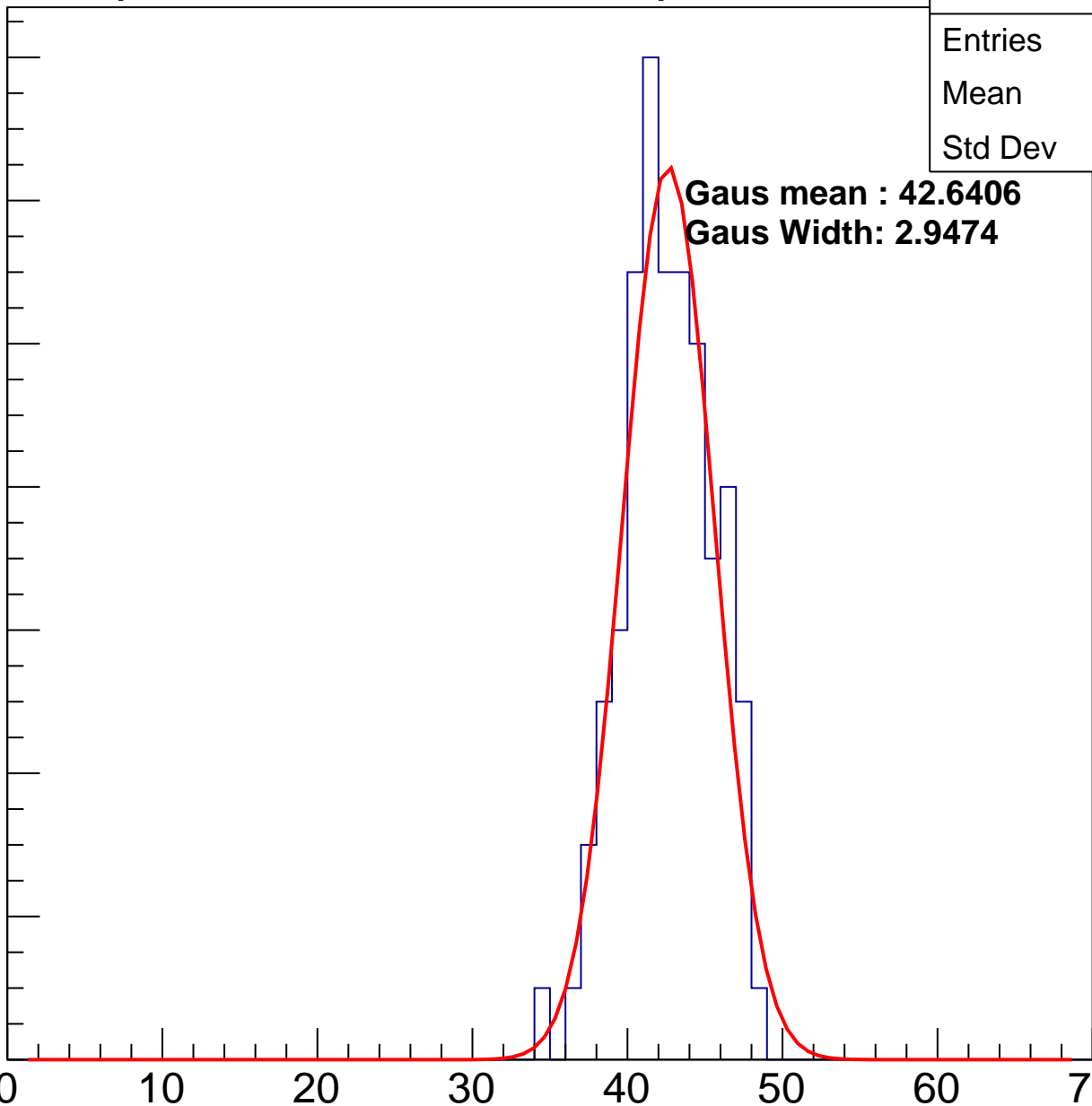
14
12
10
8
6
4
2
0

Entries	94
Mean	42.13
Std Dev	2.859

Gaus mean : 42.6406

Gaus Width: 2.9474

ampl

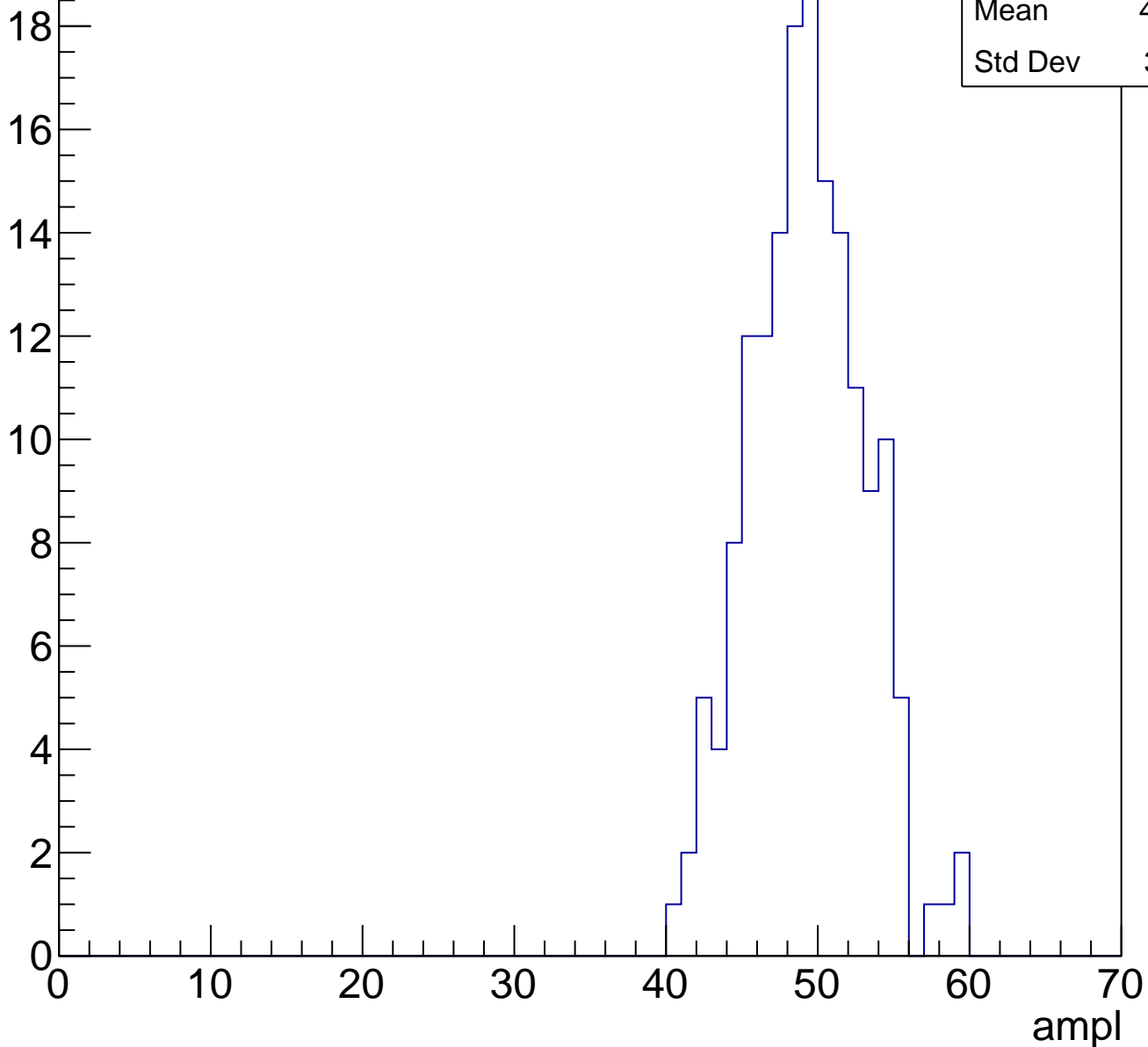


B1L001S, U21-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	48.85
Std Dev	3.721

Entry



B1L001S, U21-ch124, adc4

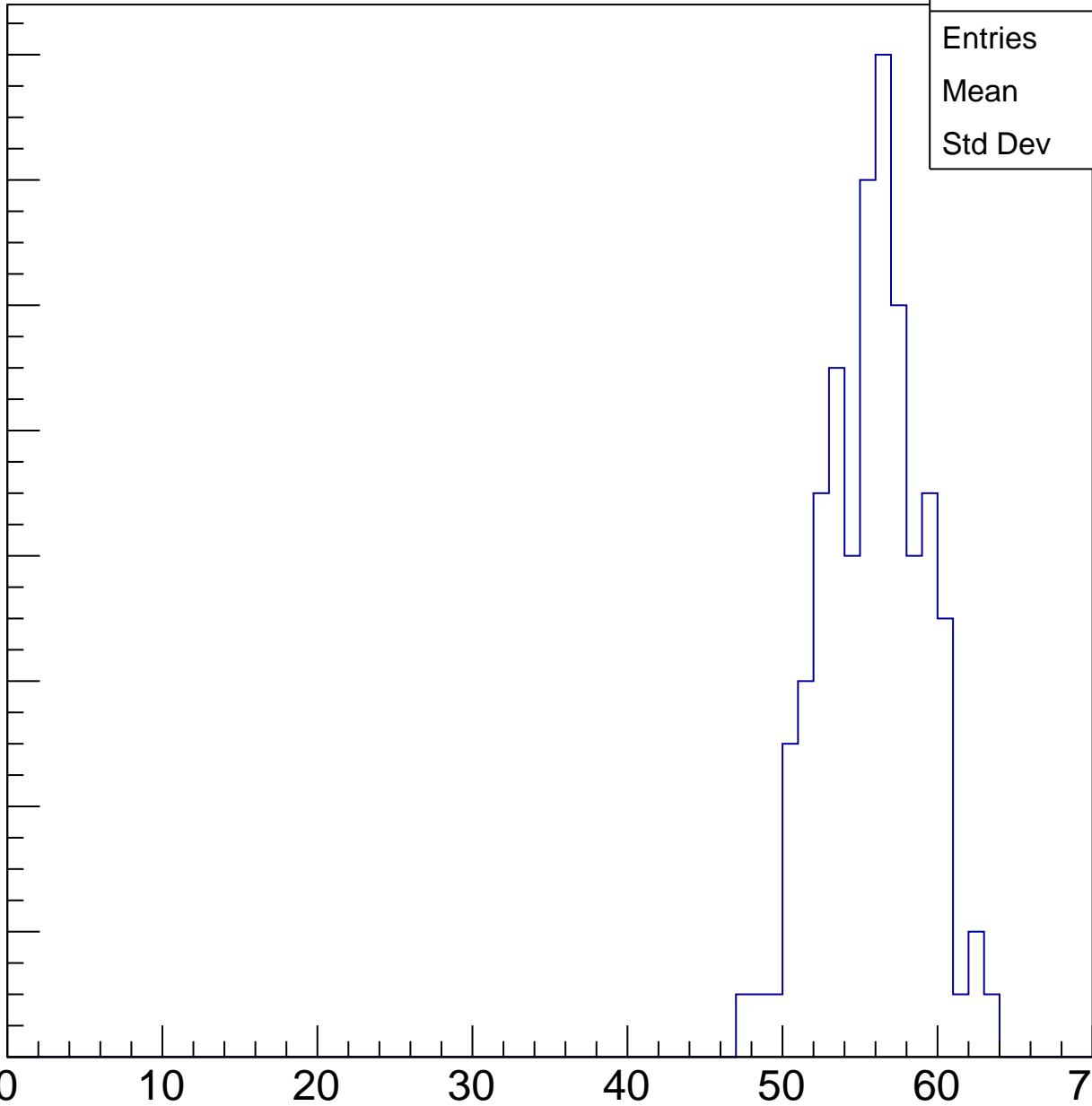
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	55.32
Std Dev	3.197

ampl



B1L001S, U21-ch124, adc5

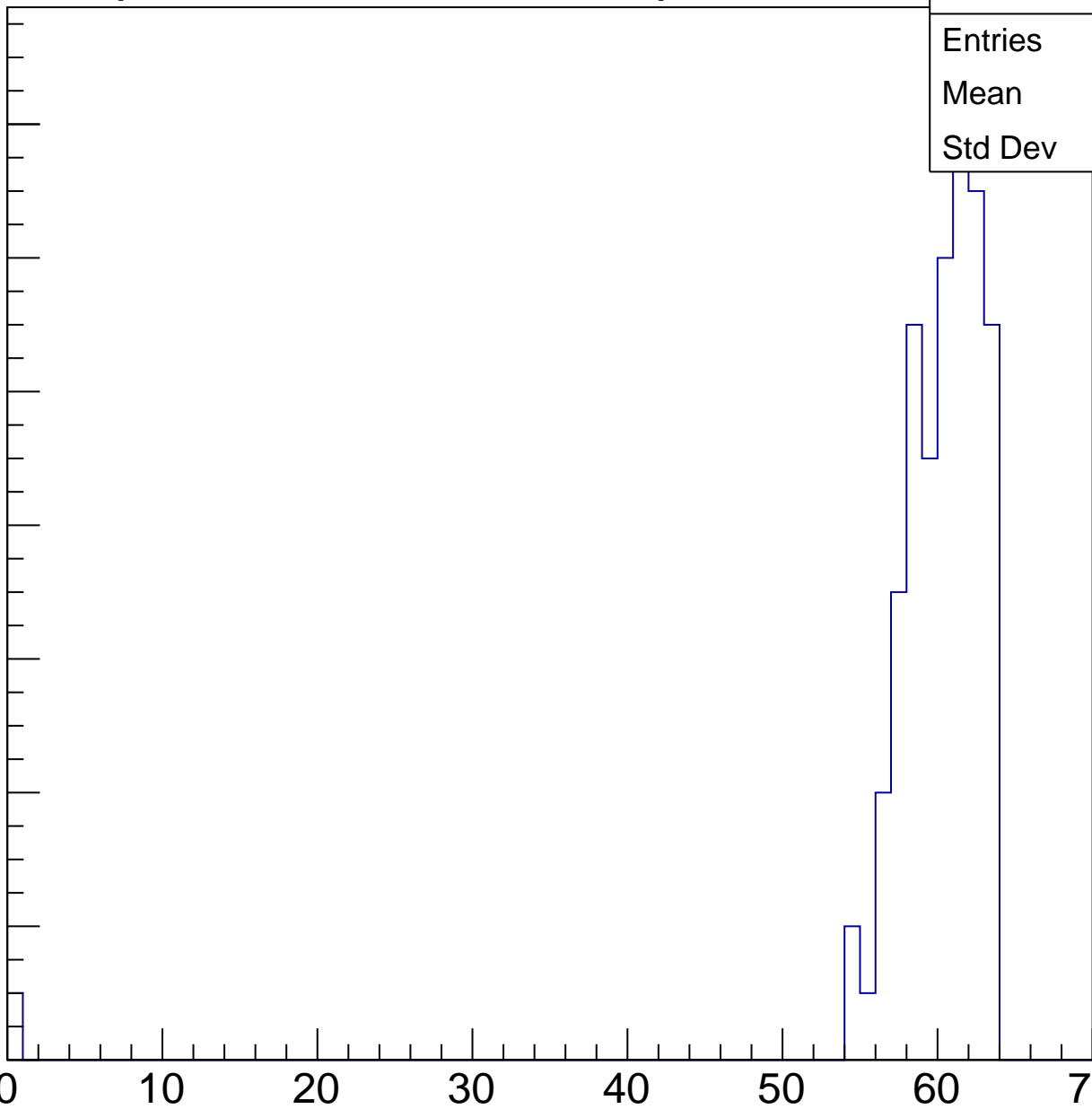
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	59.17
Std Dev	6.807

ampl

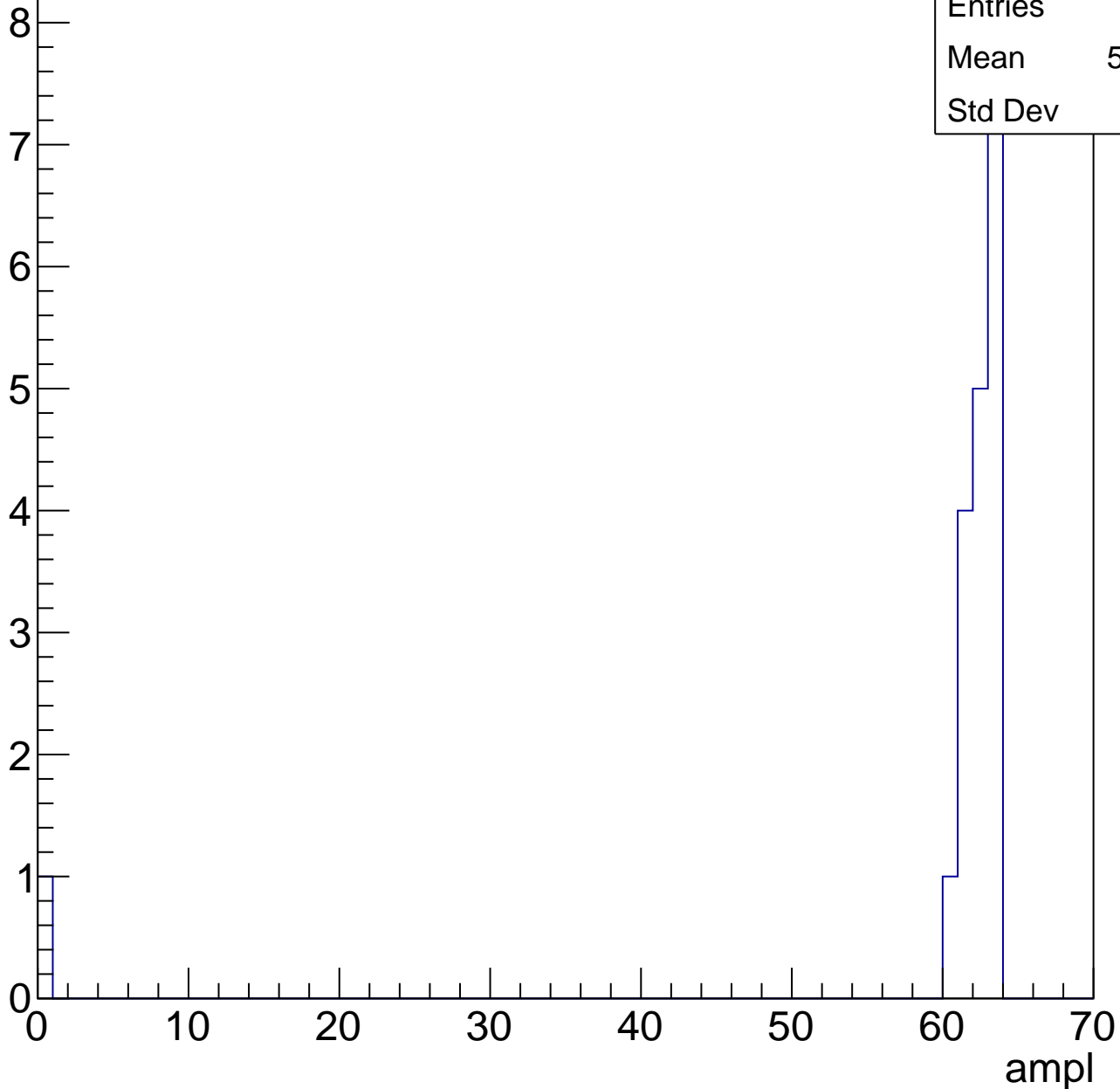


B1L001S, U21-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

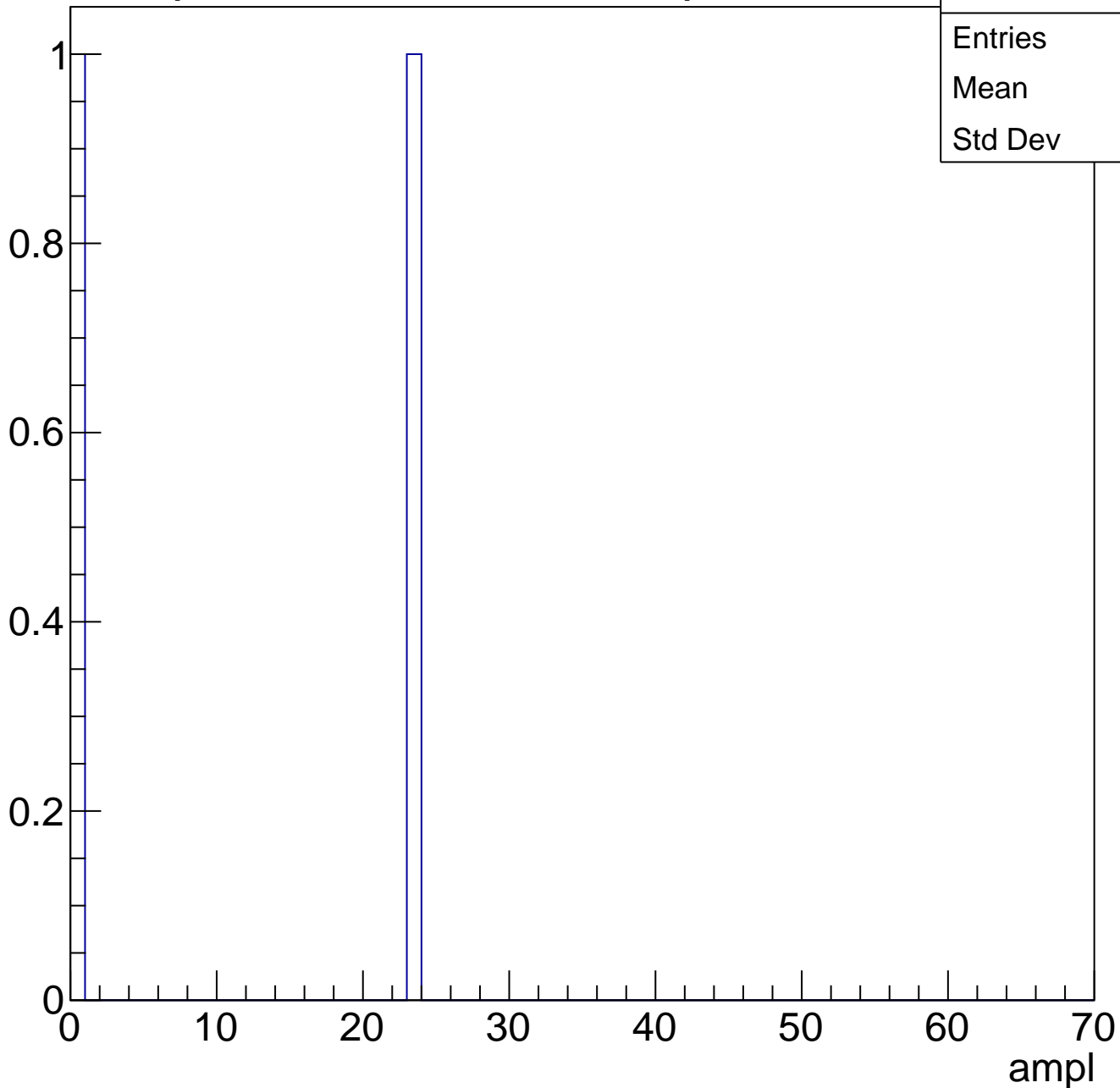
Entries	19
Mean	58.84
Std Dev	13.9



B1L001S, U21-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch125, adc0

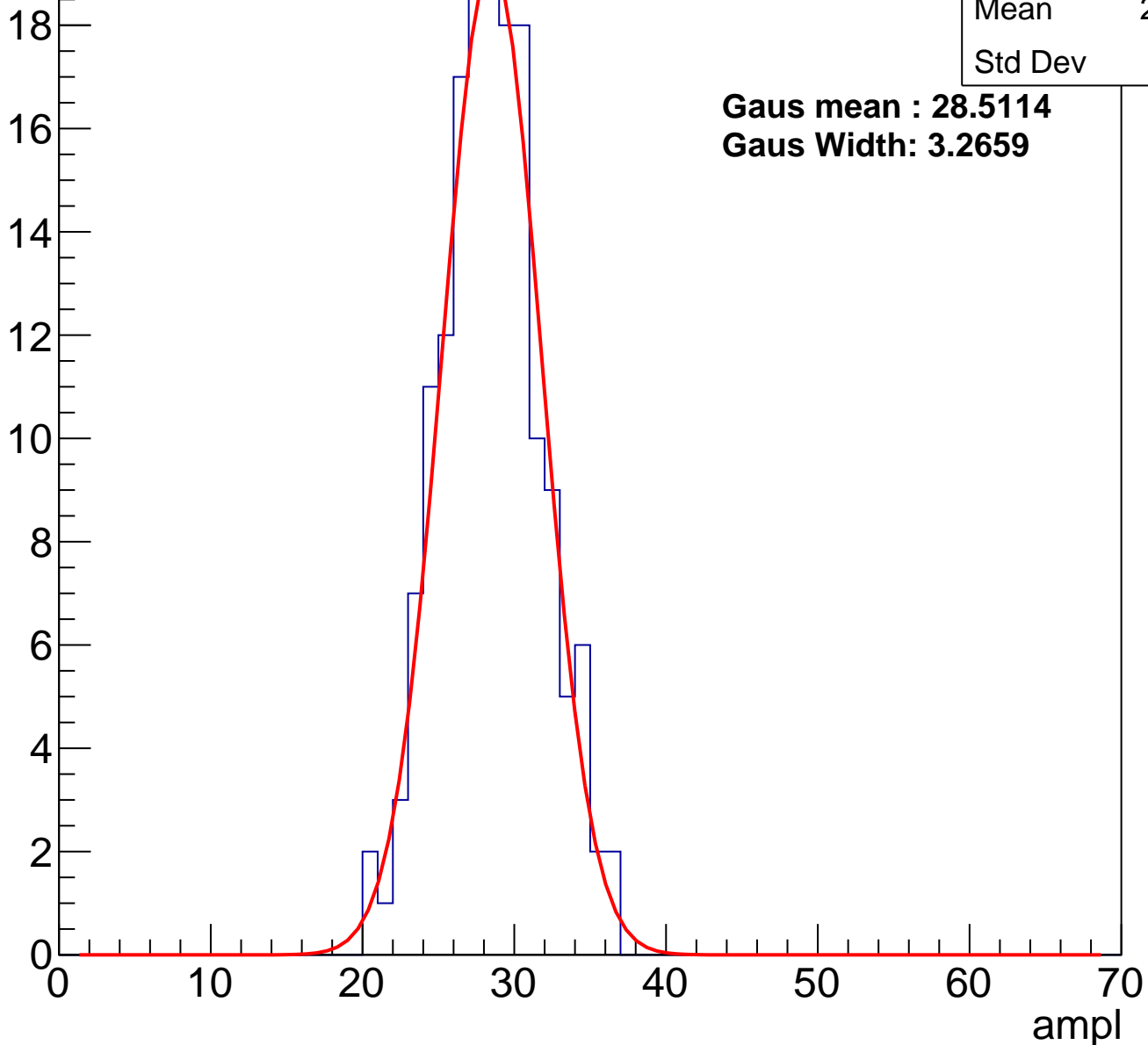
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	28.01
Std Dev	3.27

Gaus mean : 28.5114

Gaus Width: 3.2659

Entry



B1L001S, U21-ch125, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	34.68
Std Dev	2.989

Gaus mean : 35.3955

Gaus Width: 3.0537

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U21-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	41.33
Std Dev	3.58

Gaus mean : 41.7811

Gaus Width: 3.4883

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

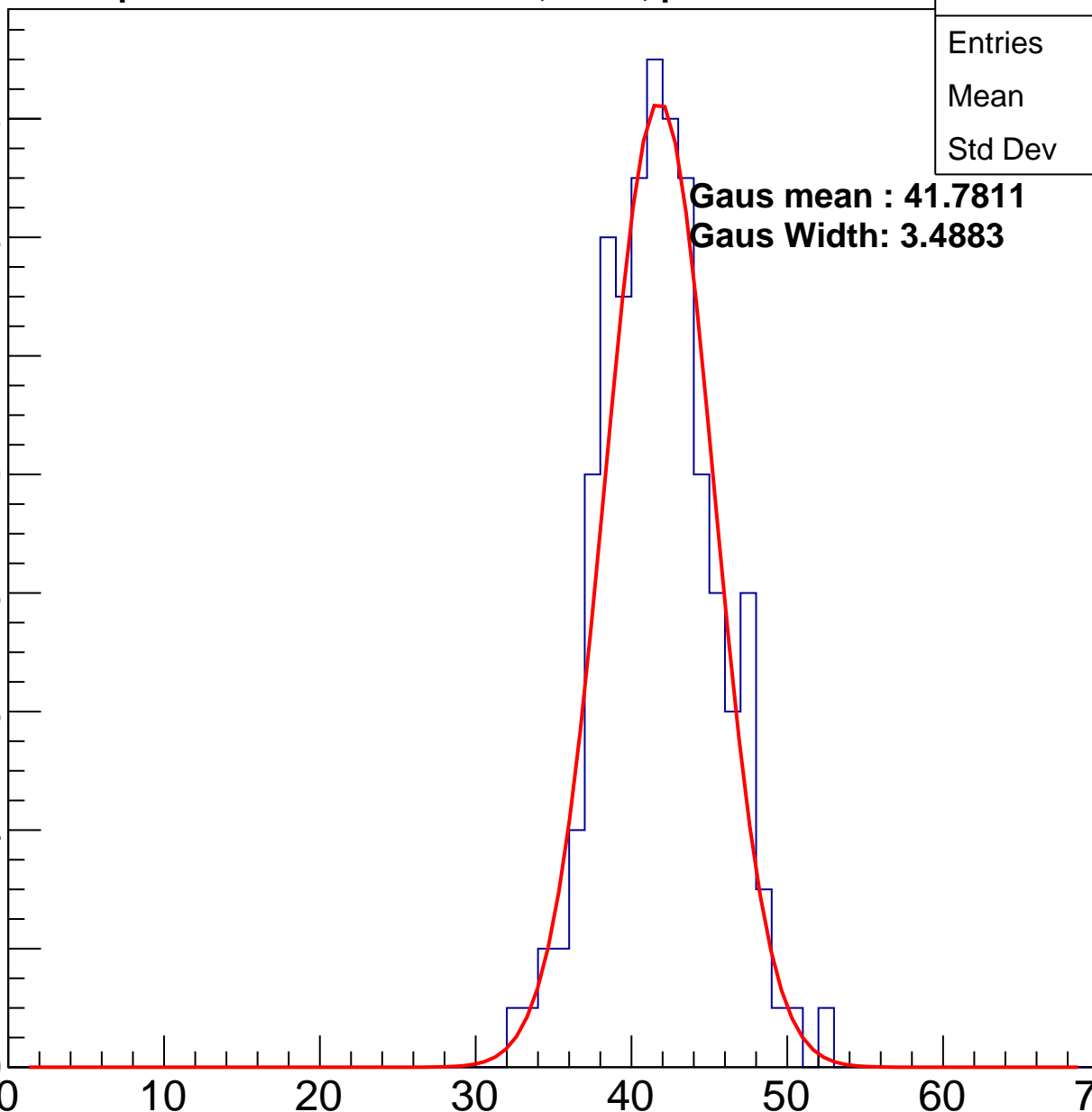
40

50

60

70

ampl



B1L001S, U21-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	47.72
Std Dev	3.34

Entry

14
12
10
8
6
4
2
0

0

10

20

30

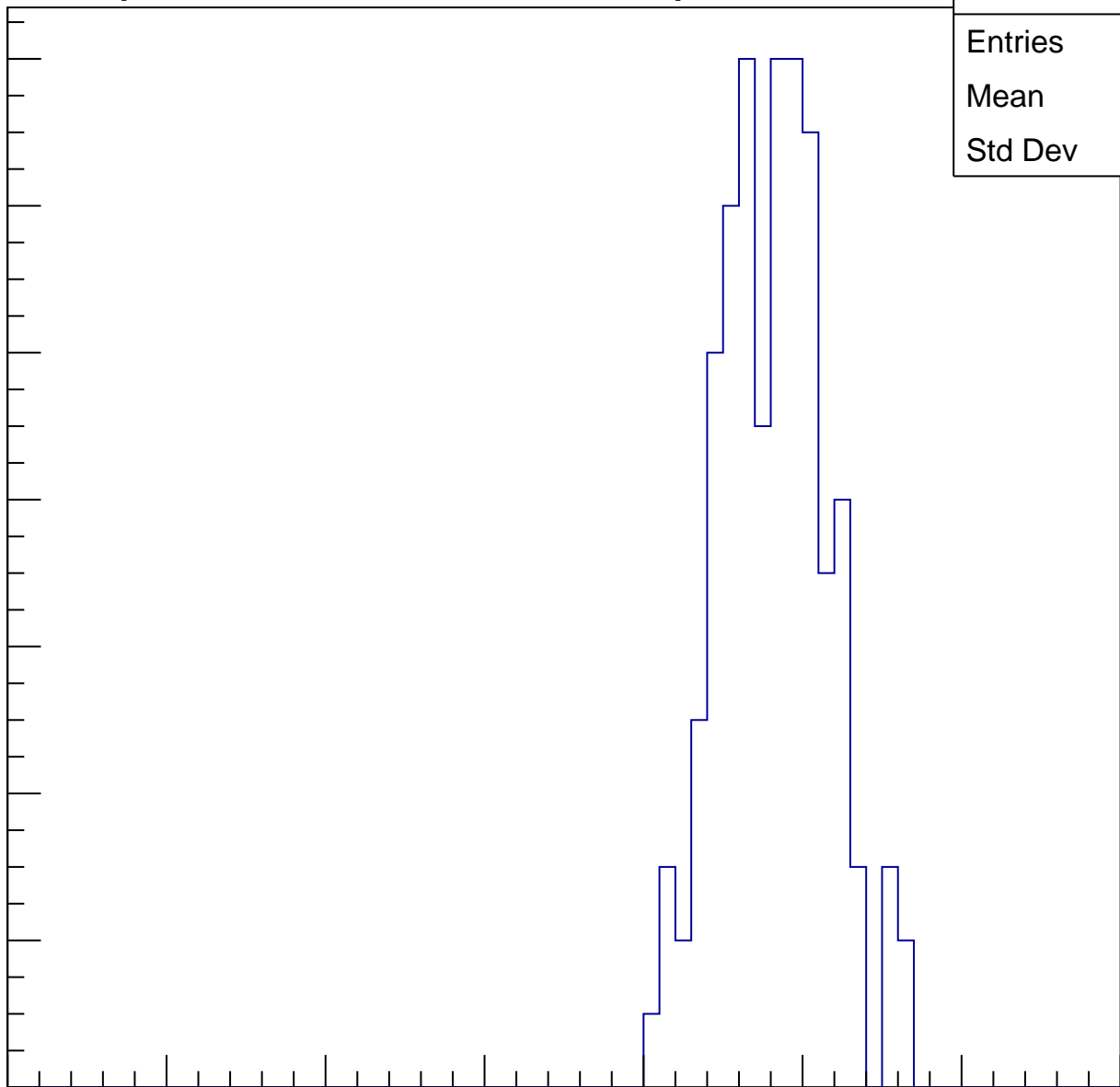
40

50

60

70

ampl



B1L001S, U21-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	54.39
Std Dev	3.52

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

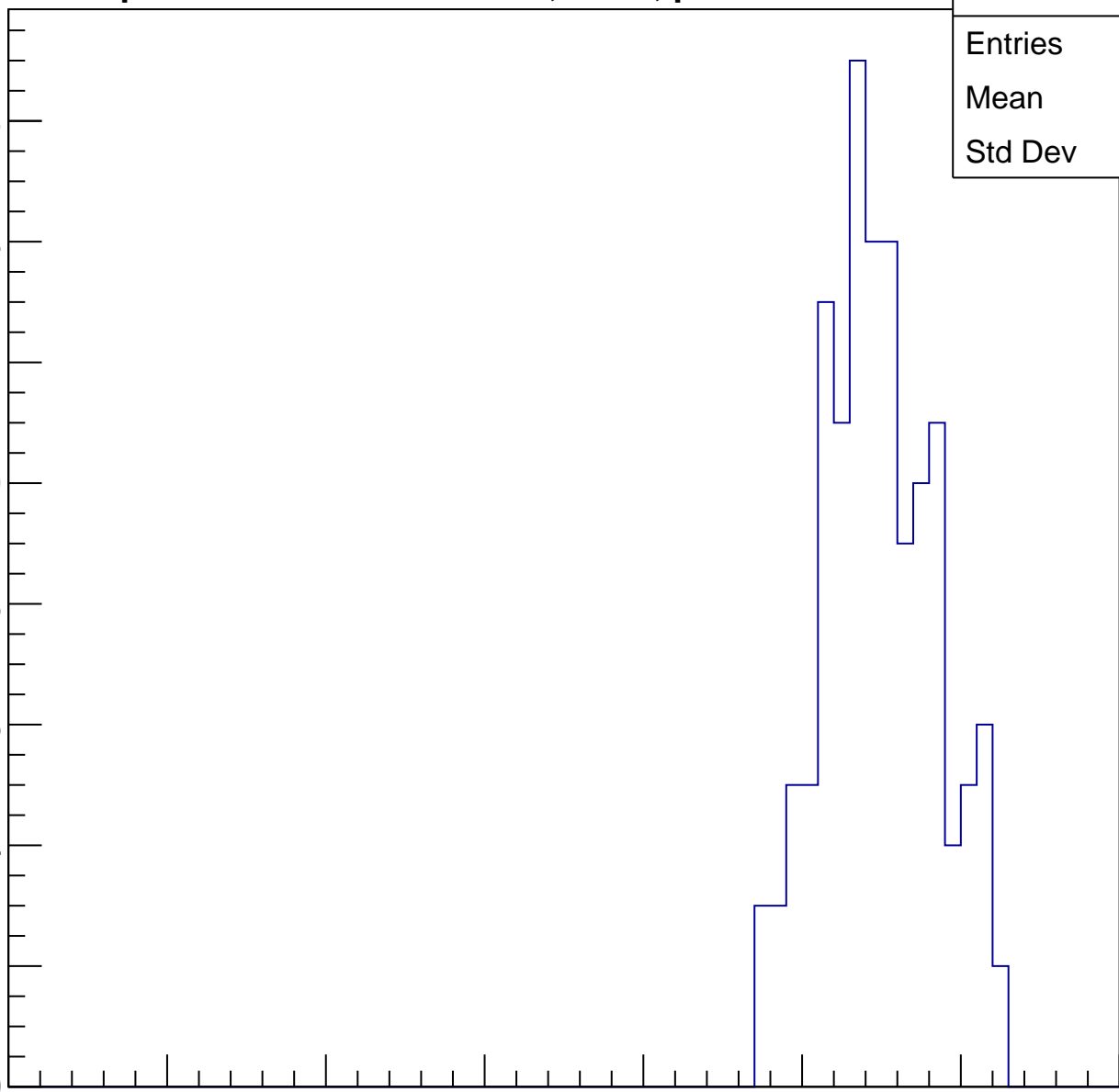
40

50

60

70

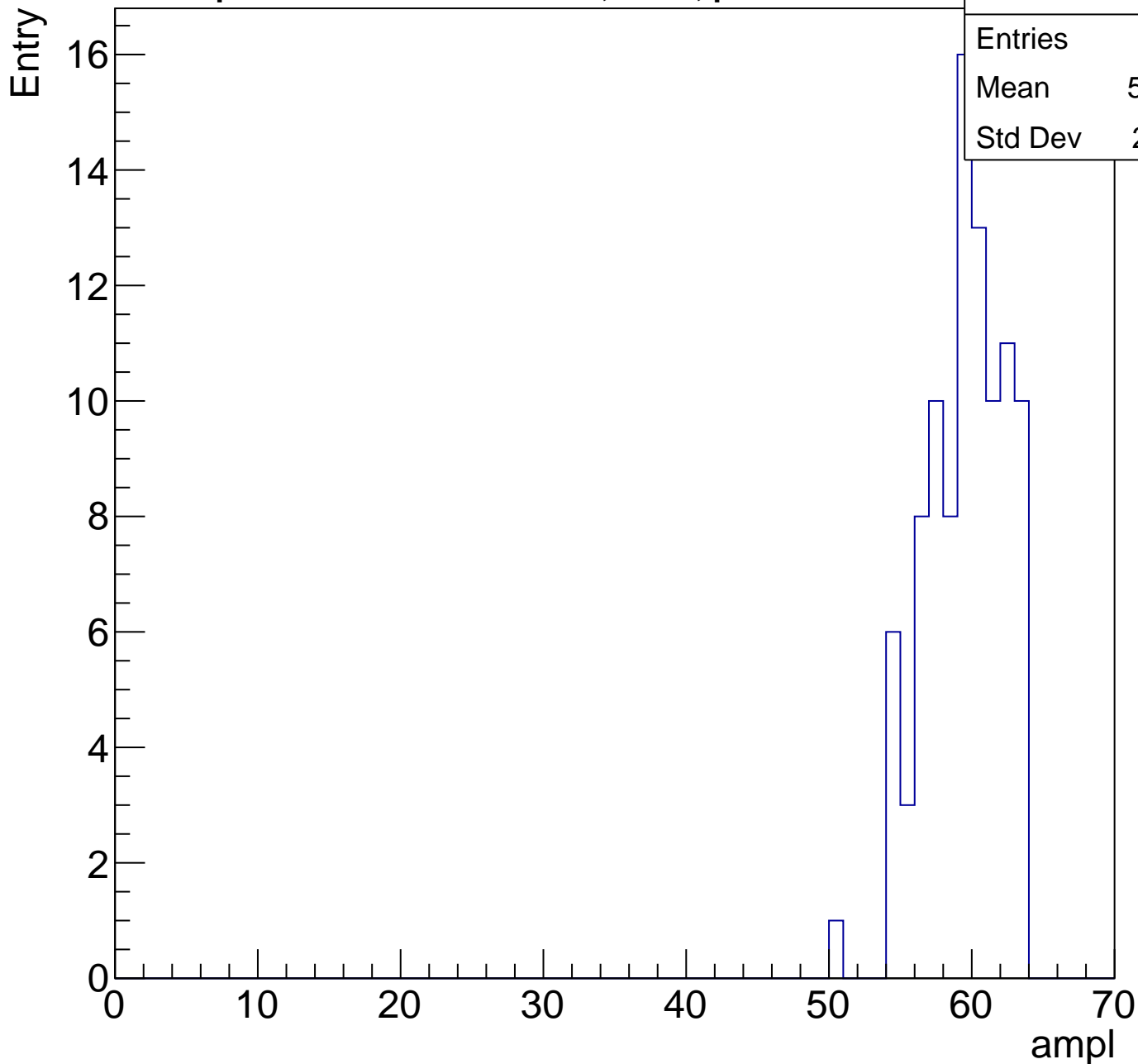
ampl



B1L001S, U21-ch125, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	59.03
Std Dev	2.721

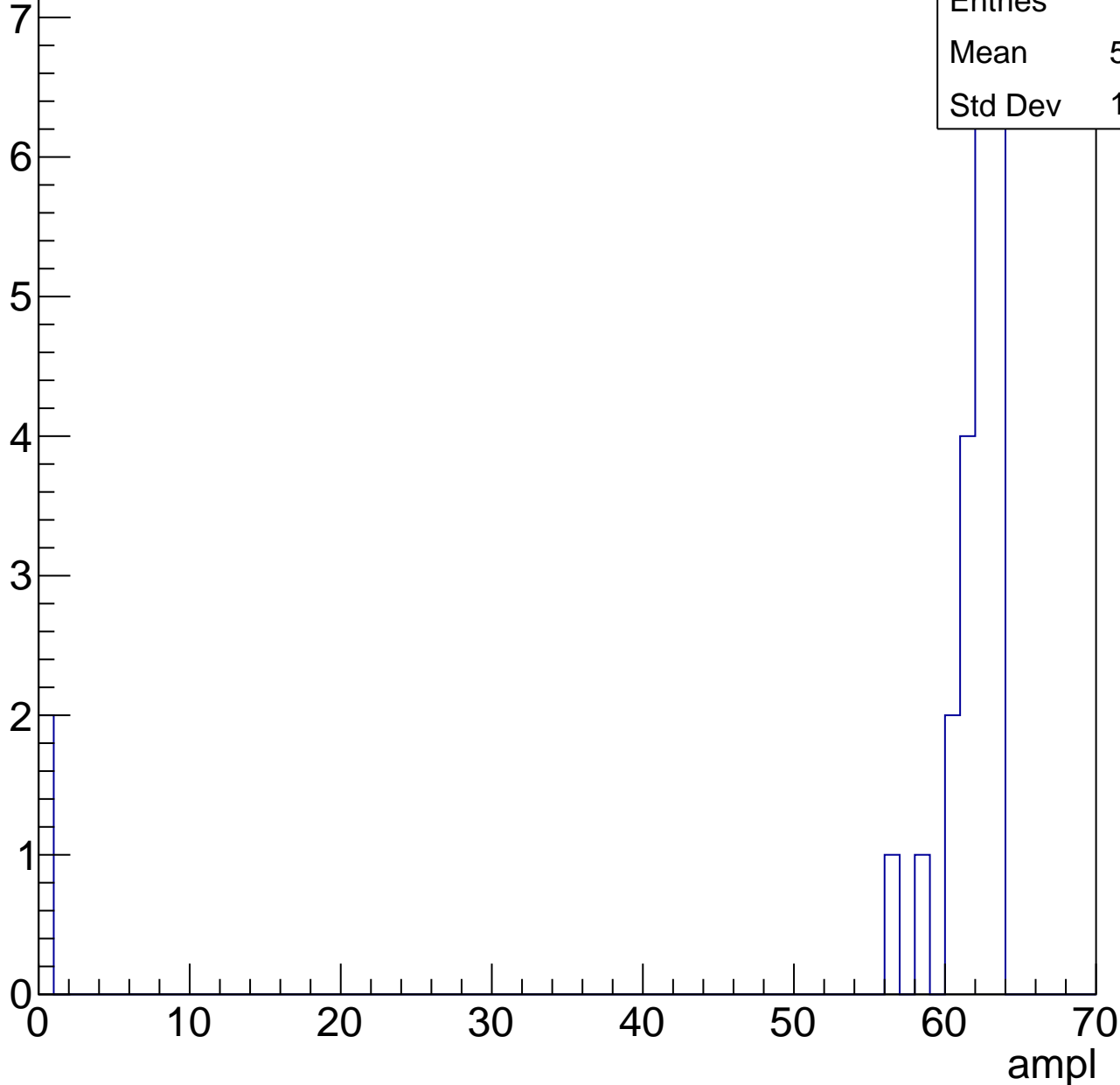


B1L001S, U21-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

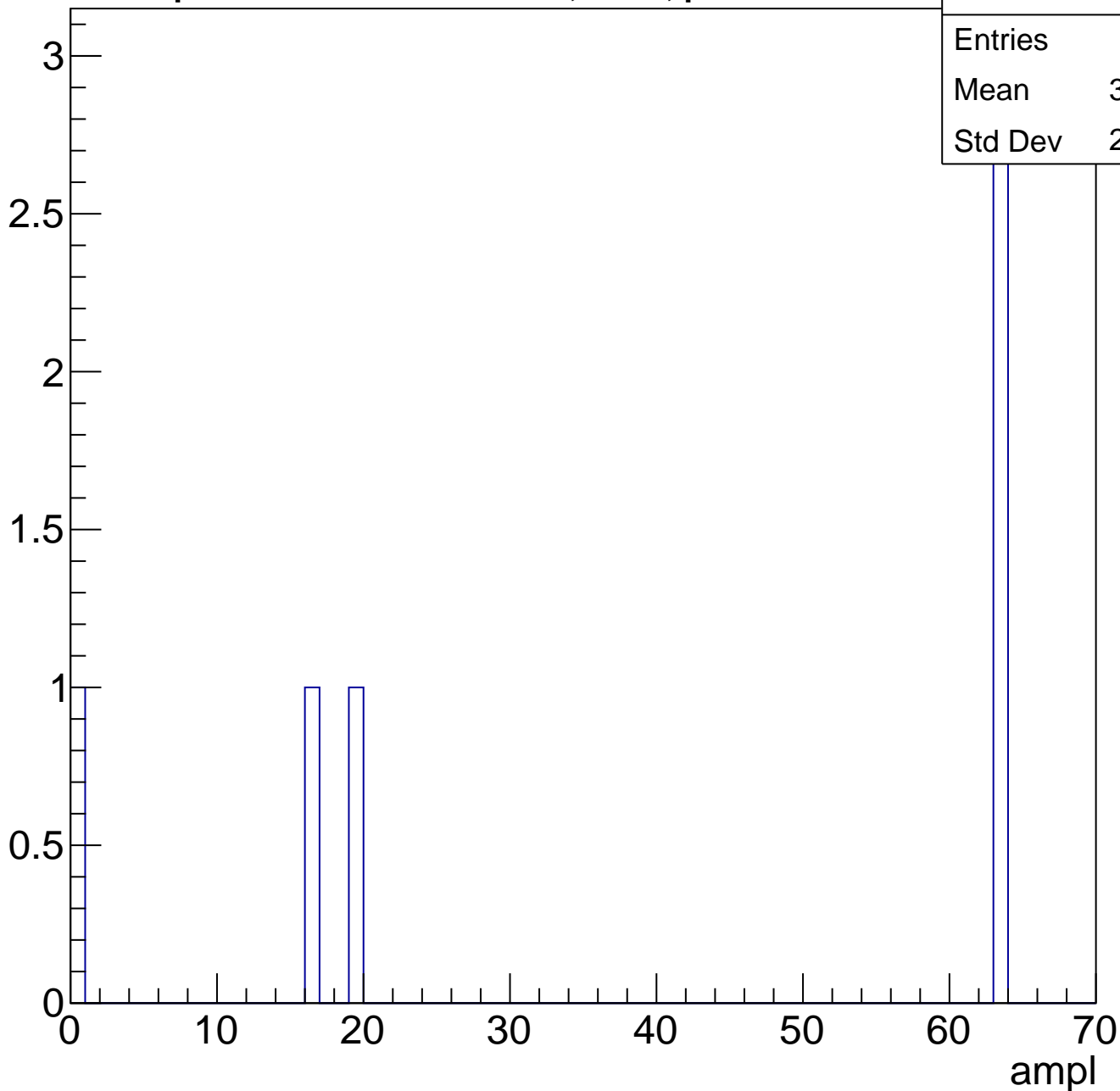
Entries	24
Mean	56.38
Std Dev	17.08



B1L001S, U21-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

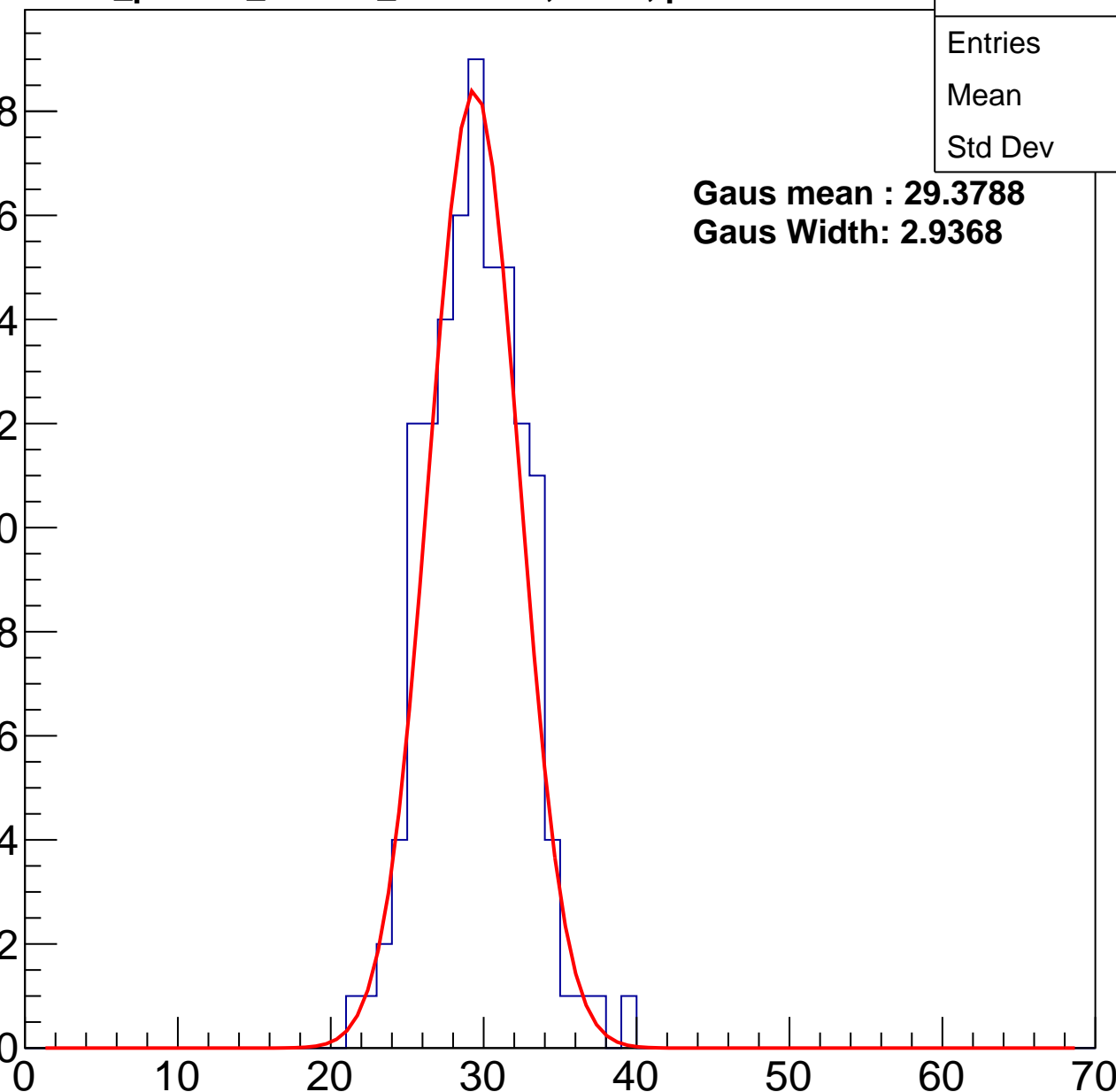
Entry

18
16
14
12
10
8
6
4
2
0

Entries	142
Mean	29.01
Std Dev	3.1

Gaus mean : 29.3788

Gaus Width: 2.9368



ampl

B1L001S, U21-ch126, adc1

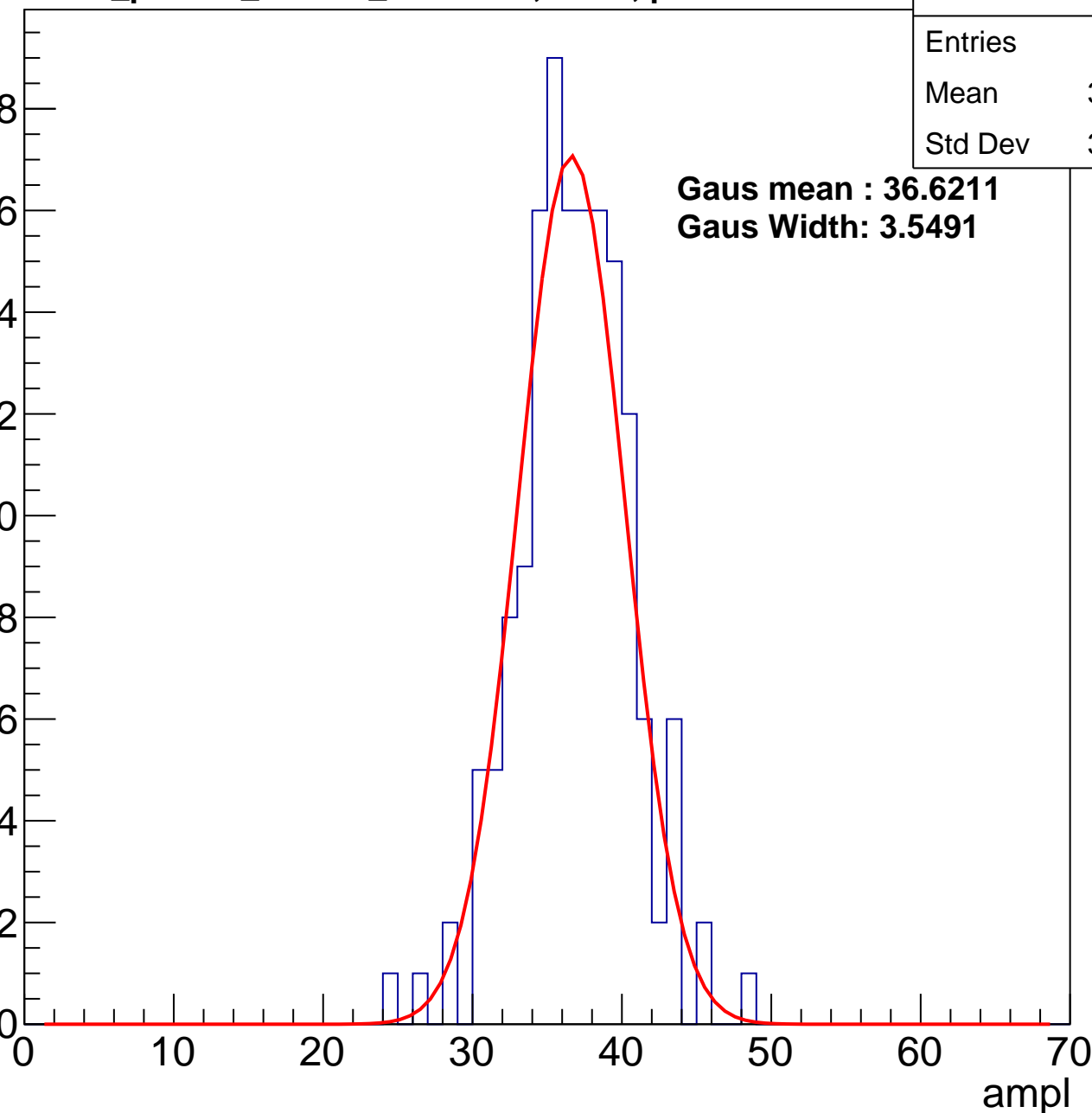
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	158
Mean	36.33
Std Dev	3.715

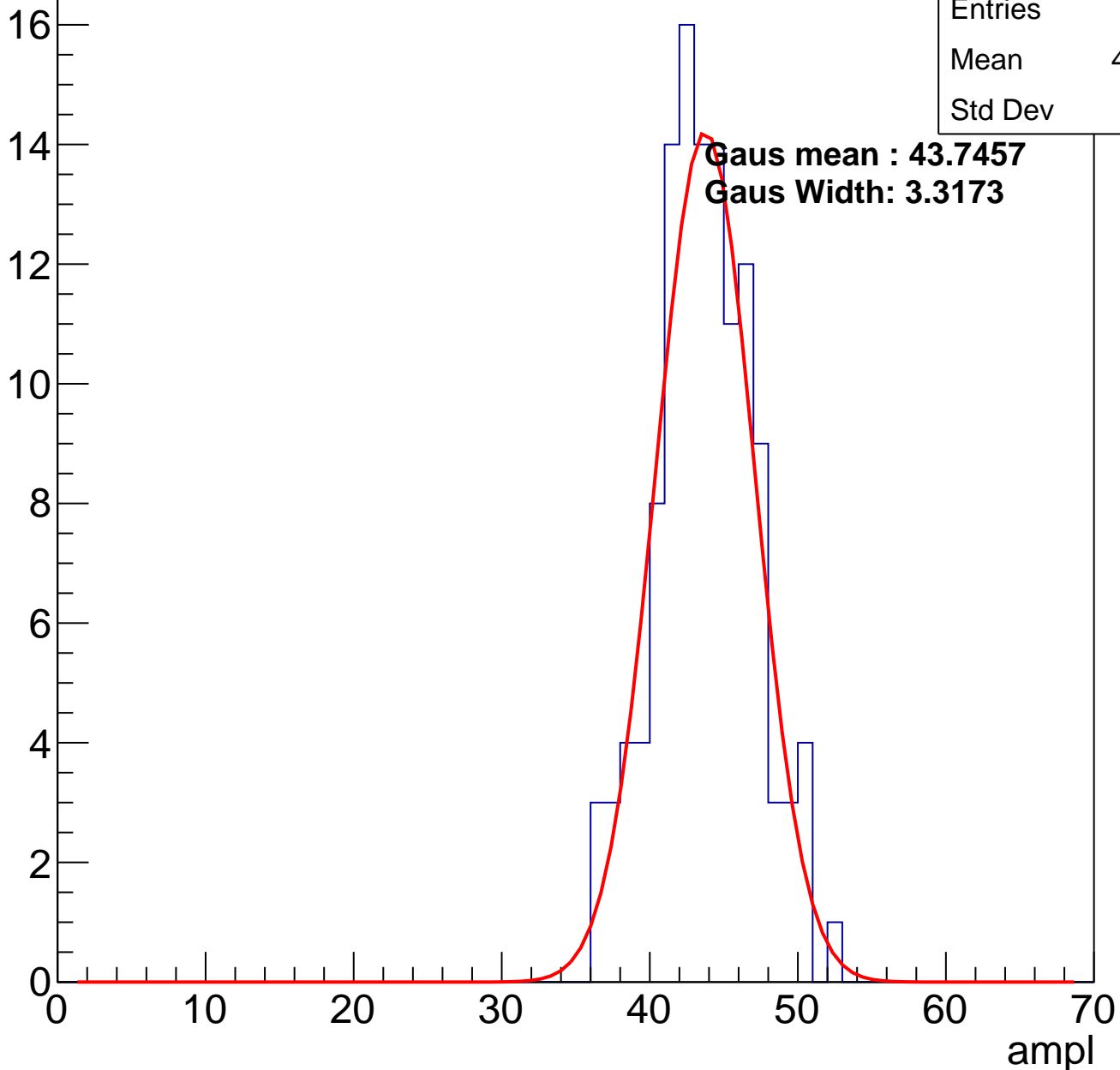
Gaus mean : 36.6211
Gaus Width: 3.5491



B1L001S, U21-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	49.99
Std Dev	3.341

Entry

16

14

12

10

8

6

4

2

0

0

10

20

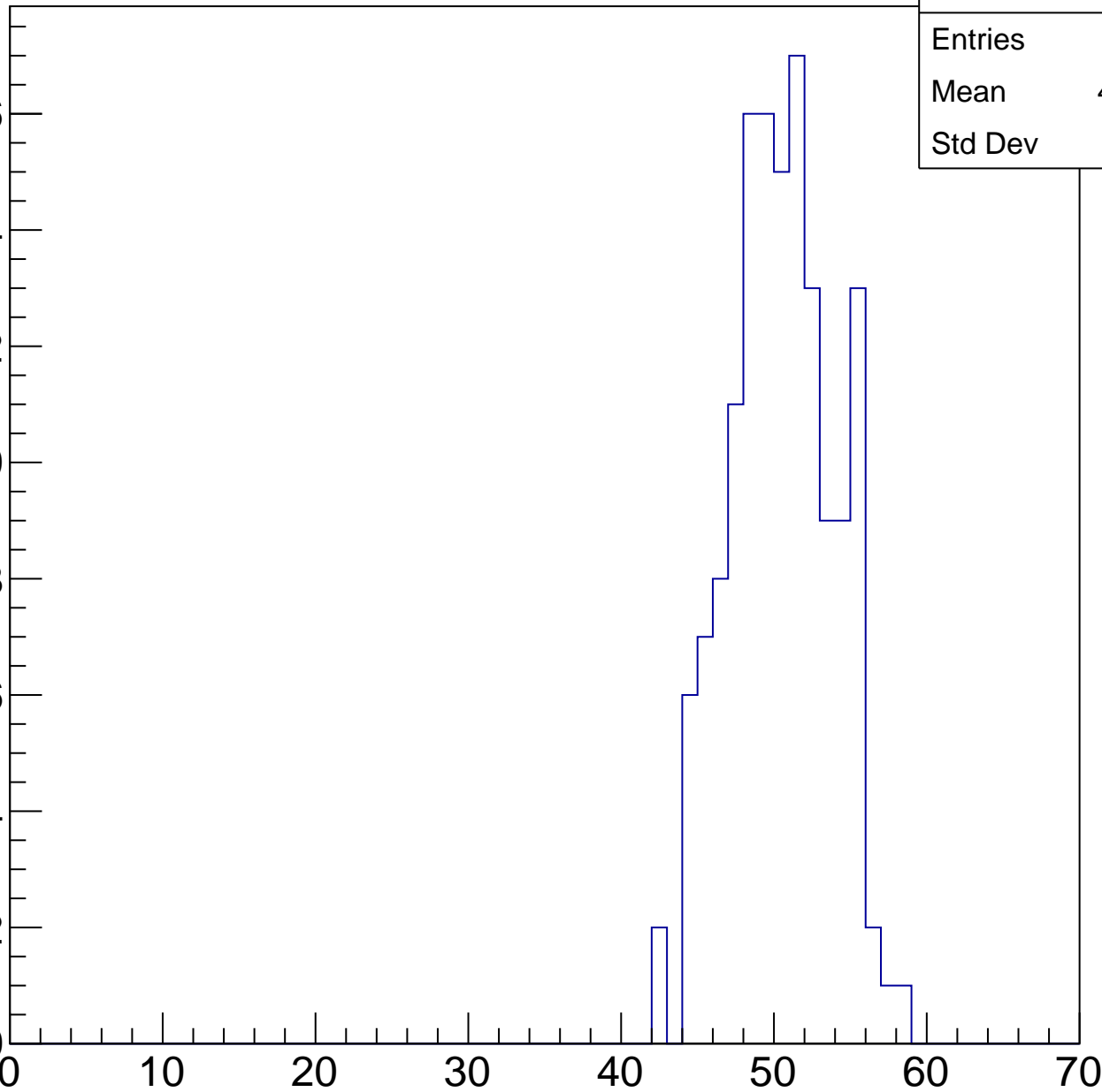
30

40

50

60

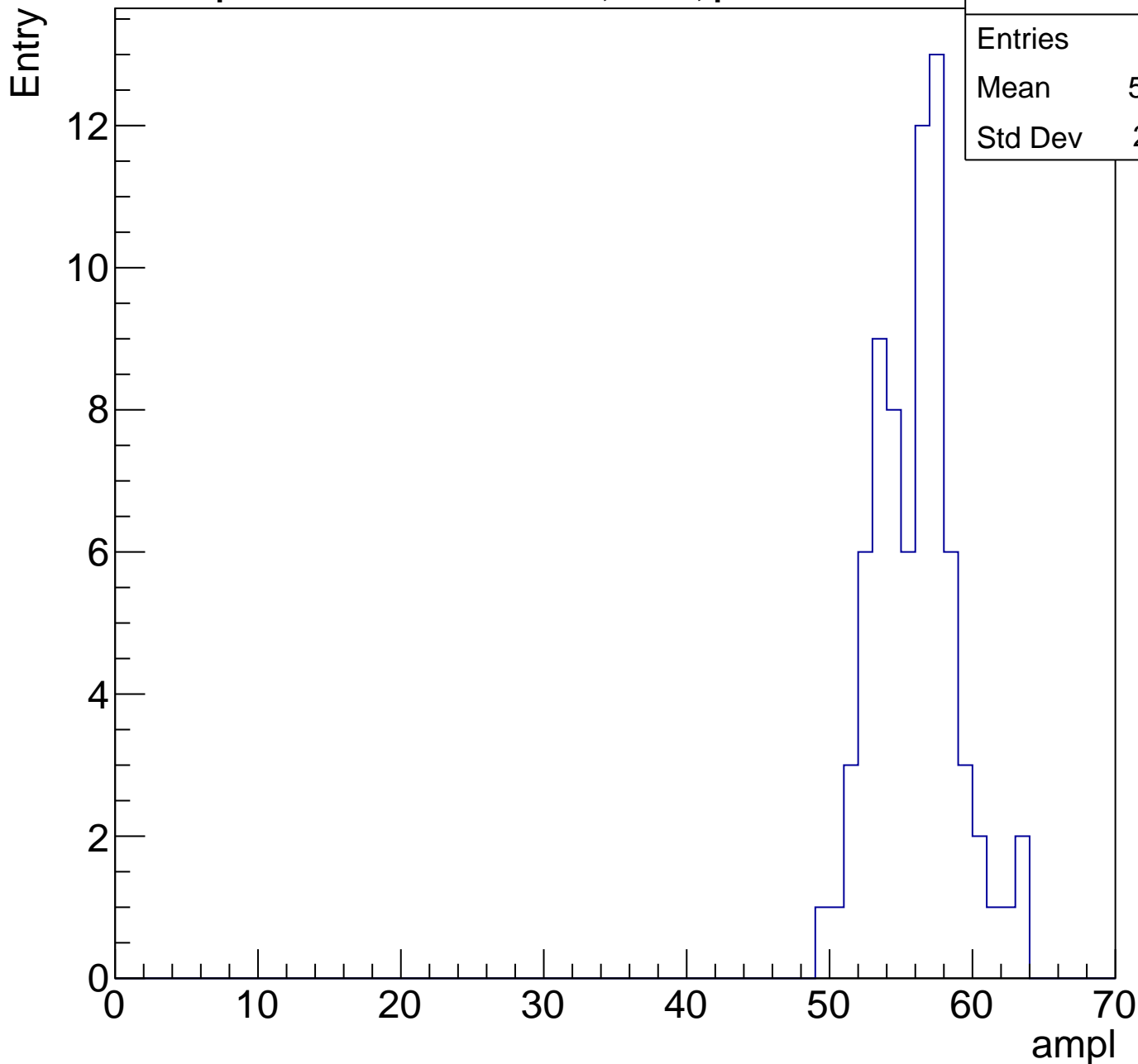
ampl



B1L001S, U21-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

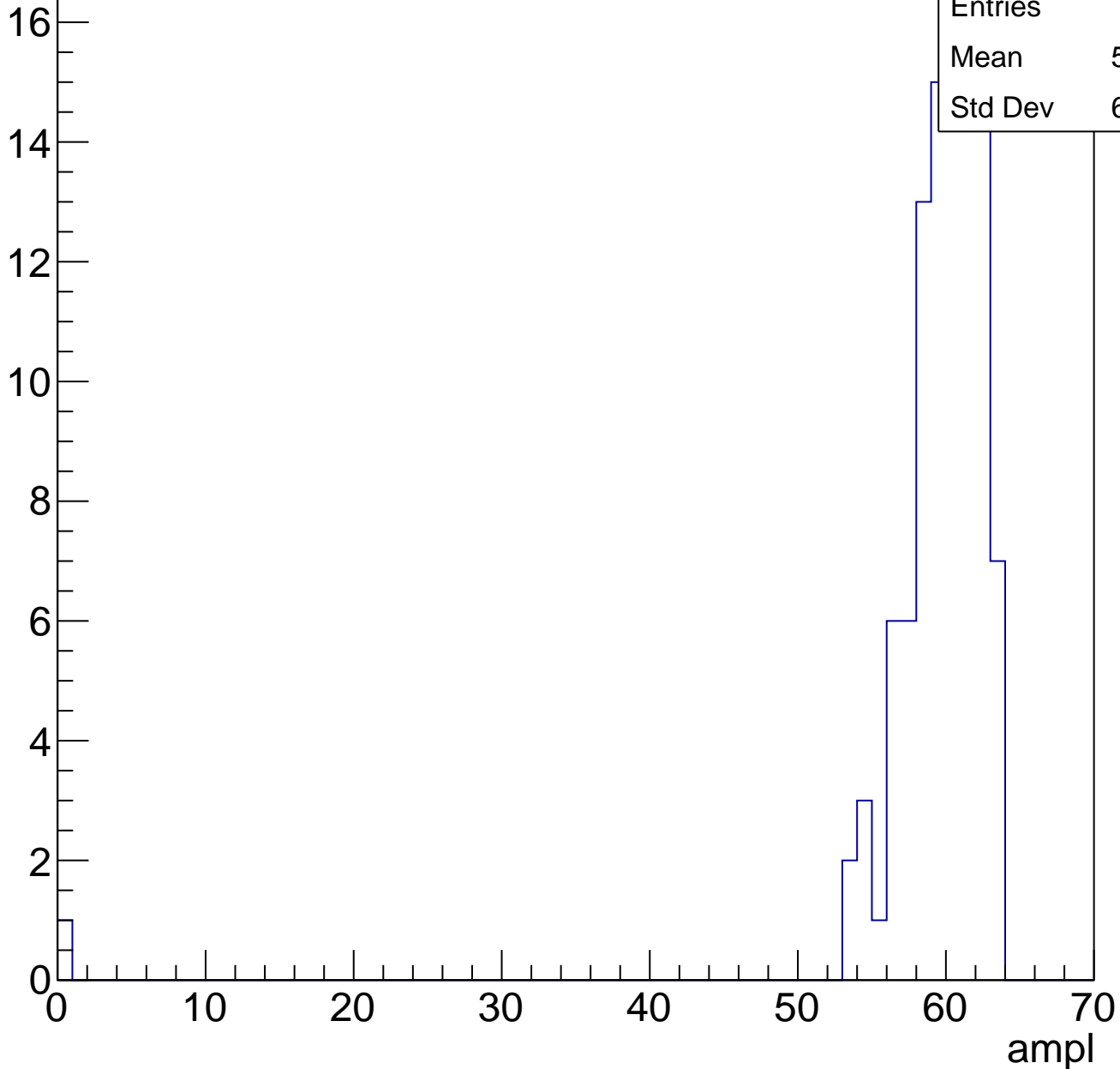
Entries	74
Mean	55.54
Std Dev	2.881



B1L001S, U21-ch126, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	24
Mean	59.29
Std Dev	12.43

B1L001S, U21-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

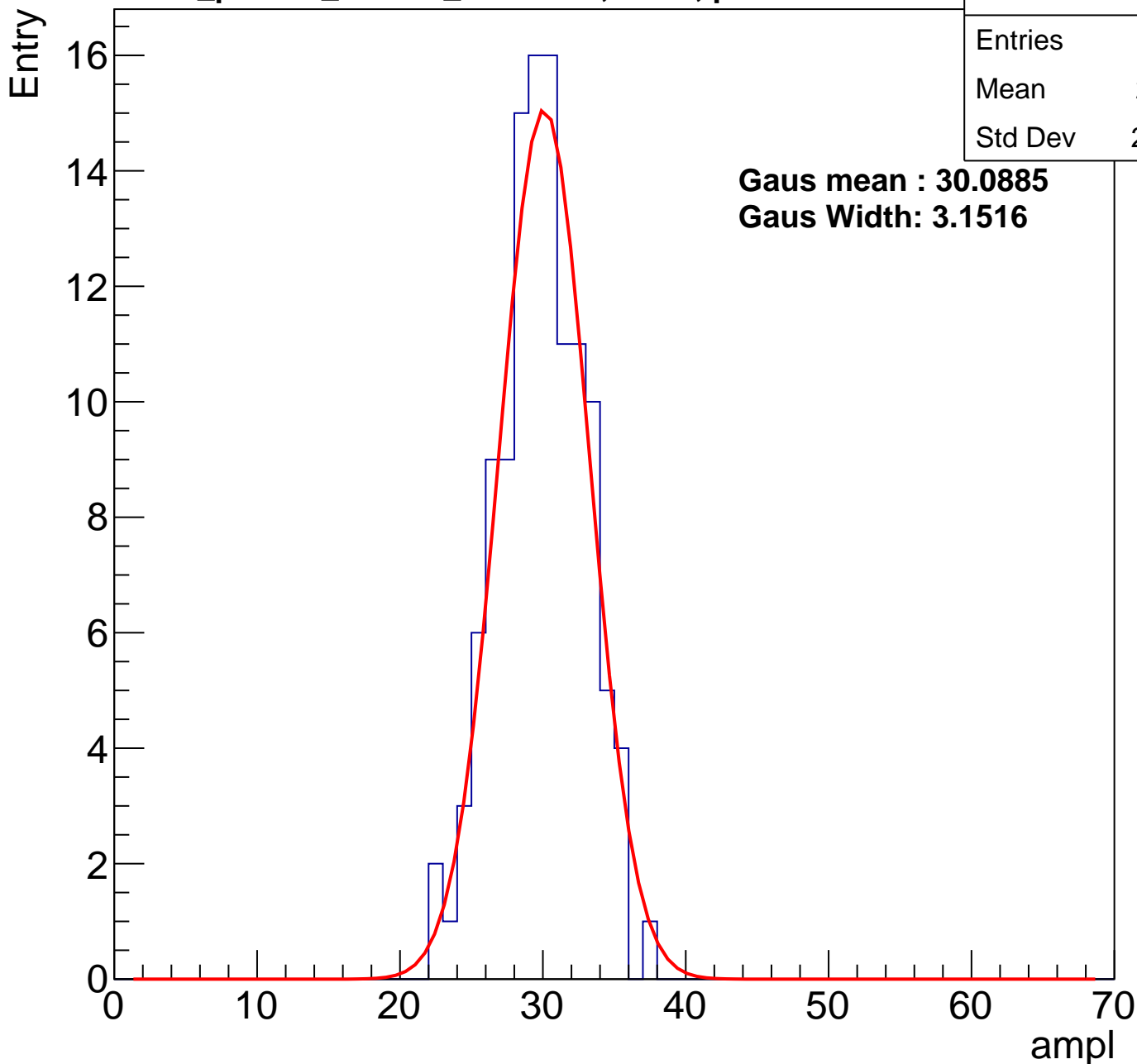
B1L001S, U21-ch127, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.41
Std Dev	2.997

Gaus mean : 30.0885

Gaus Width: 3.1516



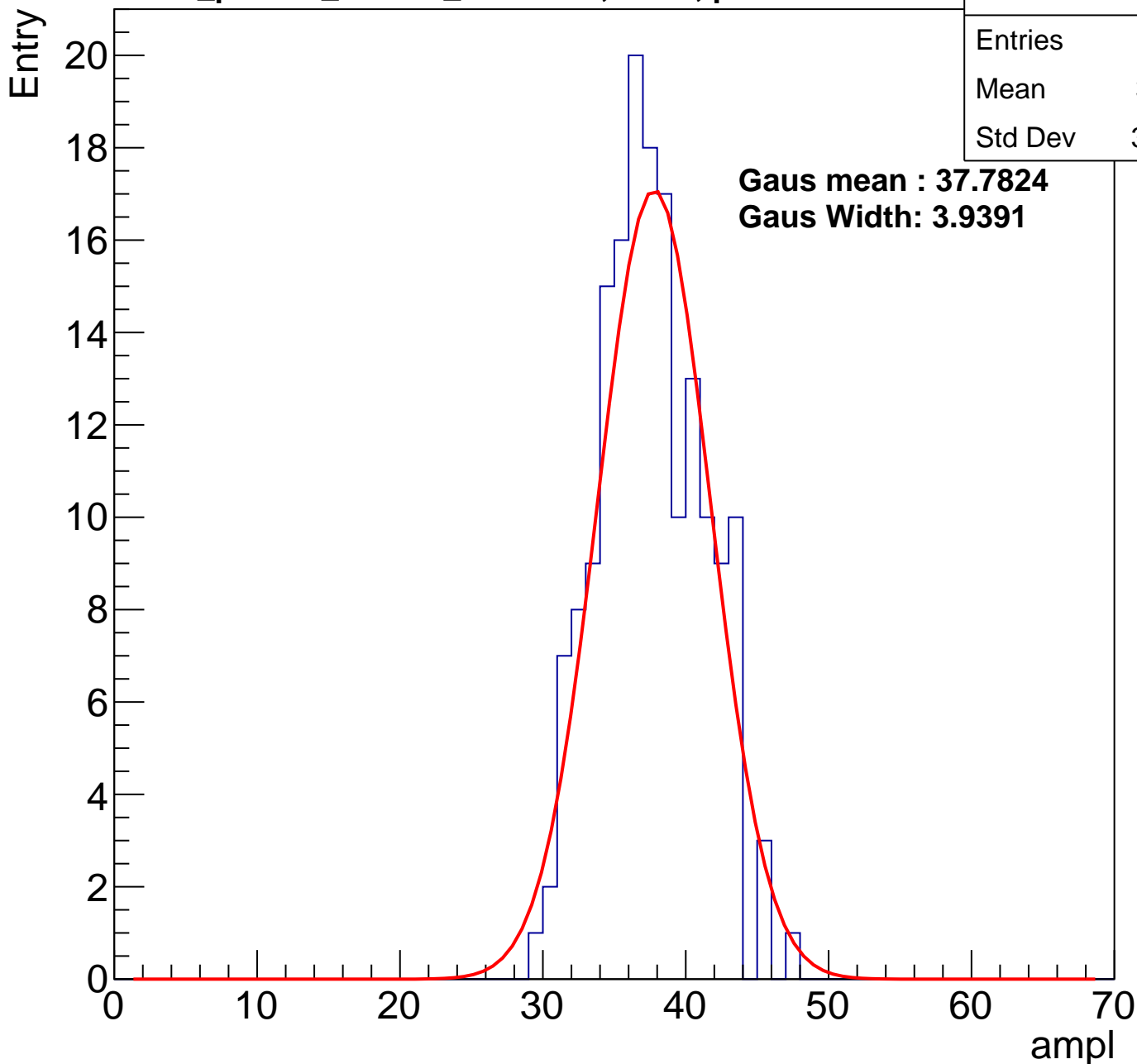
B1L001S, U21-ch127, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	37.11
Std Dev	3.597

Gaus mean : 37.7824

Gaus Width: 3.9391



B1L001S, U21-ch127, adc2

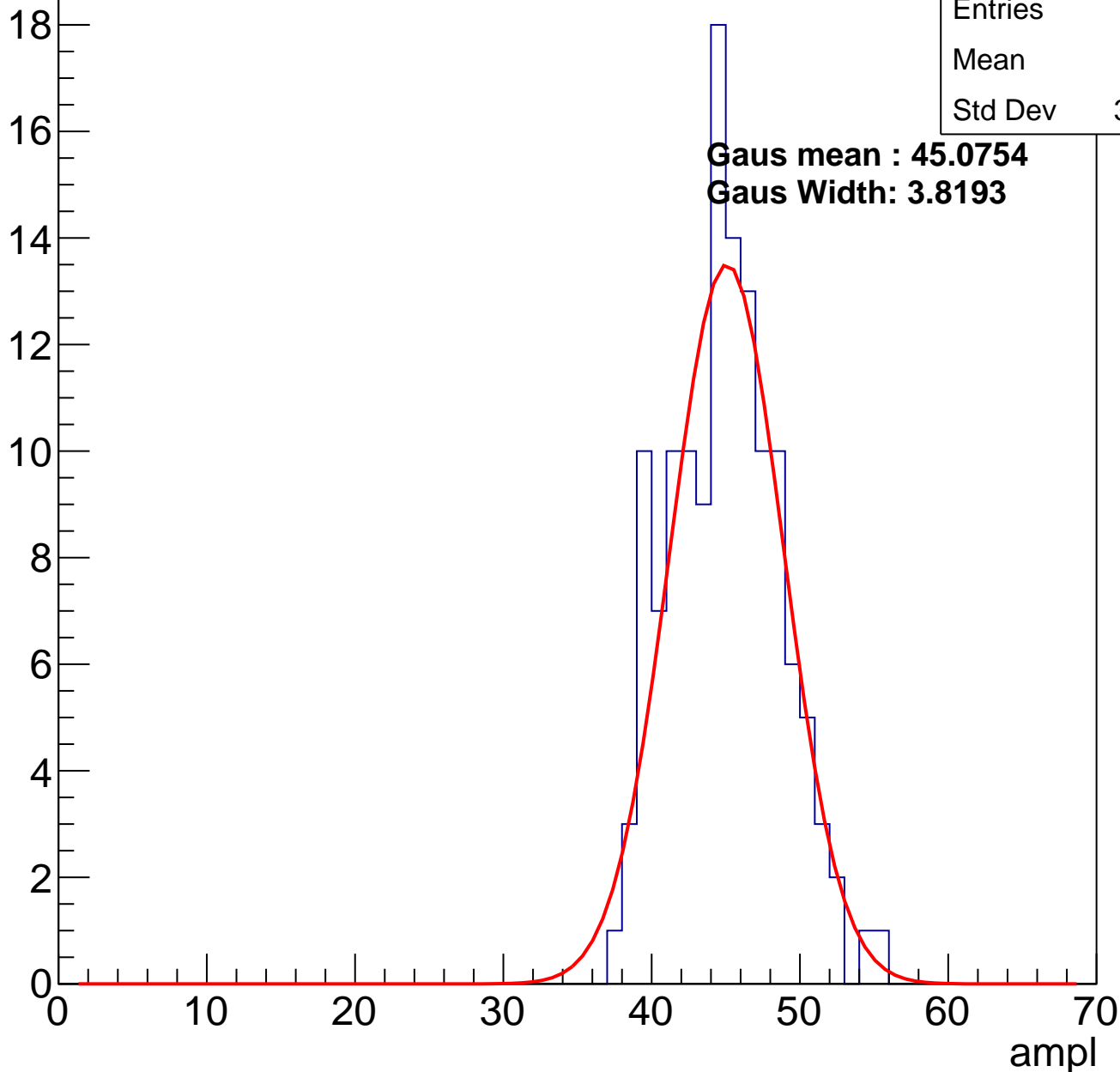
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	44.5
Std Dev	3.645

Gaus mean : 45.0754

Gaus Width: 3.8193

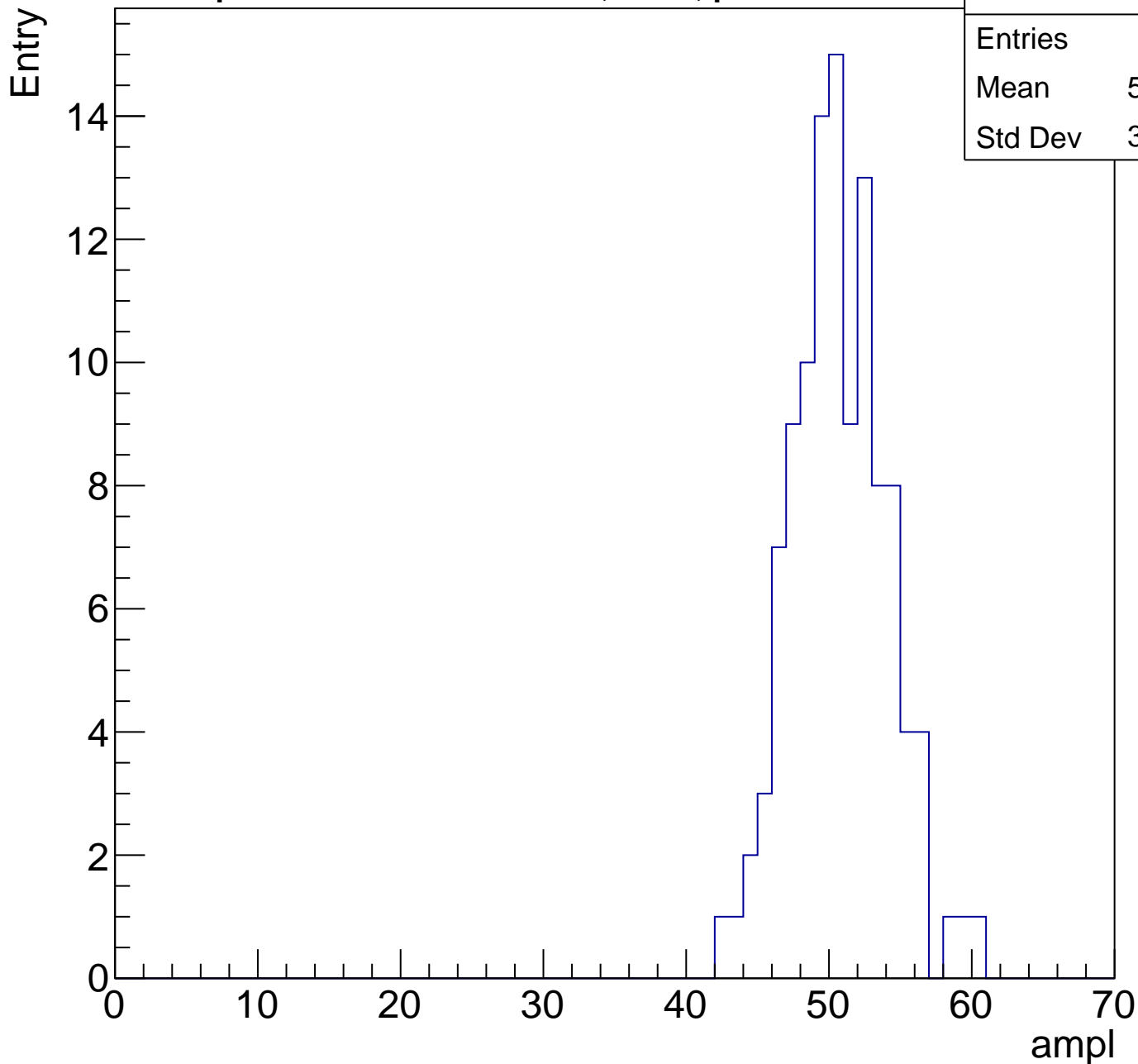
Entry



B1L001S, U21-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

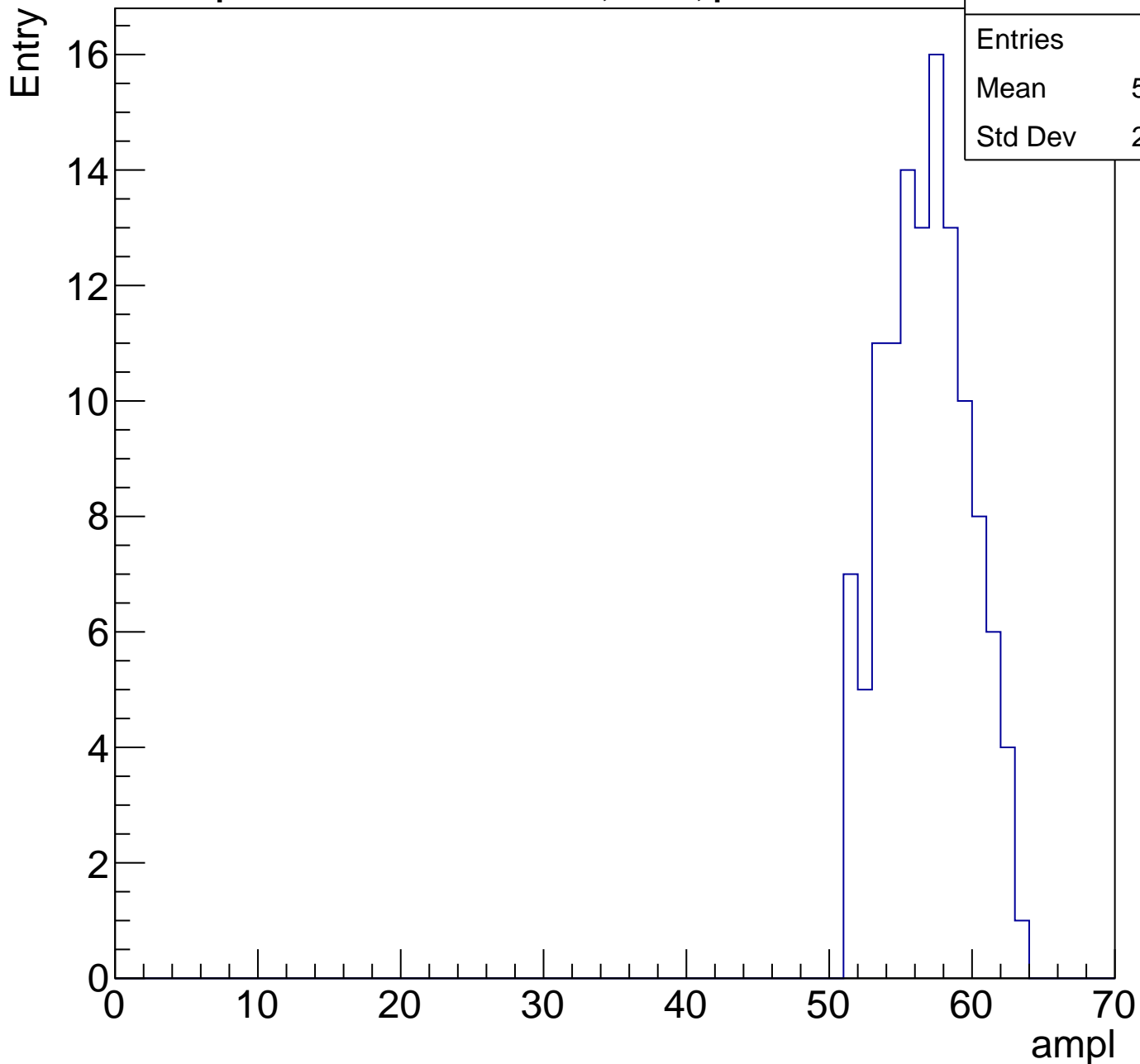
Entries	111
Mean	50.28
Std Dev	3.354



B1L001S, U21-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	56.34
Std Dev	2.938



B1L001S, U21-ch127, adc5

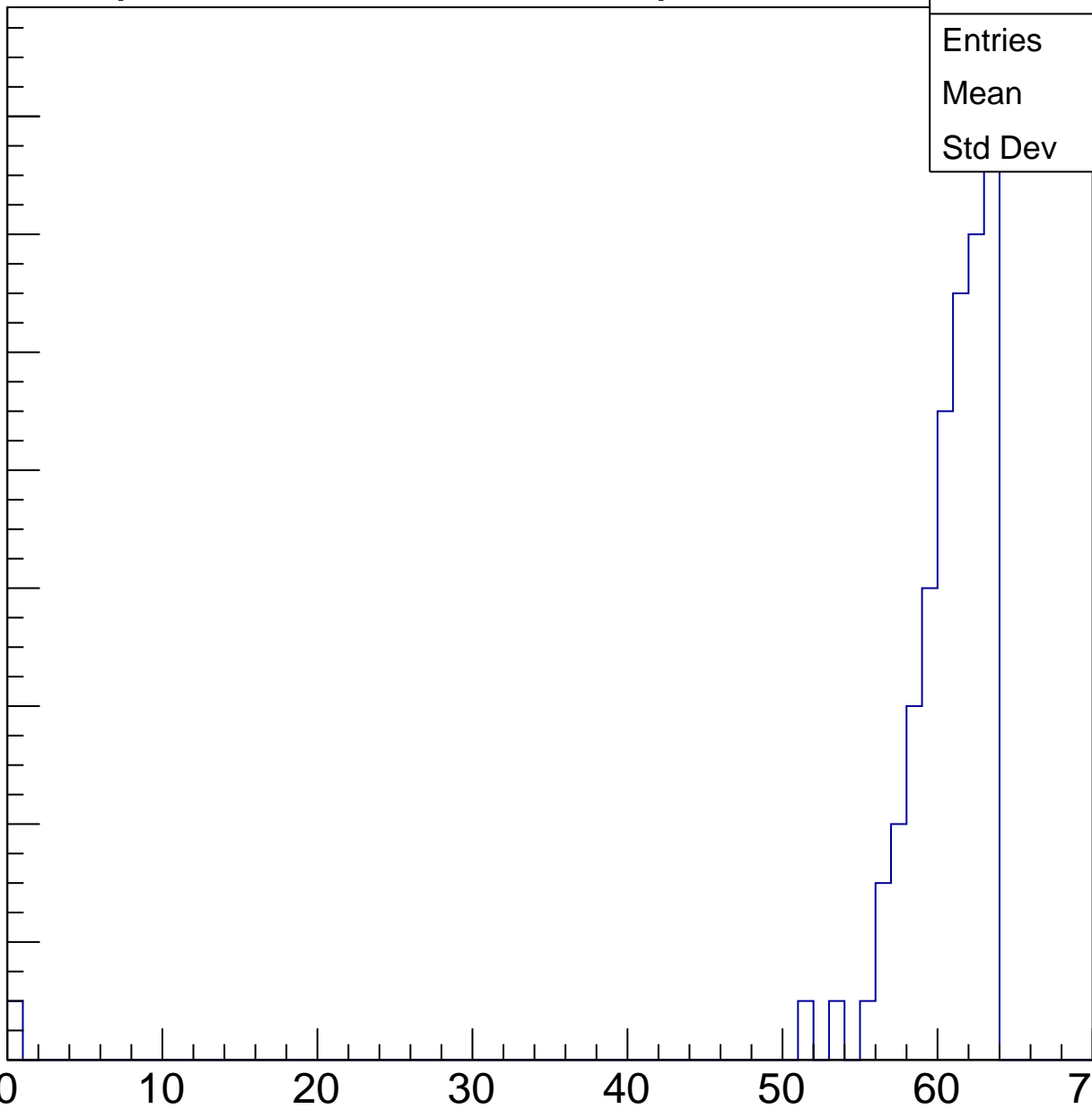
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.59
Std Dev	7.141

ampl



B1L001S, U21-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

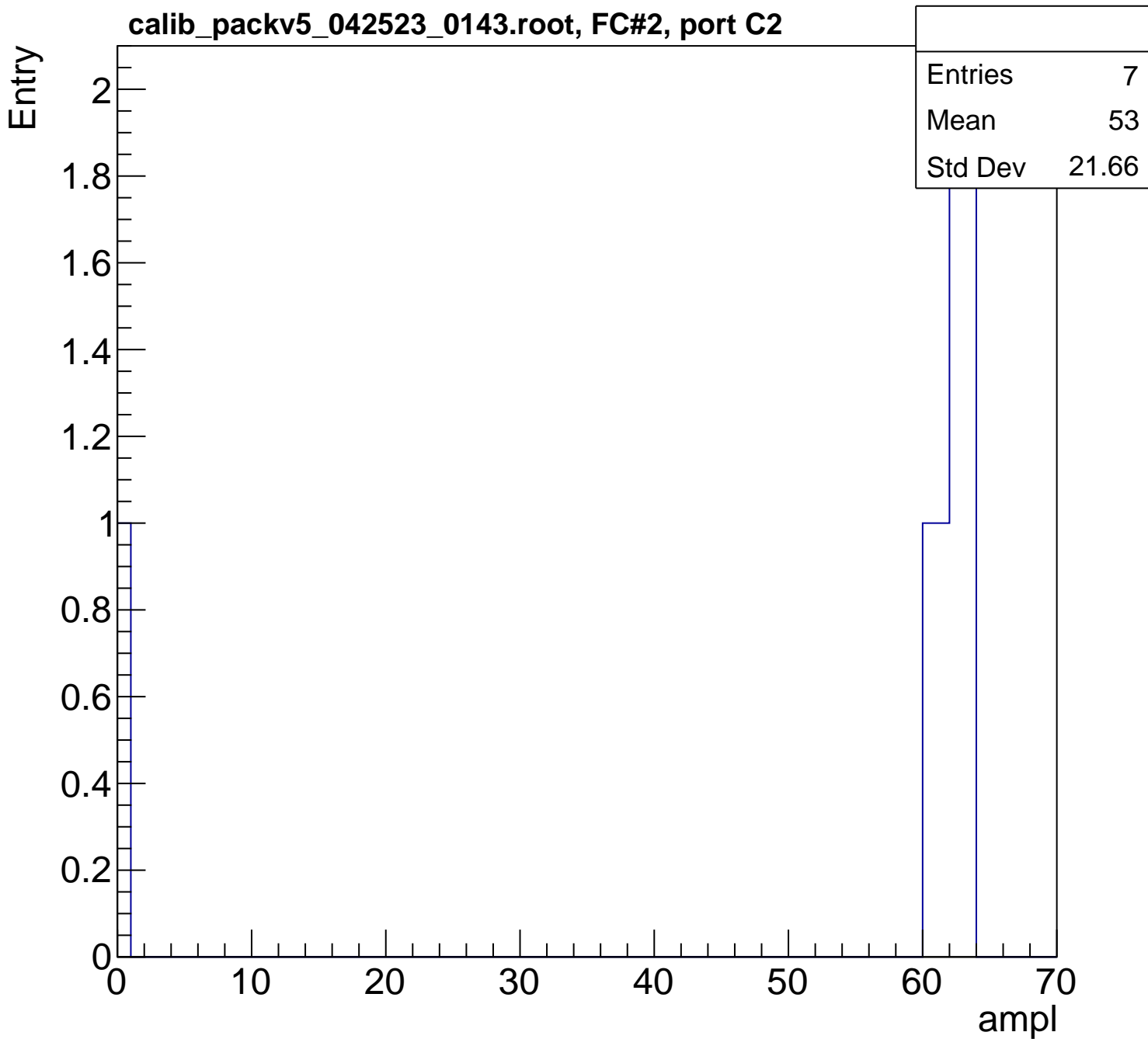
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	53
Std Dev	21.66

ampl

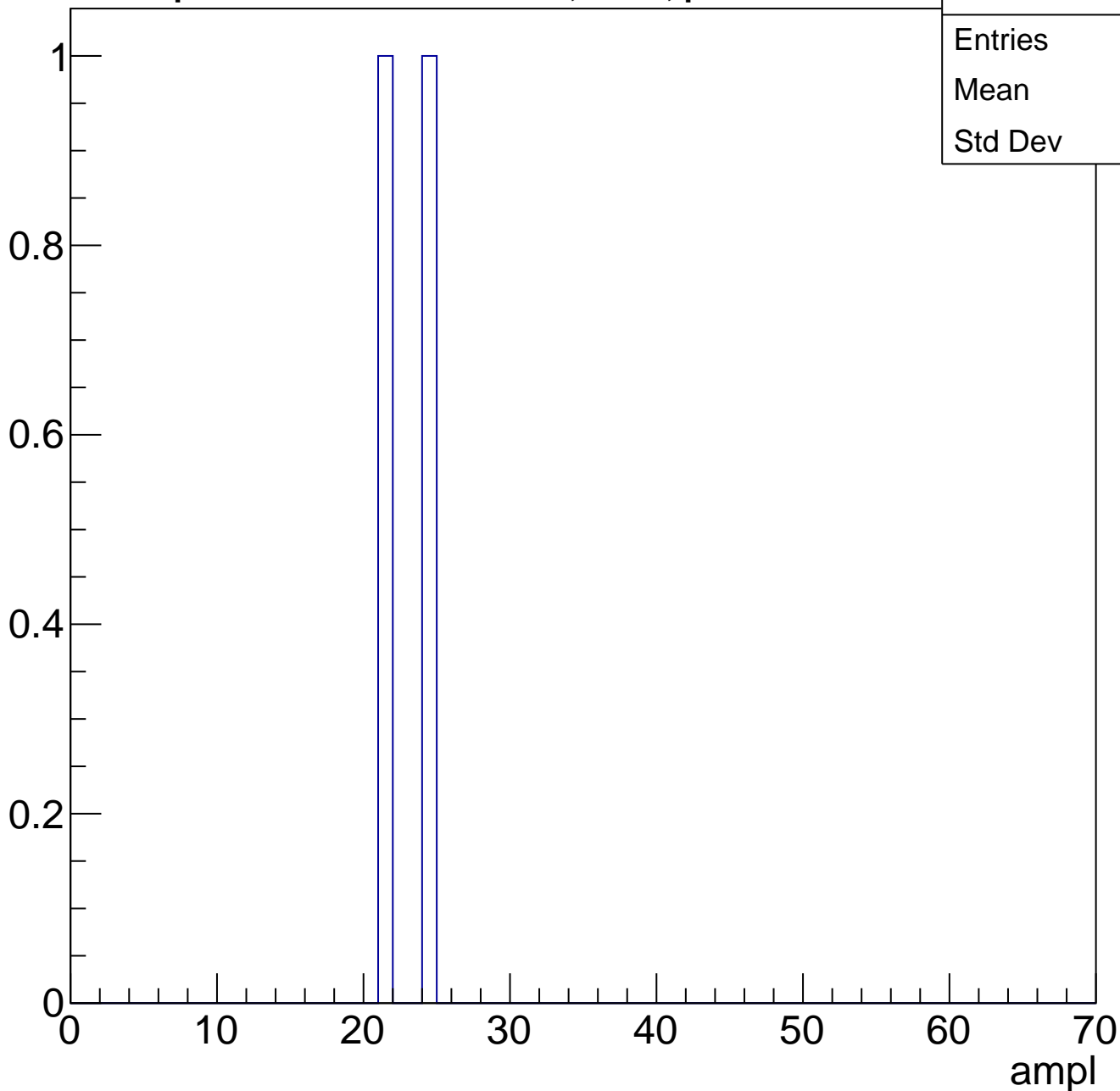
0 10 20 30 40 50 60 70



B1L001S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

