

B1L102S, U15-ch0, adc0

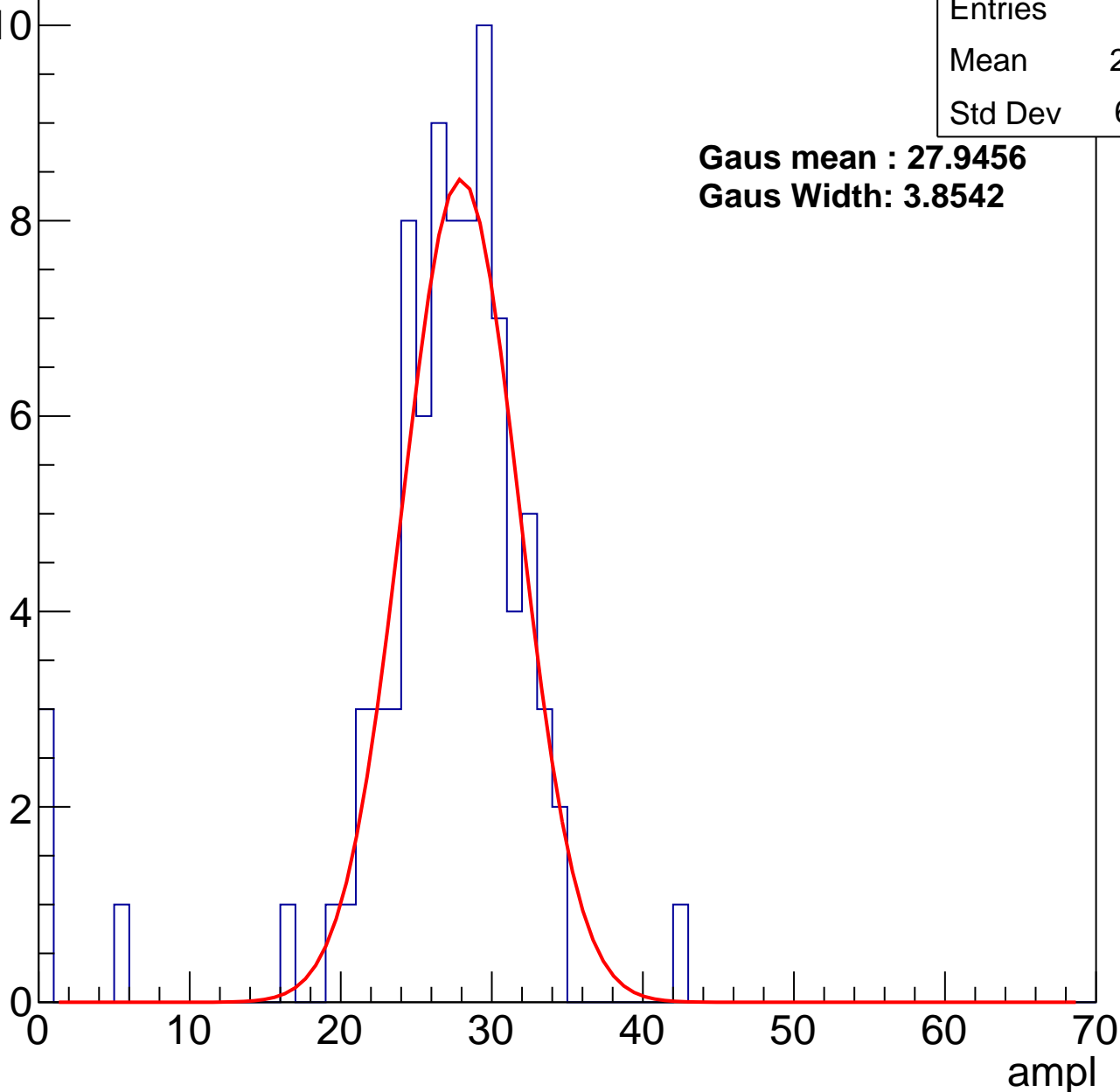
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 87 |
| Mean | 26.06 |
| Std Dev | 6.691 |

Gaus mean : 27.9456

Gaus Width: 3.8542



B1L102S, U15-ch0, adc1

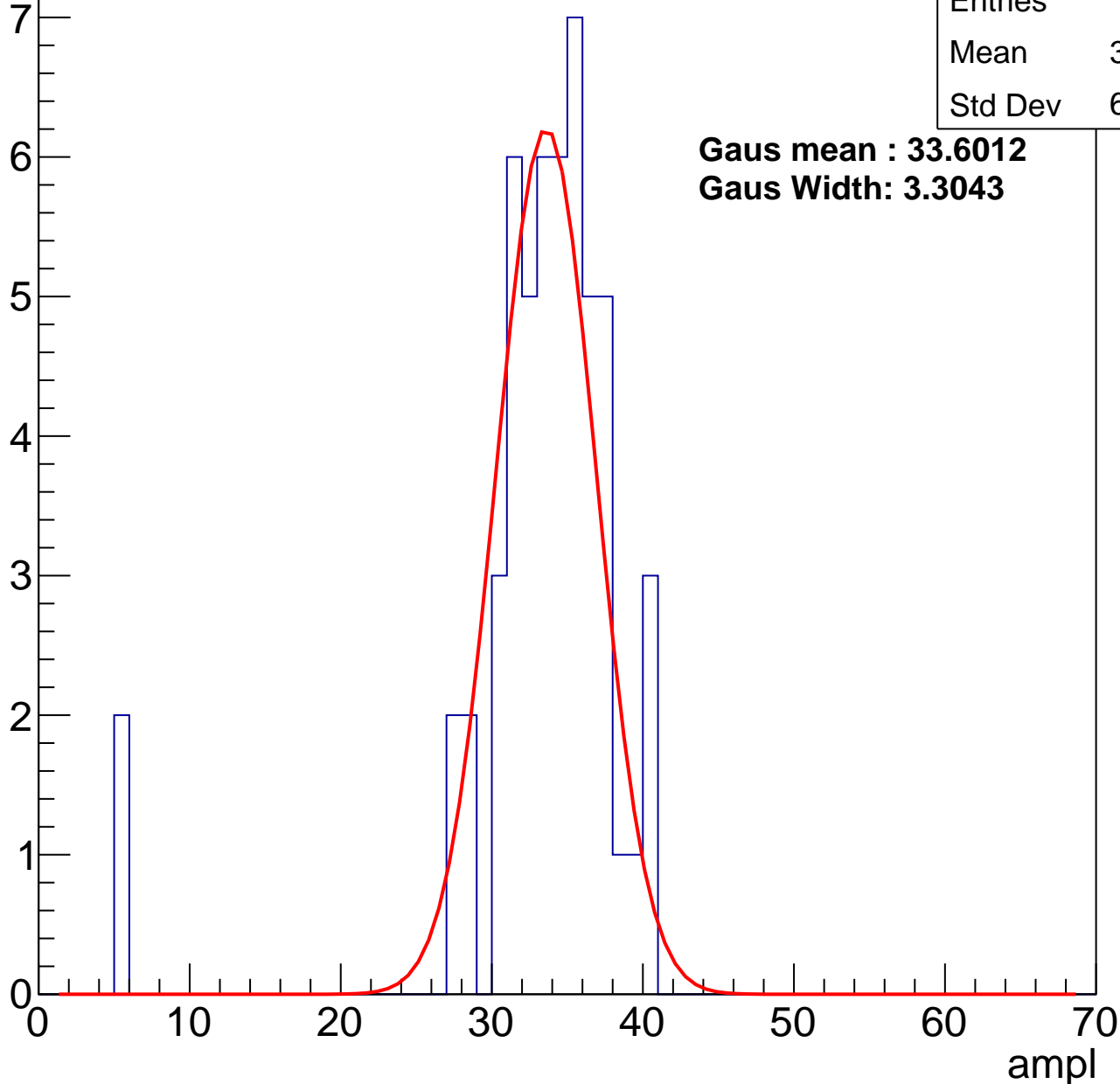
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 32.69 |
| Std Dev | 6.247 |

Gaus mean : 33.6012

Gaus Width: 3.3043



B1L102S, U15-ch0, adc2

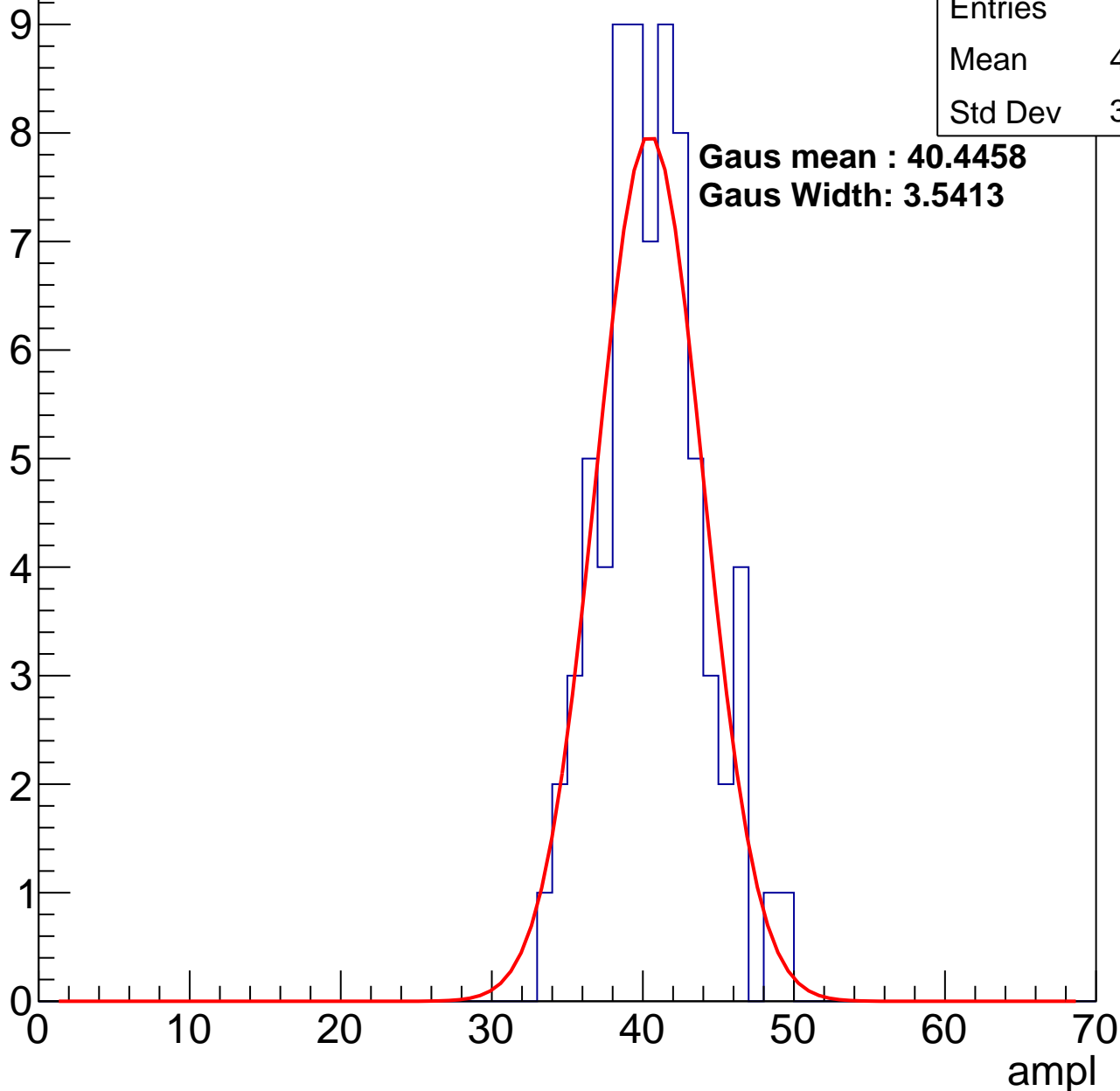
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 40.14 |
| Std Dev | 3.369 |

Gaus mean : 40.4458

Gaus Width: 3.5413

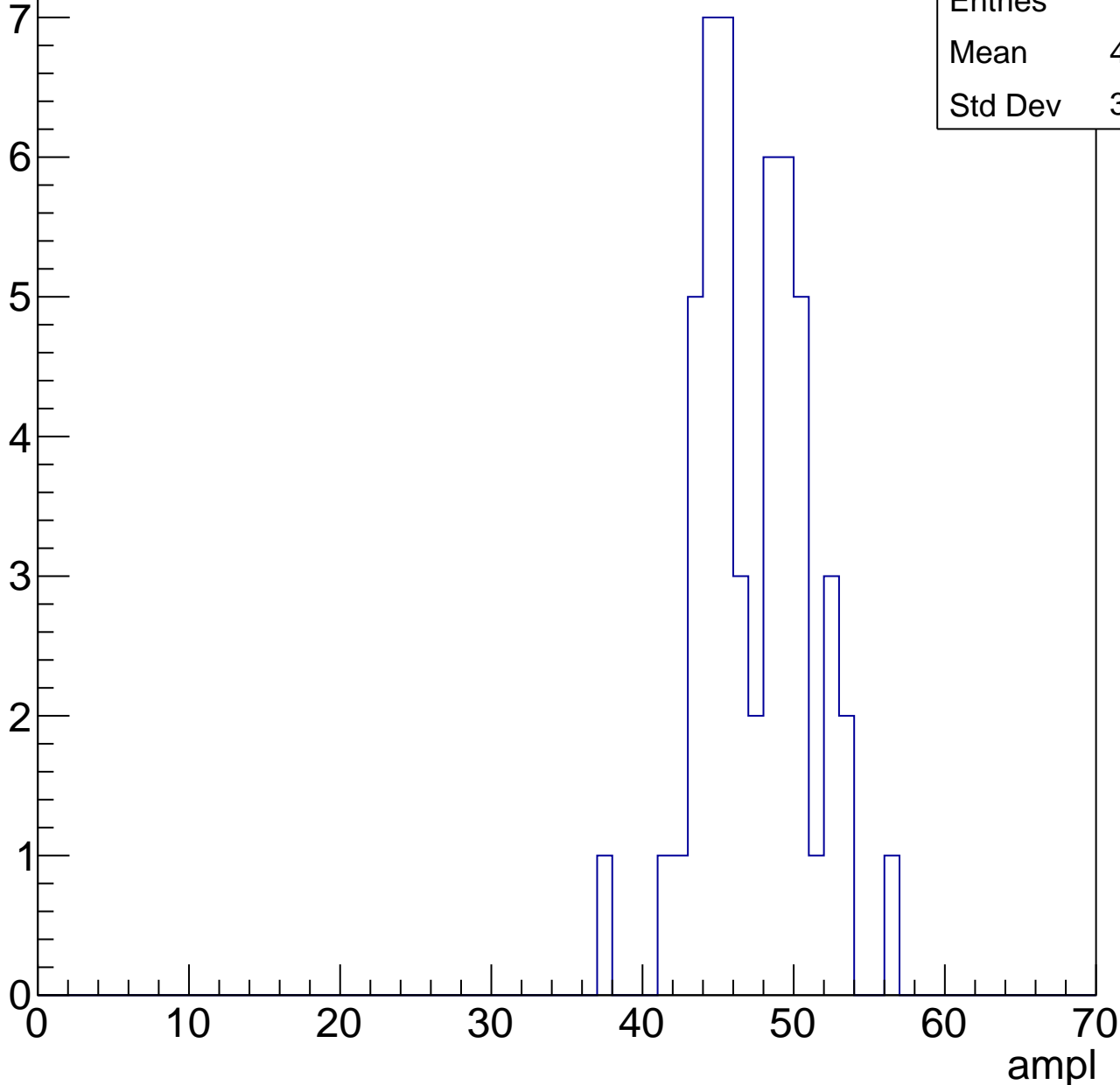


B1L102S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 46.88 |
| Std Dev | 3.579 |

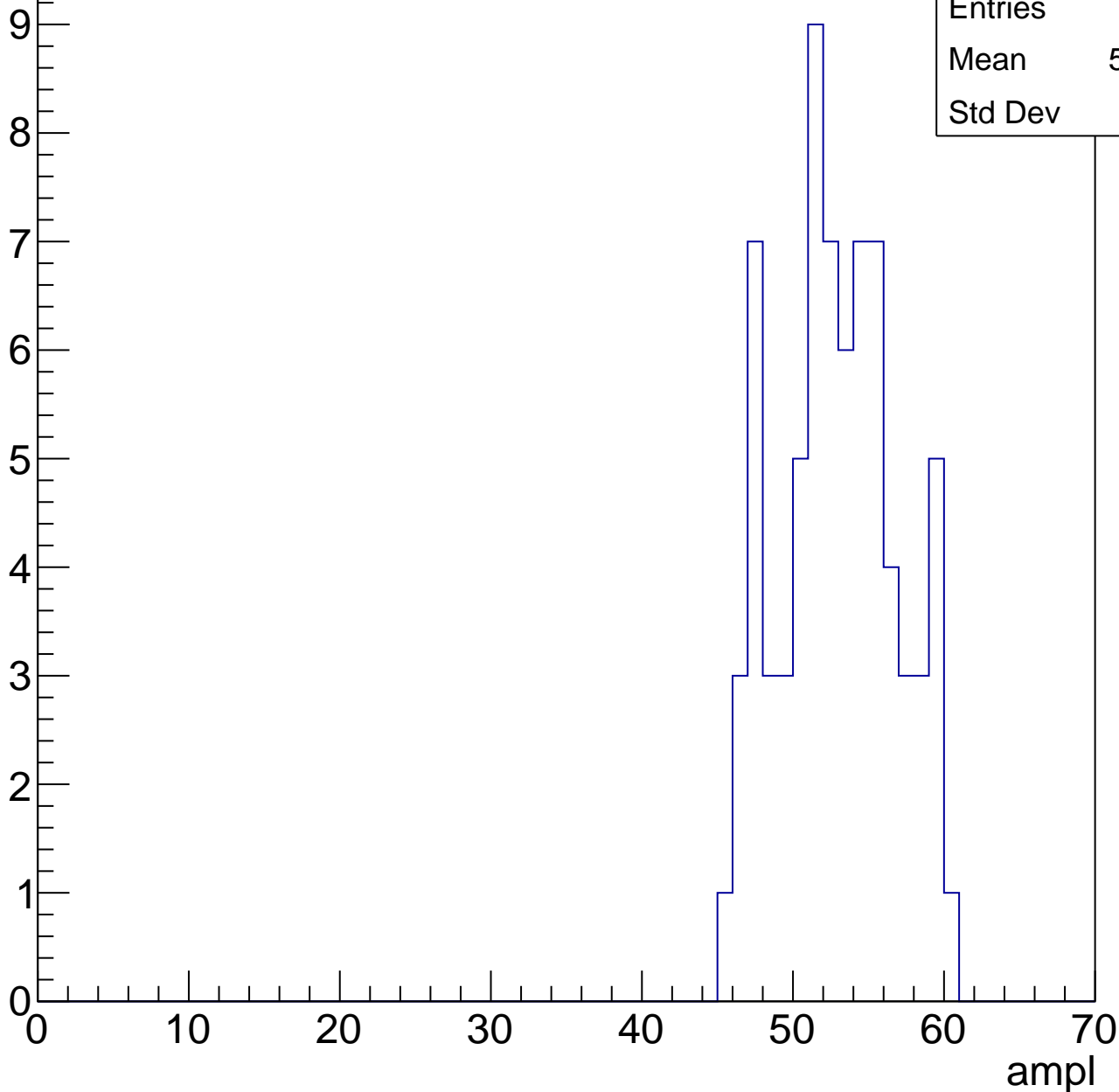


B1L102S, U15-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 52.45 |
| Std Dev | 3.81 |

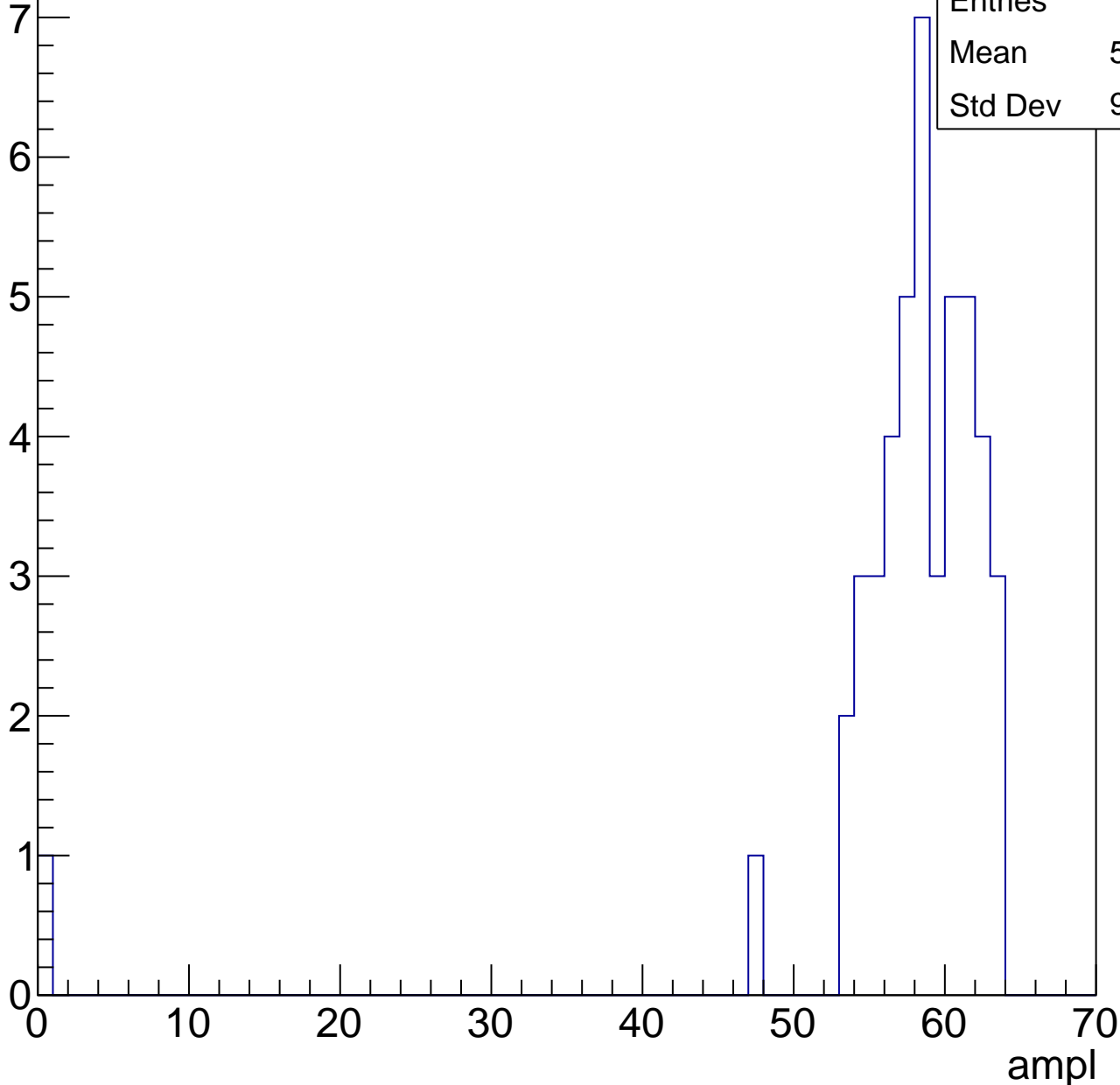


B1L102S, U15-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 56.83 |
| Std Dev | 9.059 |

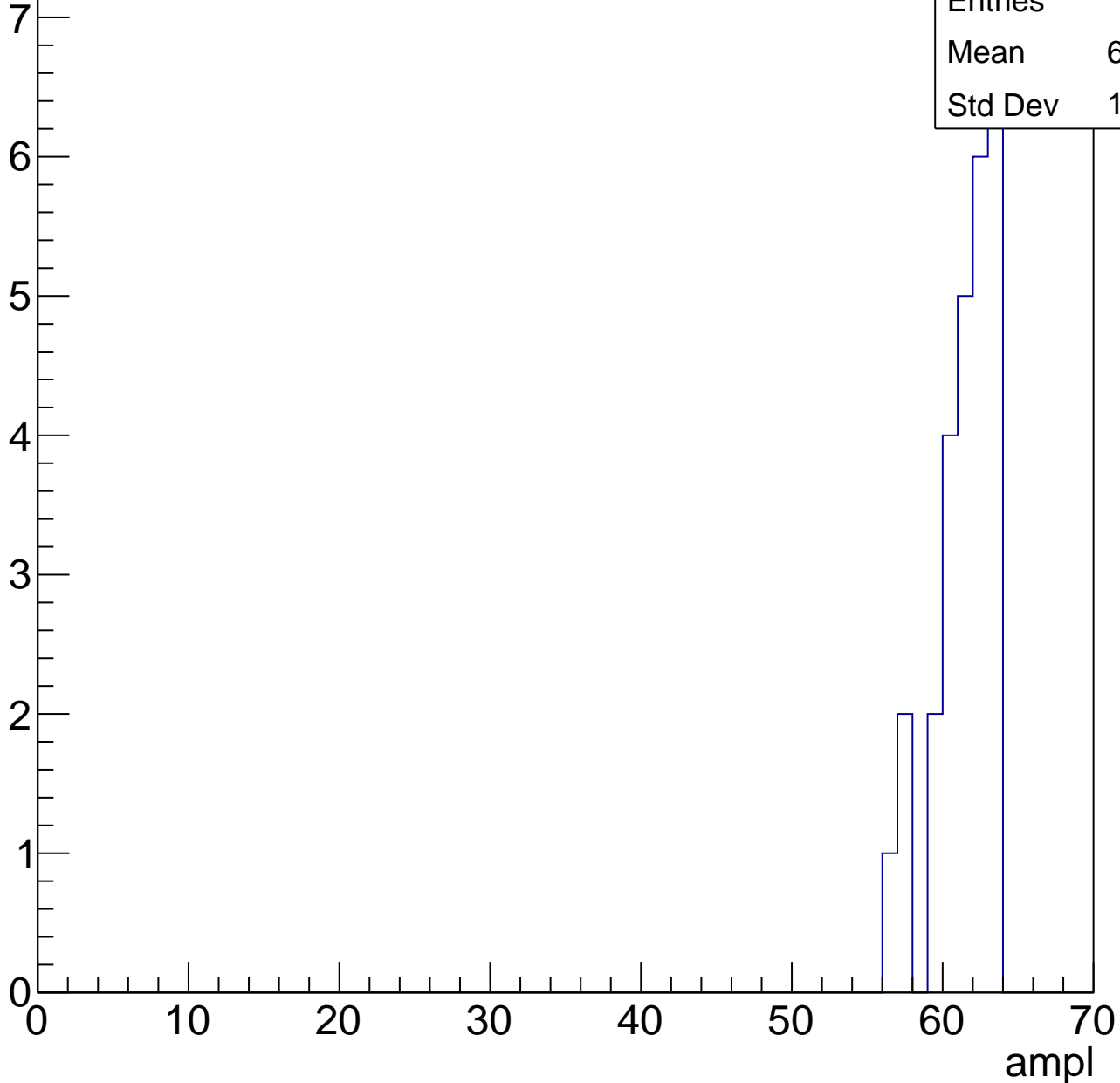


B1L102S, U15-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 27 |
| Mean | 60.96 |
| Std Dev | 1.953 |



B1L102S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 16 |
| Std Dev | 0 |

ampl

B1L102S, U15-ch1, adc0

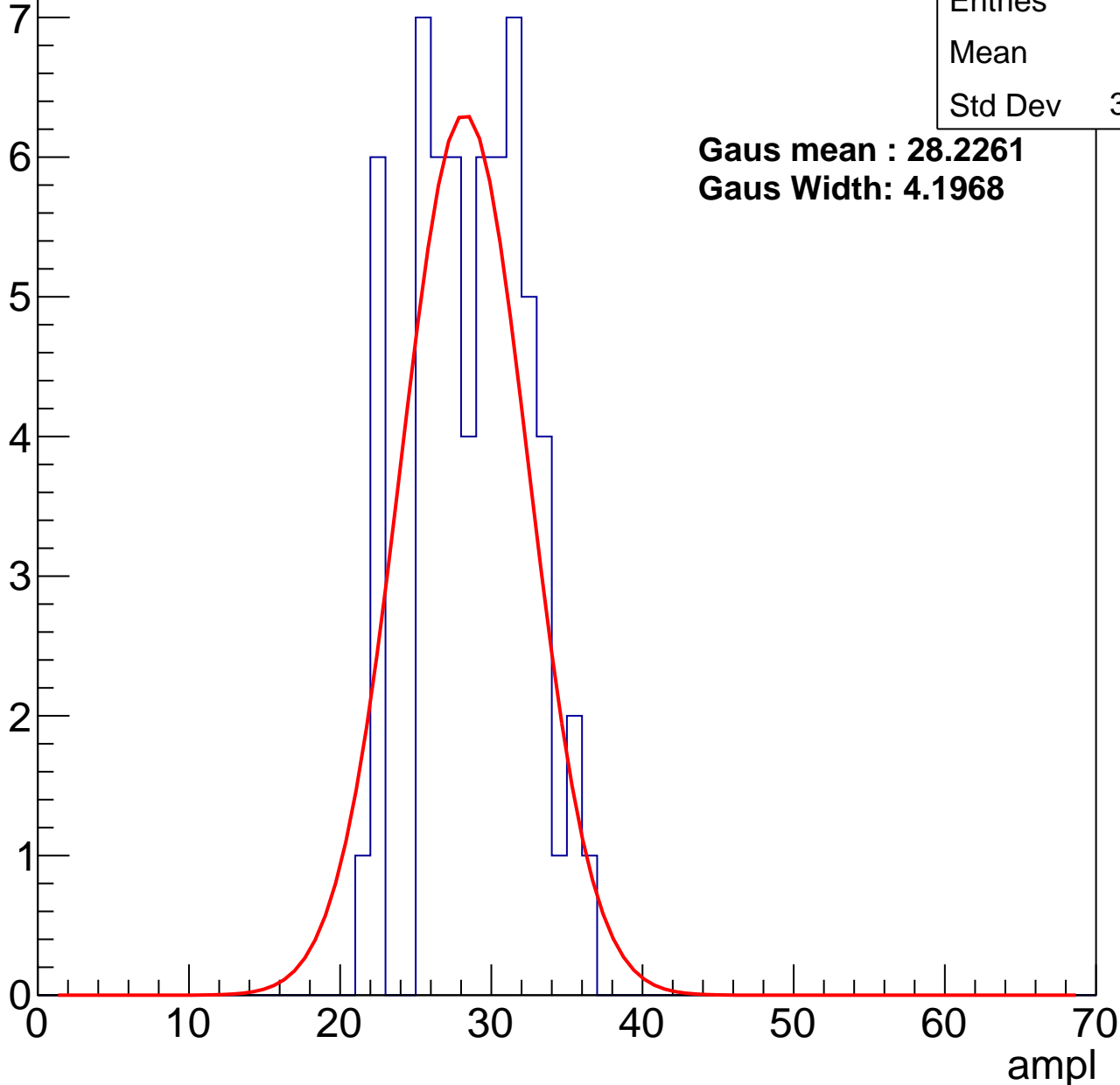
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 28.4 |
| Std Dev | 3.634 |

Gaus mean : 28.2261

Gaus Width: 4.1968



B1L102S, U15-ch1, adc1

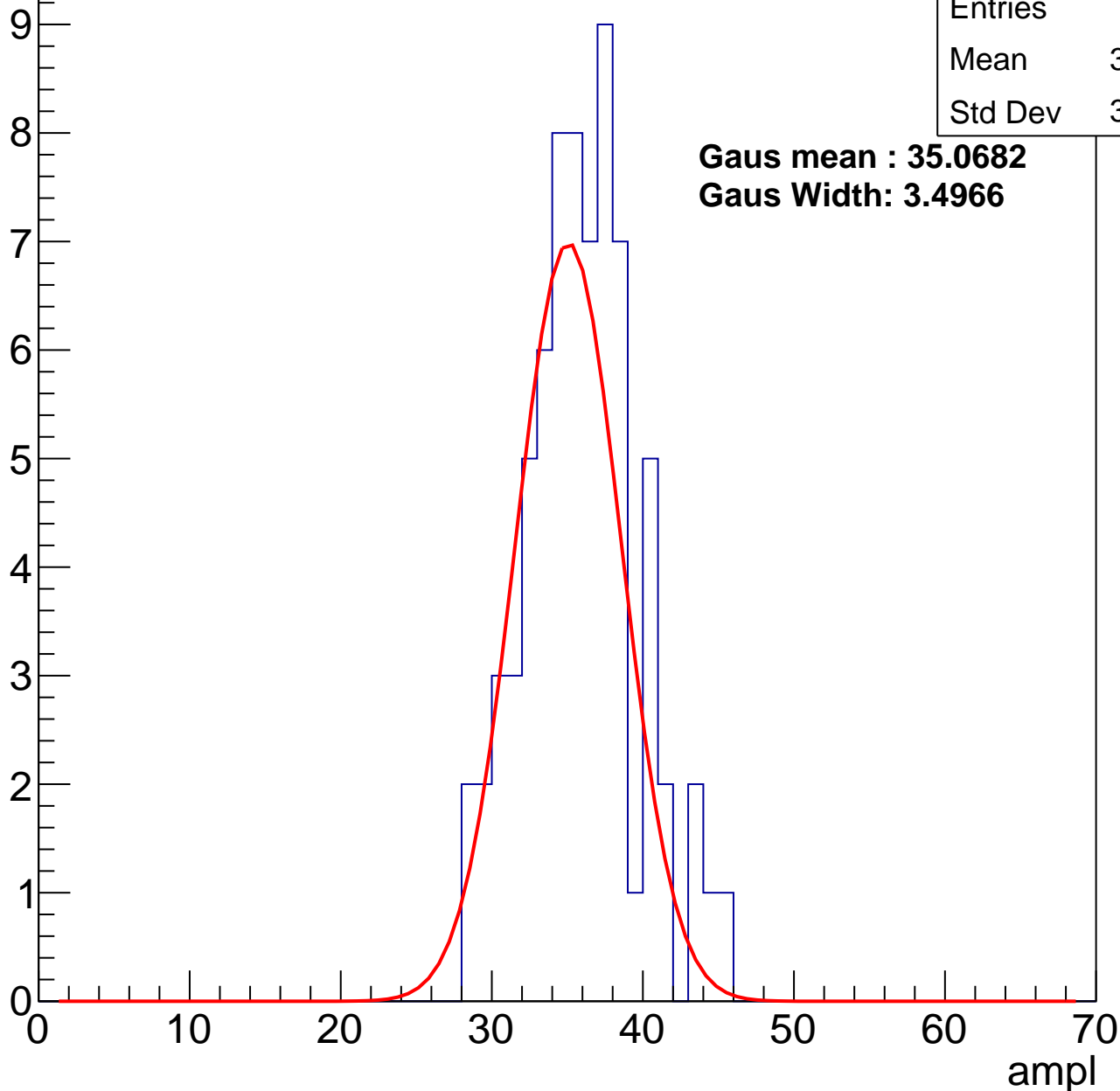
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.47 |
| Std Dev | 3.693 |

Gaus mean : 35.0682

Gaus Width: 3.4966



B1L102S, U15-ch1, adc2

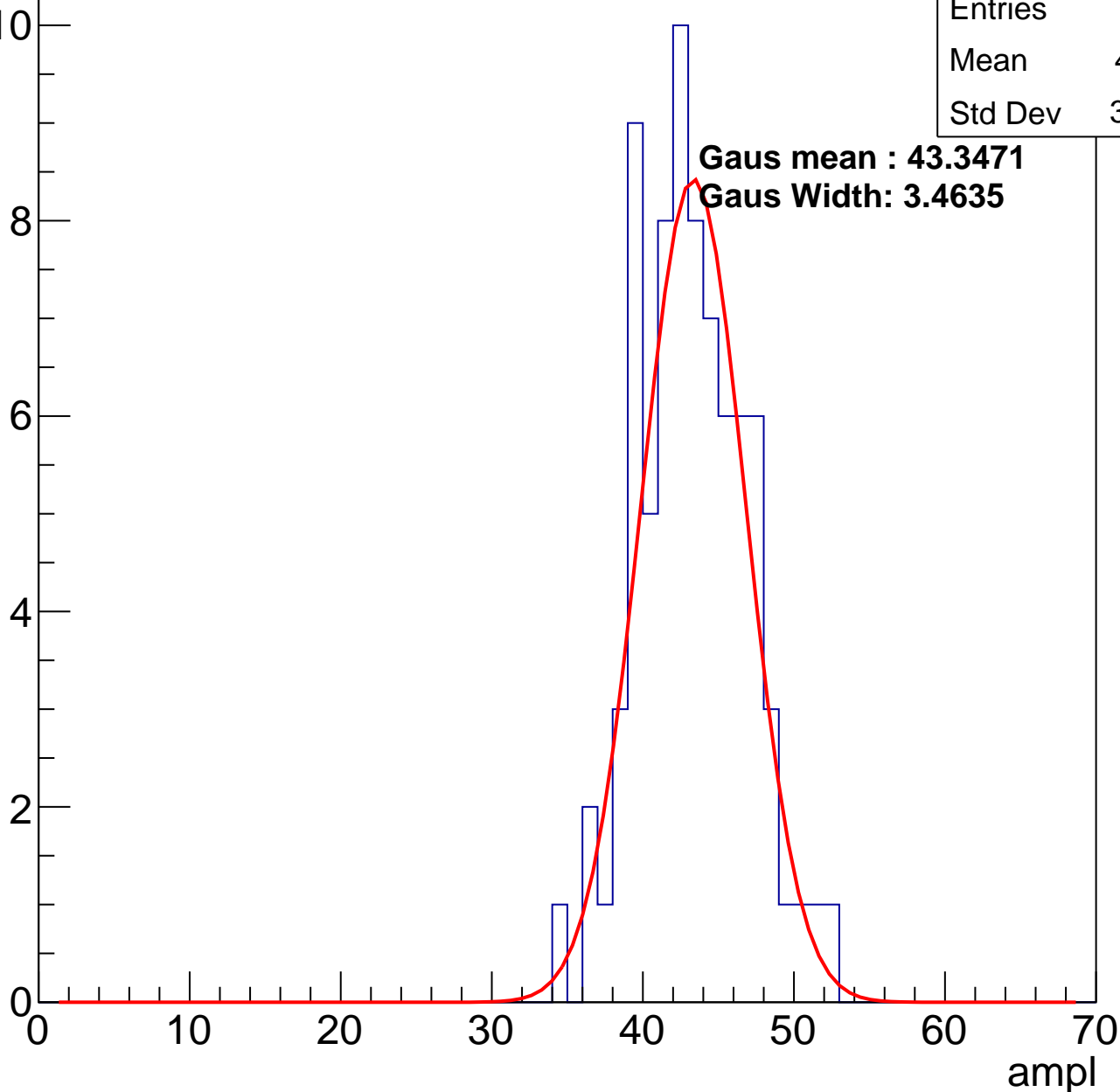
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 42.81 |
| Std Dev | 3.565 |

Gaus mean : 43.3471

Gaus Width: 3.4635

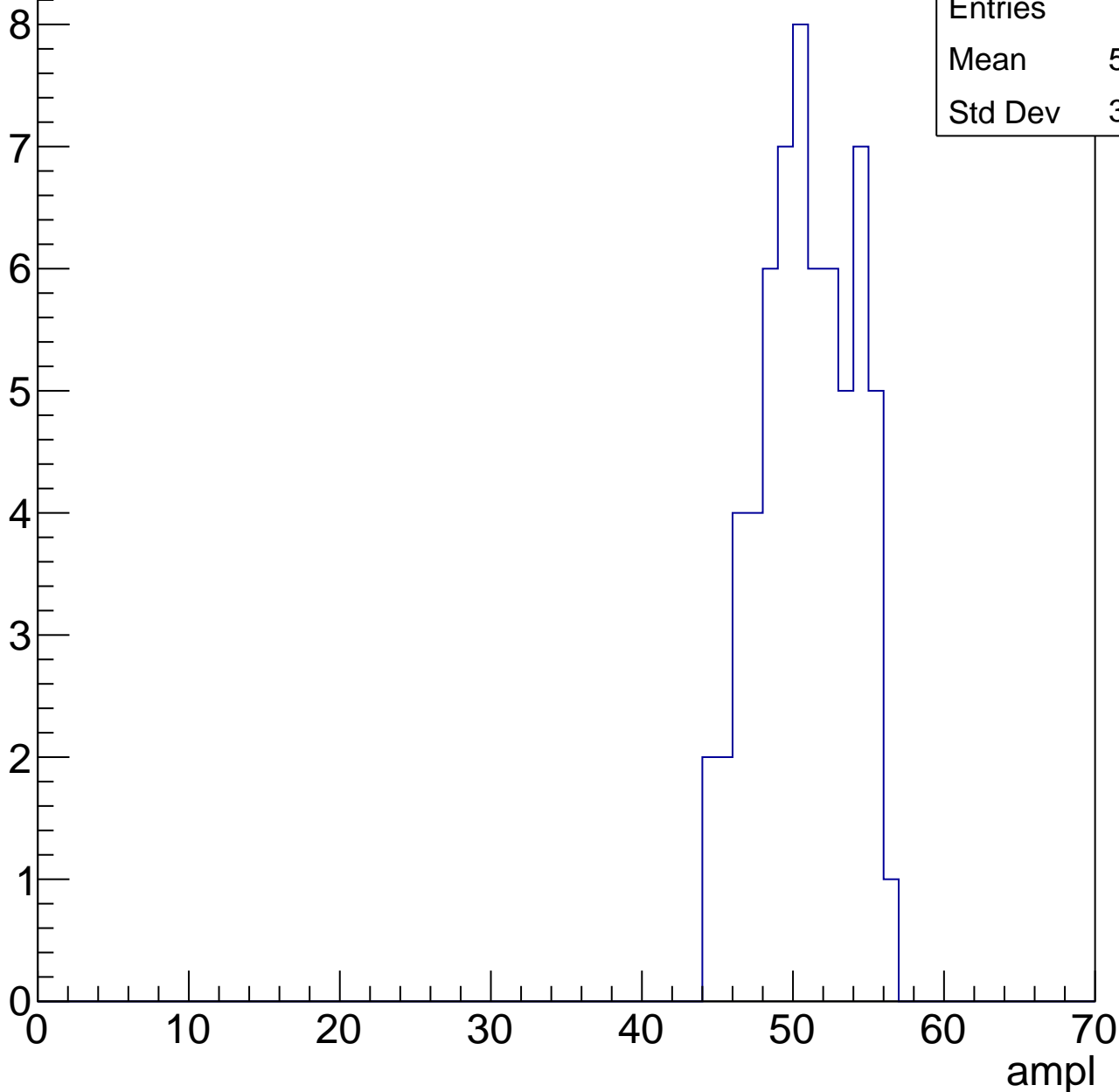


B1L102S, U15-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 50.37 |
| Std Dev | 3.067 |

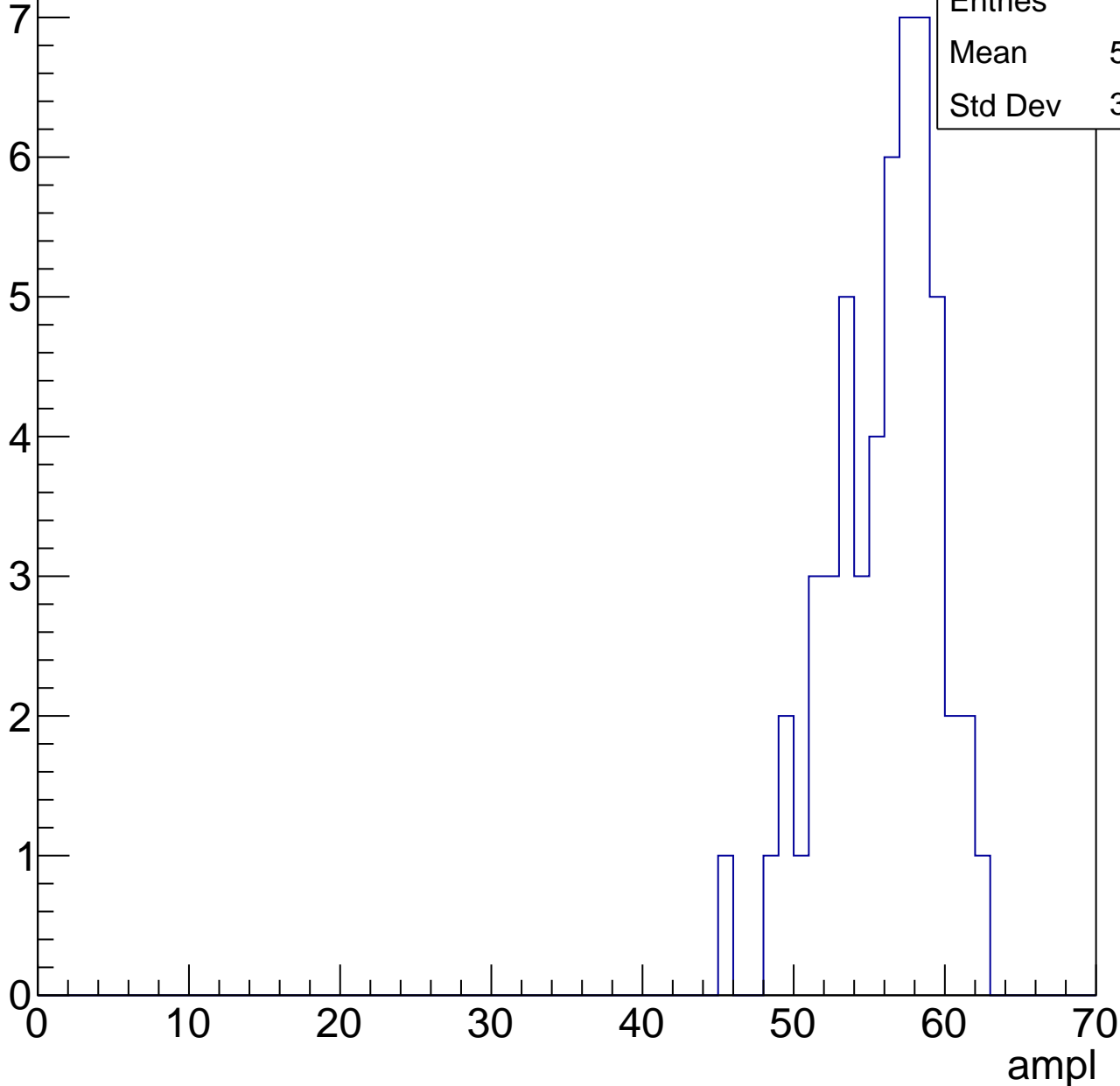


B1L102S, U15-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 55.42 |
| Std Dev | 3.589 |



B1L102S, U15-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.74 |
| Std Dev | 9.339 |

ampl

0

10

20

30

40

50

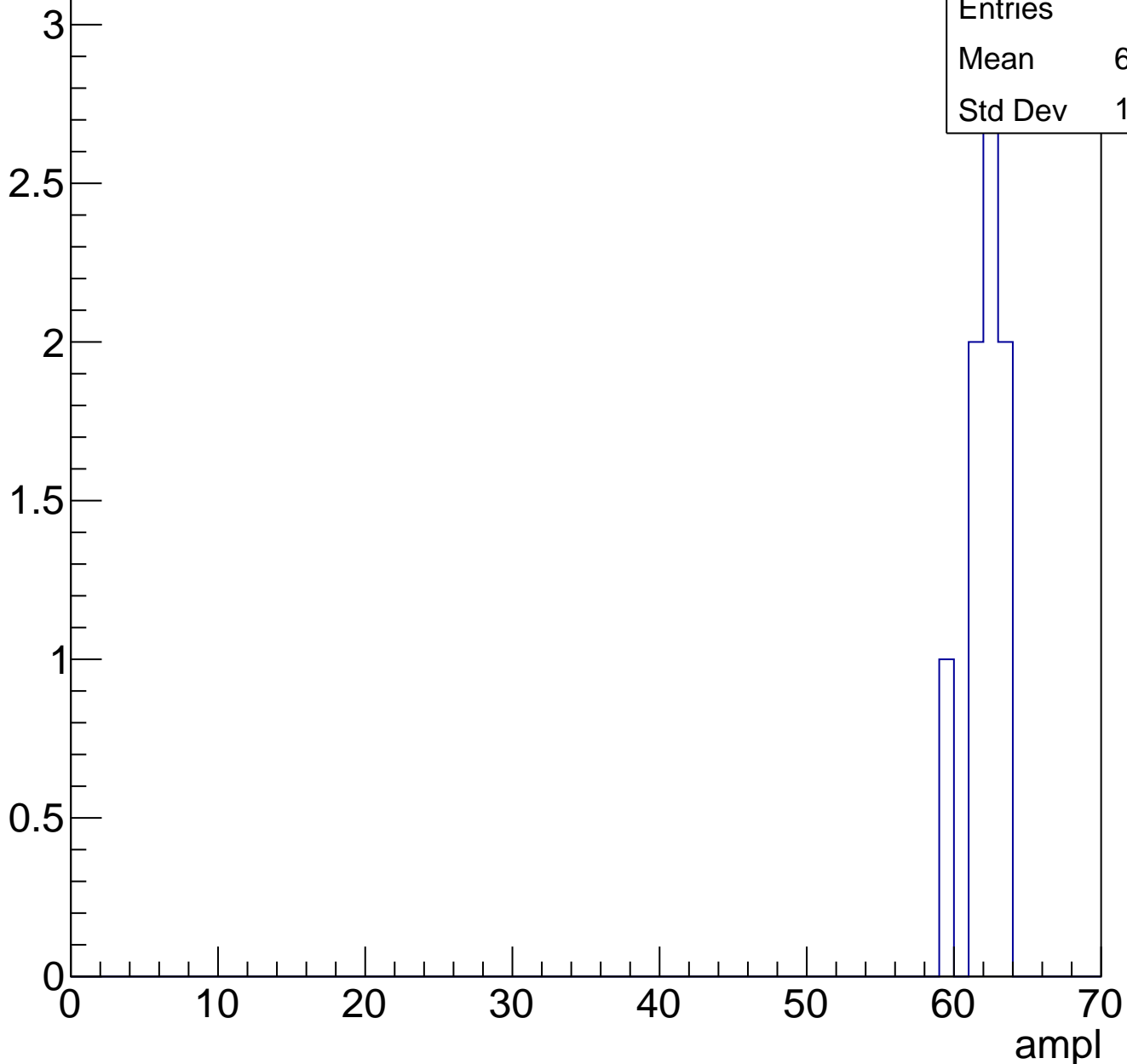
60

70

B1L102S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch2, adc0

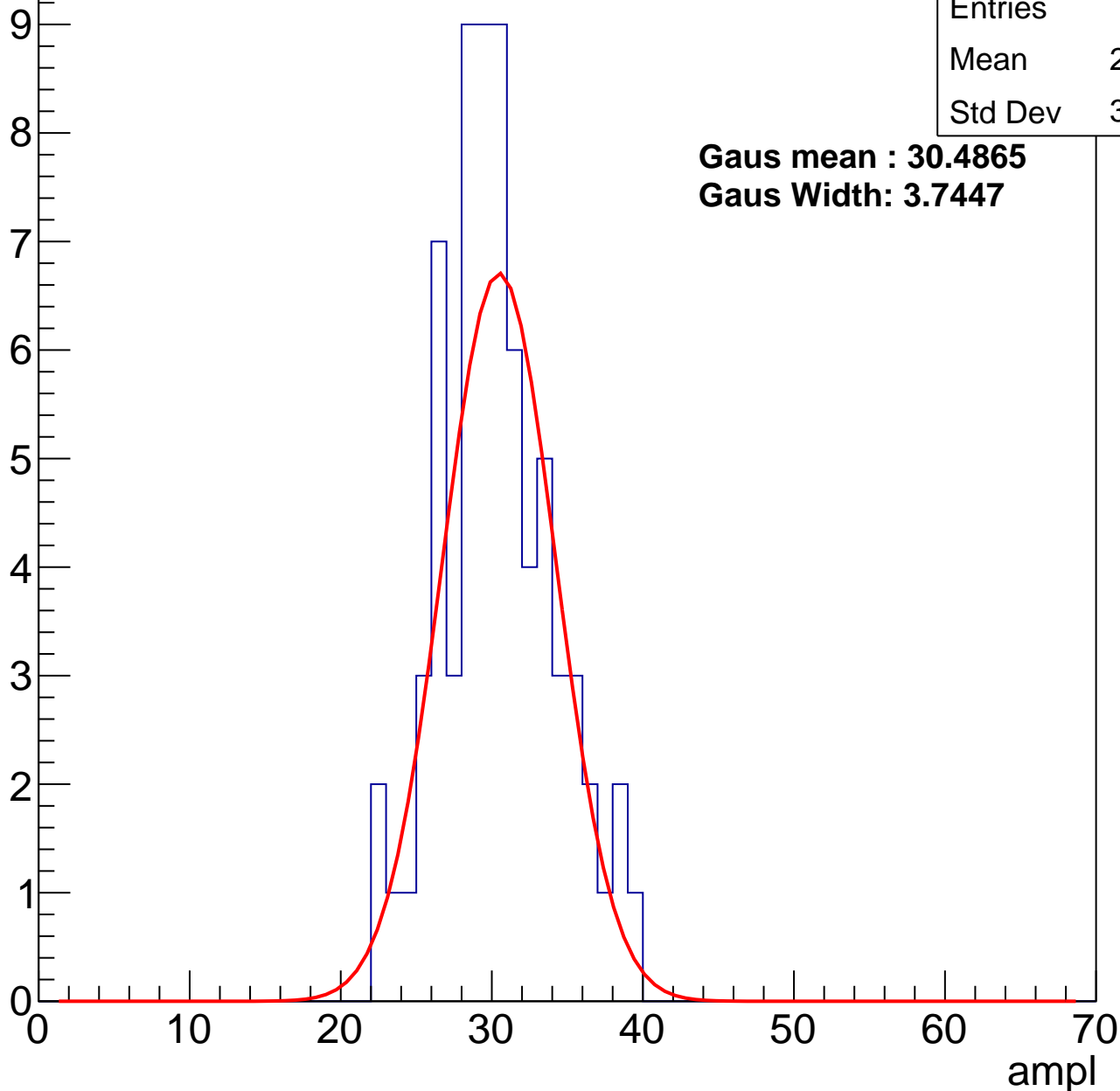
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.89 |
| Std Dev | 3.736 |

Gaus mean : 30.4865

Gaus Width: 3.7447



B1L102S, U15-ch2, adc1

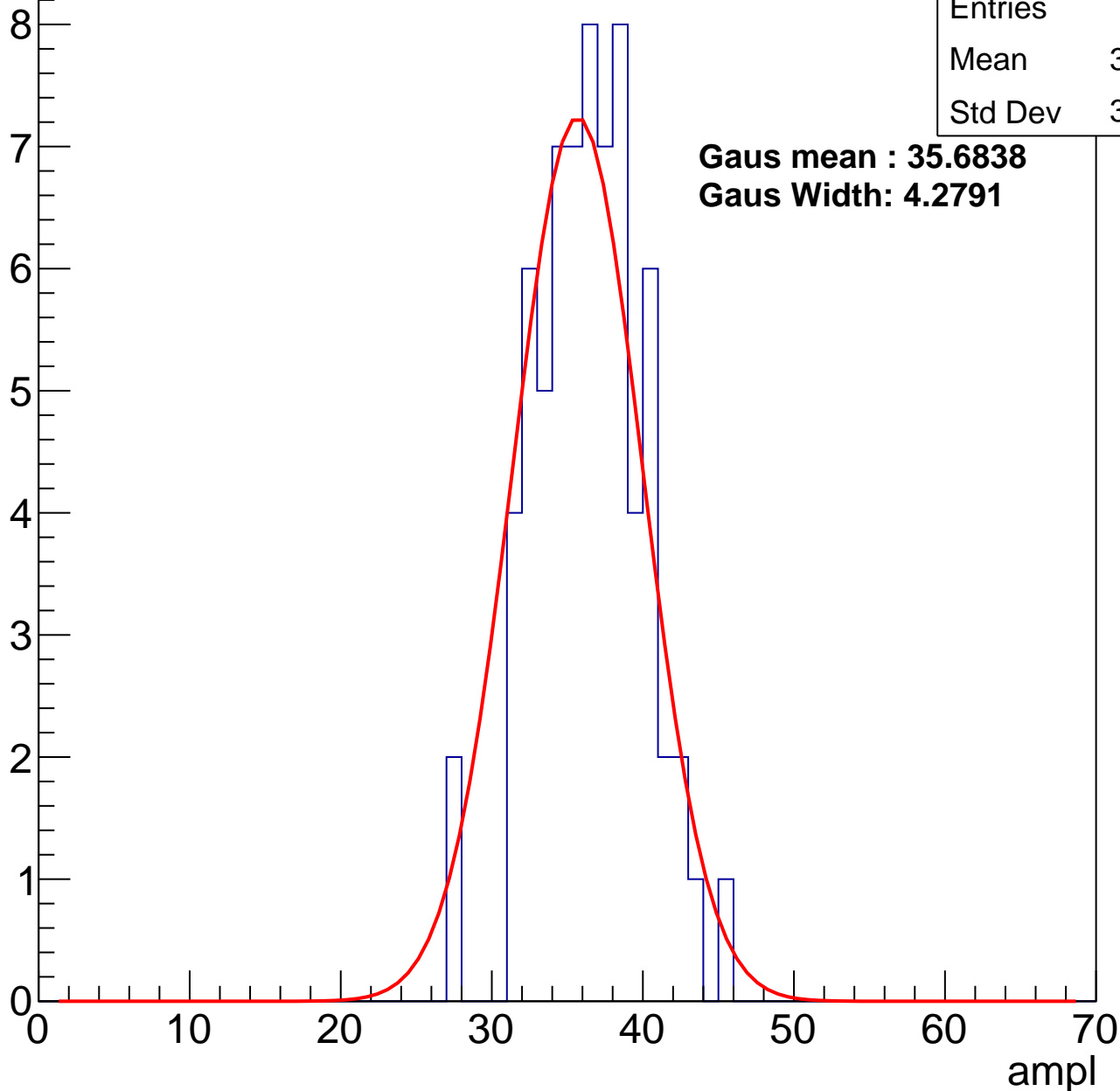
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.99 |
| Std Dev | 3.507 |

Gaus mean : 35.6838

Gaus Width: 4.2791



B1L102S, U15-ch2, adc2

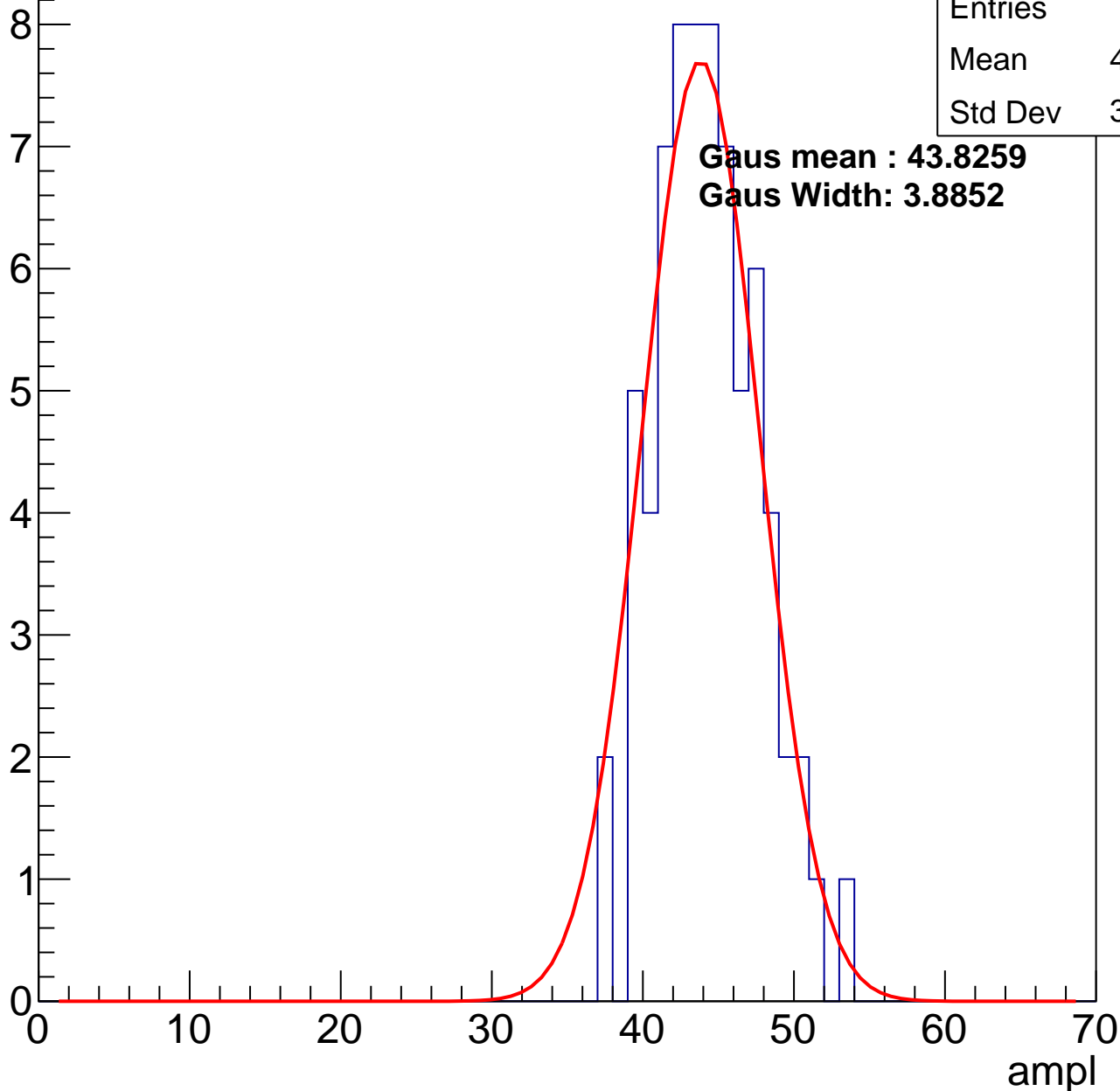
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 43.84 |
| Std Dev | 3.349 |

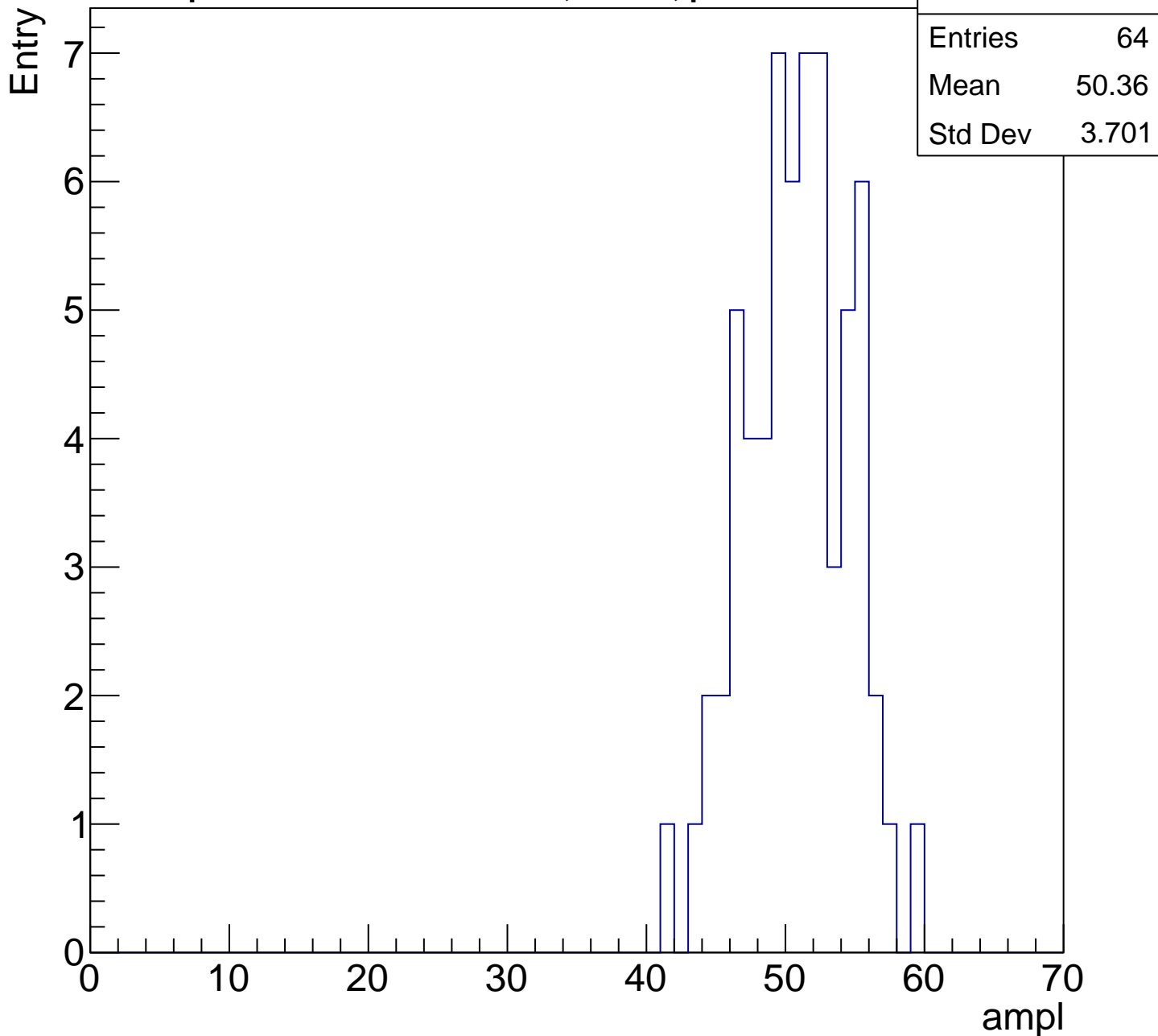
Gaus mean : 43.8259

Gaus Width: 3.8852



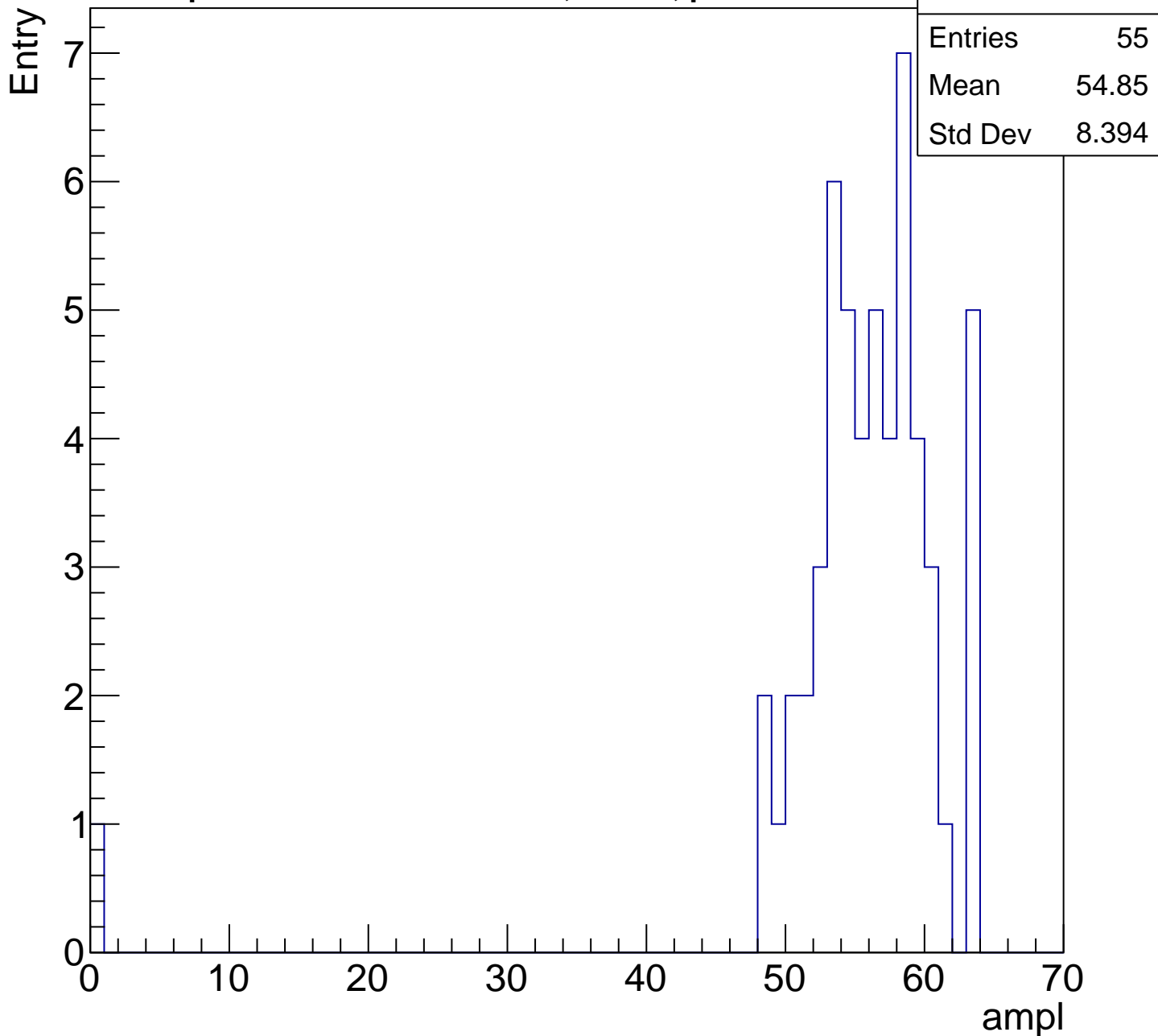
B1L102S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch2, adc4

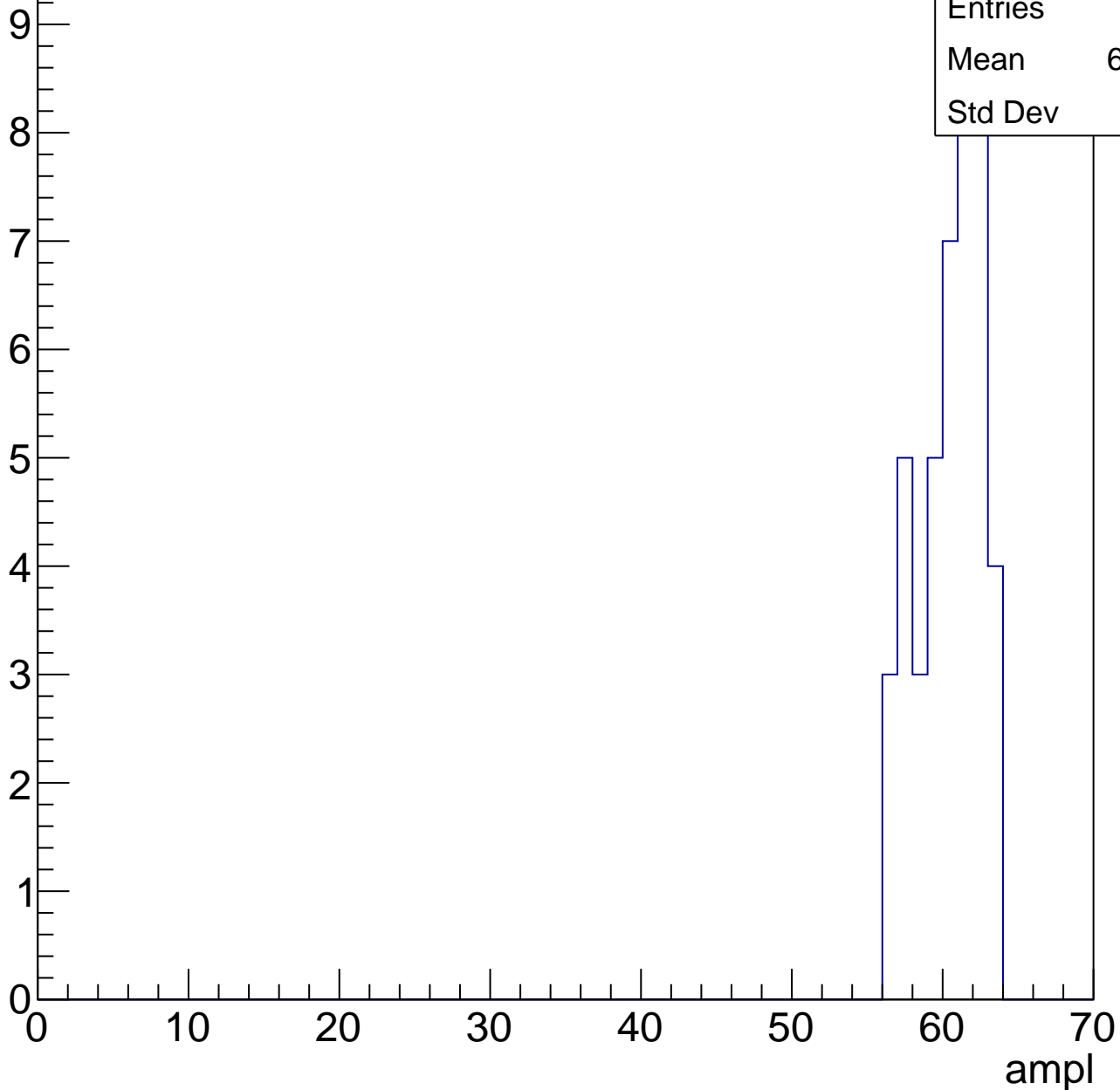
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

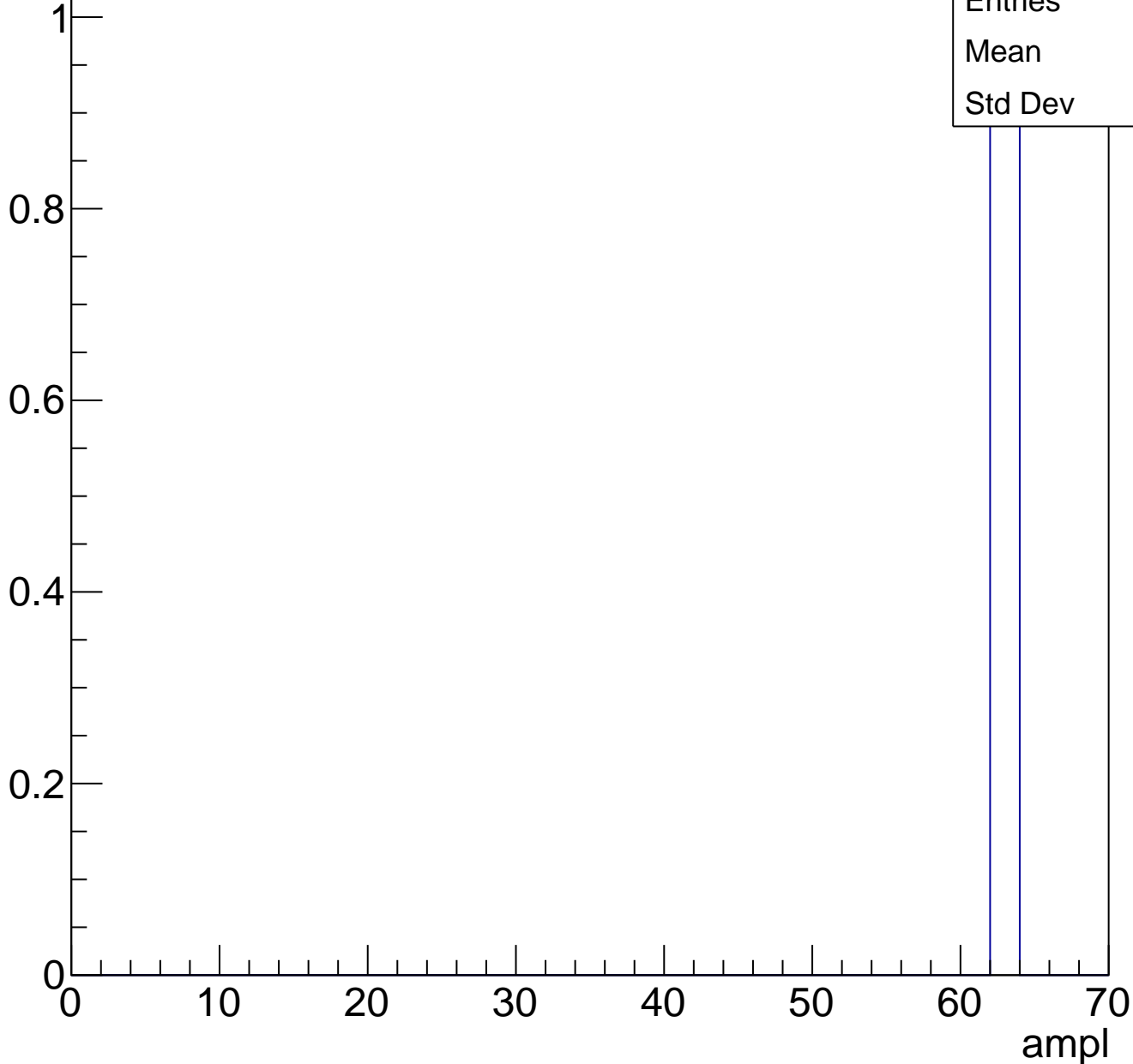


| | |
|---------|-------|
| Entries | 45 |
| Mean | 60.02 |
| Std Dev | 2.06 |

B1L102S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch3, adc0

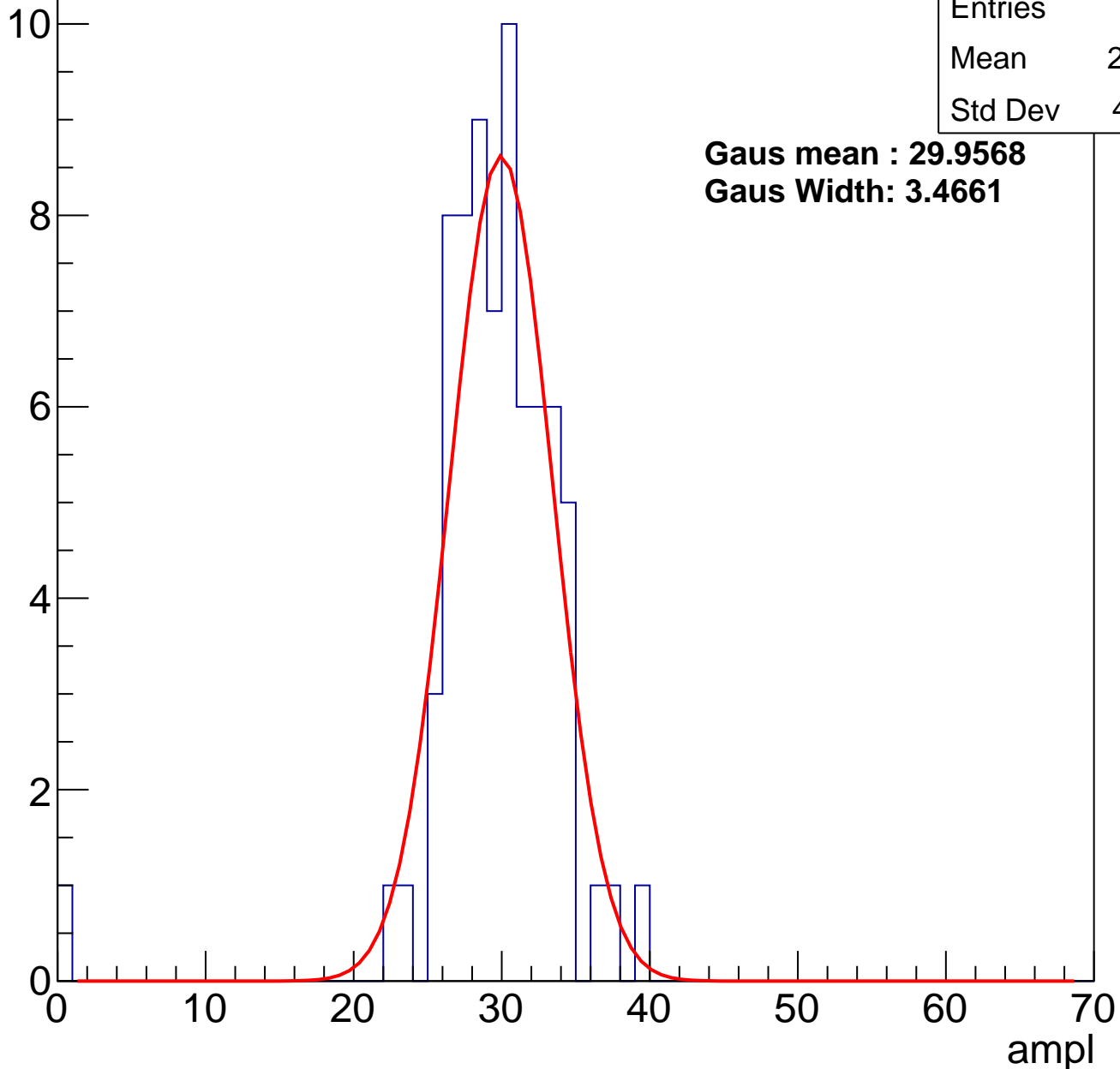
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 74 |
| Mean | 29.15 |
| Std Dev | 4.661 |

Gaus mean : 29.9568

Gaus Width: 3.4661

Entry



B1L102S, U15-ch3, adc1

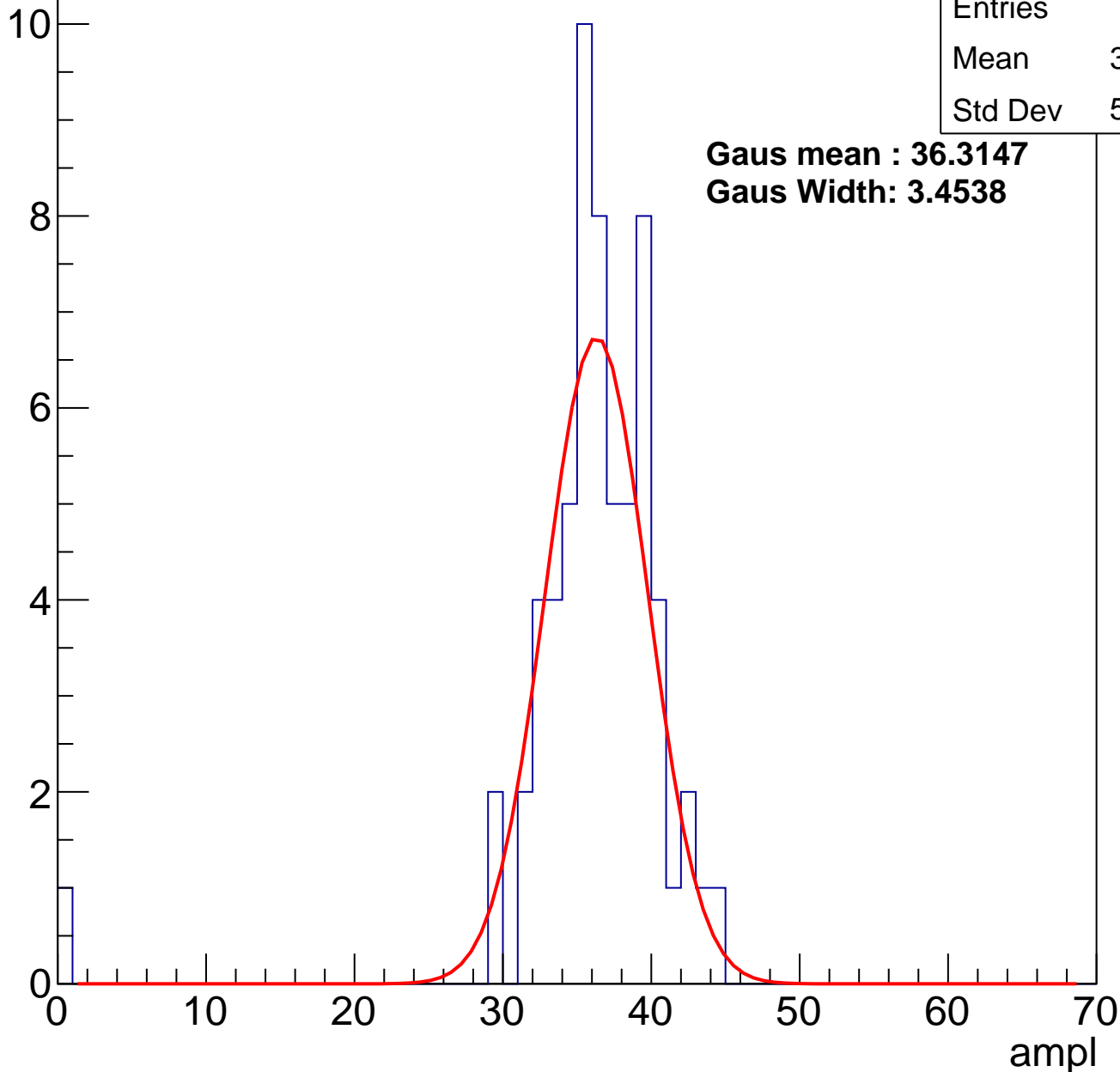
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.67 |
| Std Dev | 5.546 |

Gaus mean : 36.3147

Gaus Width: 3.4538

Entry



B1L102S, U15-ch3, adc2

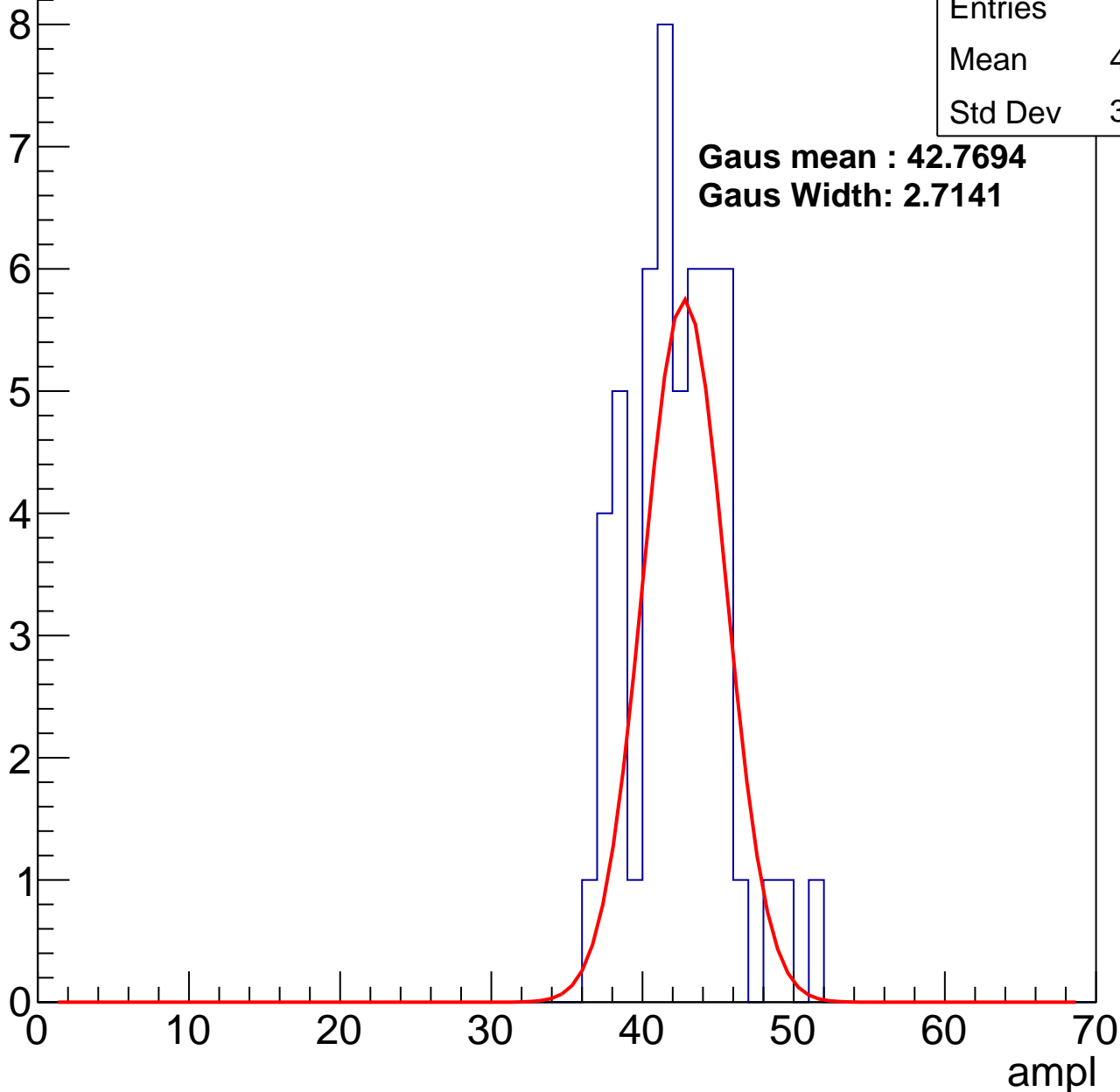
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 41.87 |
| Std Dev | 3.169 |

Gaus mean : 42.7694

Gaus Width: 2.7141



B1L102S, U15-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

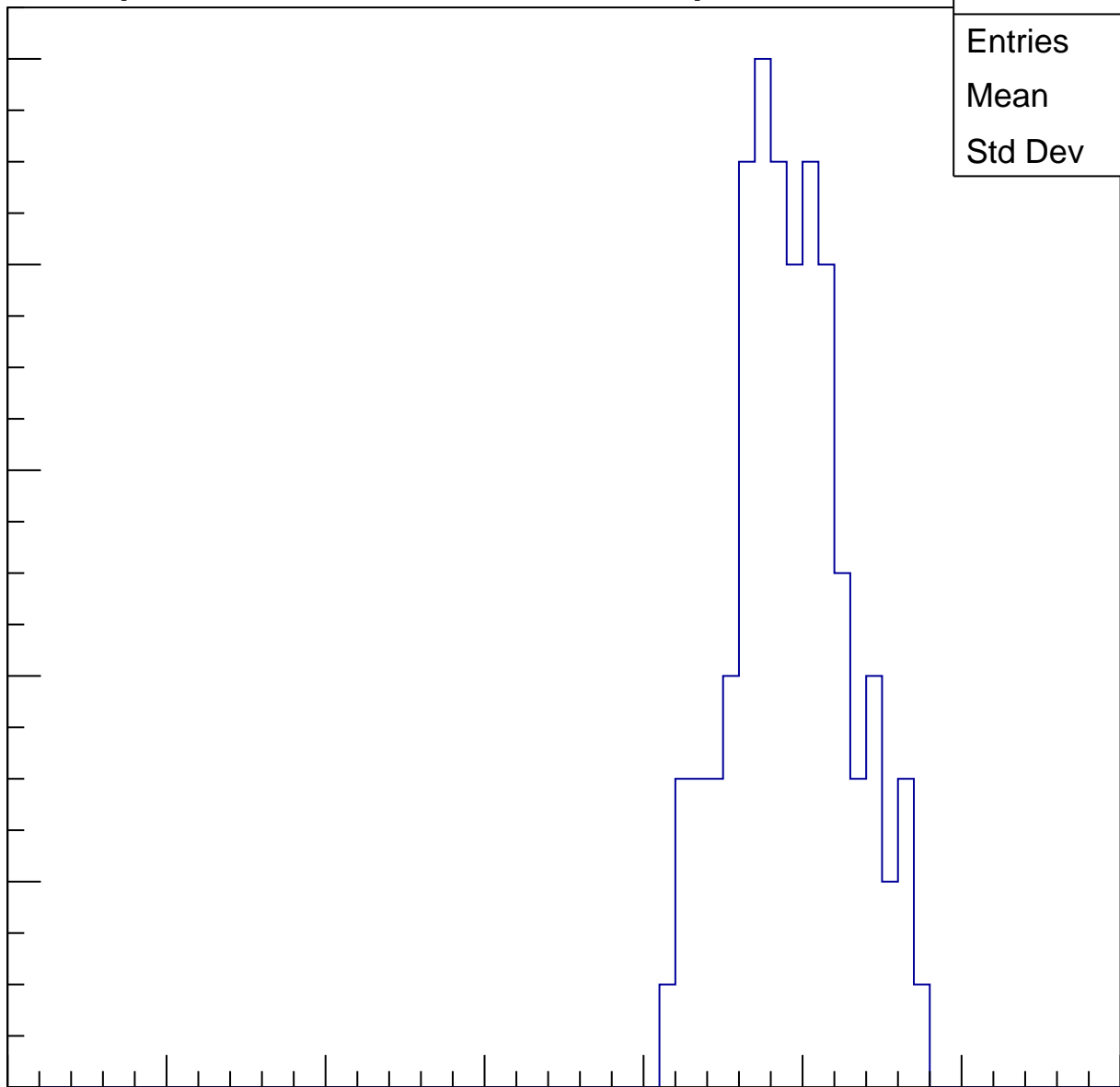
| | |
|---------|-------|
| Entries | 85 |
| Mean | 48.75 |
| Std Dev | 3.597 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

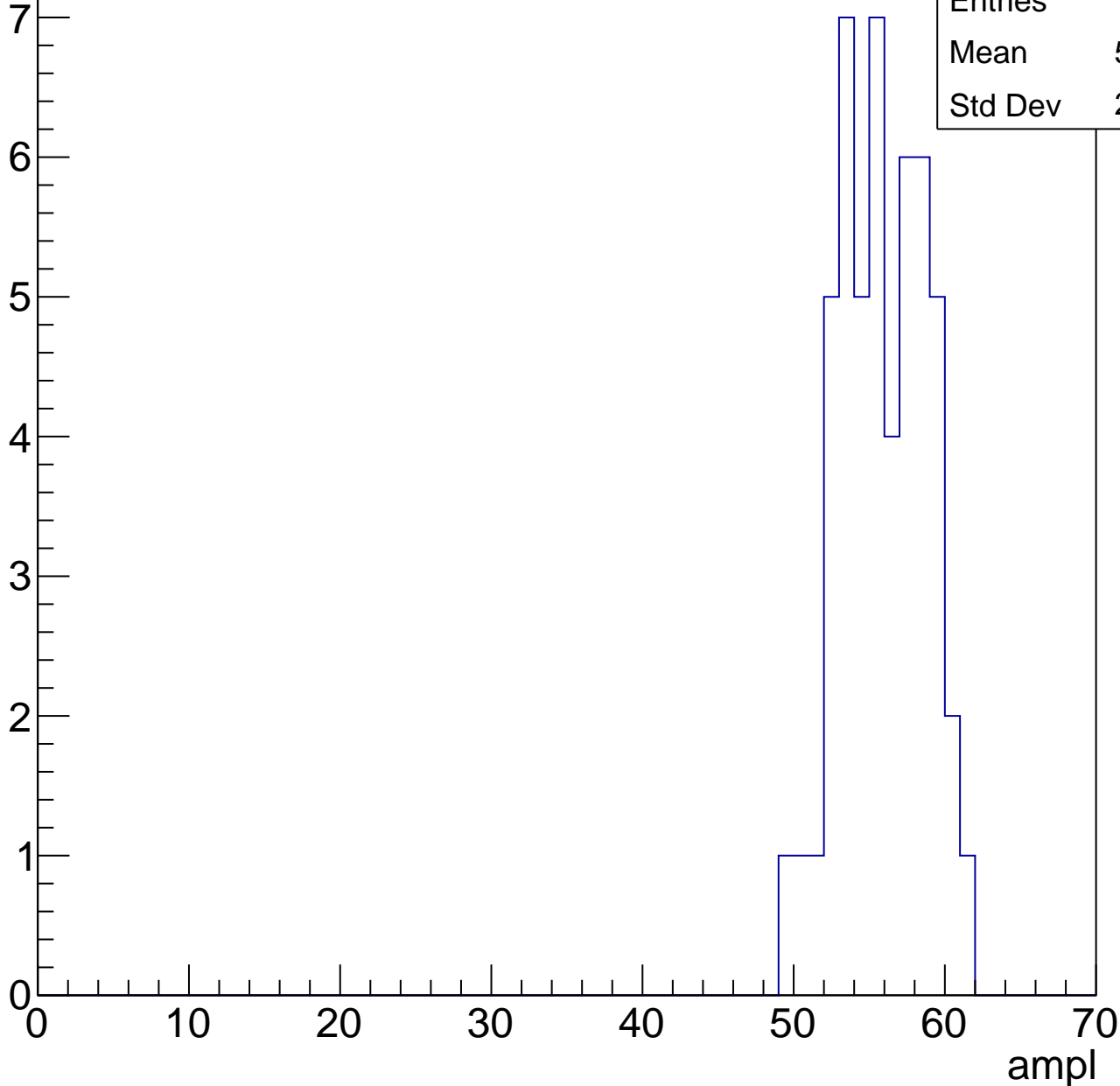


B1L102S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 55.41 |
| Std Dev | 2.781 |

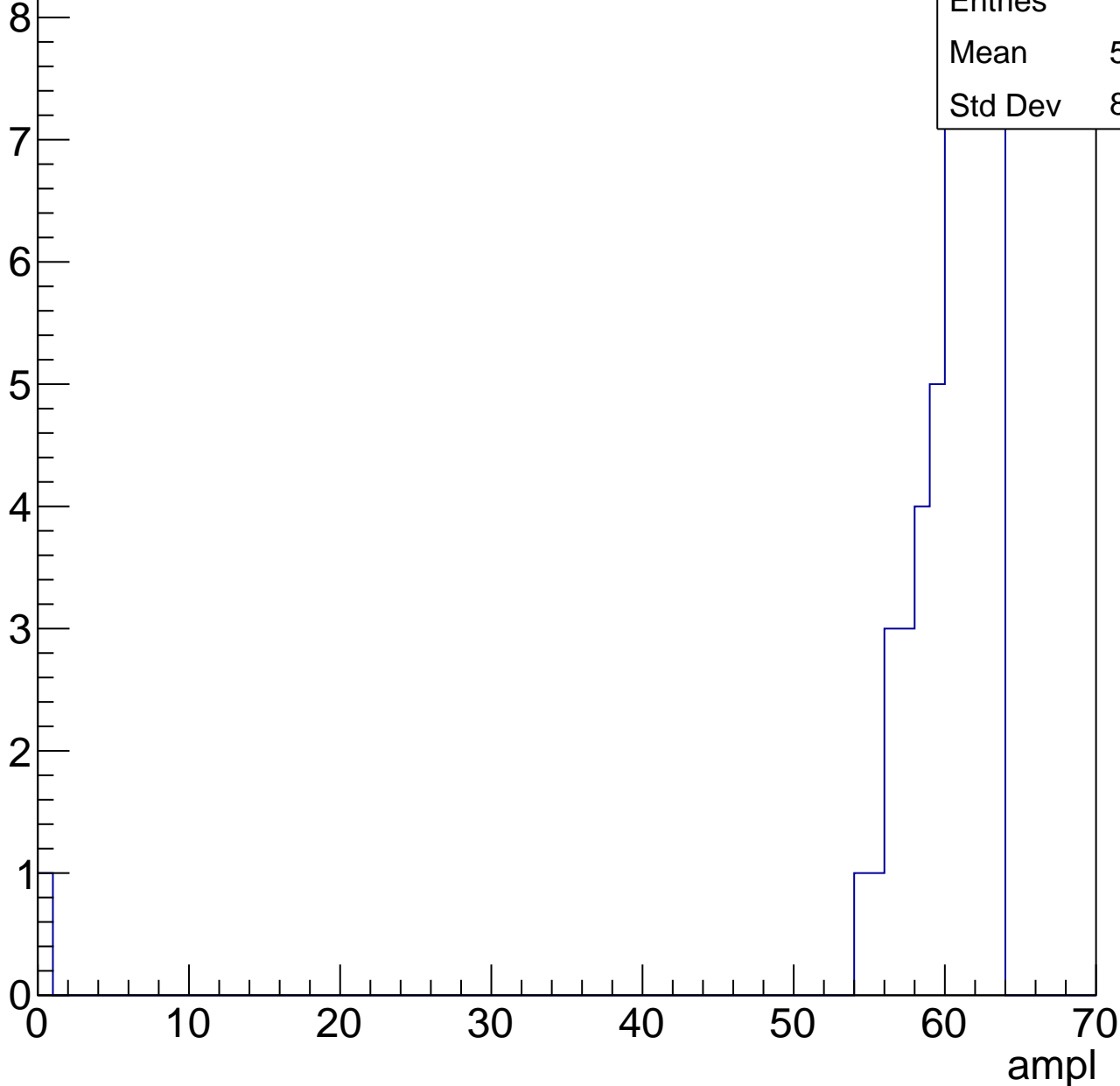


B1L102S, U15-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

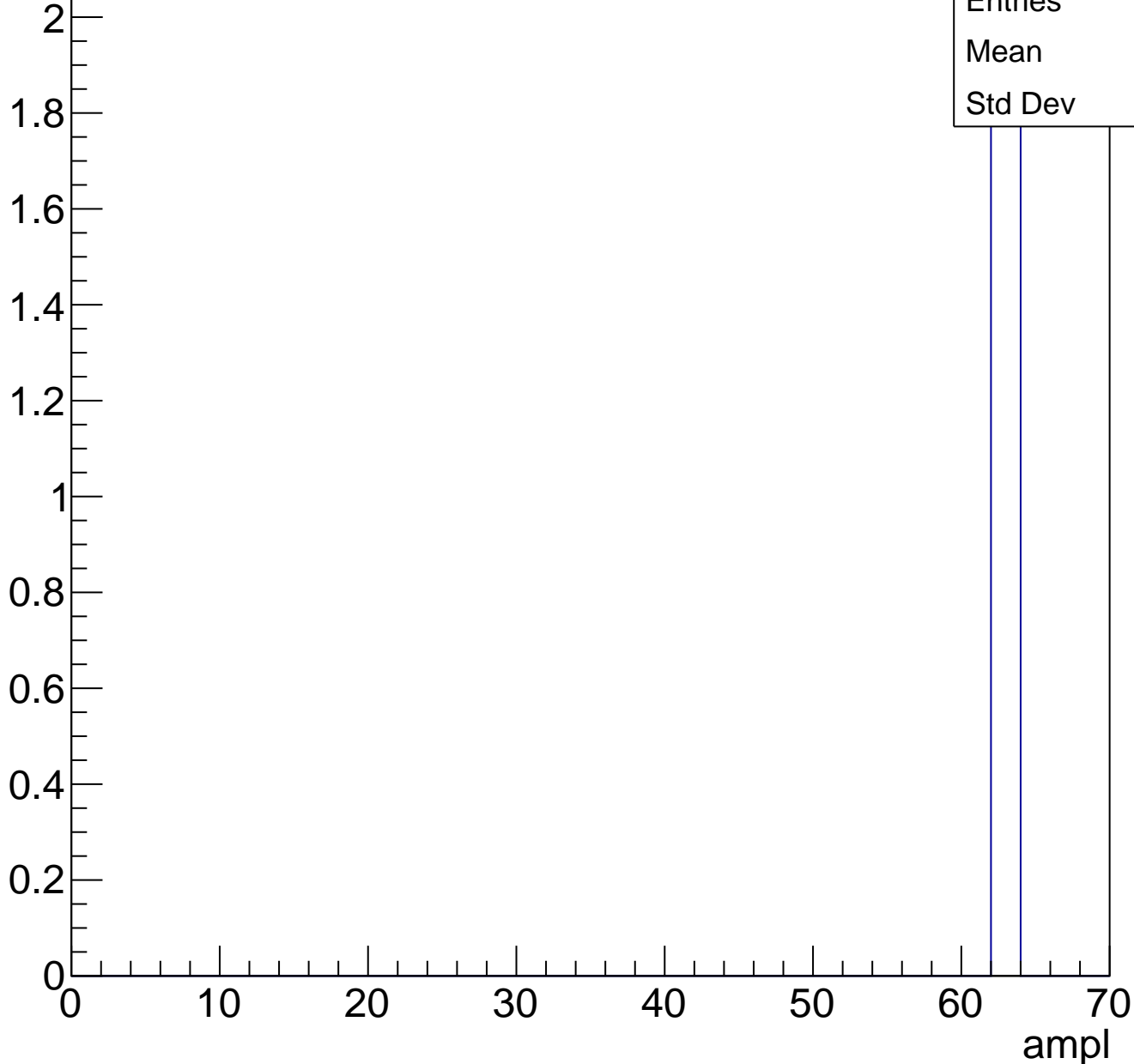
| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.86 |
| Std Dev | 8.722 |



B1L102S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

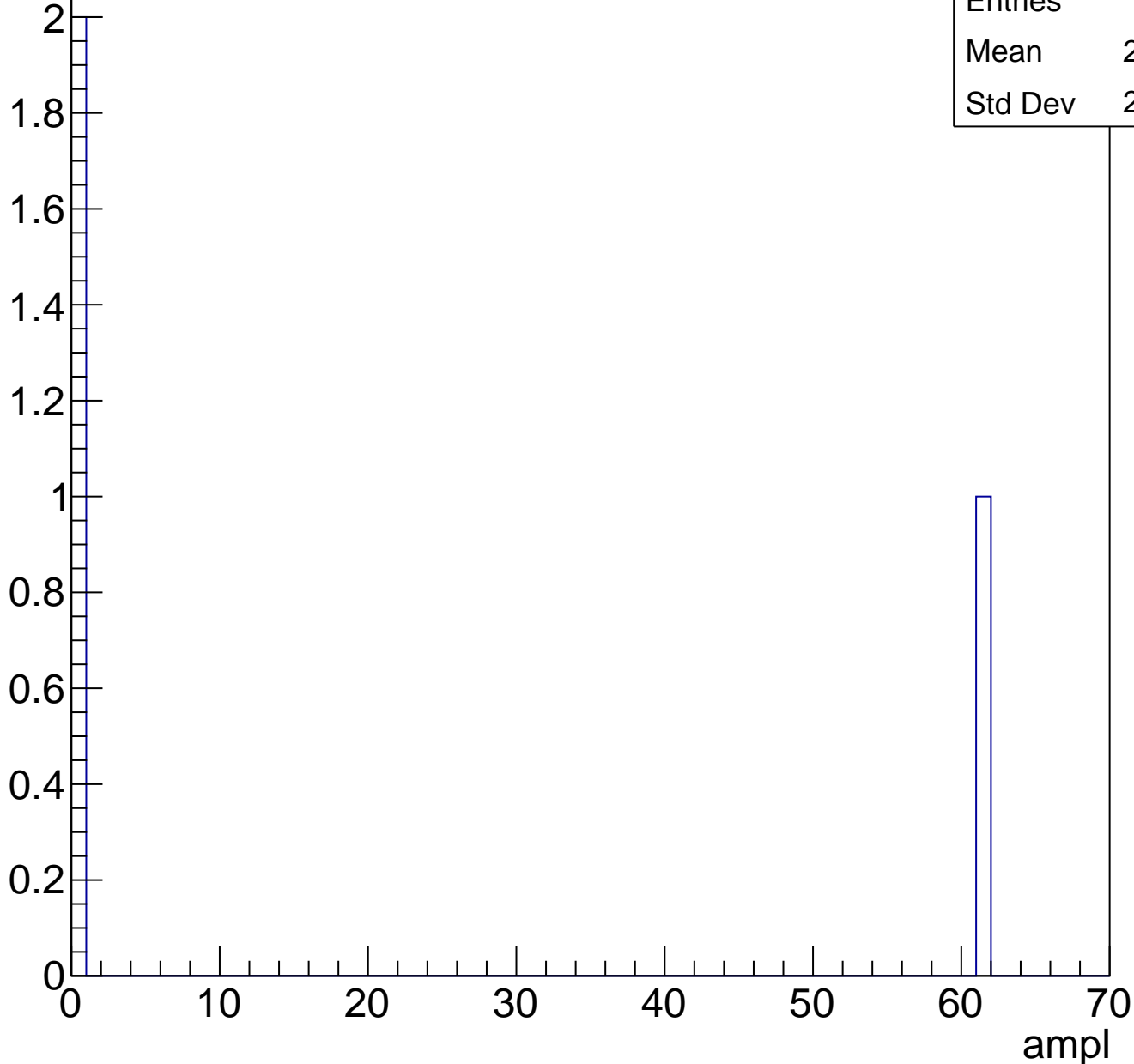
Entry



B1L102S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch4, adc0

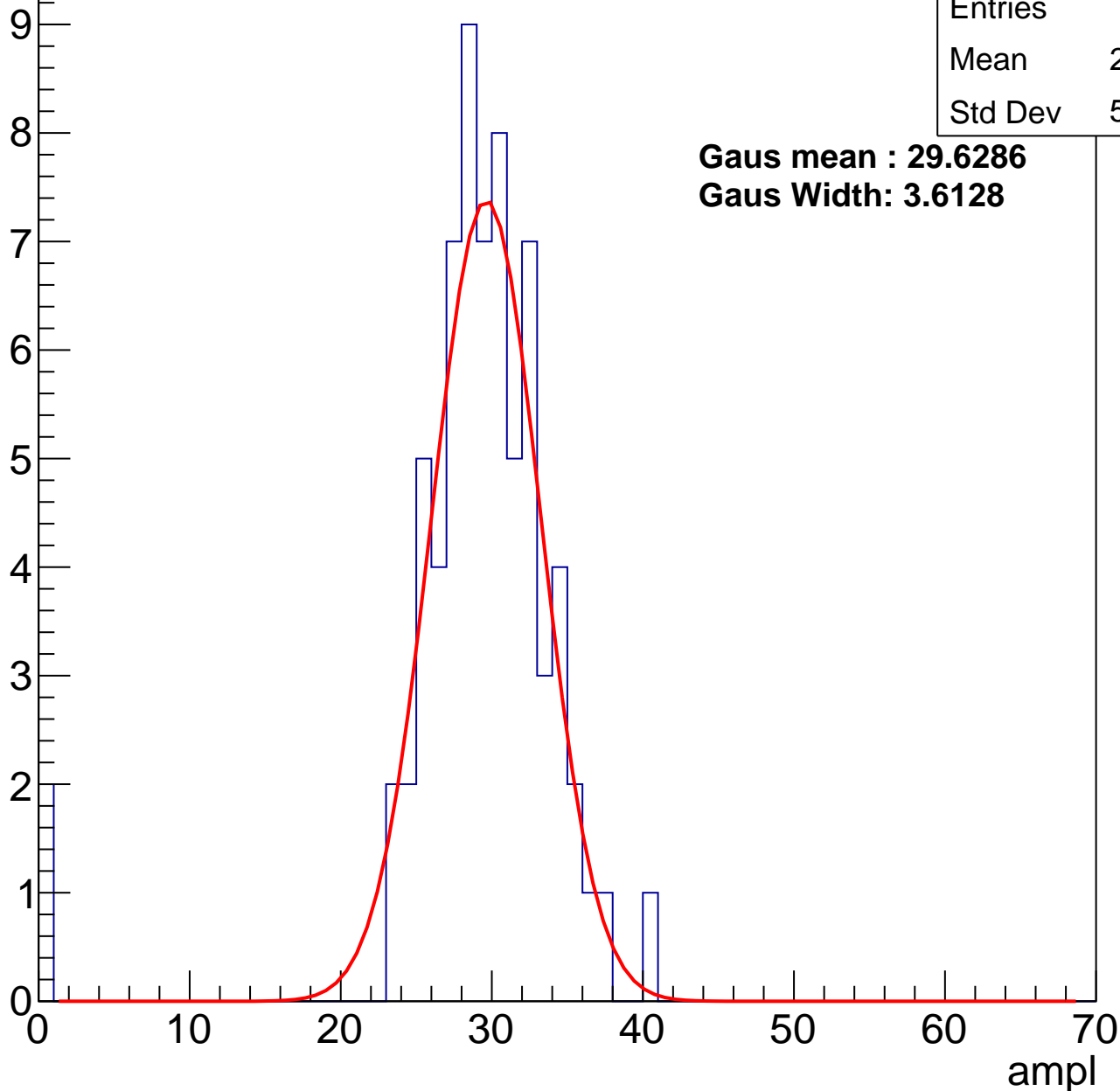
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 28.63 |
| Std Dev | 5.967 |

Gaus mean : 29.6286

Gaus Width: 3.6128



B1L102S, U15-ch4, adc1

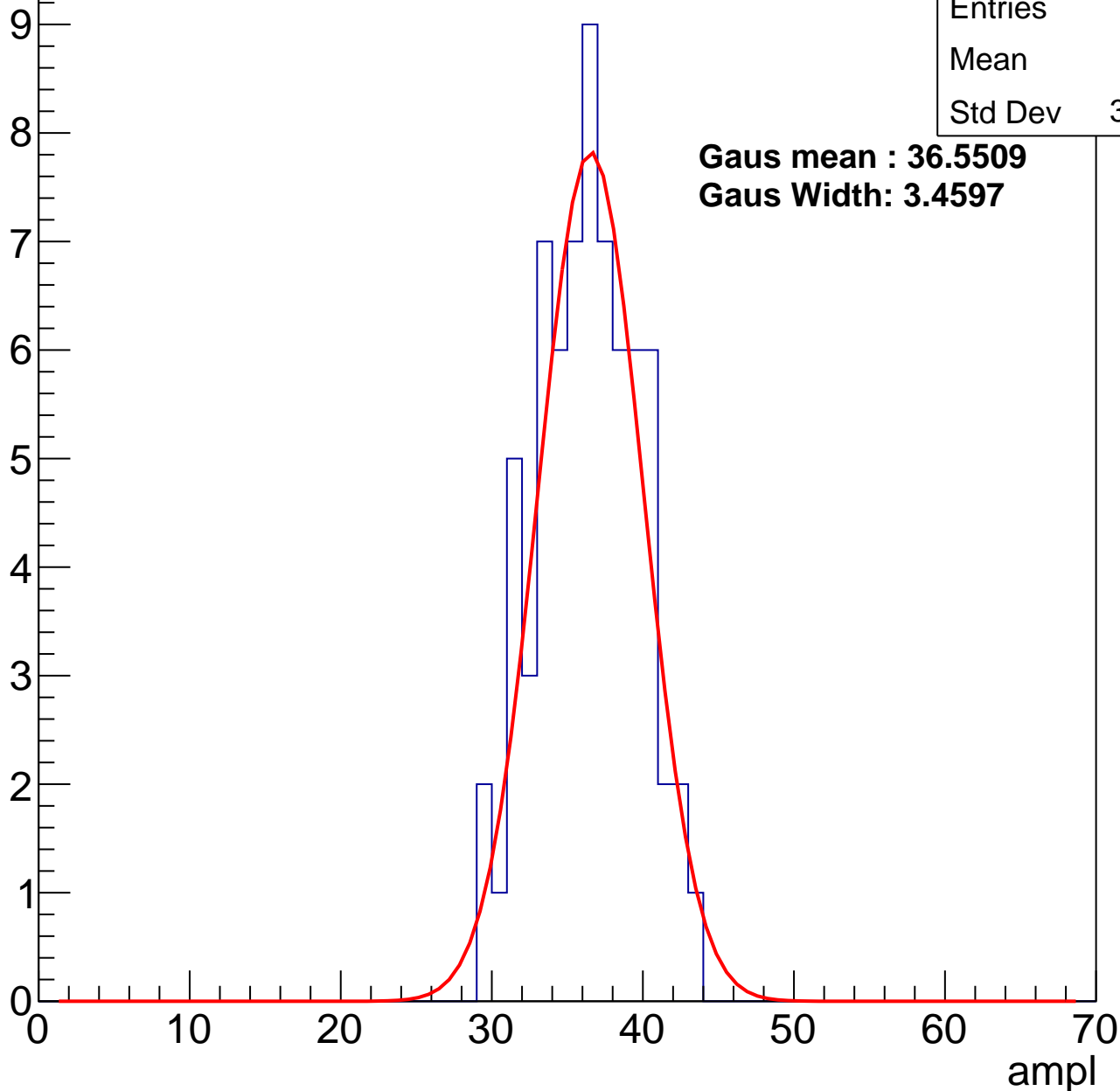
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.9 |
| Std Dev | 3.278 |

Gaus mean : 36.5509

Gaus Width: 3.4597



B1L102S, U15-ch4, adc2

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 43.13 |
| Std Dev | 3.363 |

Gaus mean : 43.9240

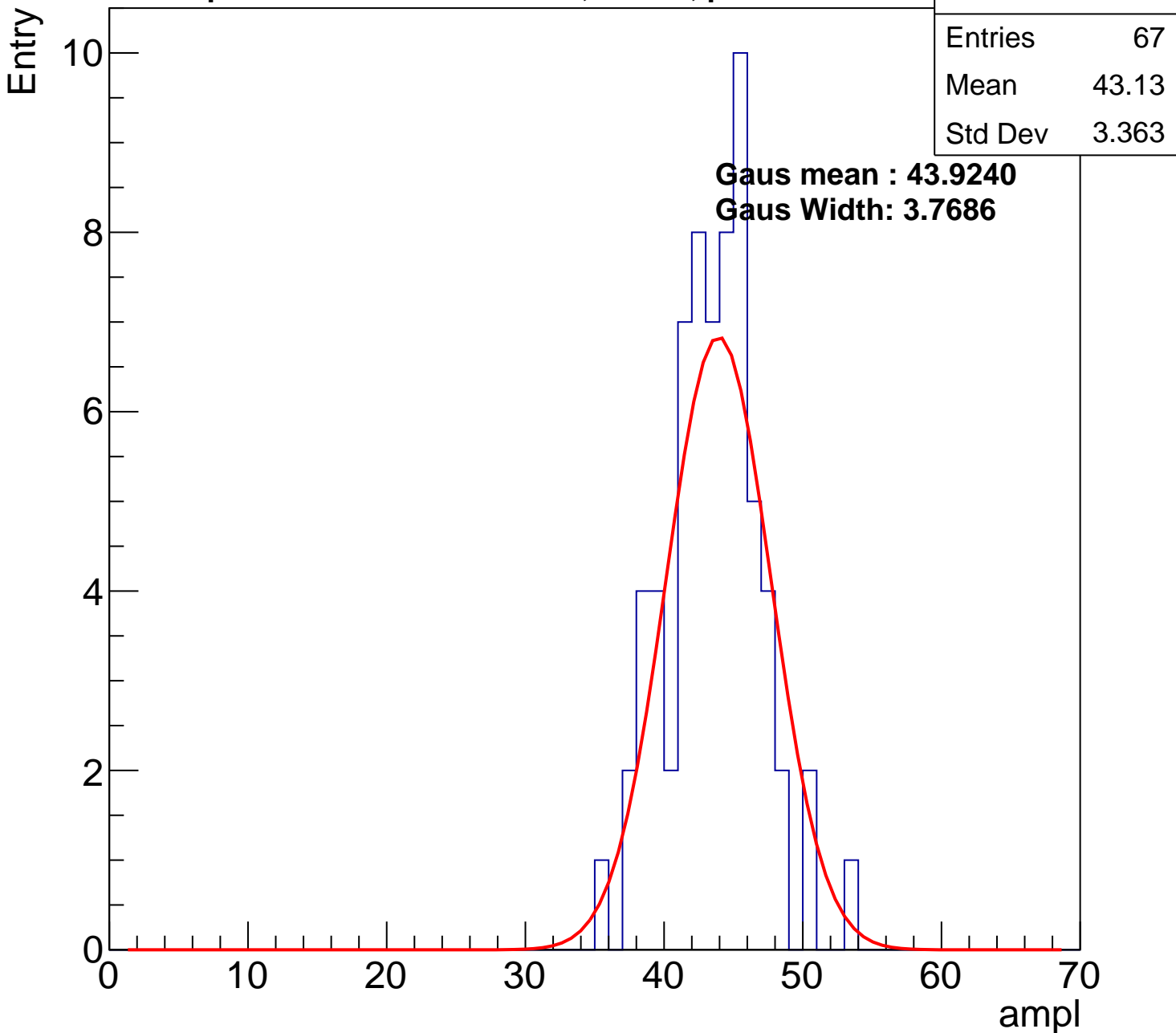
Gaus Width: 3.7686

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

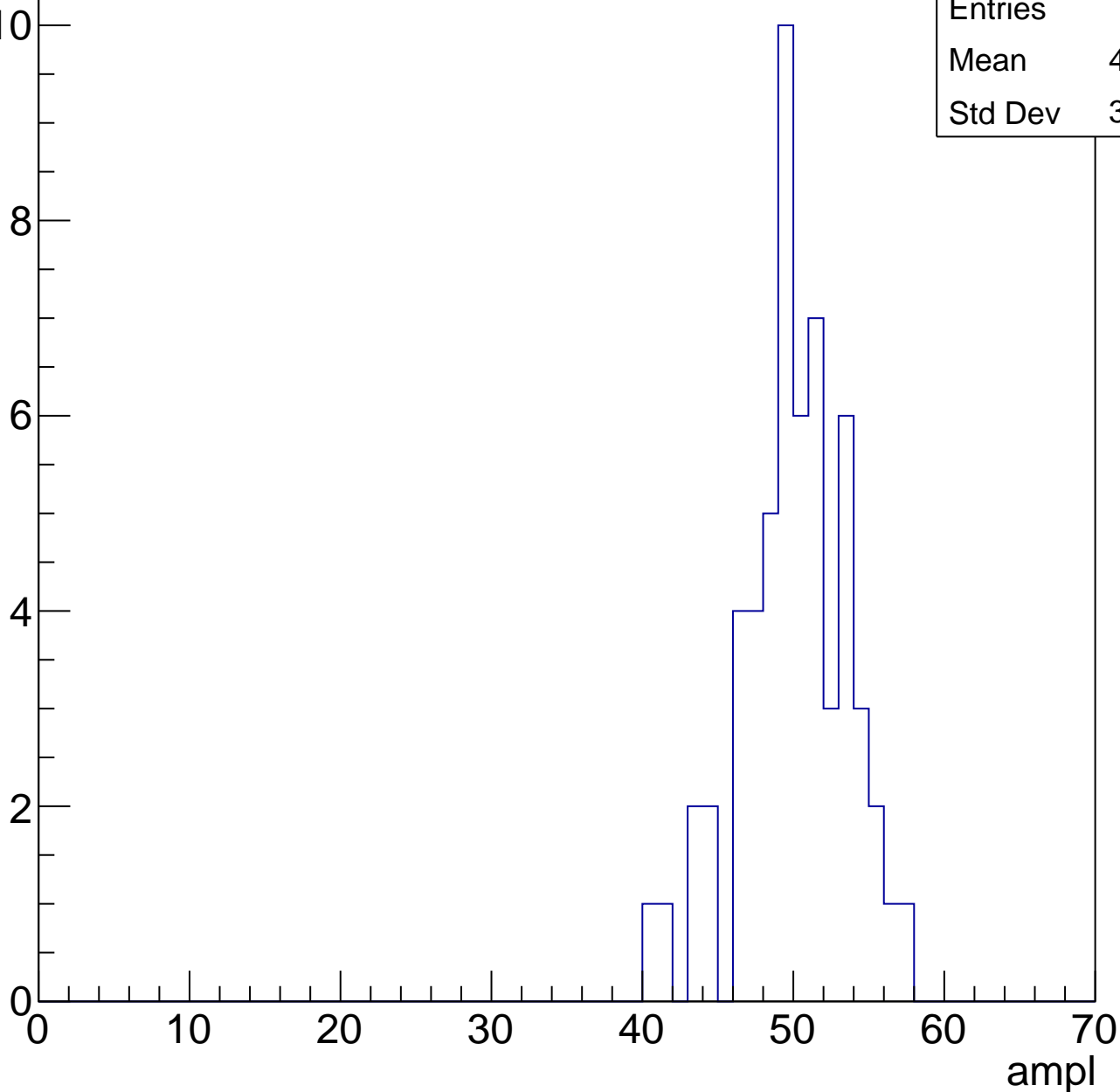


B1L102S, U15-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 49.53 |
| Std Dev | 3.534 |

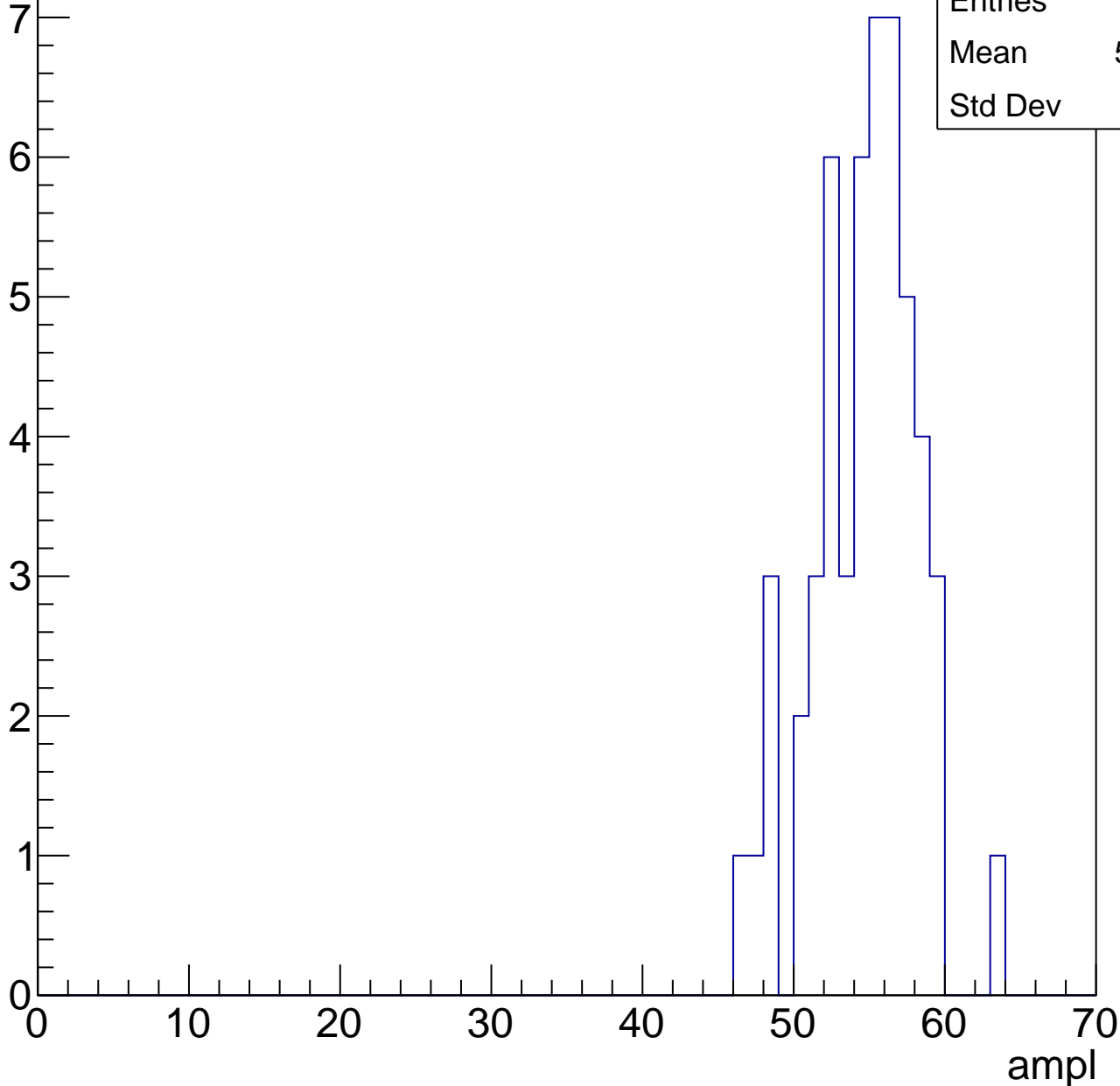


B1L102S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 54.21 |
| Std Dev | 3.41 |

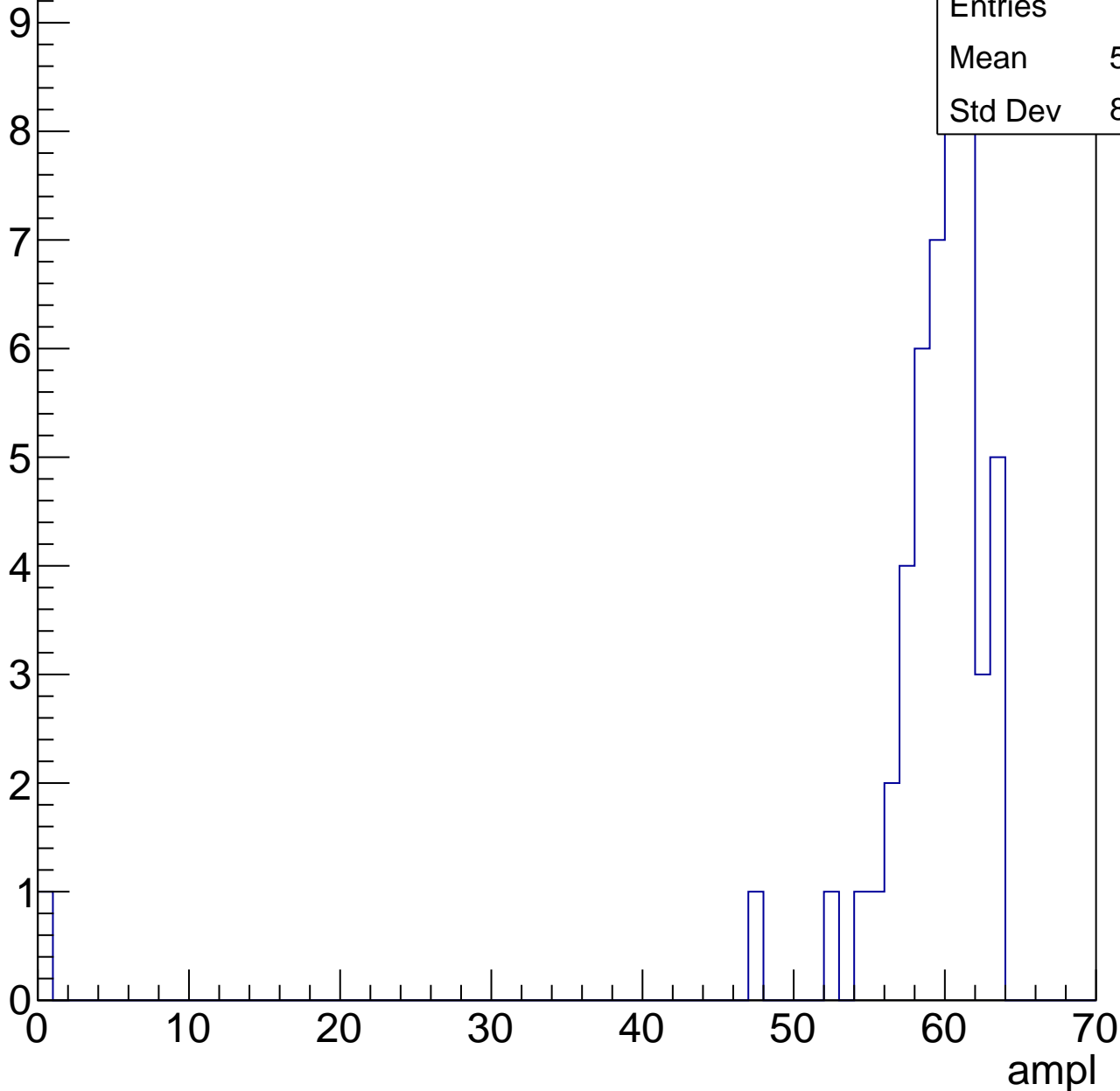


B1L102S, U15-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.94 |
| Std Dev | 8.865 |

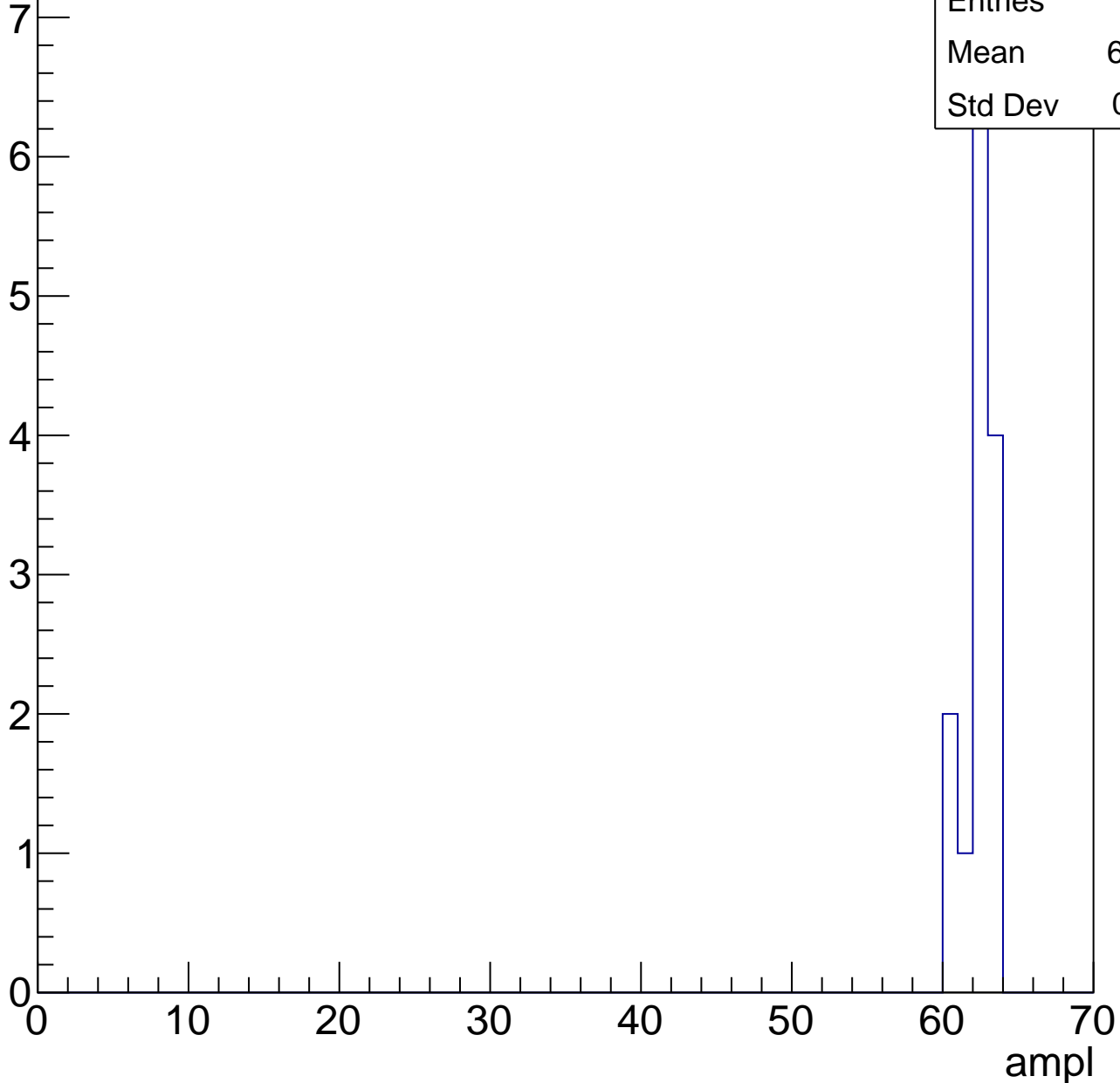


B1L102S, U15-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.93 |
| Std Dev | 0.961 |



B1L102S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch5, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 28.32 |
| Std Dev | 3.74 |

Gaus mean : 28.7752

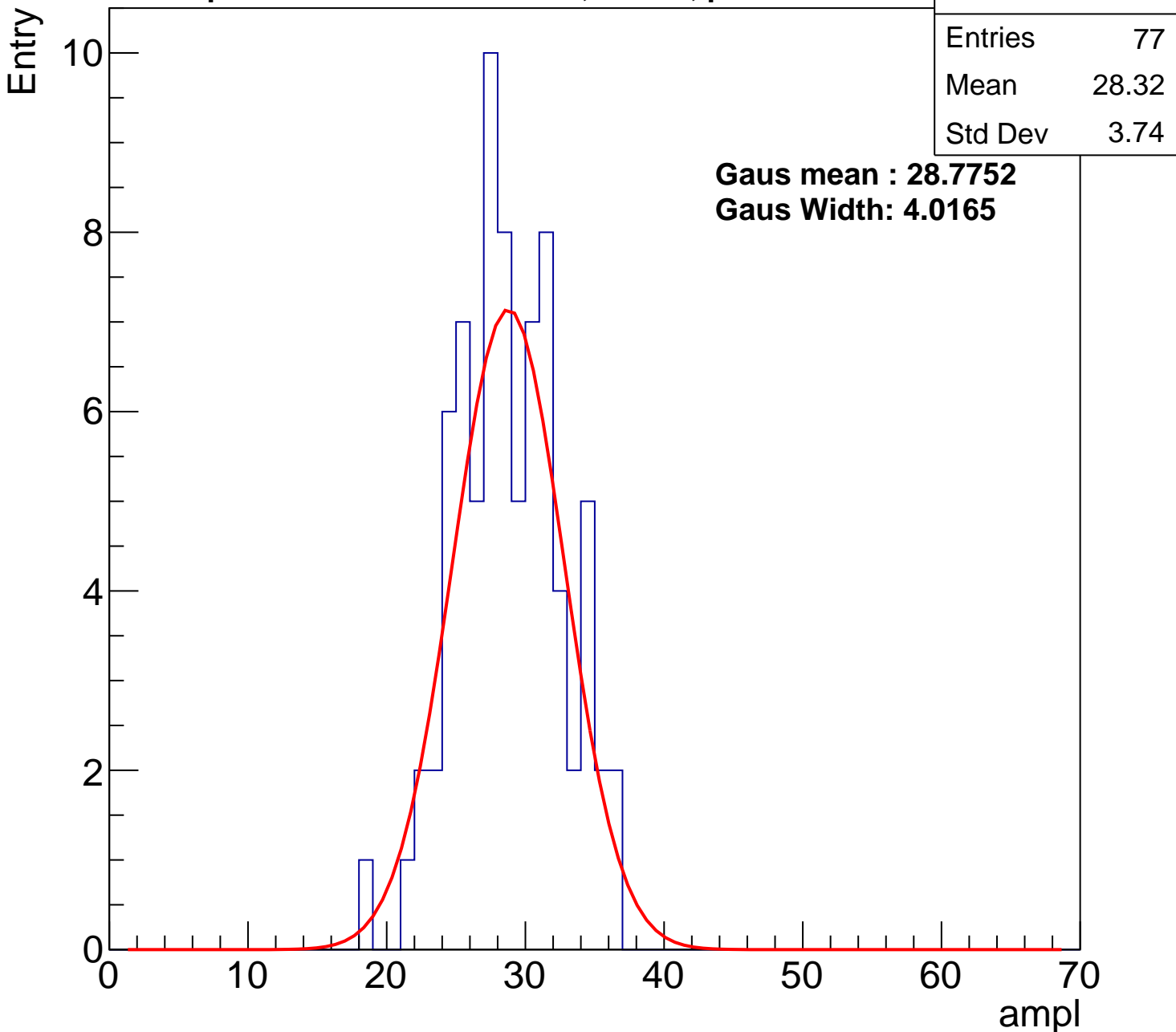
Gaus Width: 4.0165

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch5, adc1

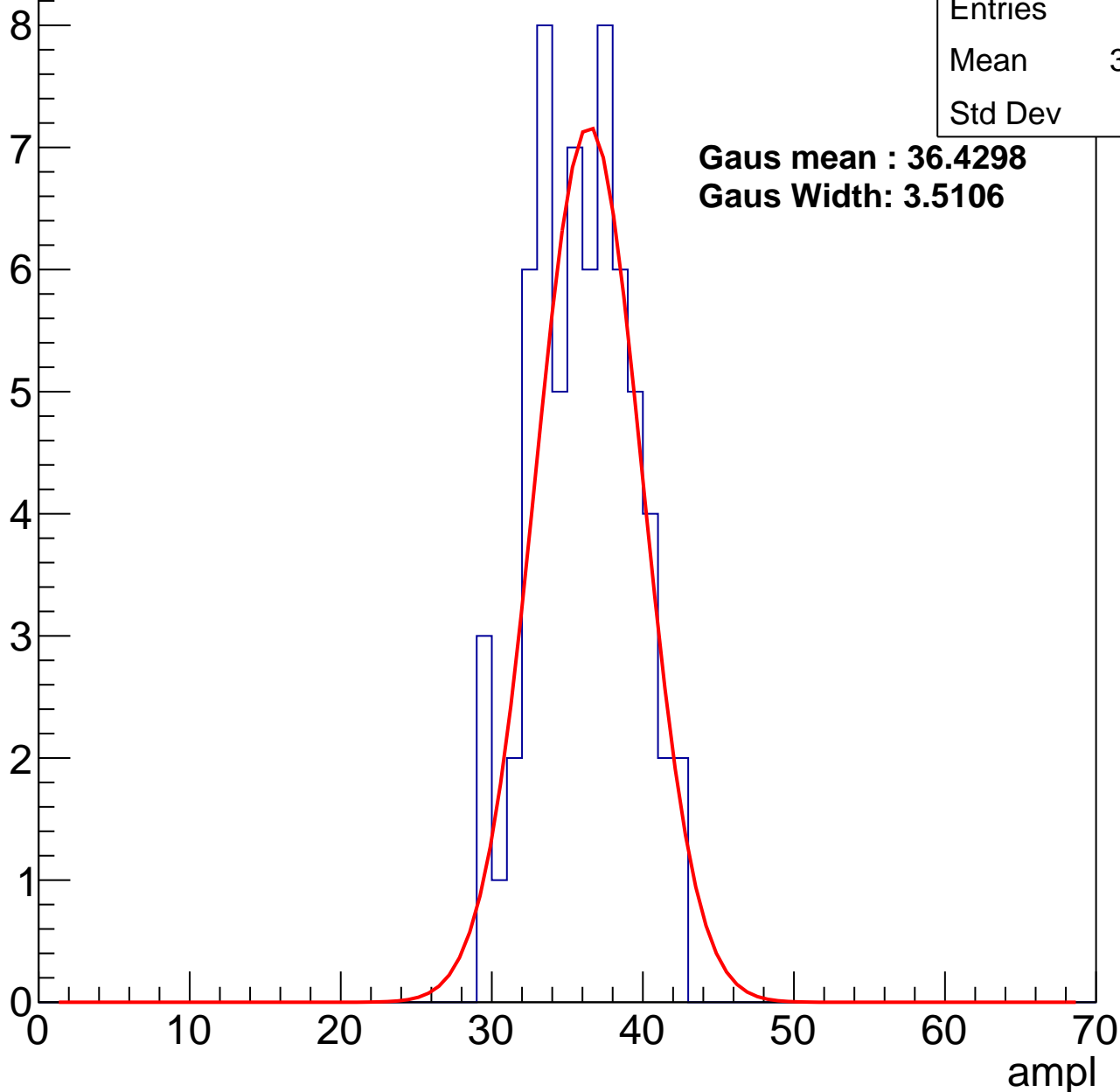
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.55 |
| Std Dev | 3.23 |

Gaus mean : 36.4298

Gaus Width: 3.5106



B1L102S, U15-ch5, adc2

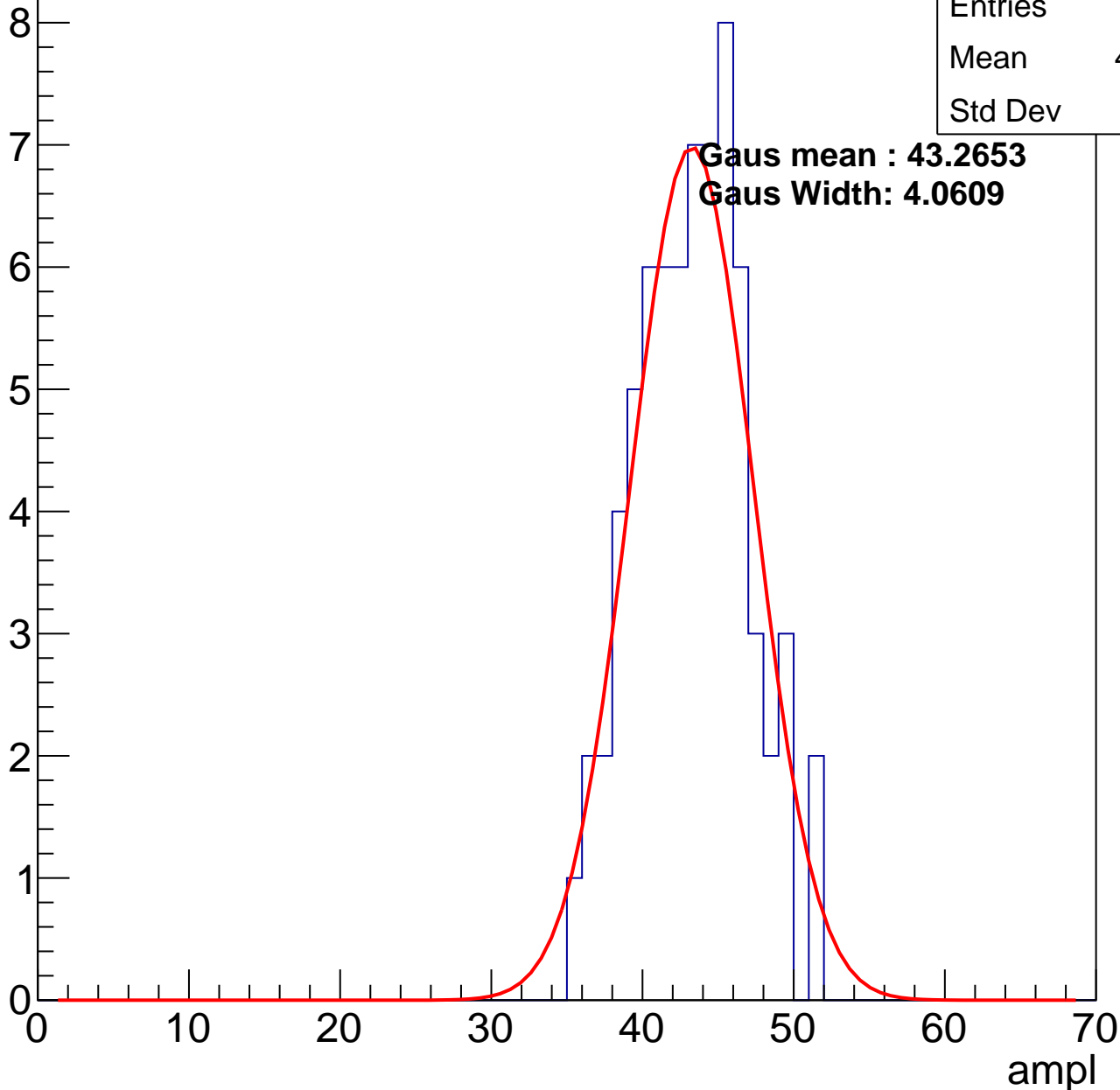
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.81 |
| Std Dev | 3.63 |

Gaus mean : 43.2653

Gaus Width: 4.0609

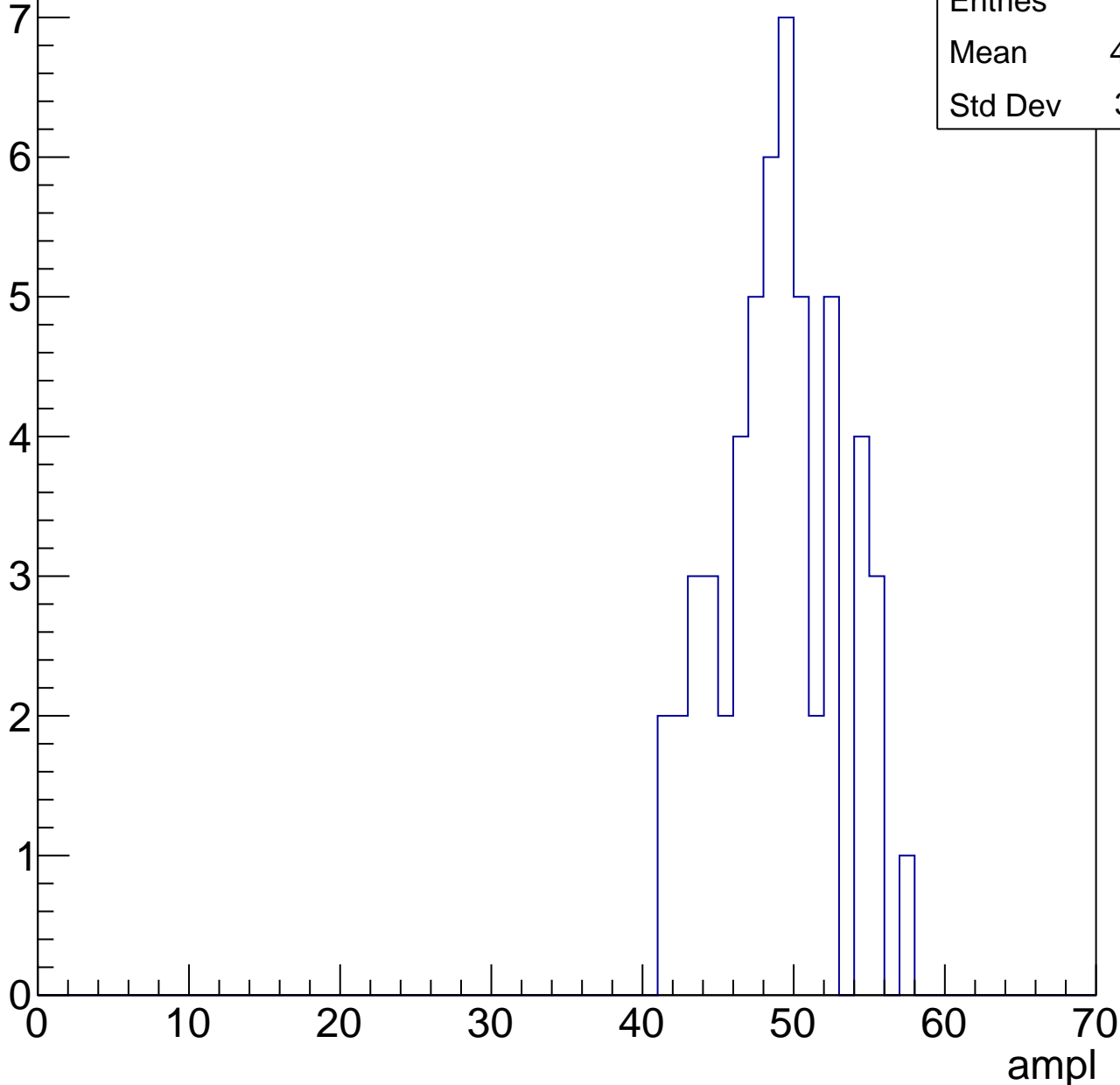


B1L102S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 48.46 |
| Std Dev | 3.891 |

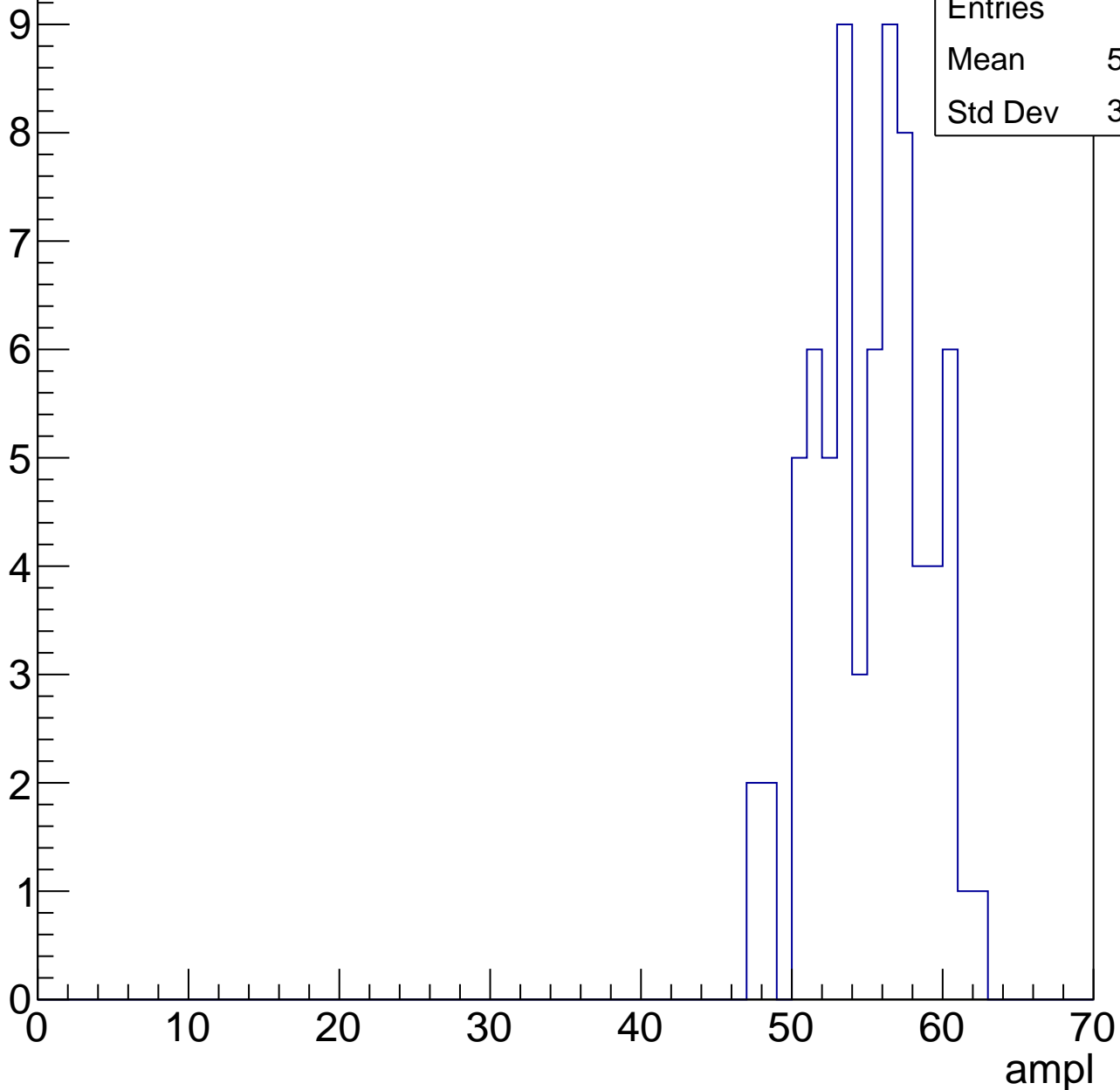


B1L102S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 54.73 |
| Std Dev | 3.564 |

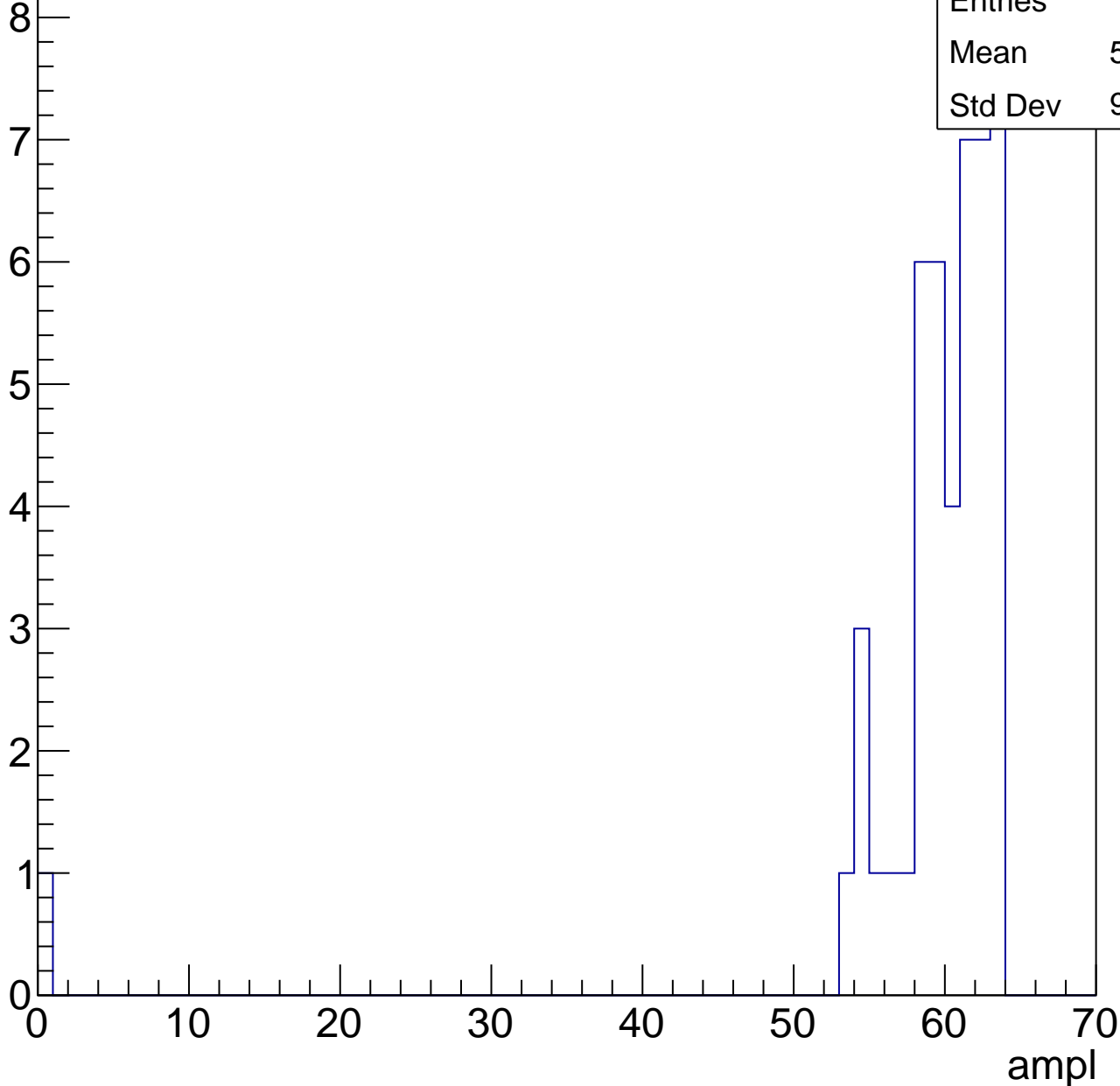


B1L102S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.48 |
| Std Dev | 9.134 |



B1L102S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|--------|
| Entries | 6 |
| Mean | 62 |
| Std Dev | 0.8165 |

0 10 20 30 40 50 60 70

ampl

B1L102S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch6, adc0

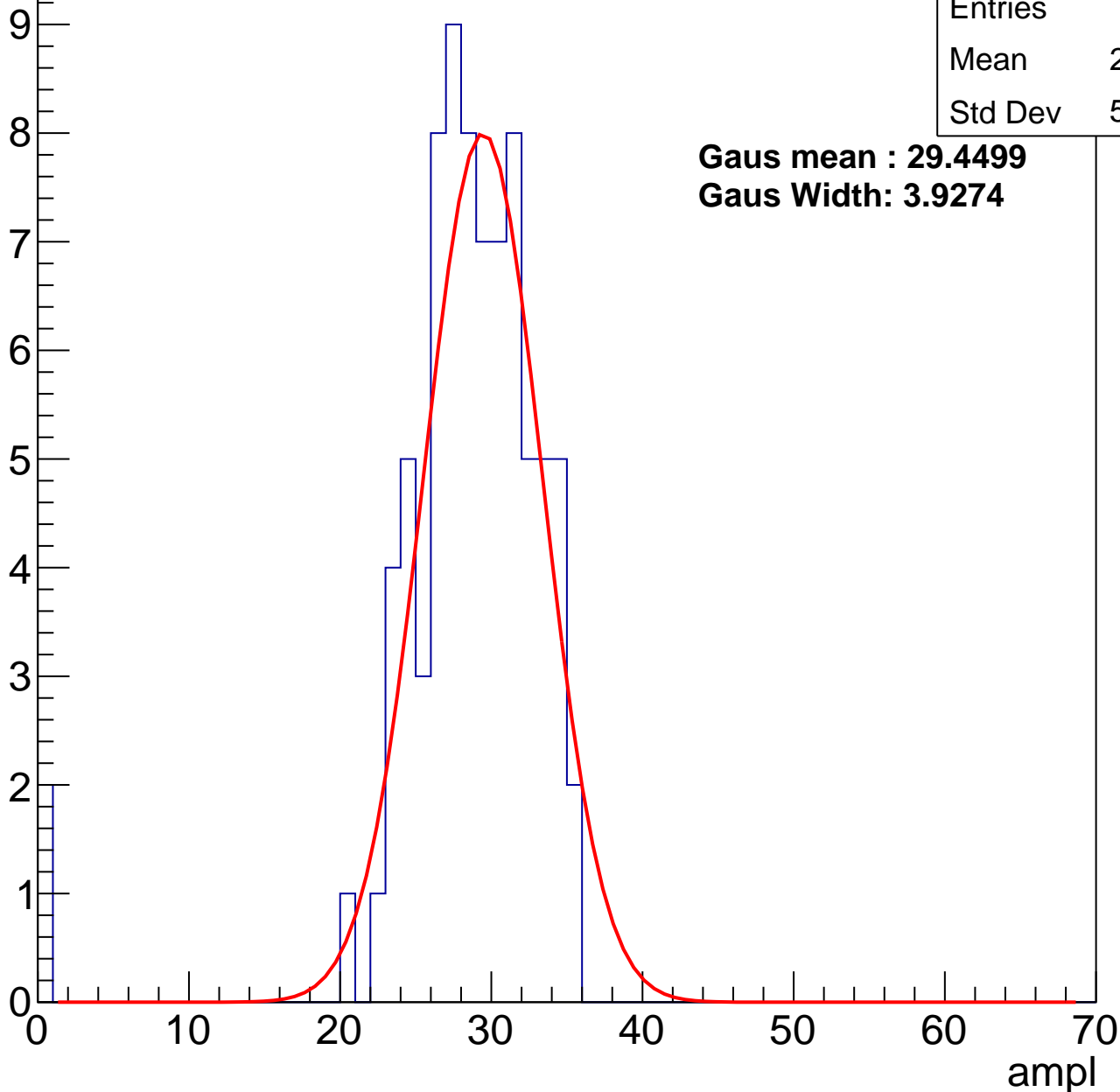
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 27.88 |
| Std Dev | 5.595 |

Gaus mean : 29.4499

Gaus Width: 3.9274



B1L102S, U15-ch6, adc1

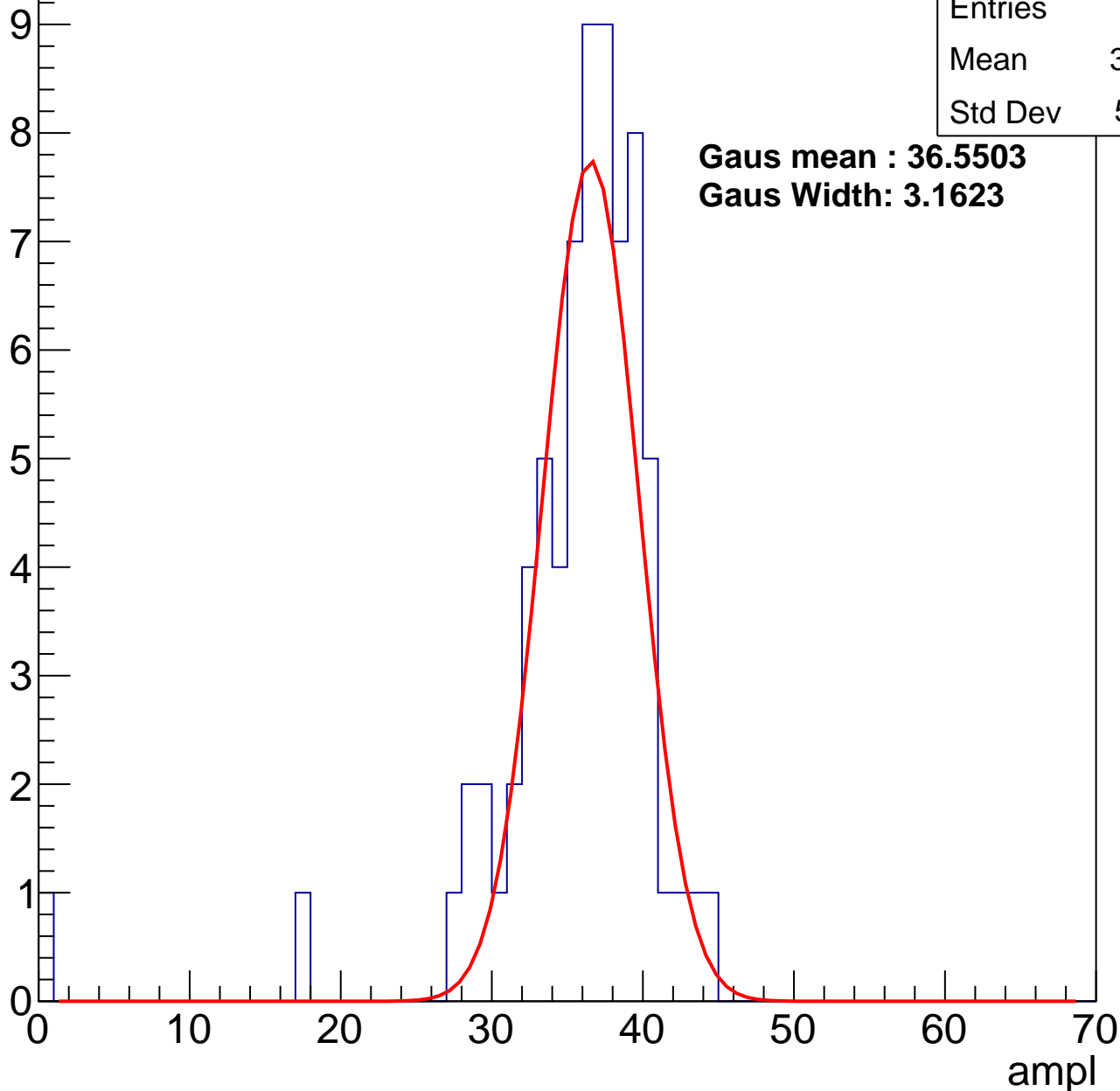
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.12 |
| Std Dev | 5.881 |

Gaus mean : 36.5503

Gaus Width: 3.1623



B1L102S, U15-ch6, adc2

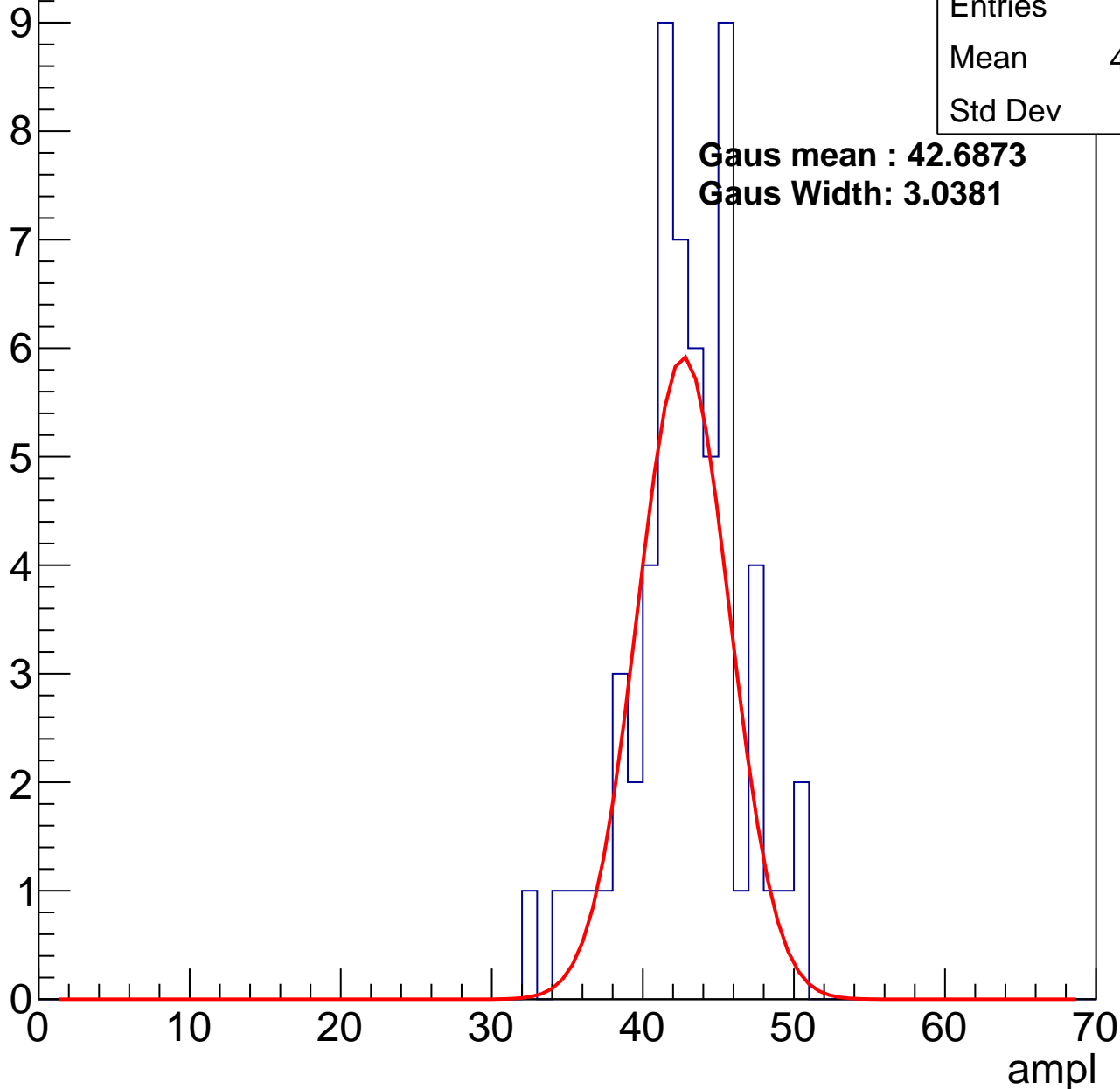
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.42 |
| Std Dev | 3.66 |

Gaus mean : 42.6873

Gaus Width: 3.0381

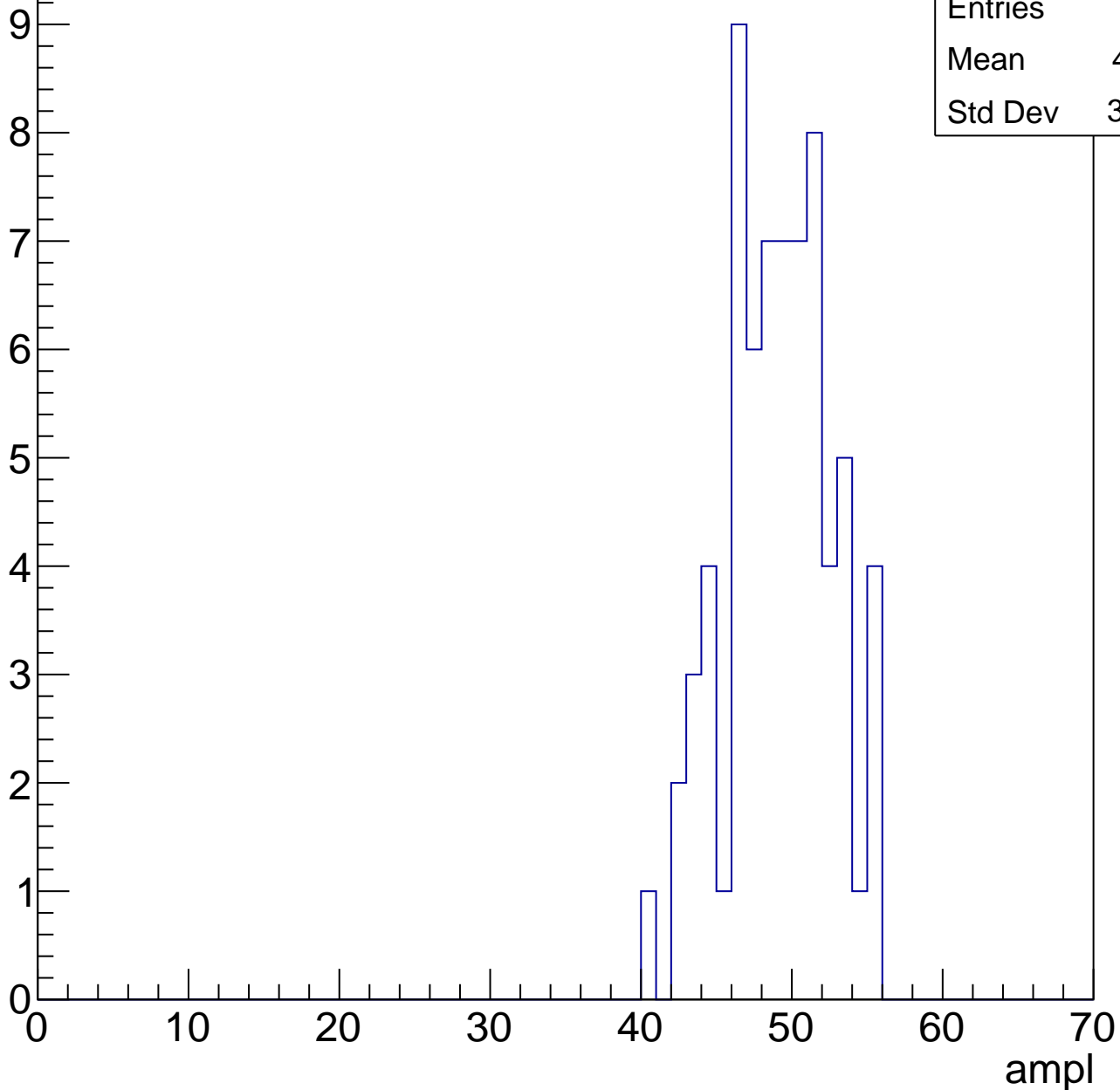


B1L102S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

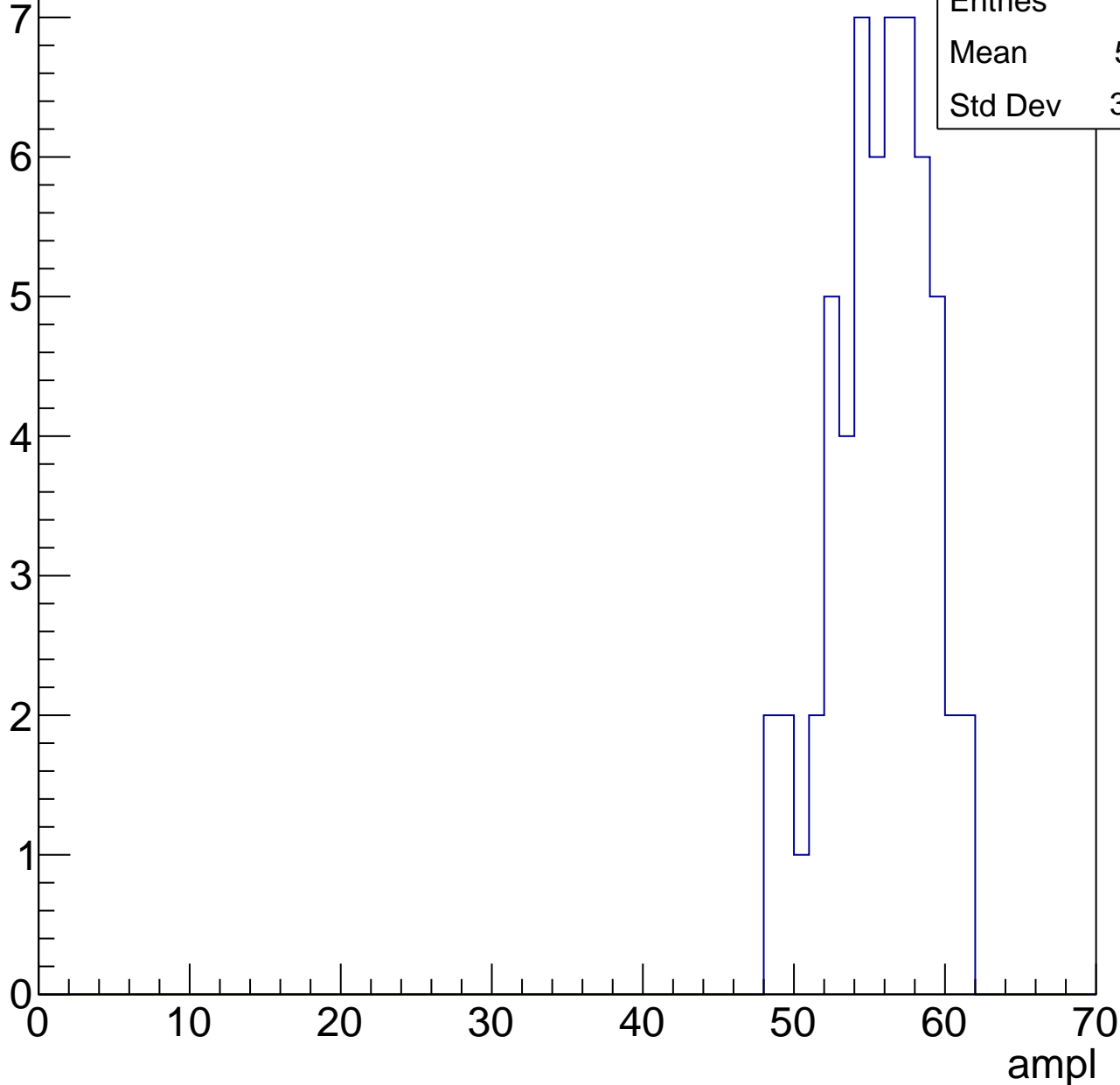
| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.61 |
| Std Dev | 3.465 |



B1L102S, U15-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



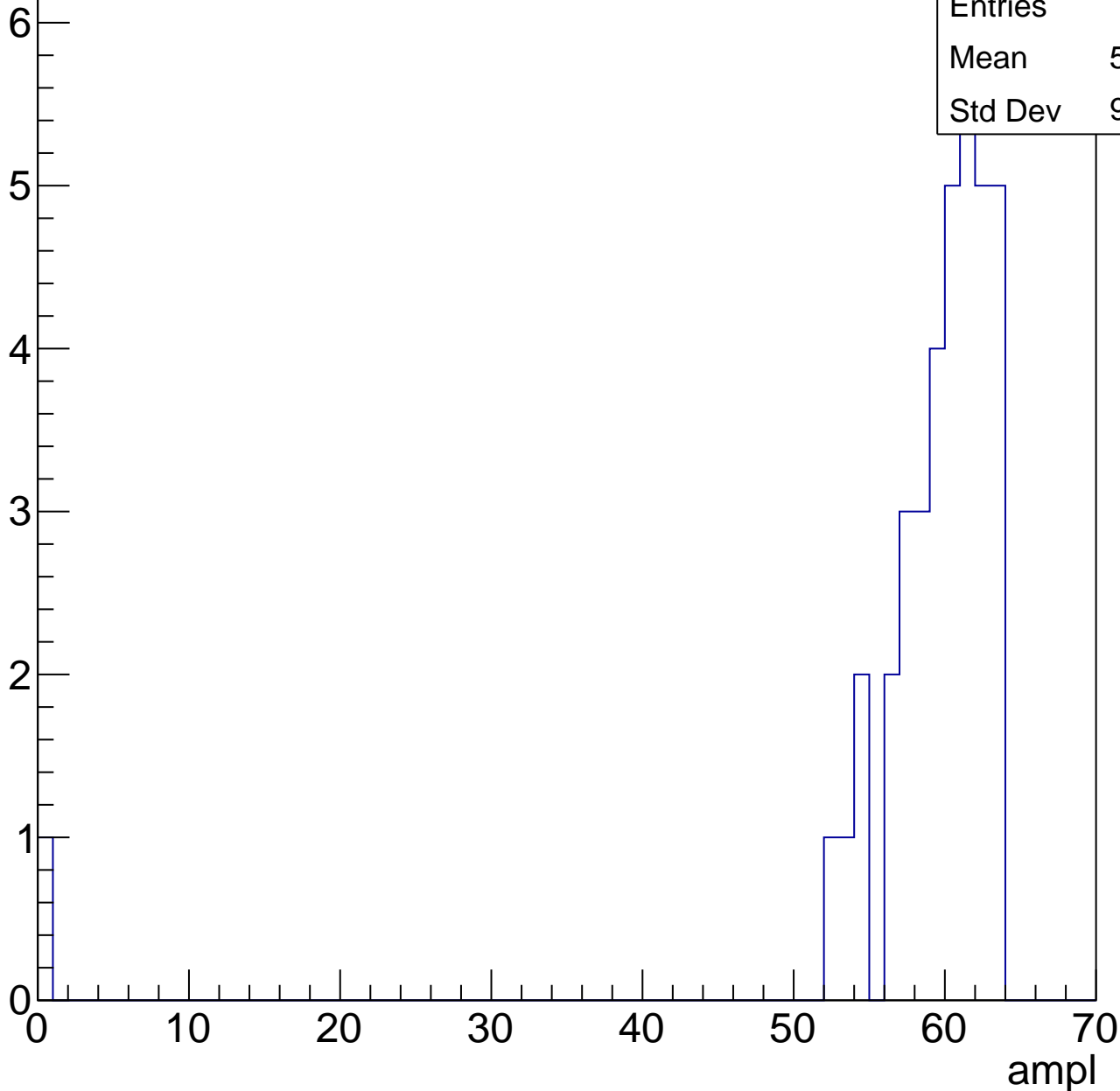
| | |
|---------|-------|
| Entries | 58 |
| Mean | 55.21 |
| Std Dev | 3.172 |

B1L102S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 57.82 |
| Std Dev | 9.934 |

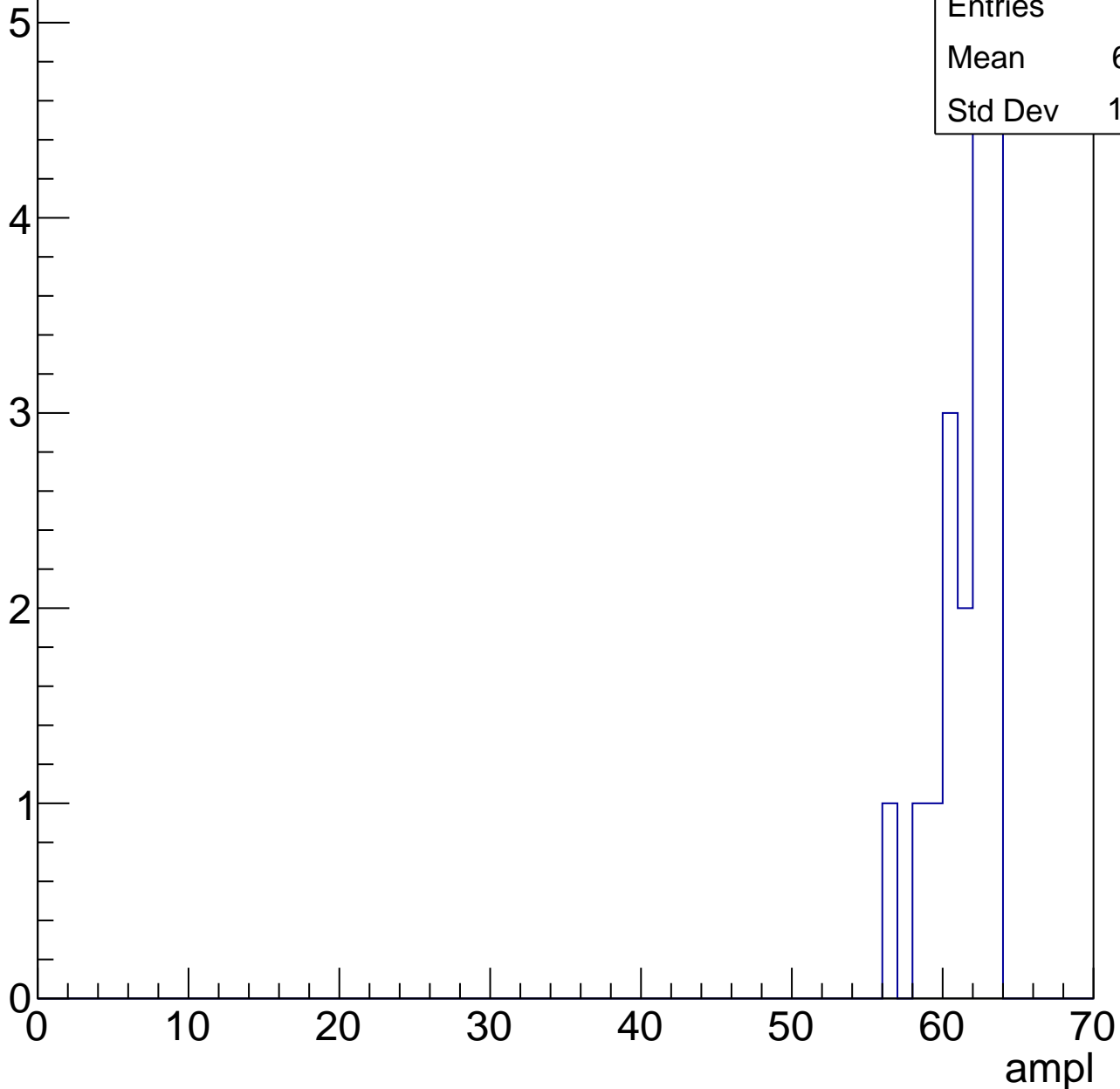


B1L102S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 61.11 |
| Std Dev | 1.912 |



B1L102S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch7, adc0

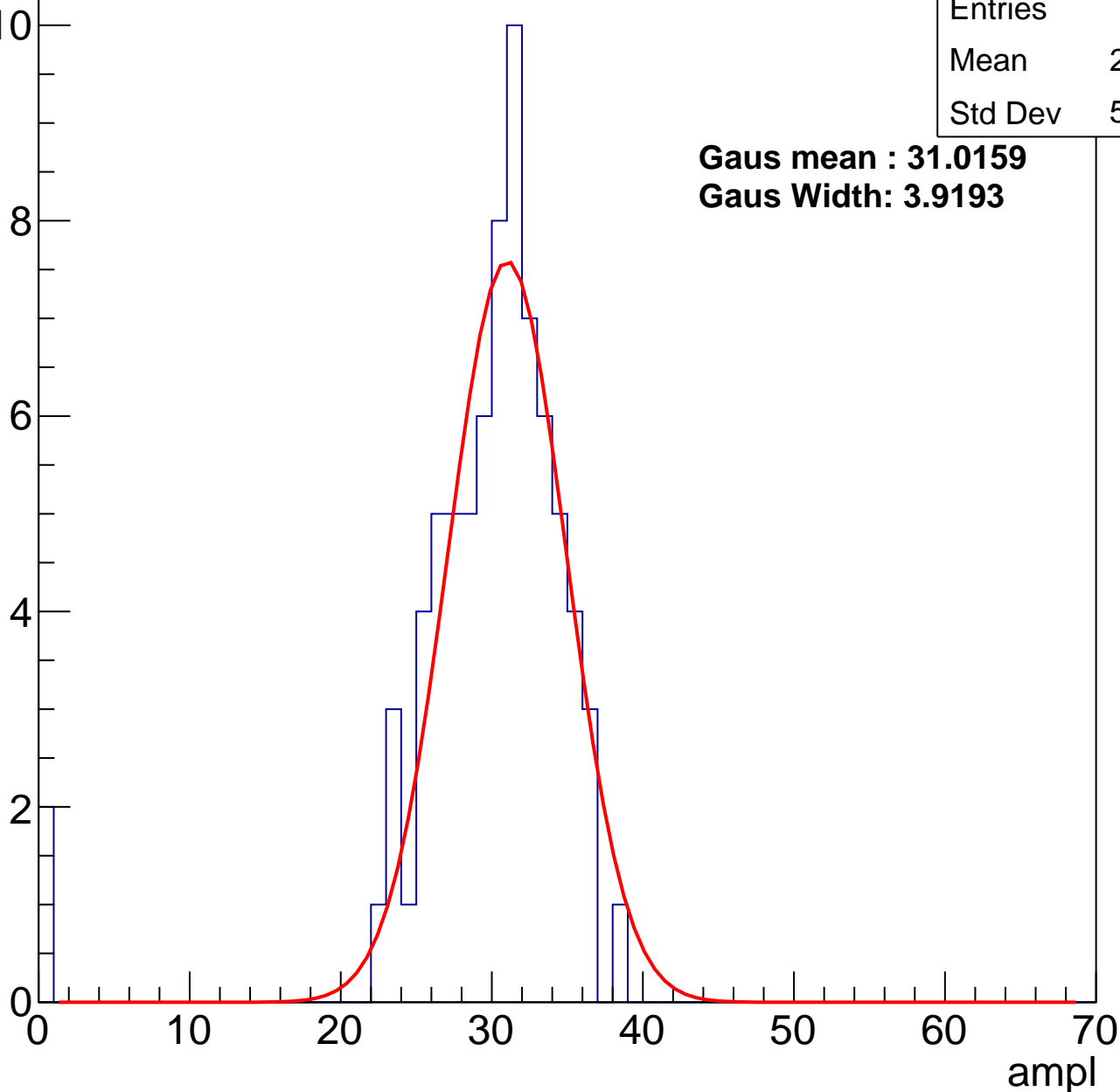
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.24 |
| Std Dev | 5.958 |

Gaus mean : 31.0159

Gaus Width: 3.9193



B1L102S, U15-ch7, adc1

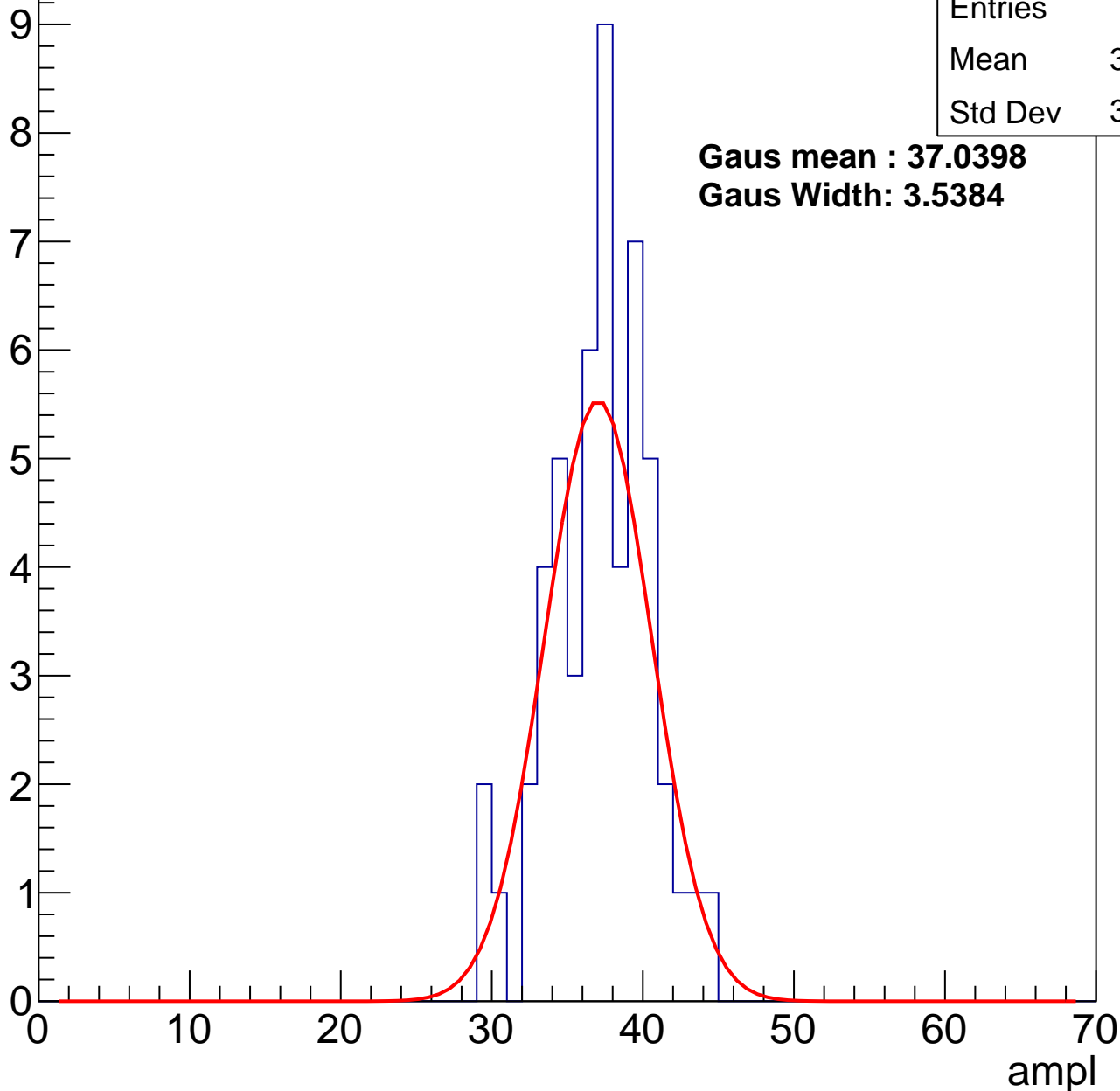
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 36.68 |
| Std Dev | 3.272 |

Gaus mean : 37.0398

Gaus Width: 3.5384



B1L102S, U15-ch7, adc2

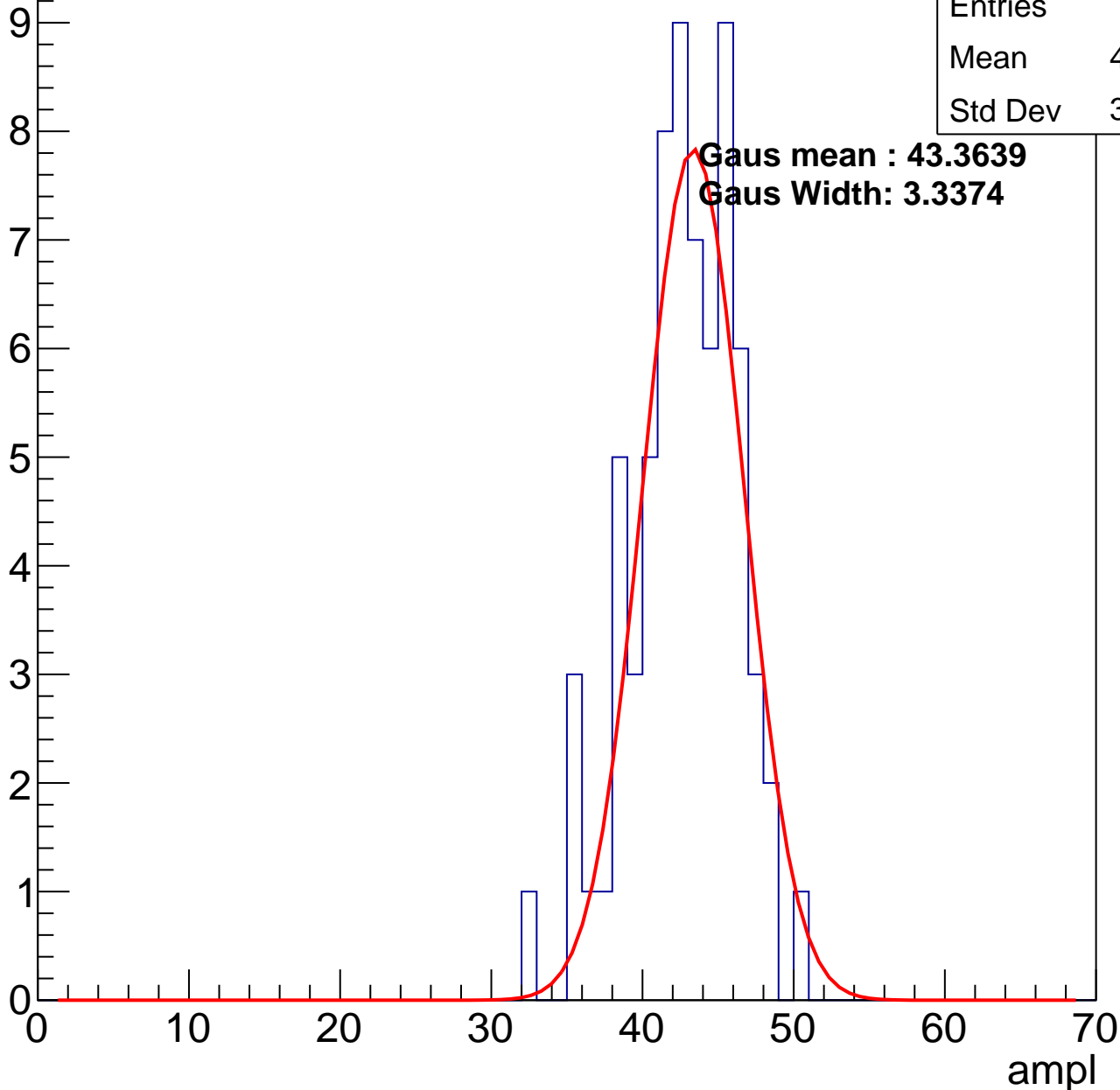
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.23 |
| Std Dev | 3.502 |

Gaus mean : 43.3639

Gaus Width: 3.3374

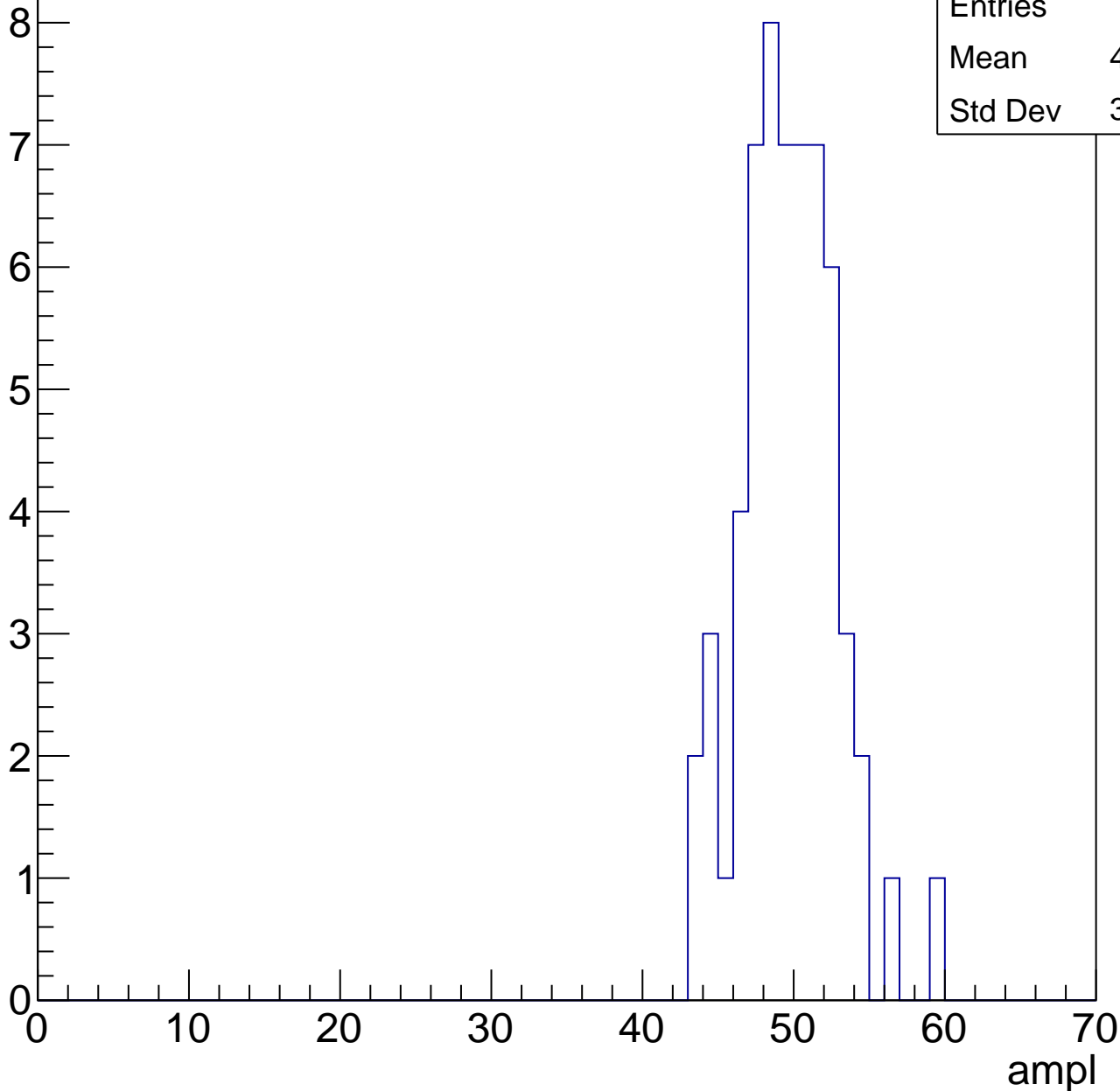


B1L102S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

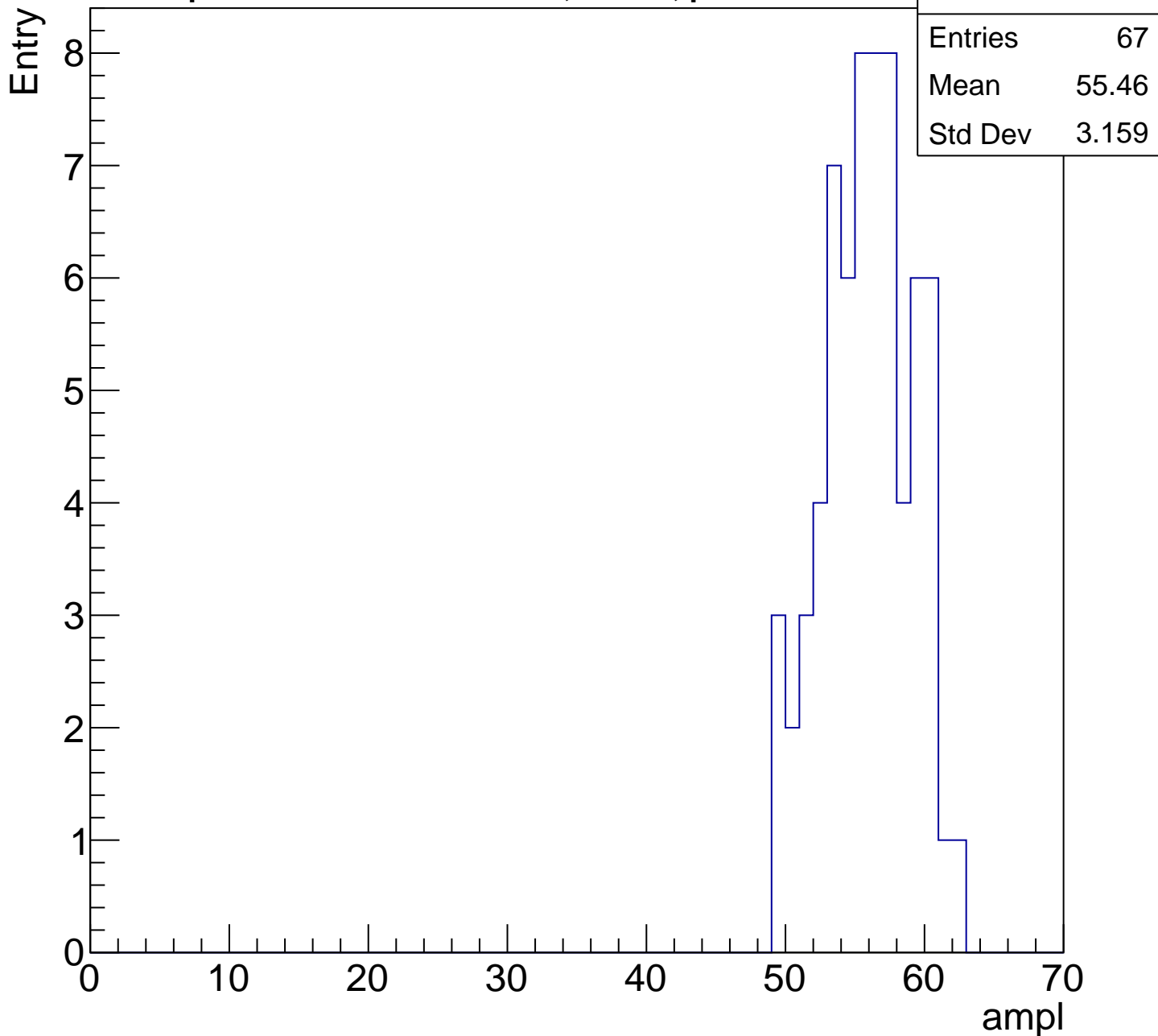
Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 49.22 |
| Std Dev | 3.103 |



B1L102S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

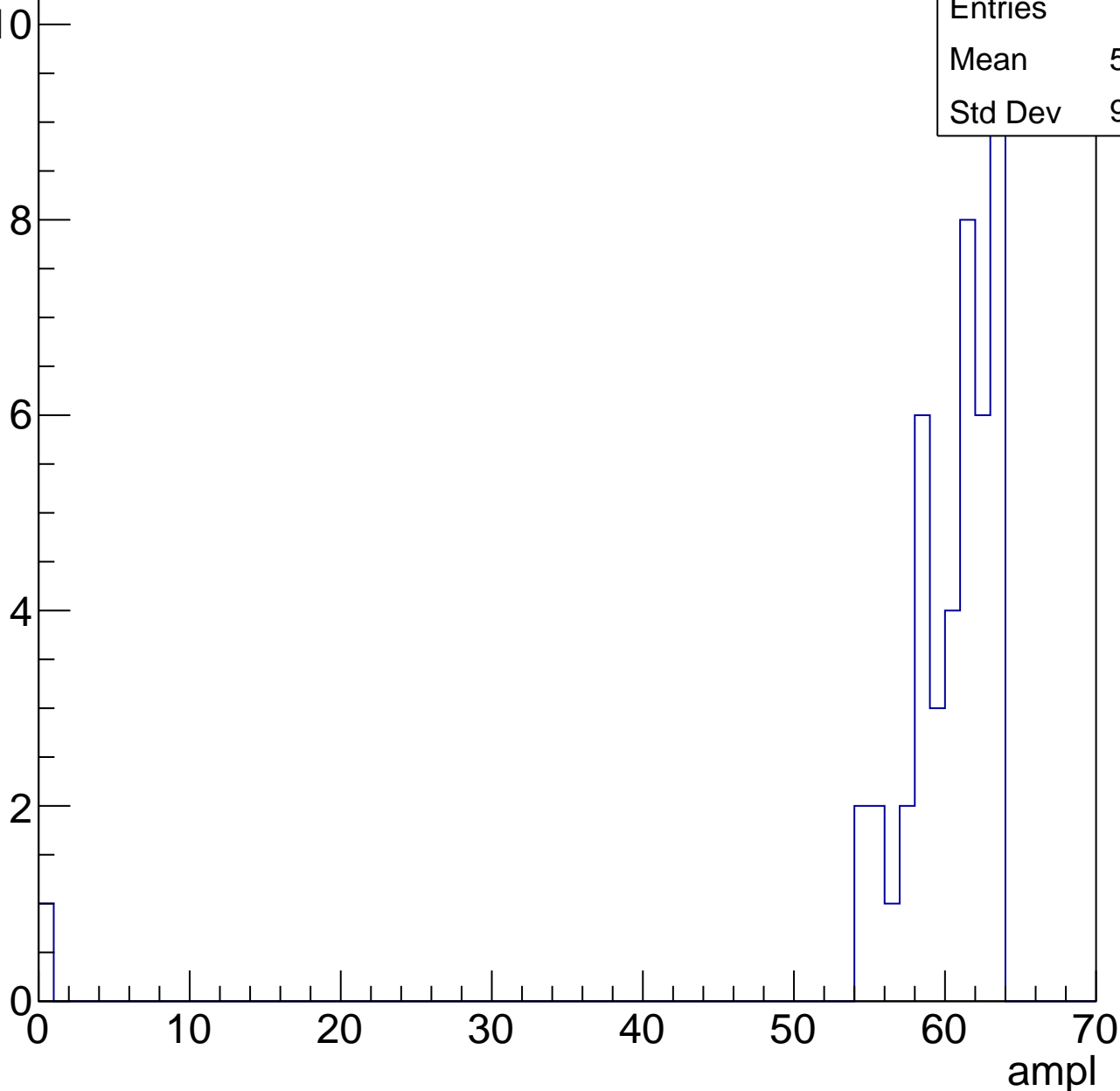


B1L102S, U15-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.73 |
| Std Dev | 9.229 |



B1L102S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch8, adc0

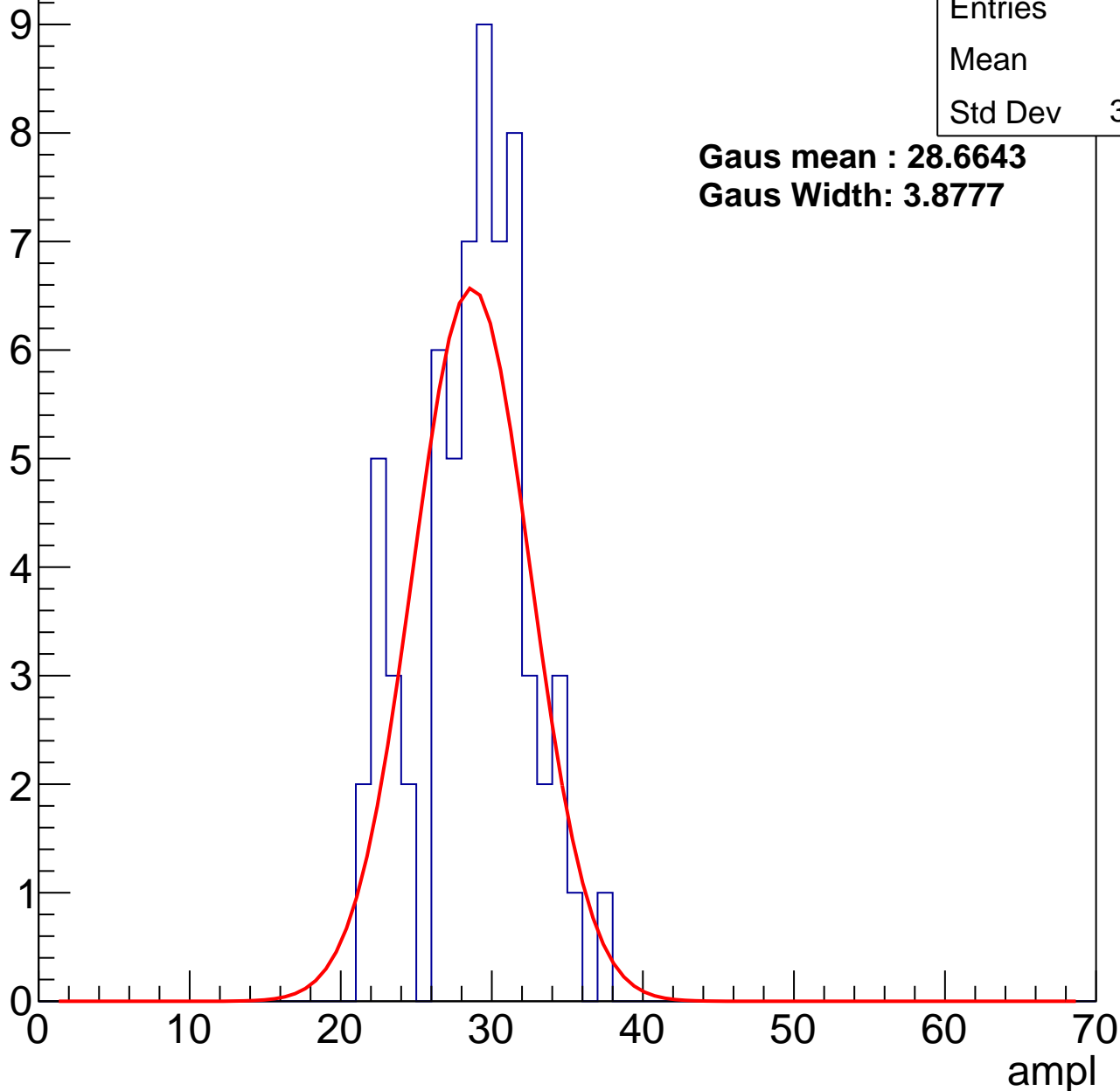
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 28.3 |
| Std Dev | 3.652 |

Gaus mean : 28.6643

Gaus Width: 3.8777



B1L102S, U15-ch8, adc1

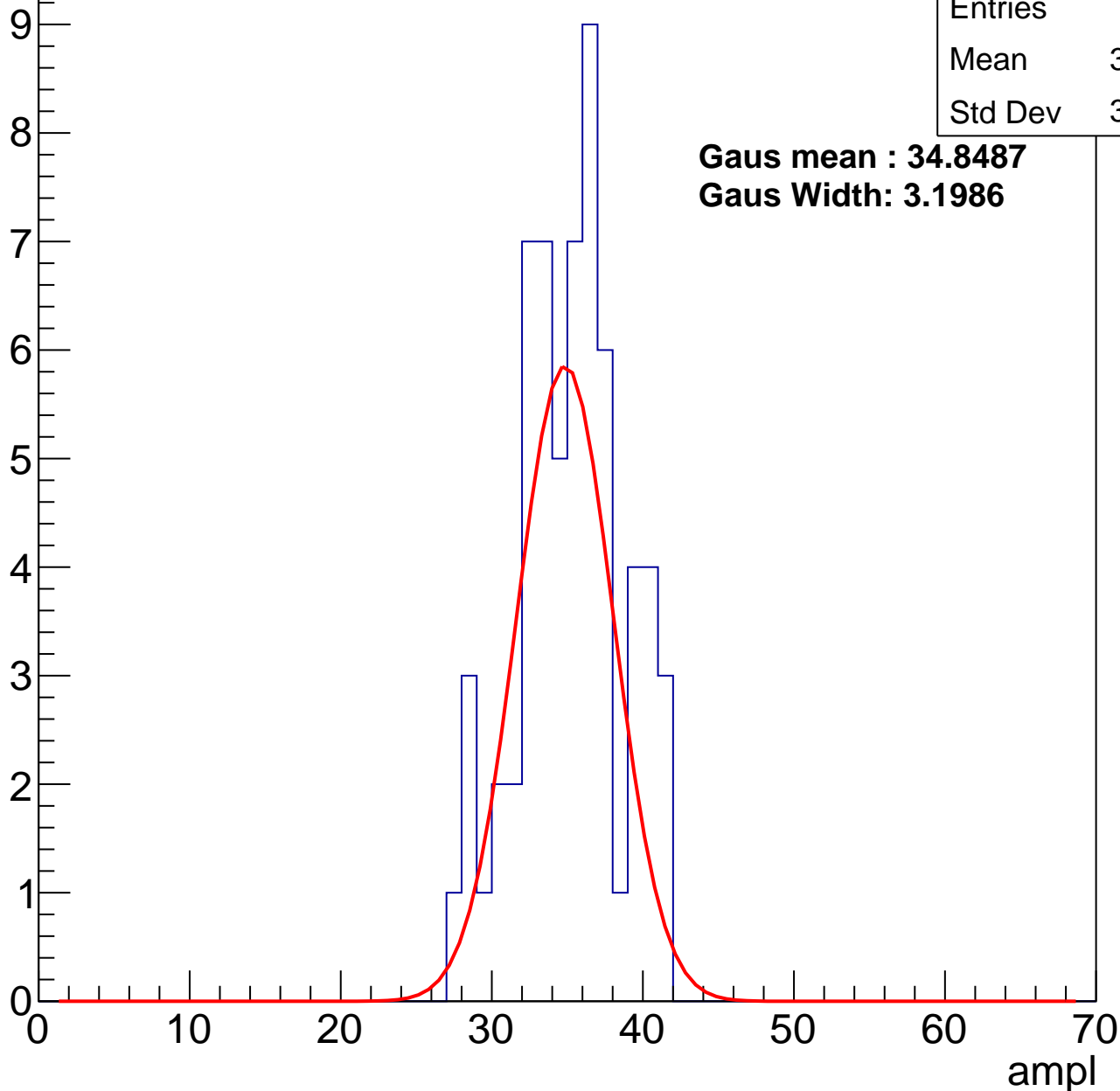
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 34.76 |
| Std Dev | 3.444 |

Gaus mean : 34.8487

Gaus Width: 3.1986



B1L102S, U15-ch8, adc2

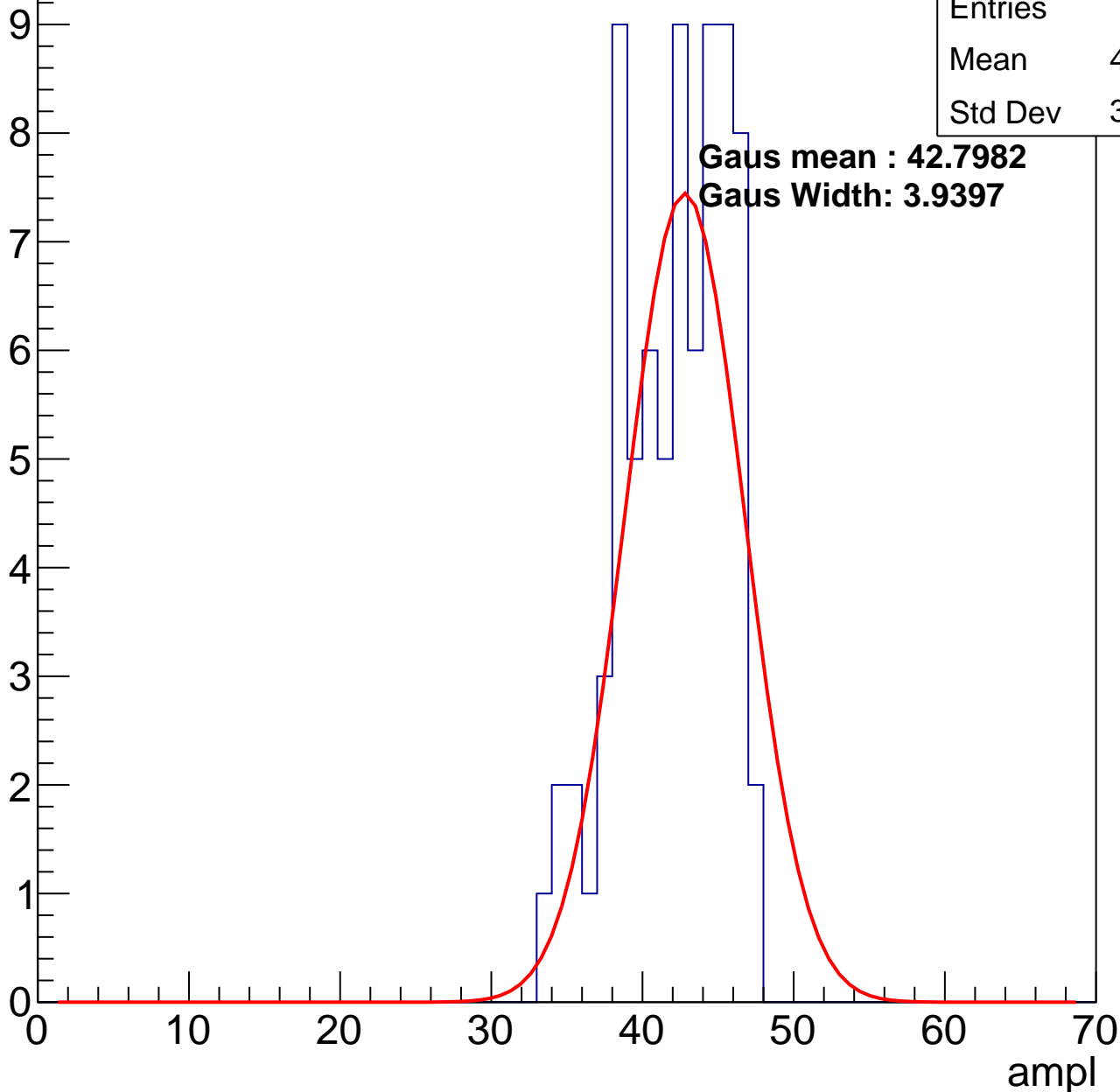
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 41.55 |
| Std Dev | 3.459 |

Gaus mean : 42.7982

Gaus Width: 3.9397

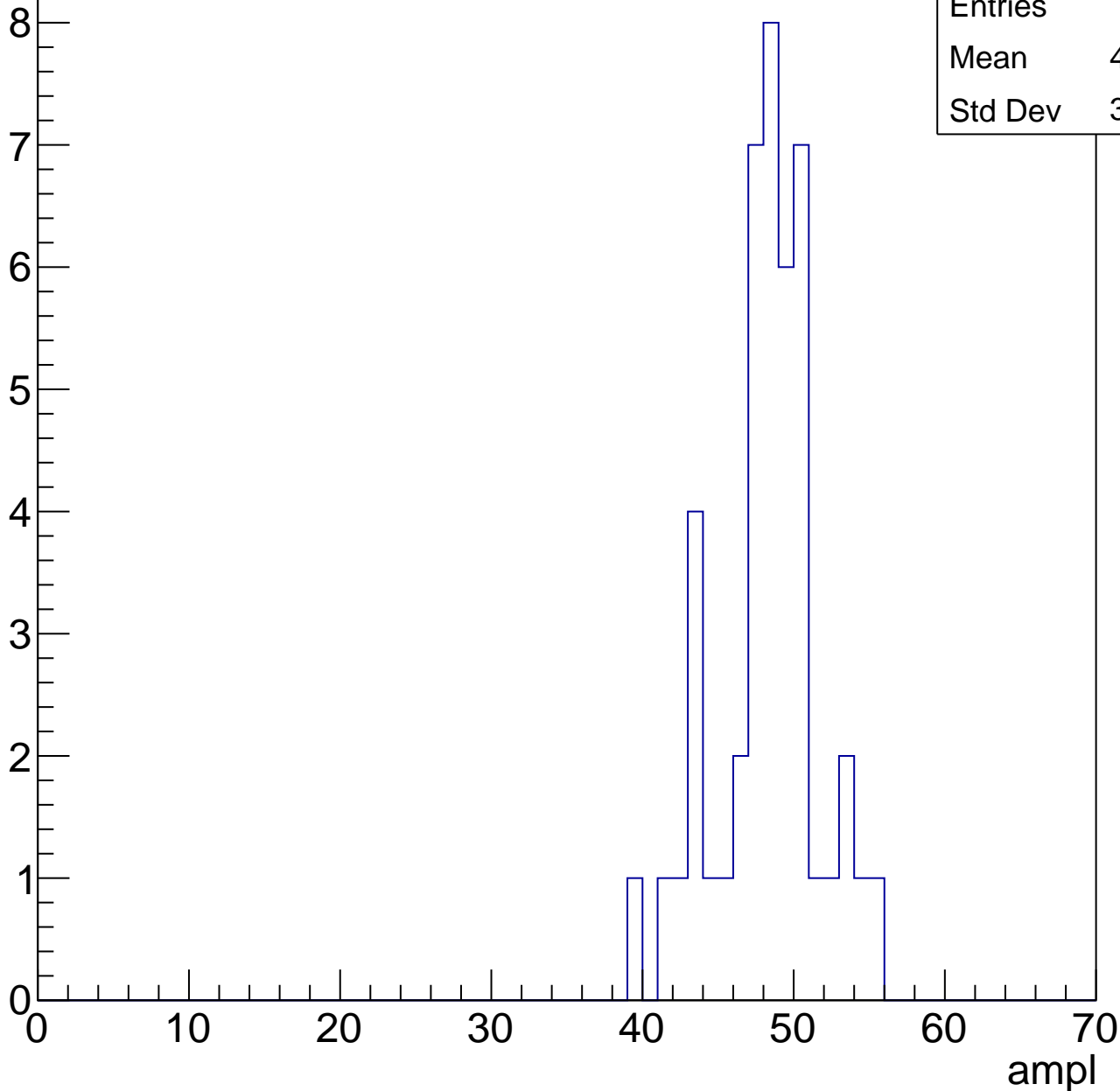


B1L102S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 47.78 |
| Std Dev | 3.326 |

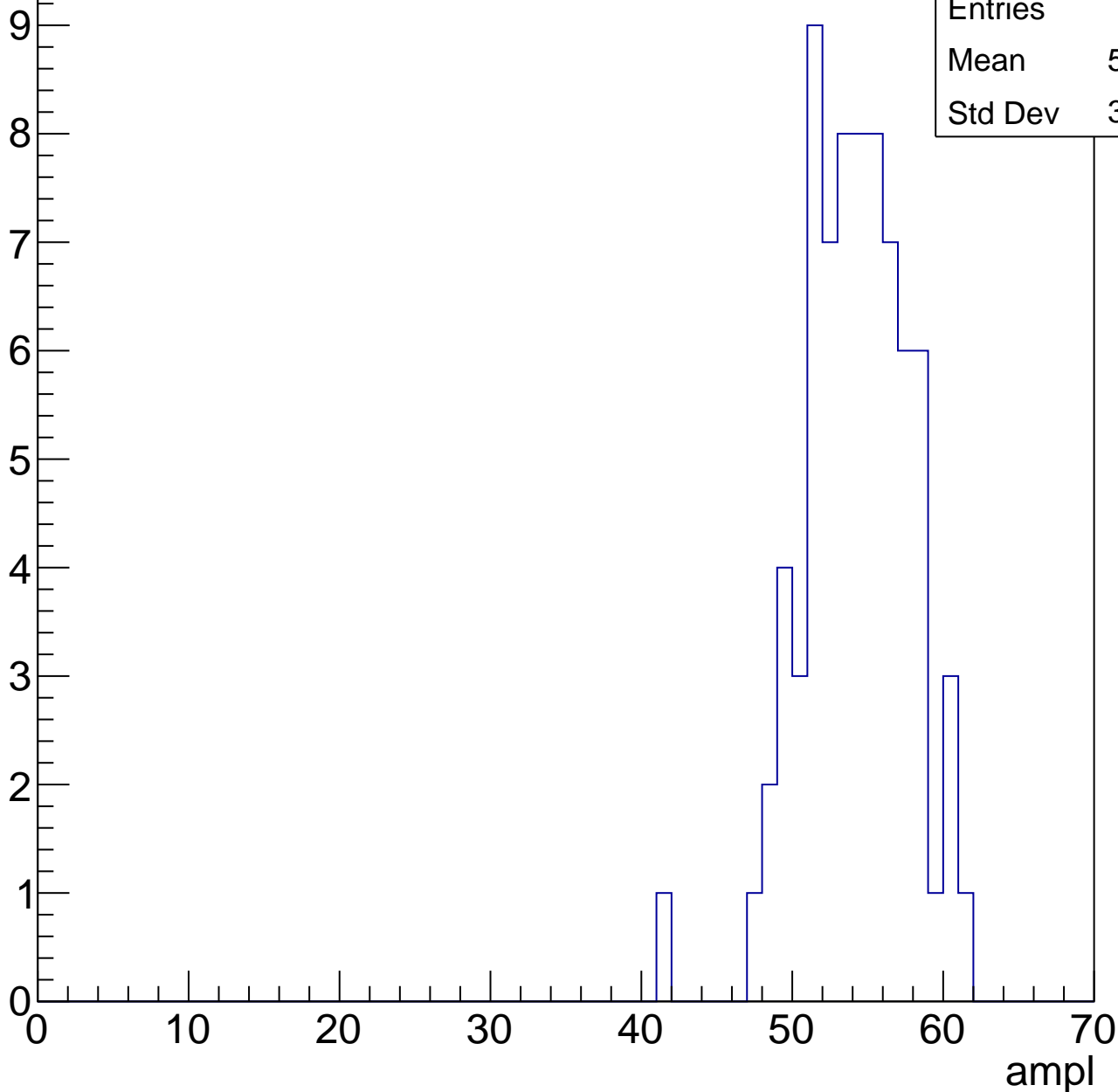


B1L102S, U15-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 53.75 |
| Std Dev | 3.499 |



B1L102S, U15-ch8, adc5

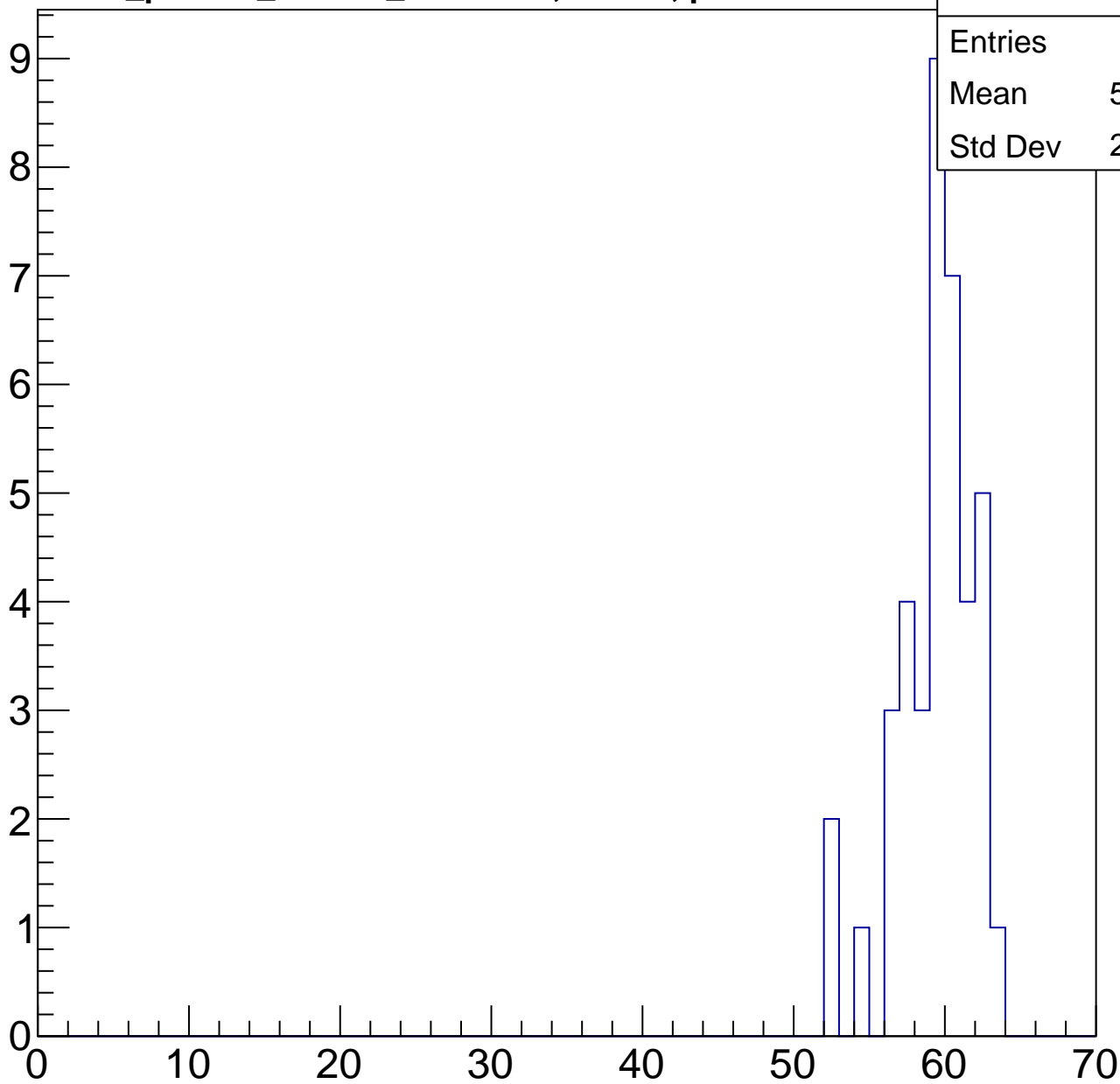
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.87 |
| Std Dev | 2.544 |

ampl

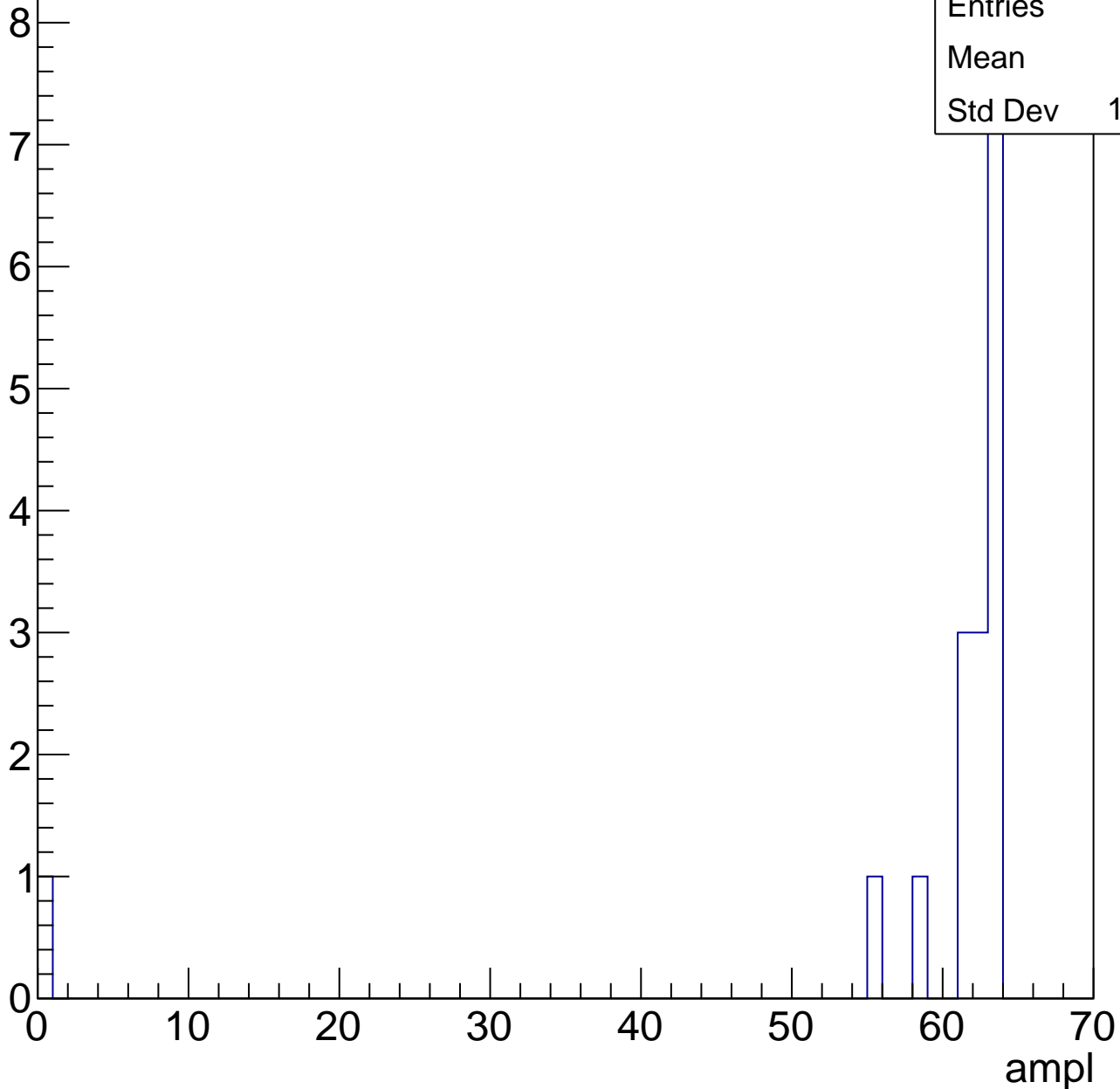


B1L102S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 58 |
| Std Dev | 14.65 |



B1L102S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

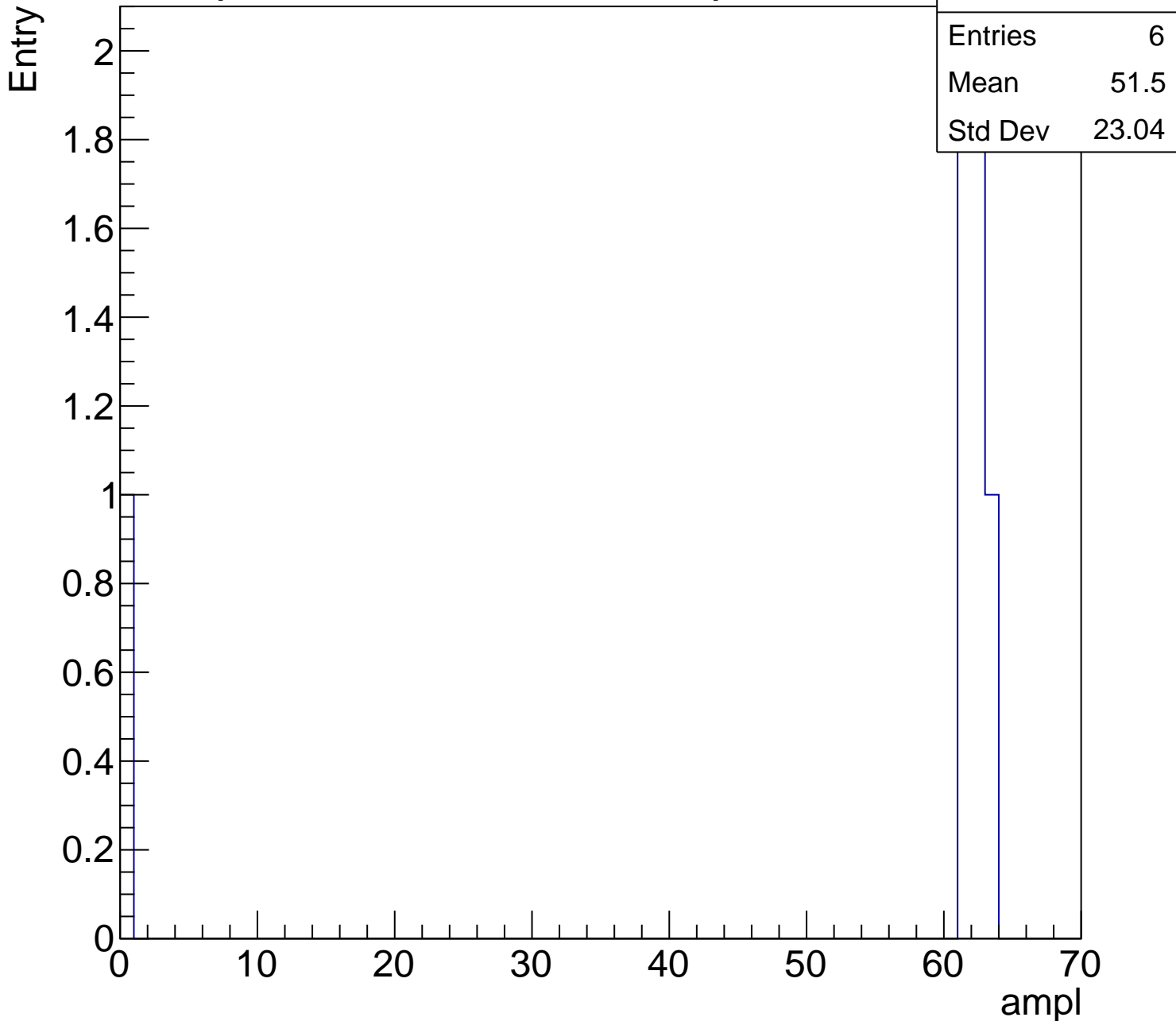
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.5 |
| Std Dev | 23.04 |

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch9, adc0

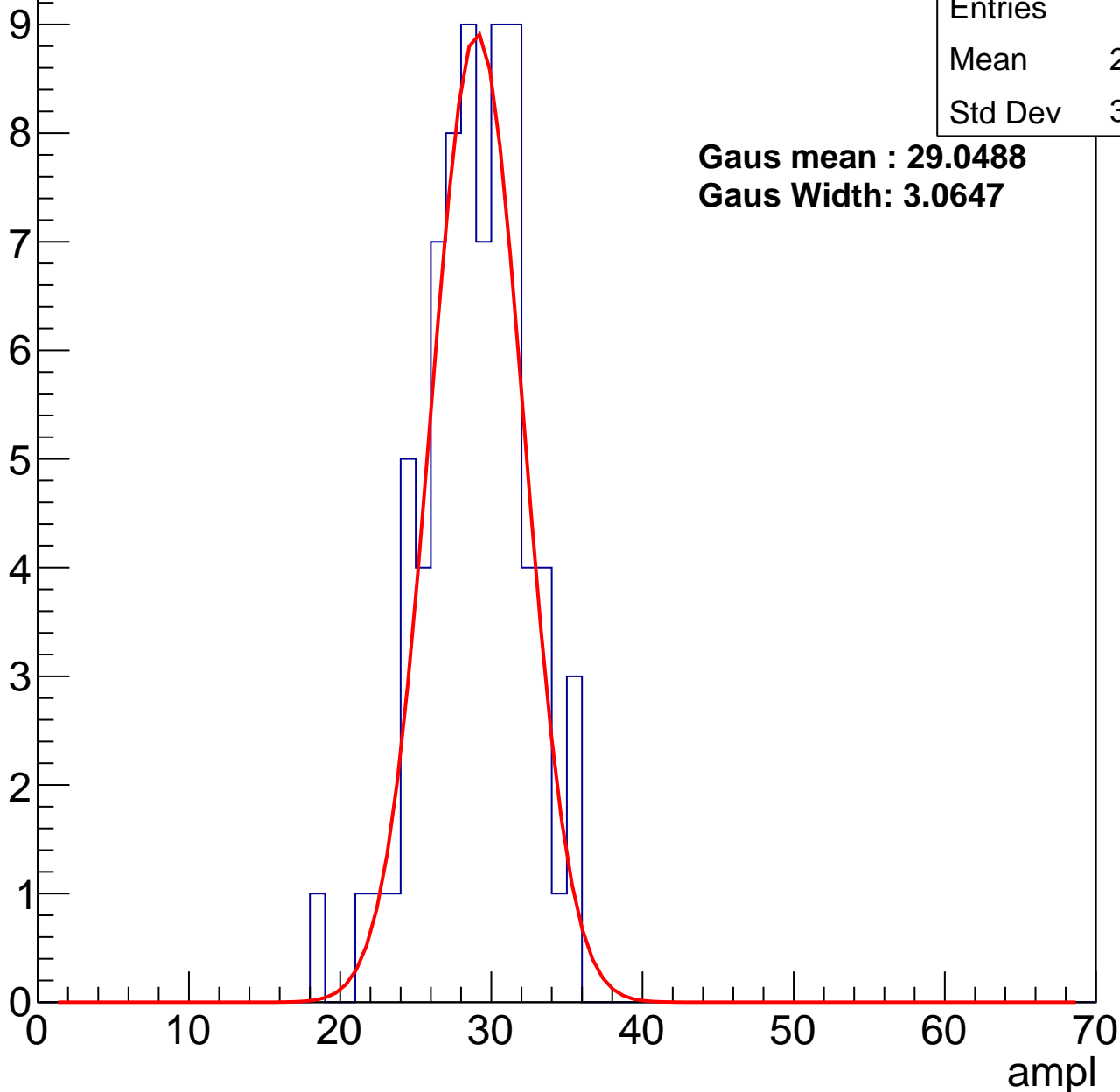
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.45 |
| Std Dev | 3.325 |

Gaus mean : 29.0488

Gaus Width: 3.0647



B1L102S, U15-ch9, adc1

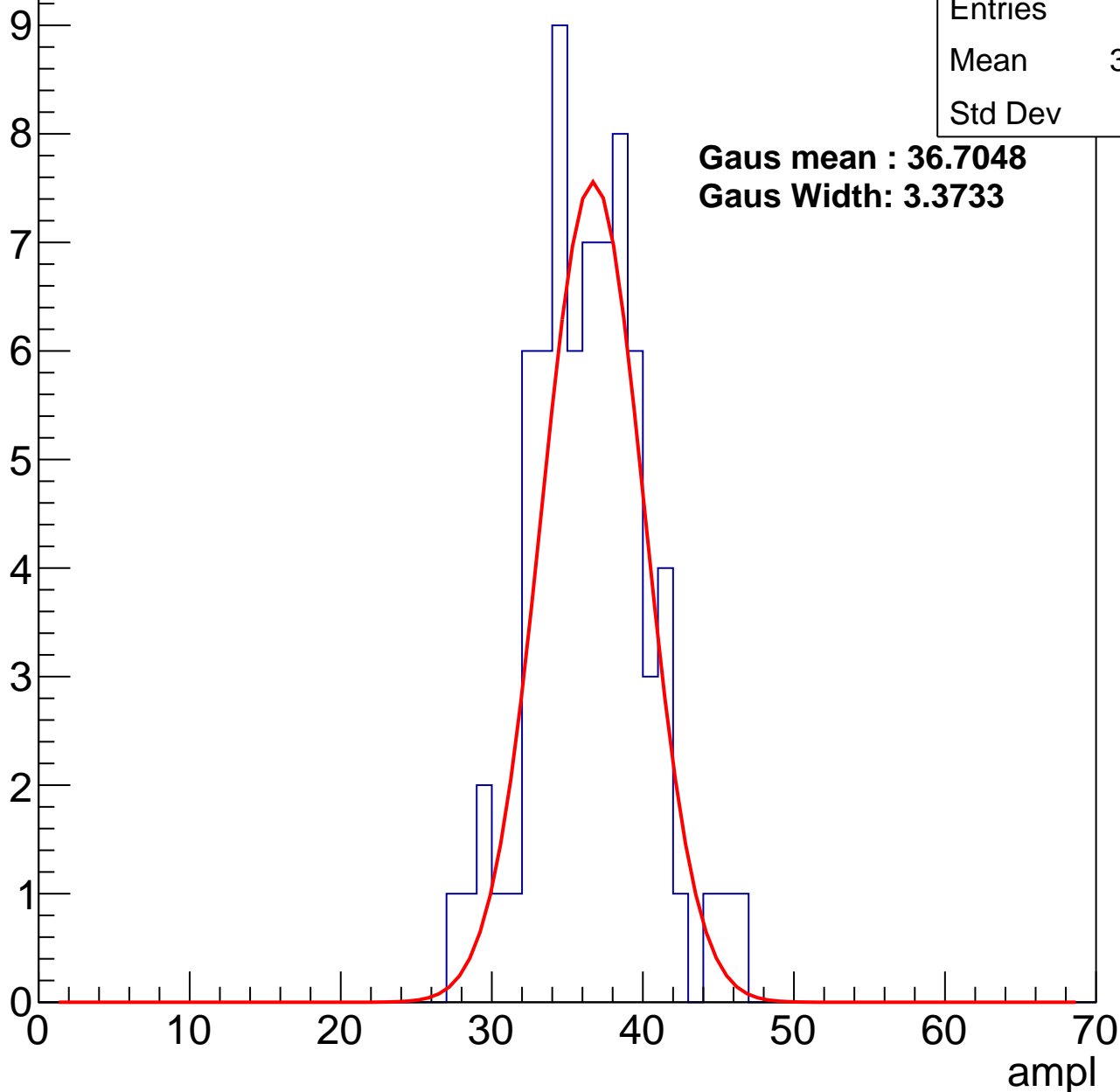
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 35.97 |
| Std Dev | 3.76 |

Gaus mean : 36.7048

Gaus Width: 3.3733

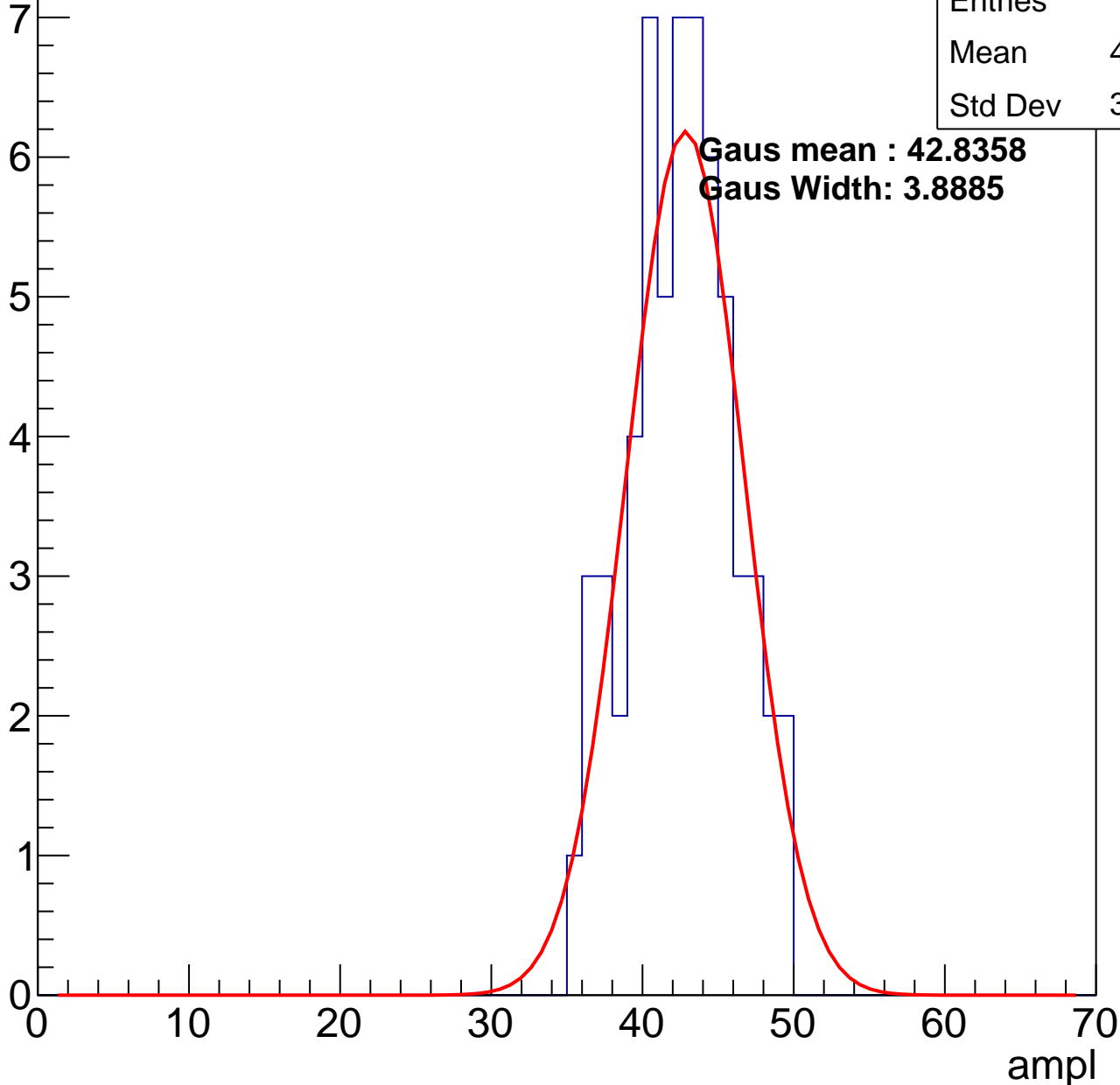


B1L102S, U15-ch9, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.13 |
| Std Dev | 3.418 |

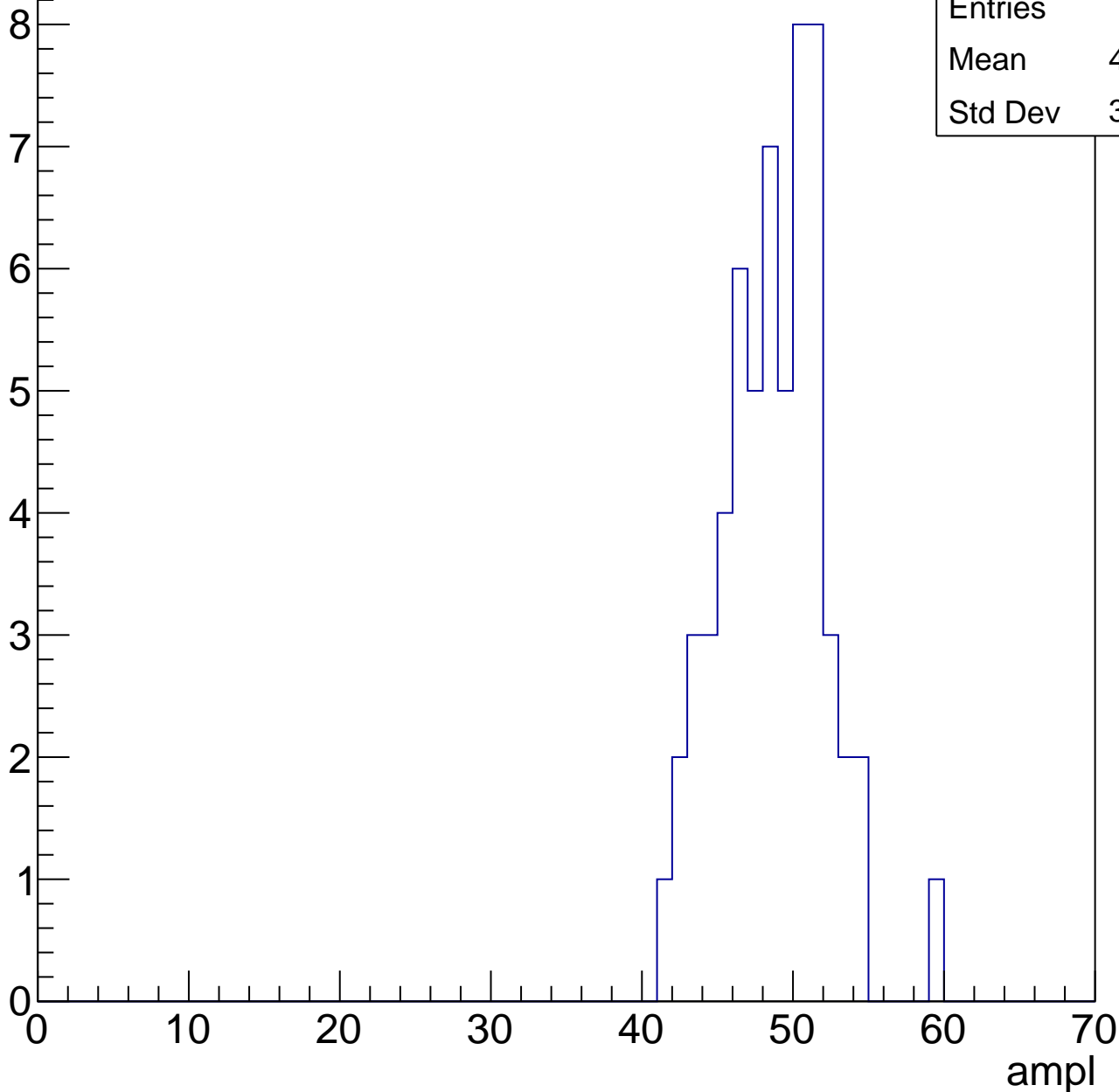


B1L102S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

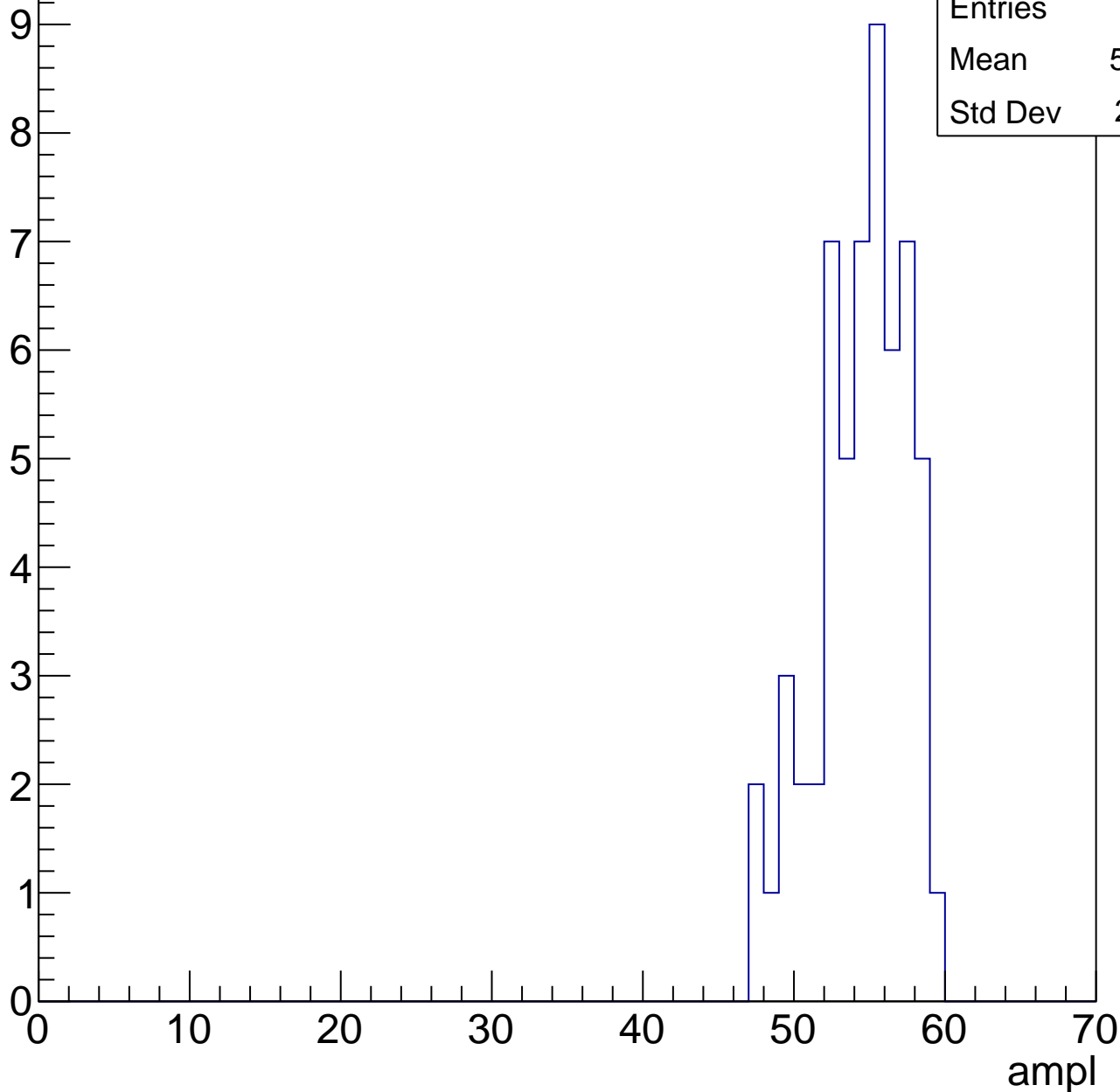
| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.25 |
| Std Dev | 3.428 |



B1L102S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



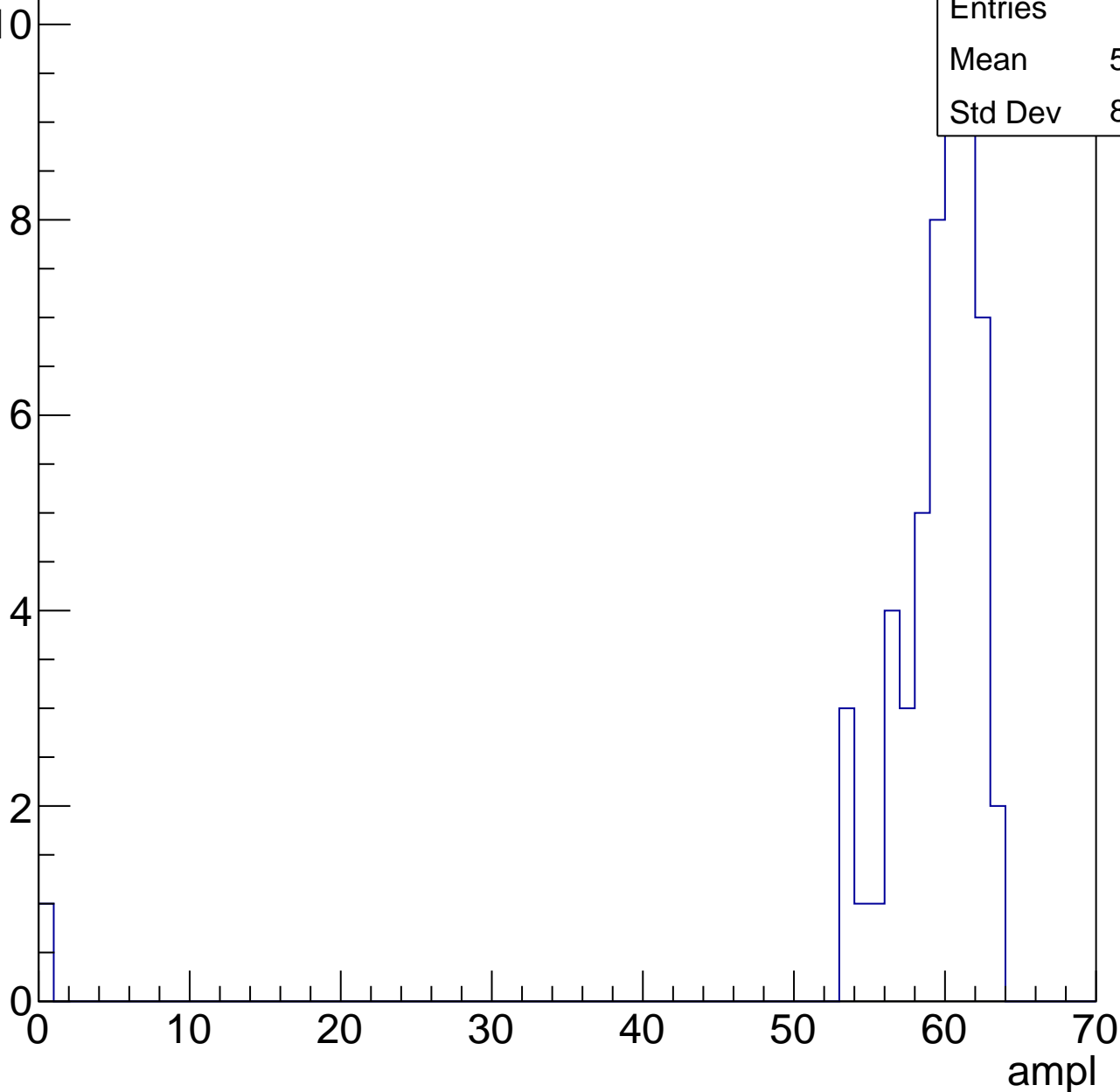
| | |
|---------|-------|
| Entries | 57 |
| Mean | 53.98 |
| Std Dev | 2.941 |

B1L102S, U15-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.06 |
| Std Dev | 8.365 |

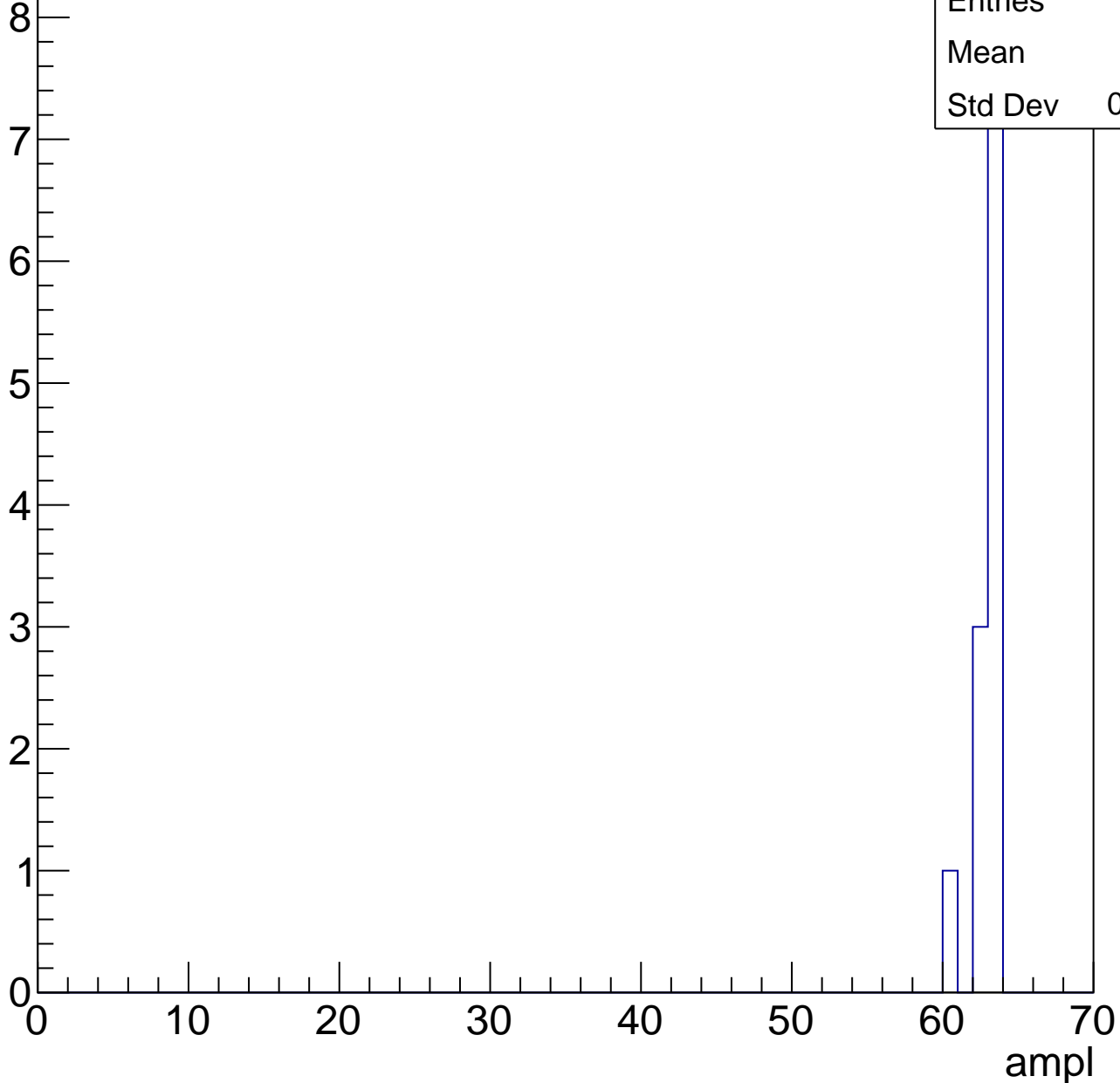


B1L102S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 62.5 |
| Std Dev | 0.866 |



B1L102S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch10, adc0

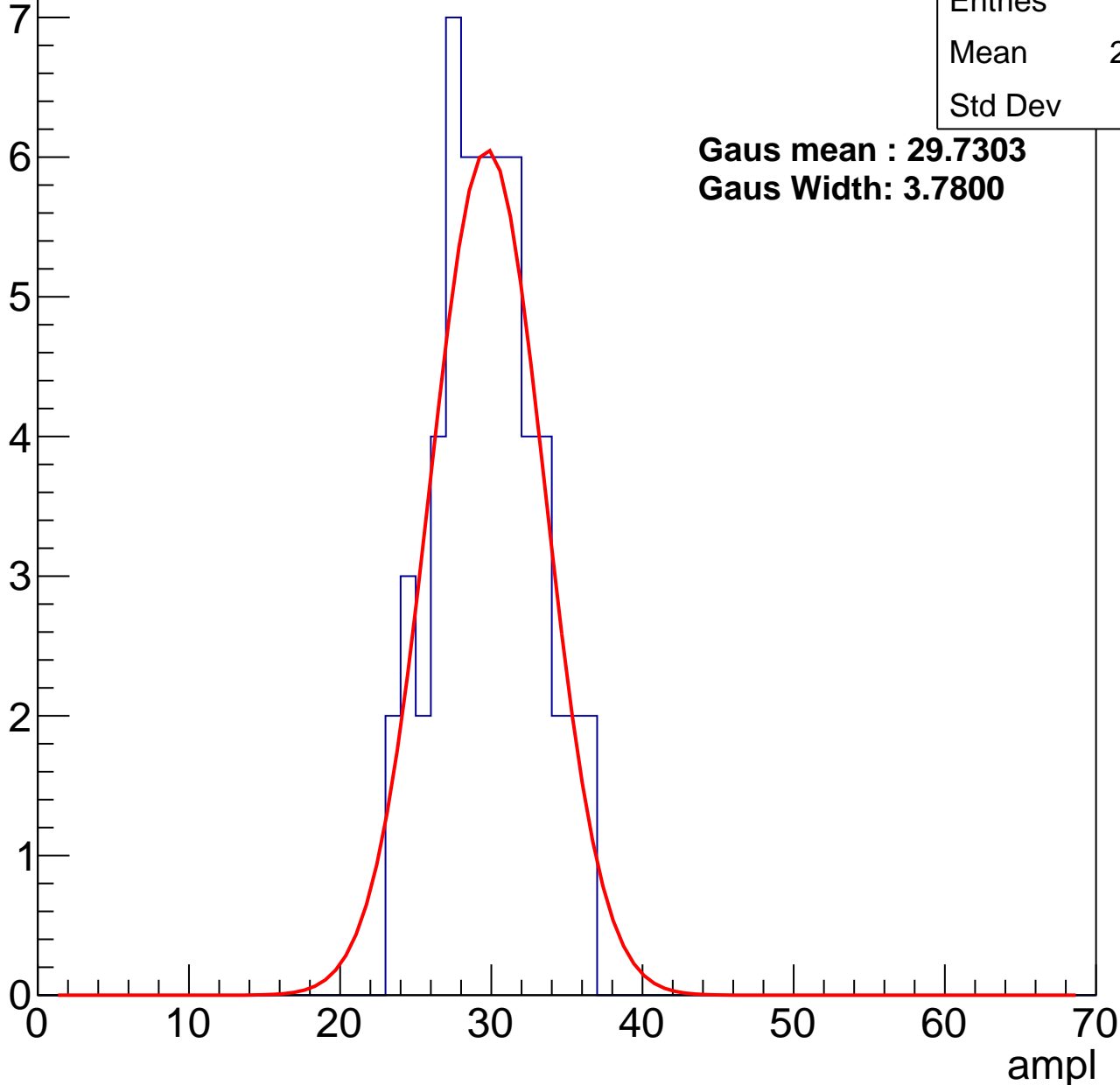
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 29.27 |
| Std Dev | 3.26 |

Gaus mean : 29.7303

Gaus Width: 3.7800



B1L102S, U15-ch10, adc1

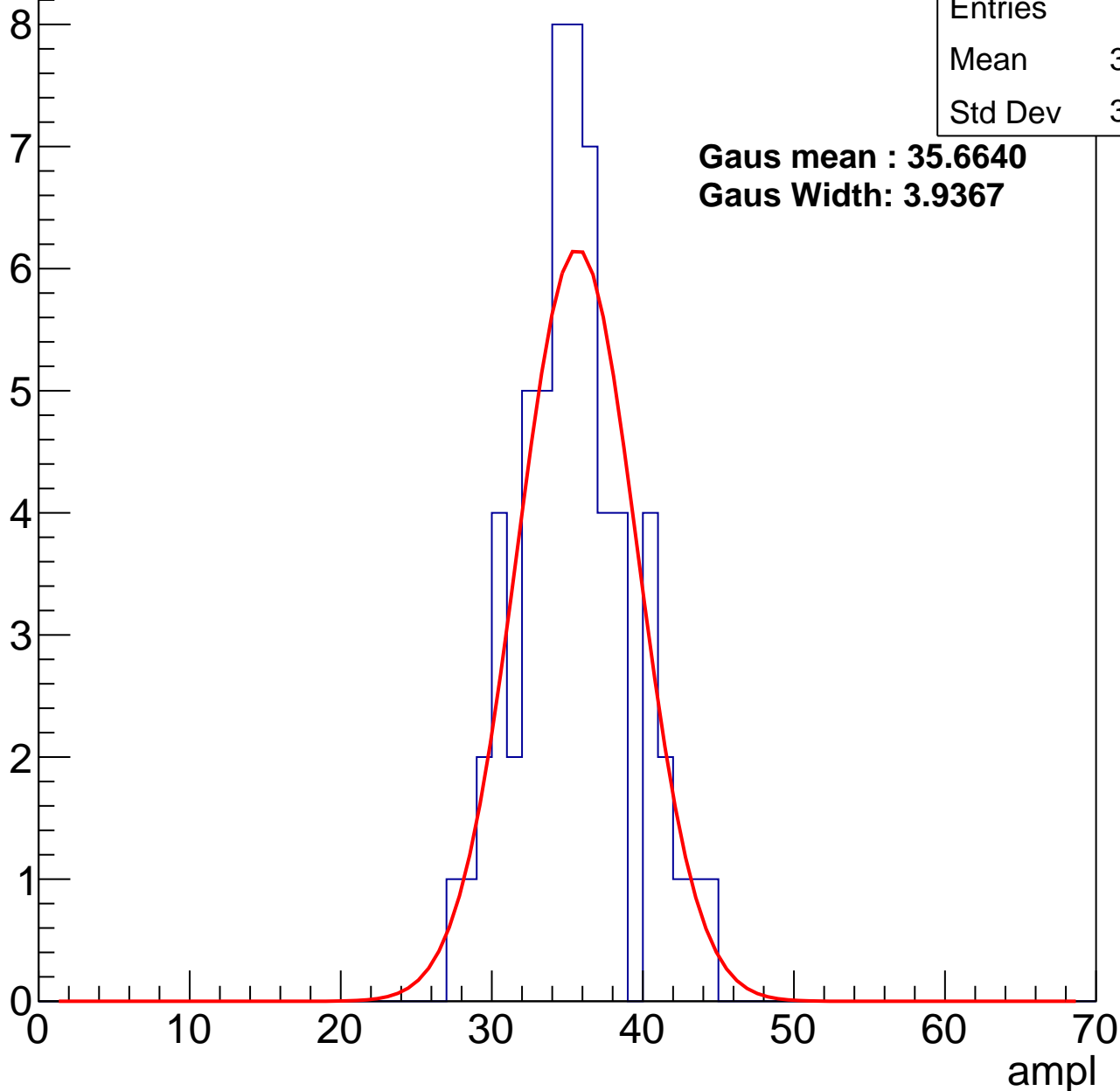
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 34.92 |
| Std Dev | 3.685 |

Gaus mean : 35.6640

Gaus Width: 3.9367



B1L102S, U15-ch10, adc2

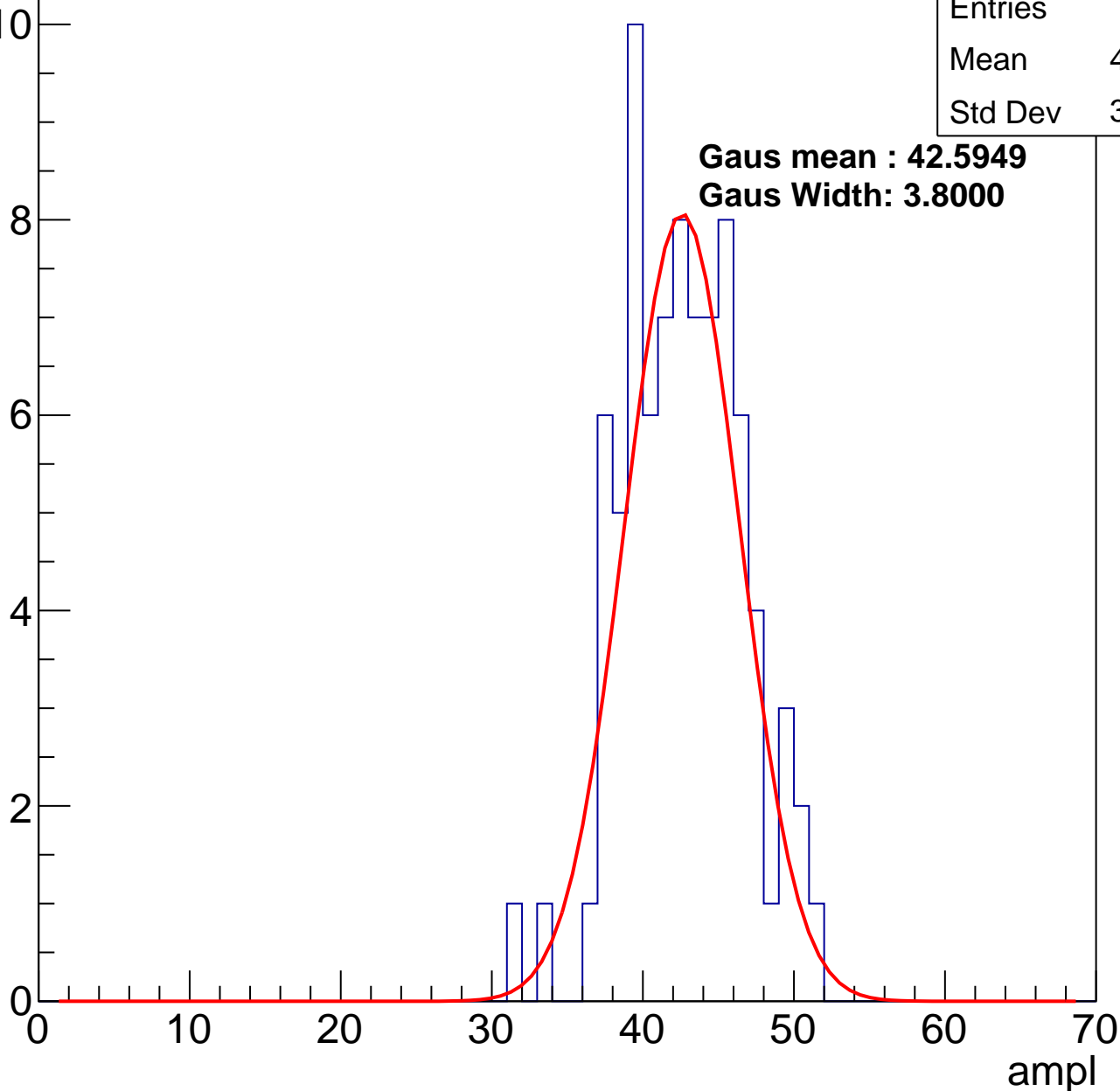
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 42.19 |
| Std Dev | 3.905 |

Gaus mean : 42.5949

Gaus Width: 3.8000

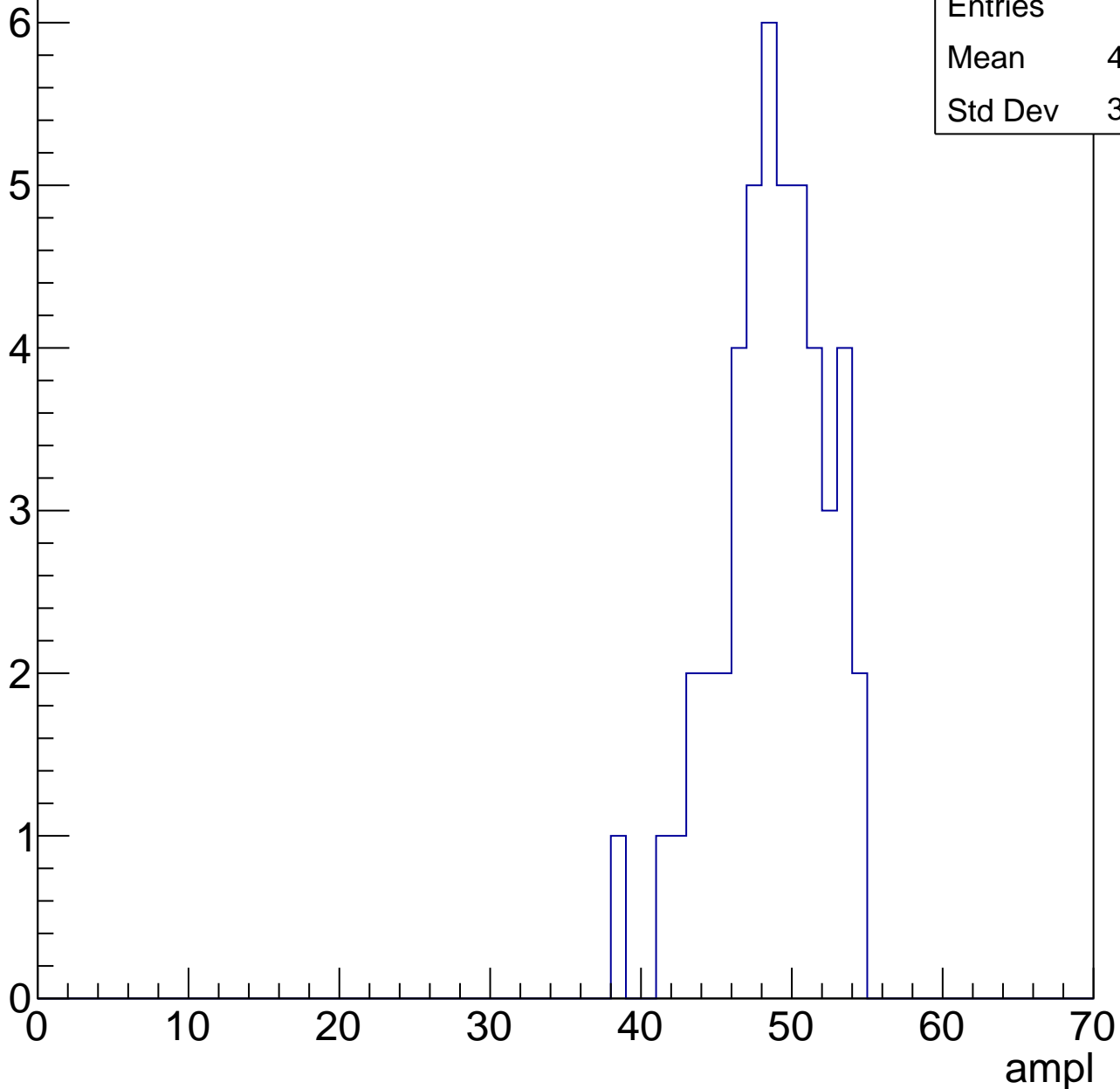


B1L102S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 48.23 |
| Std Dev | 3.526 |



B1L102S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 55.01 |
| Std Dev | 3.775 |

Entry

10

8

6

4

2

0

0

10

20

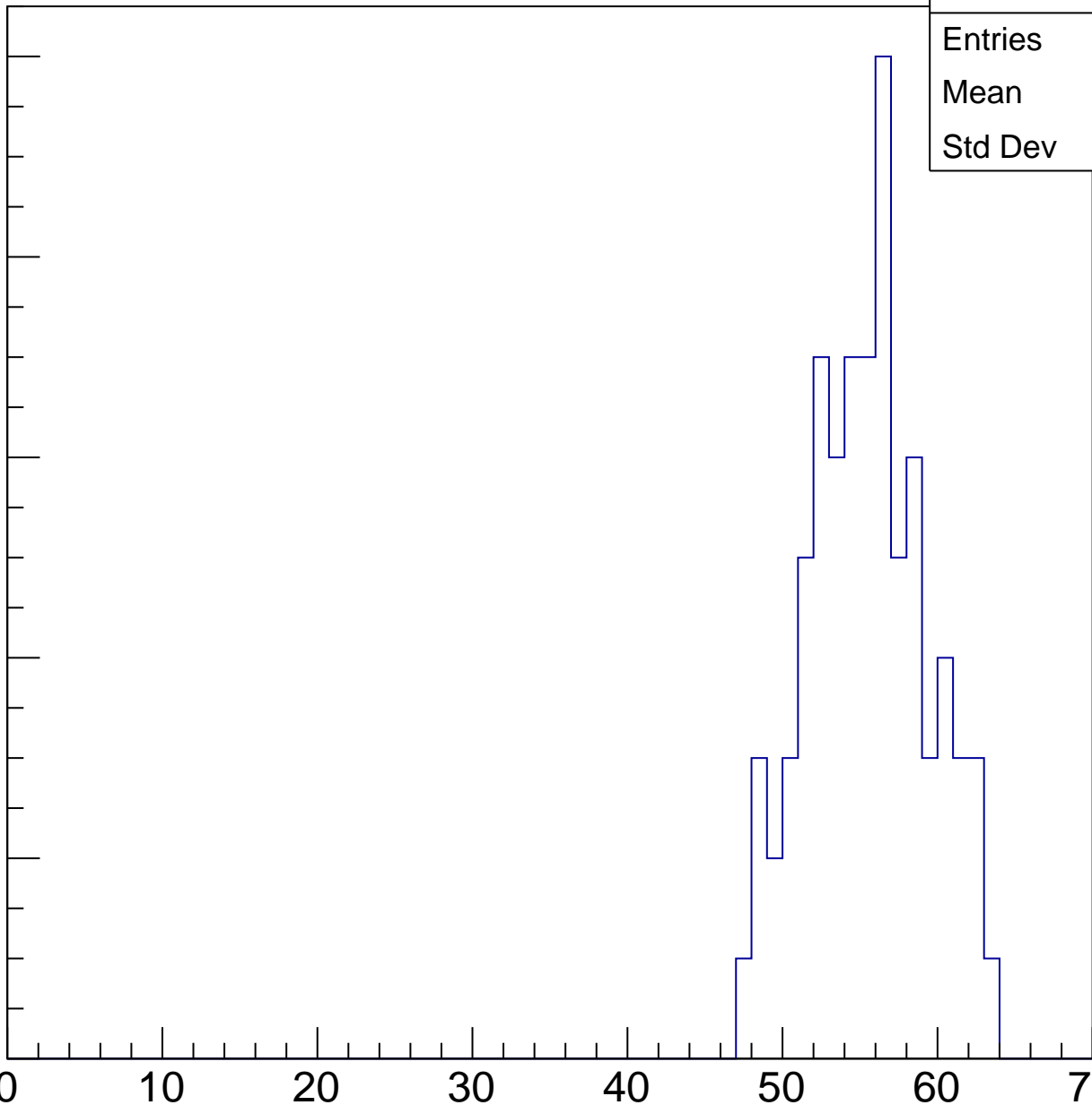
30

40

50

60

ampl

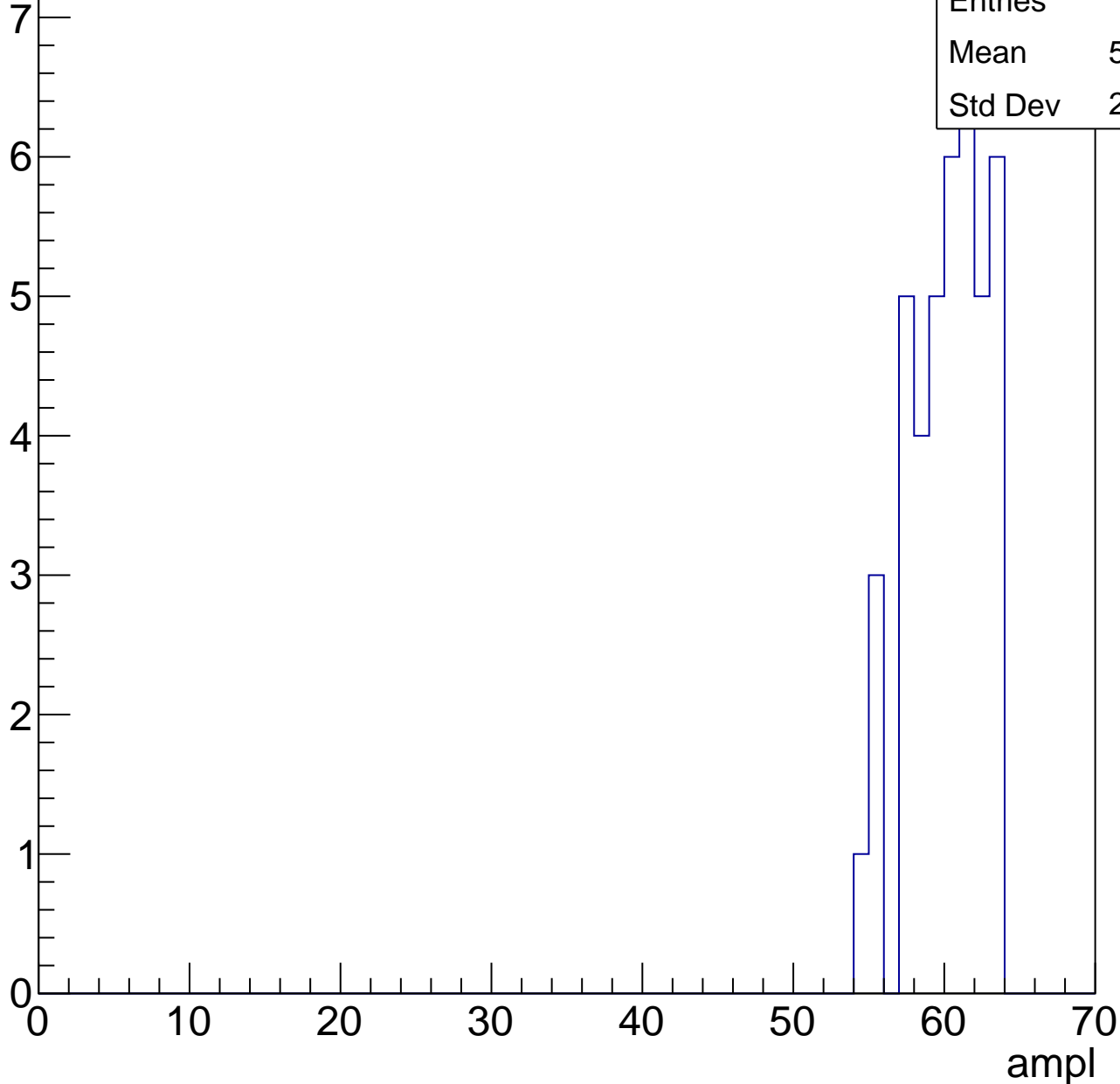


B1L102S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

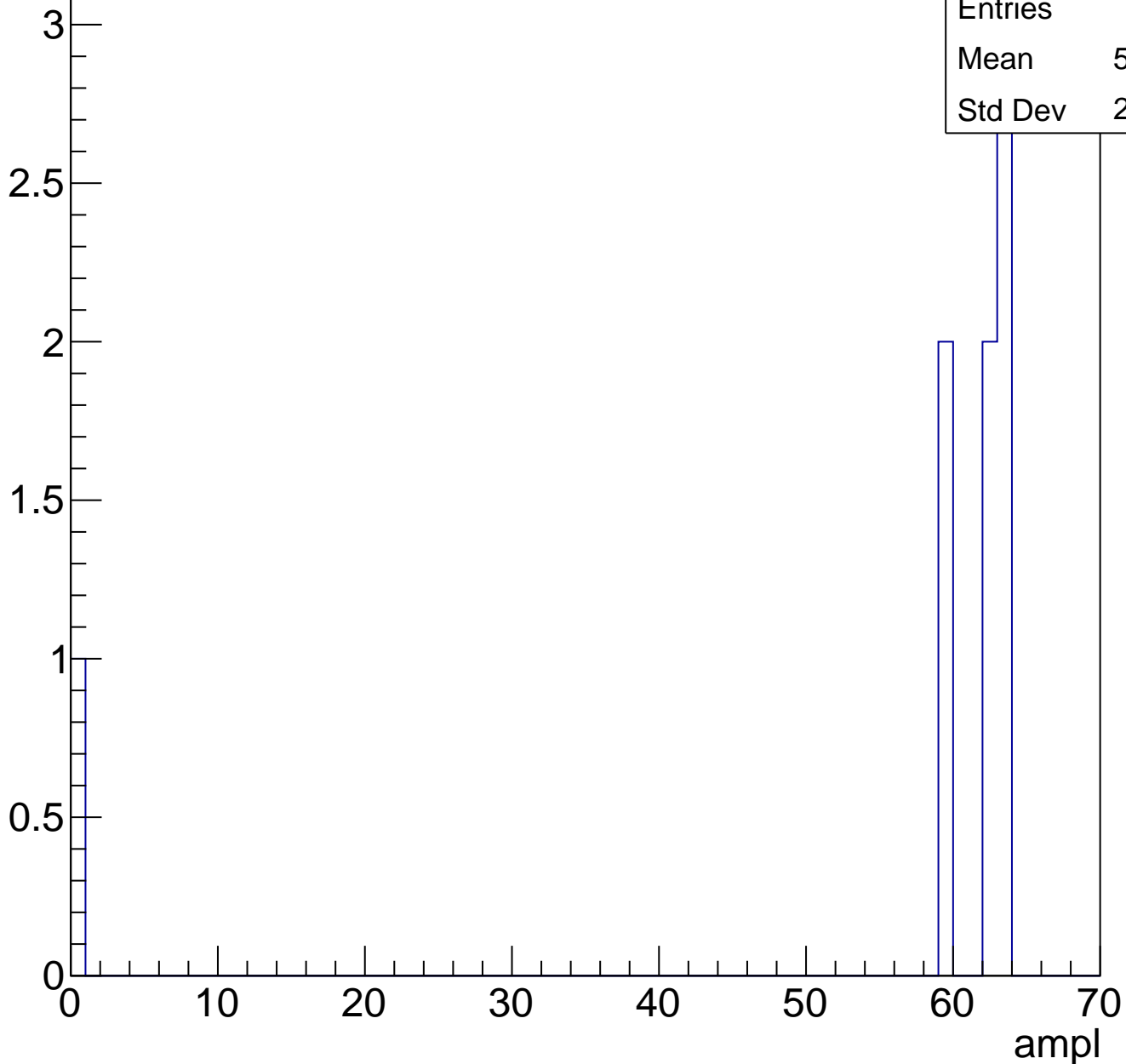
| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.67 |
| Std Dev | 2.456 |



B1L102S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch11, adc0

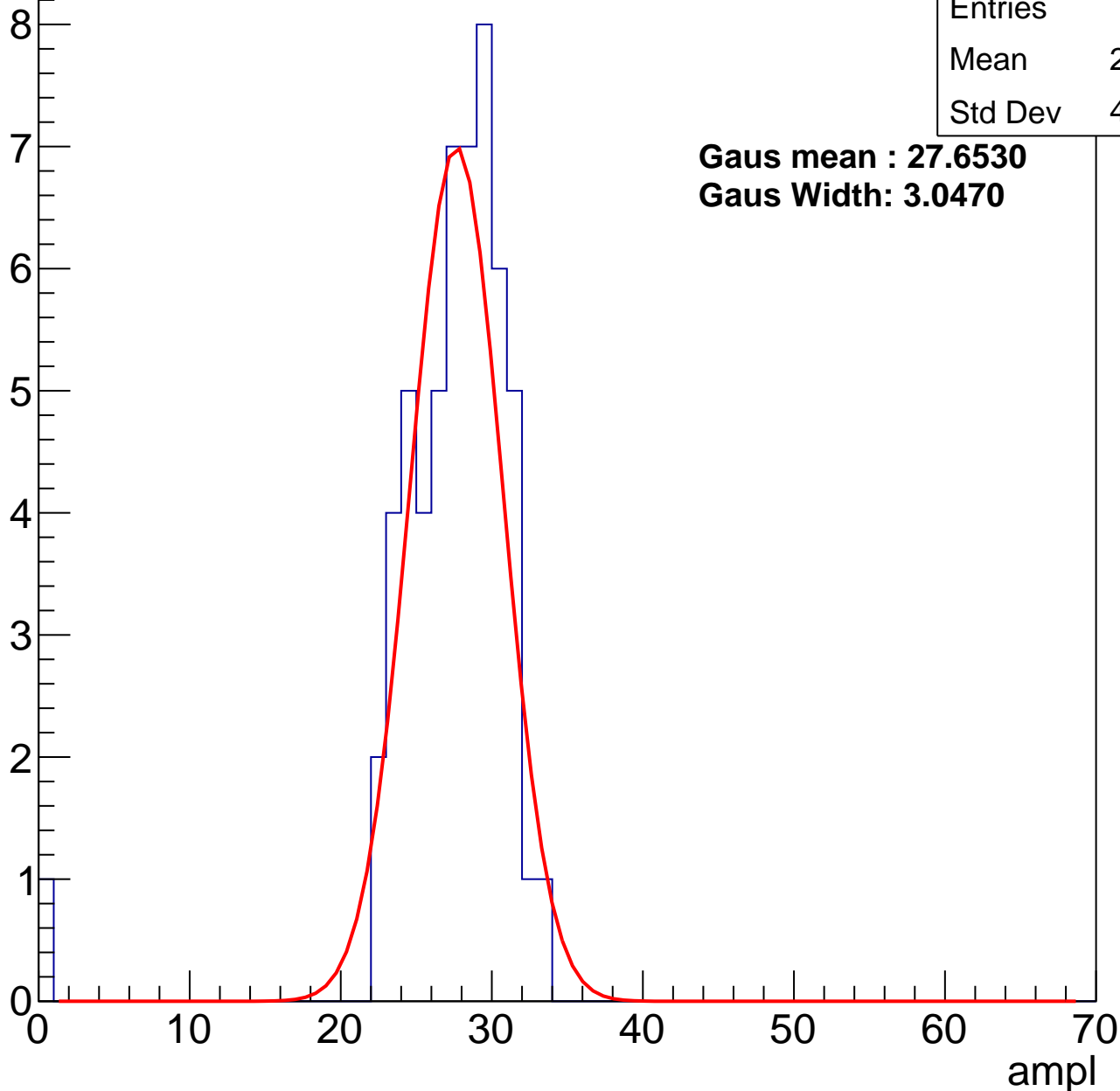
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 26.84 |
| Std Dev | 4.515 |

Gaus mean : 27.6530

Gaus Width: 3.0470



B1L102S, U15-ch11, adc1

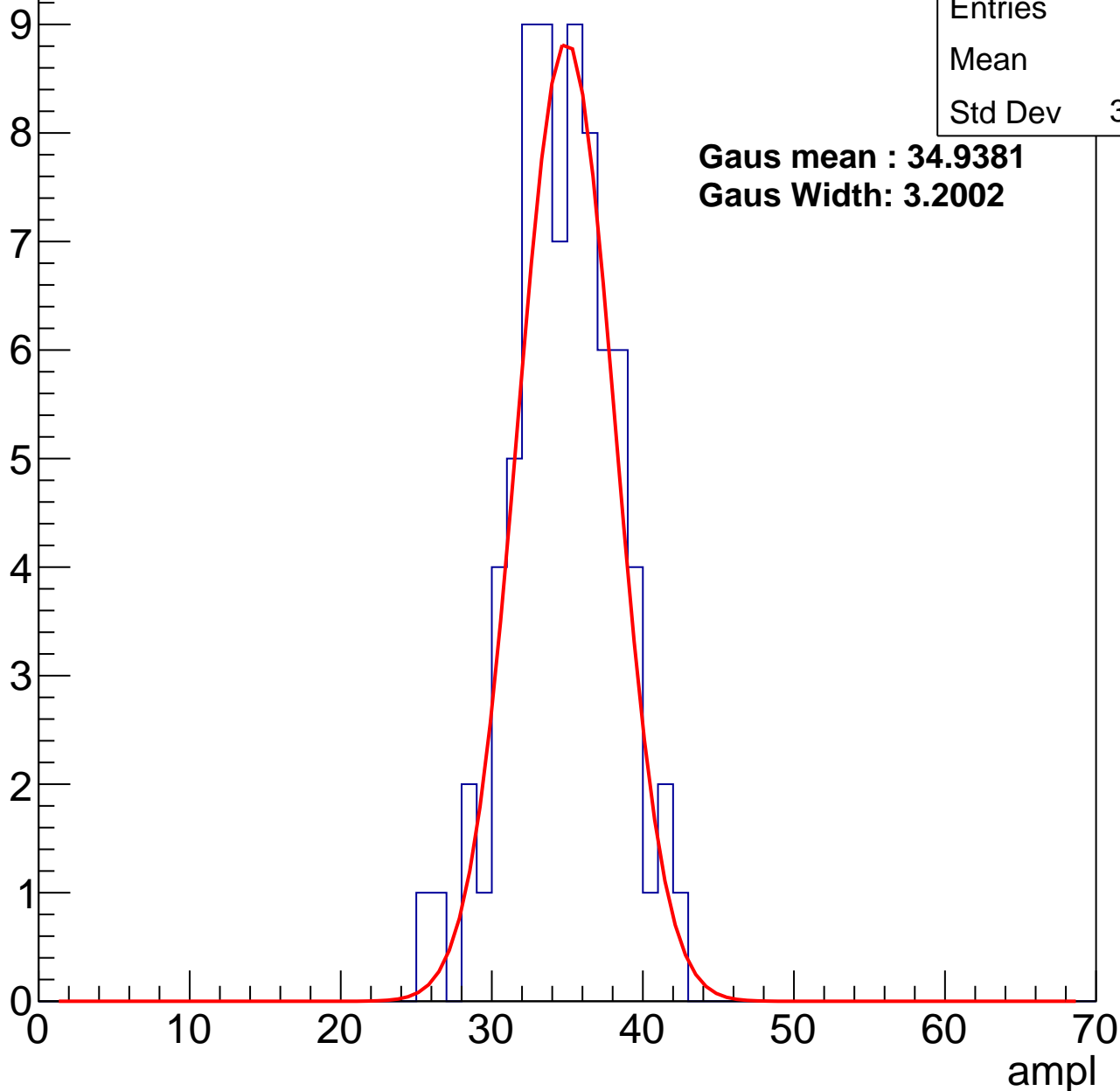
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 34.3 |
| Std Dev | 3.399 |

Gaus mean : 34.9381

Gaus Width: 3.2002



B1L102S, U15-ch11, adc2

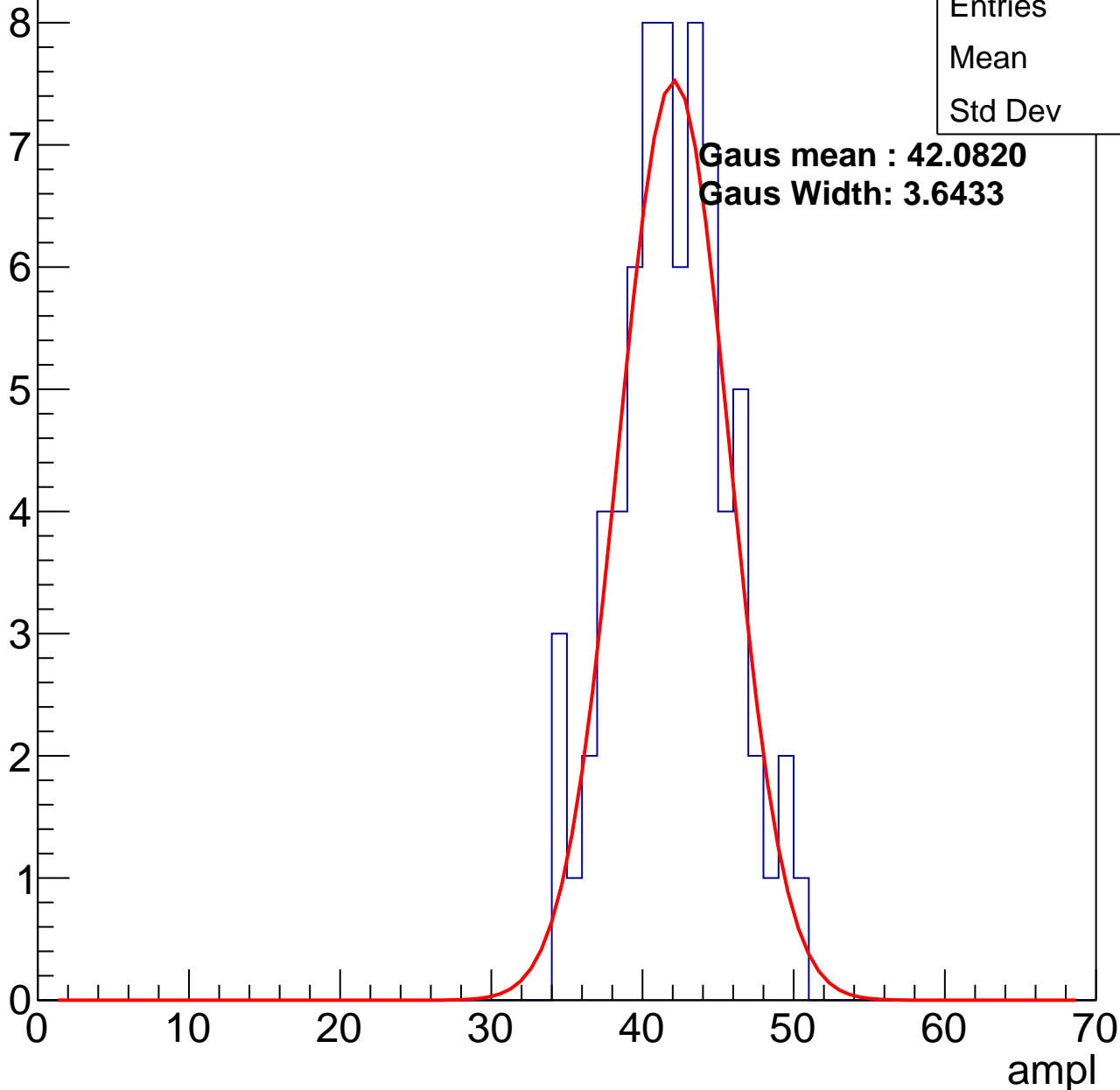
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 72 |
| Mean | 41.6 |
| Std Dev | 3.65 |

Gaus mean : 42.0820

Gaus Width: 3.6433

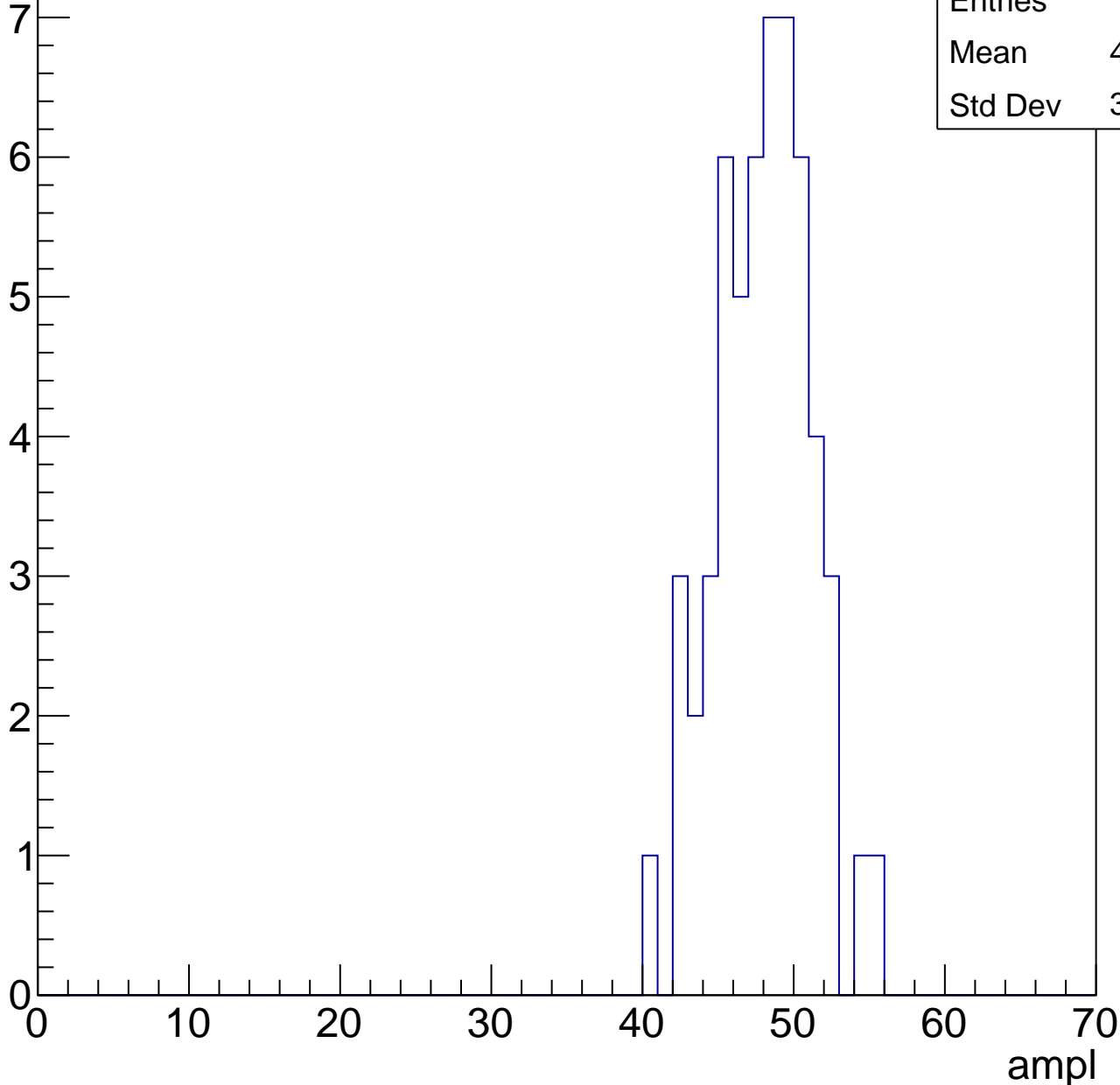


B1L102S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 47.53 |
| Std Dev | 3.127 |

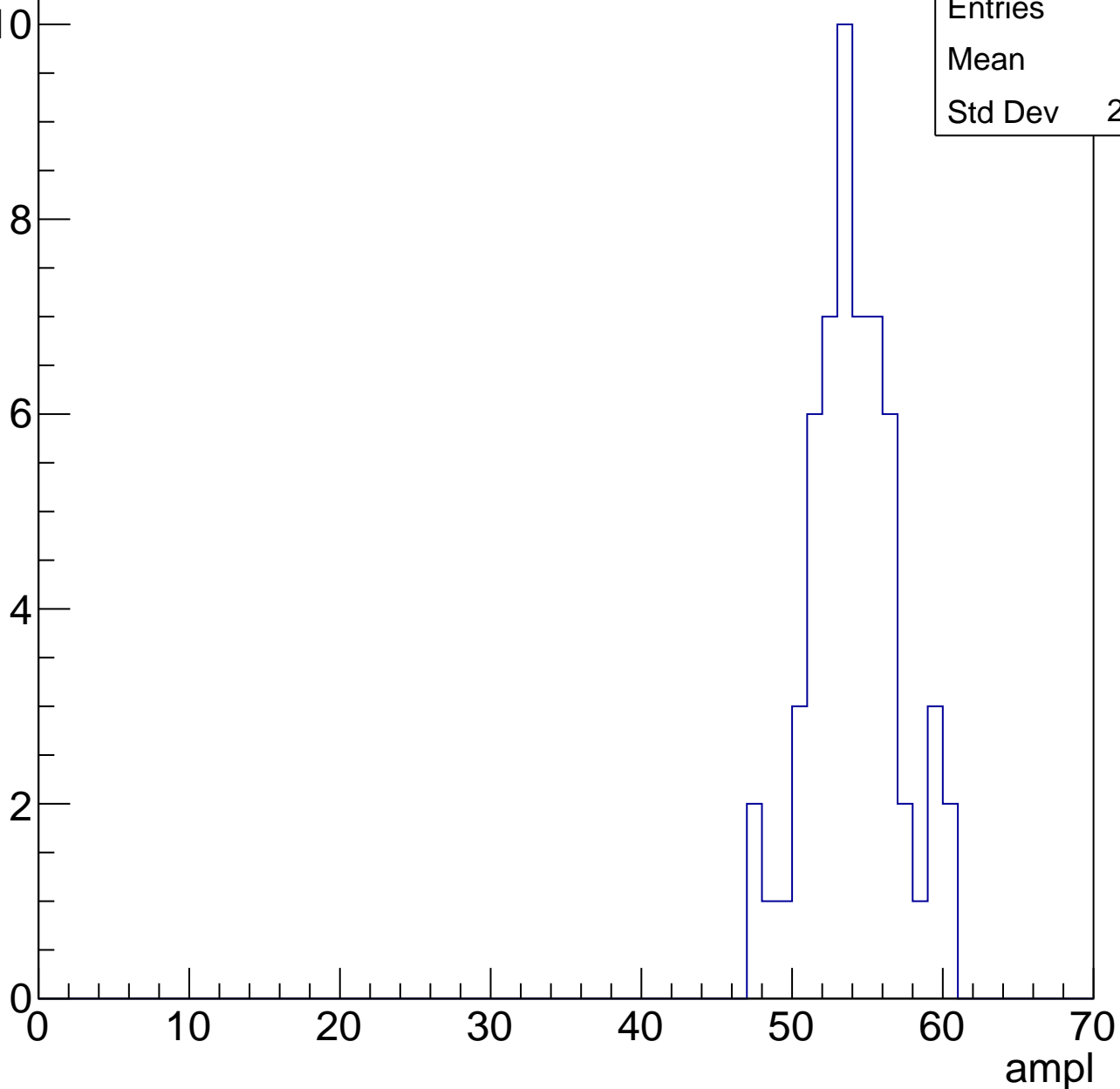


B1L102S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 53.6 |
| Std Dev | 2.942 |

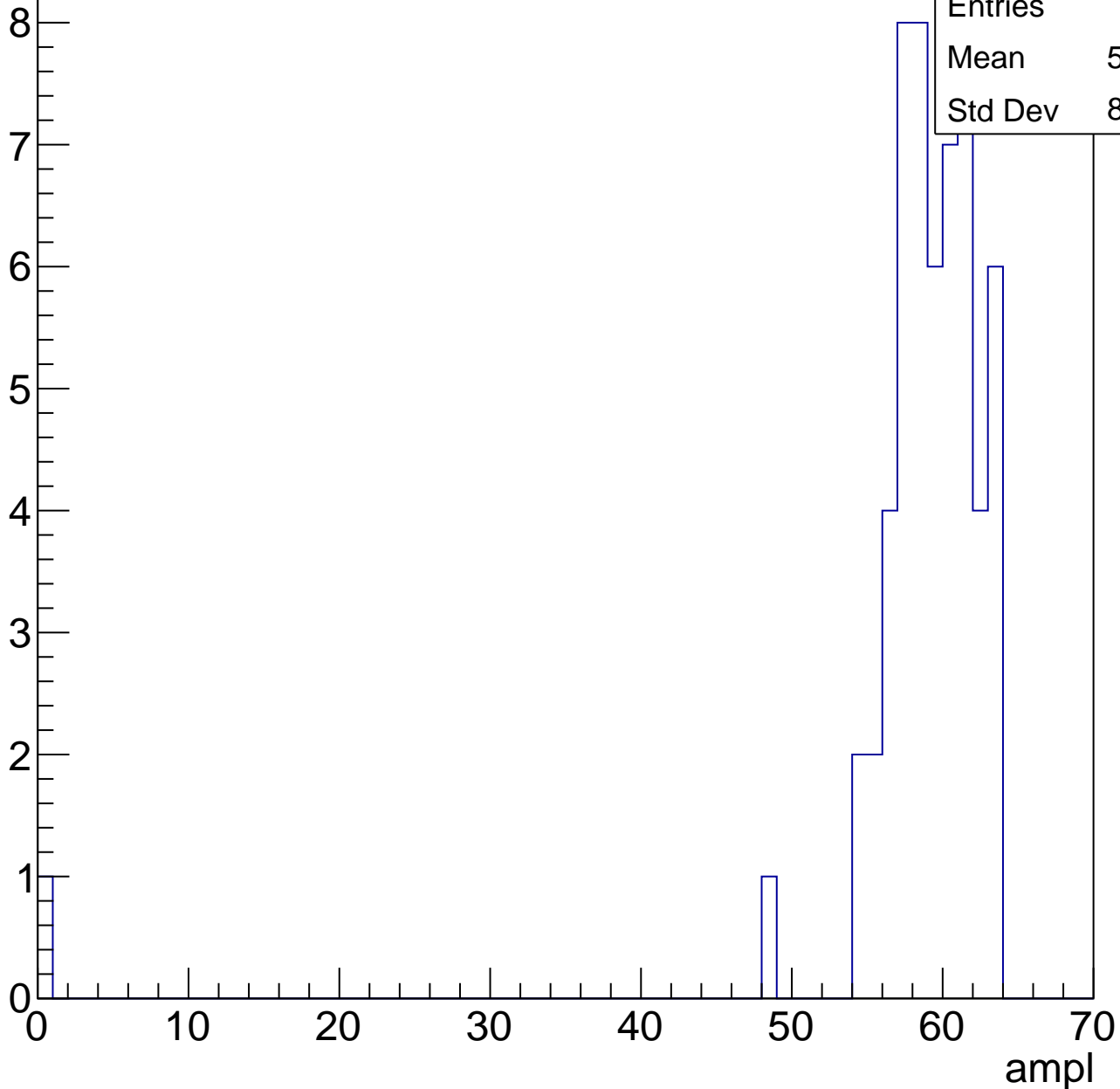


B1L102S, U15-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.86 |
| Std Dev | 8.226 |

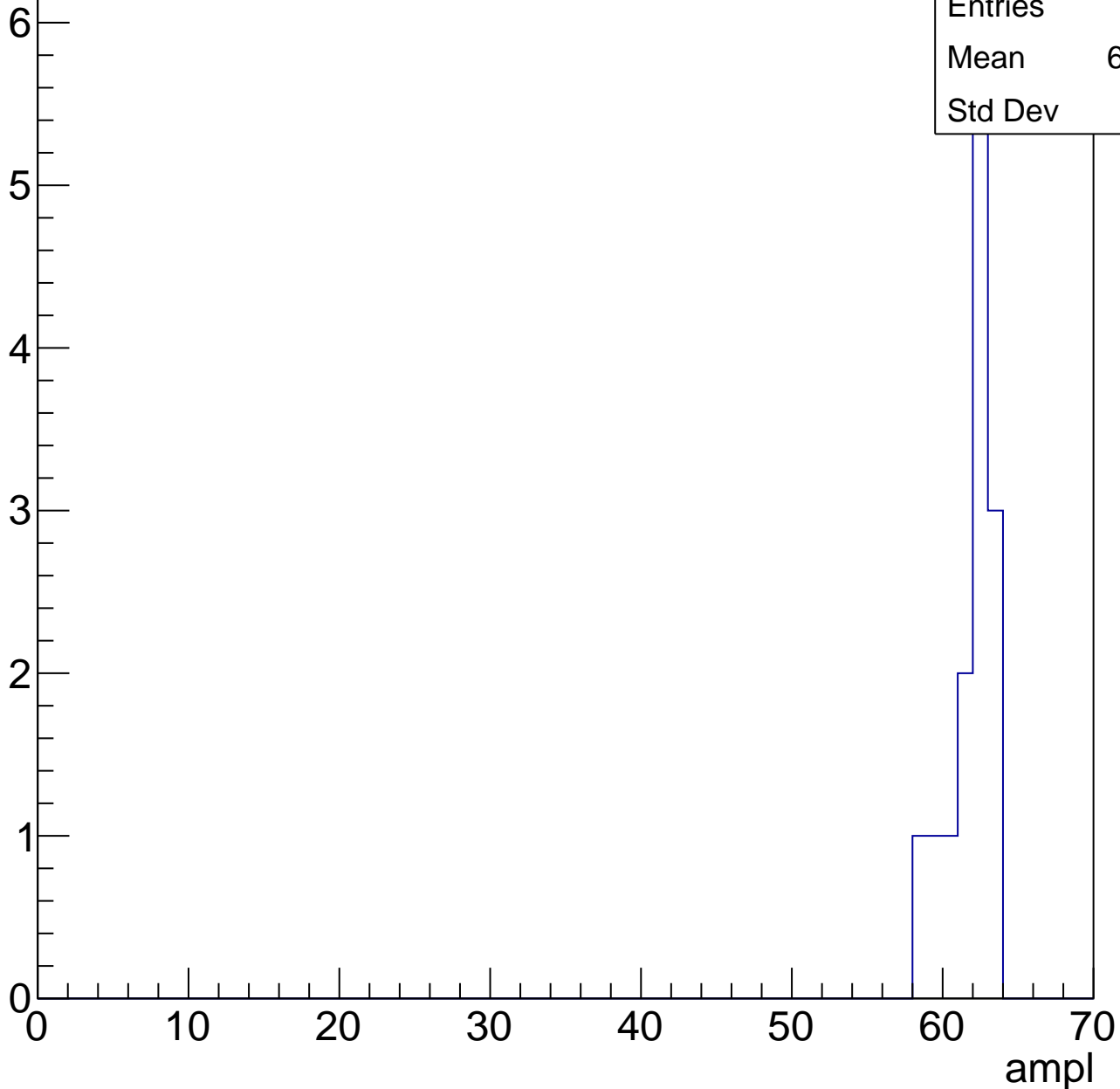


B1L102S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.43 |
| Std Dev | 1.45 |



B1L102S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch12, adc0

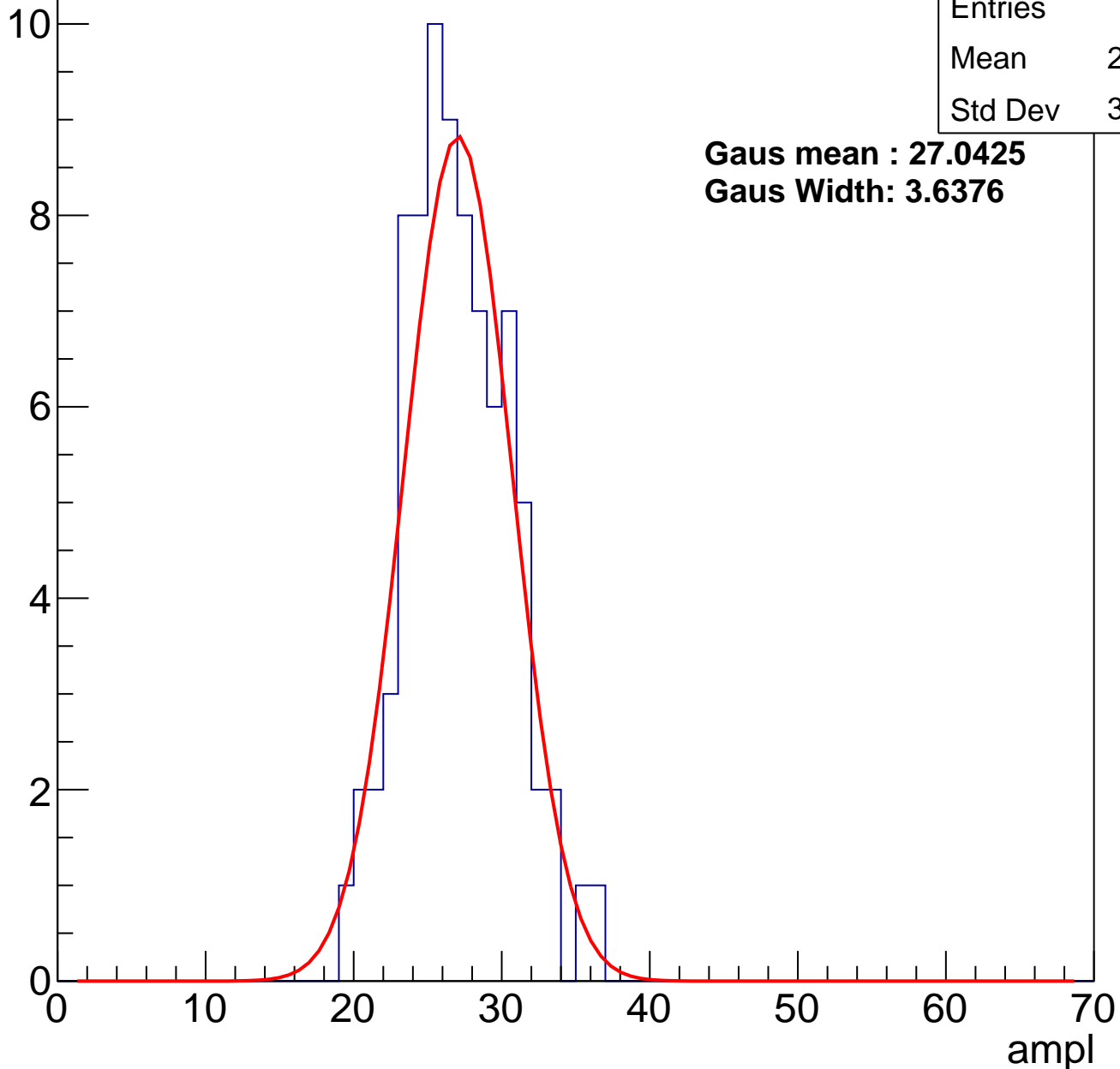
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 26.57 |
| Std Dev | 3.464 |

Gaus mean : 27.0425

Gaus Width: 3.6376

Entry



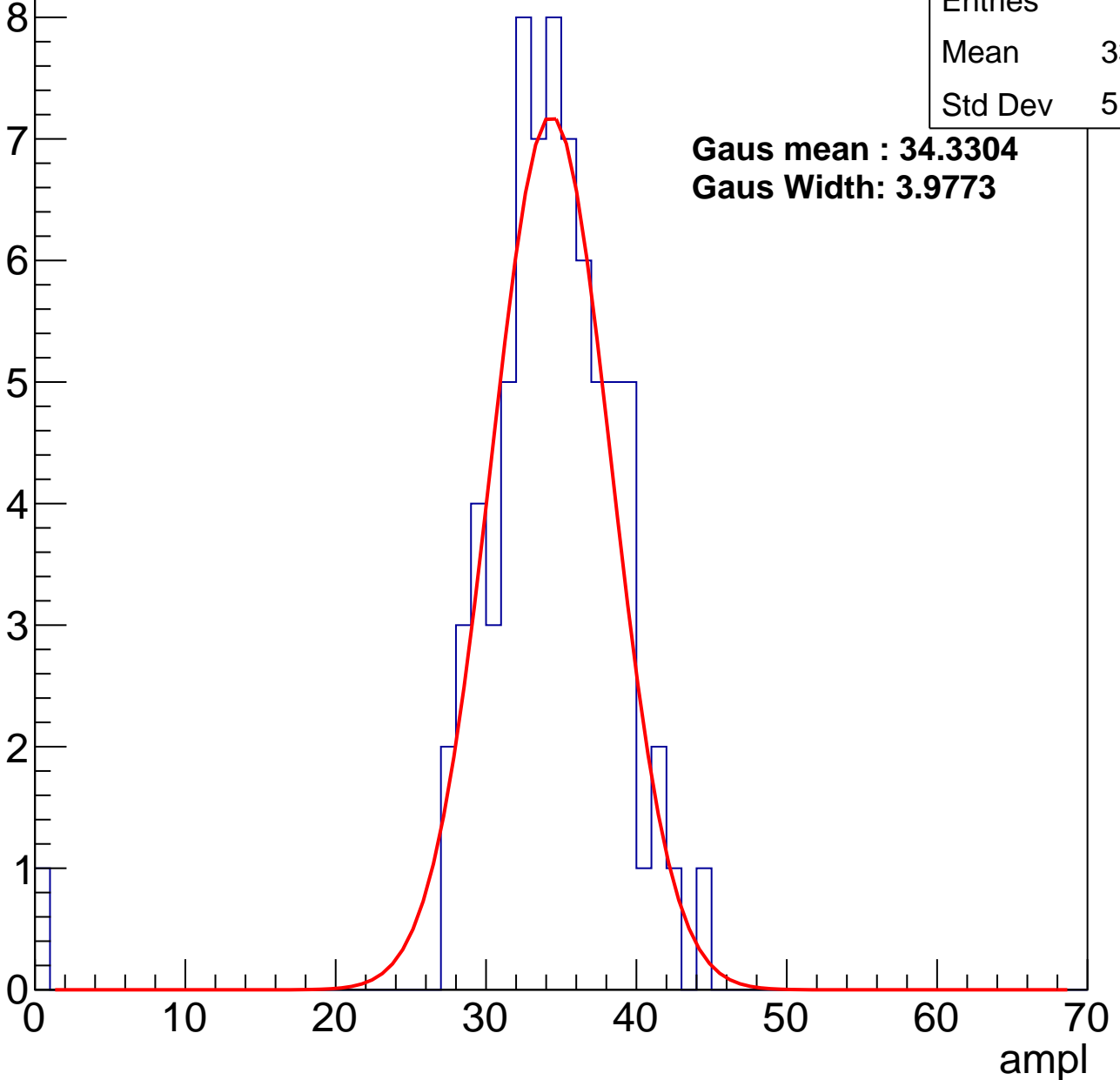
B1L102S, U15-ch12, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 33.74 |
| Std Dev | 5.413 |

Gaus mean : 34.3304
Gaus Width: 3.9773



B1L102S, U15-ch12, adc2

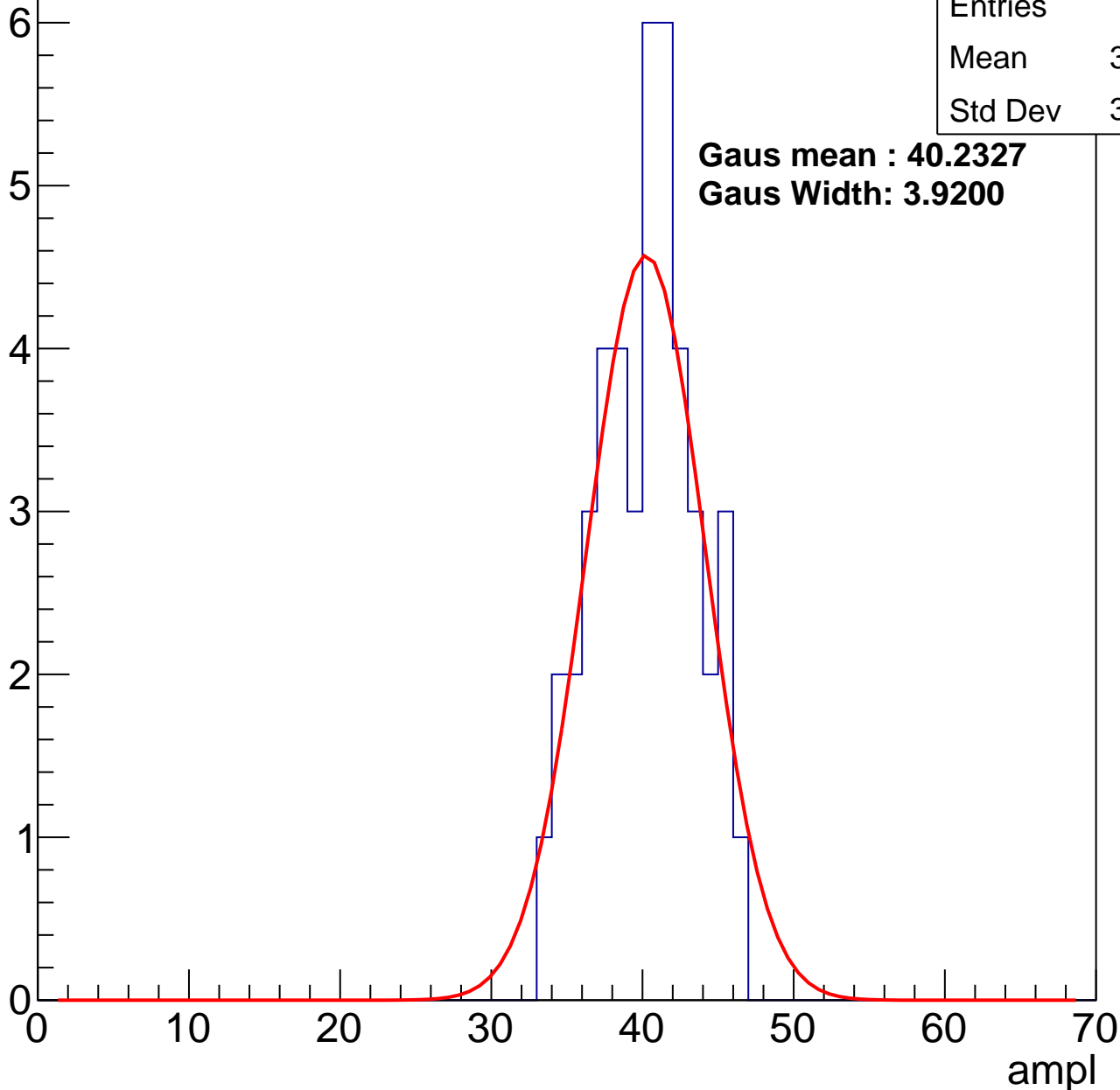
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 39.73 |
| Std Dev | 3.243 |

Gaus mean : 40.2327

Gaus Width: 3.9200

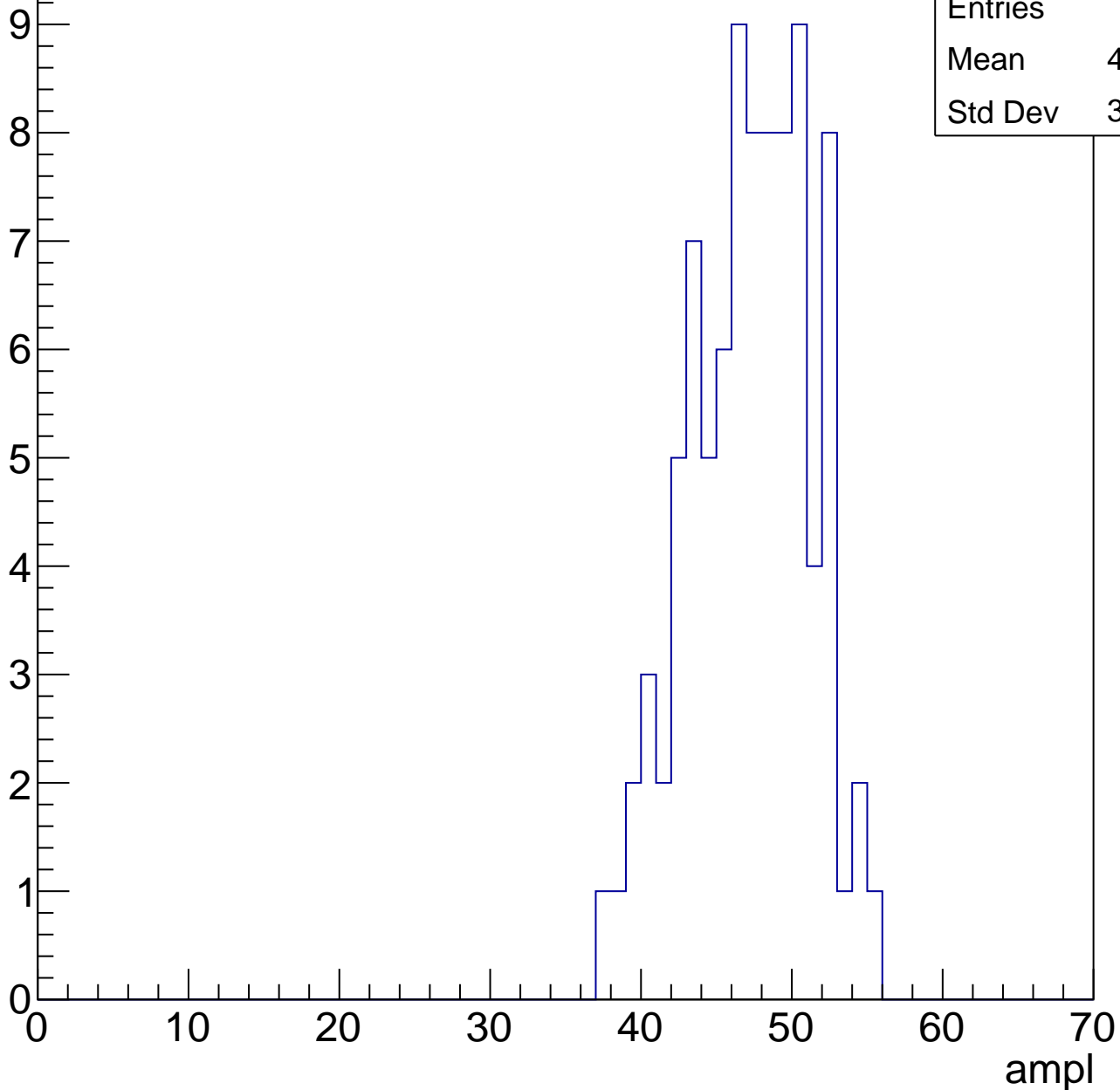


B1L102S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

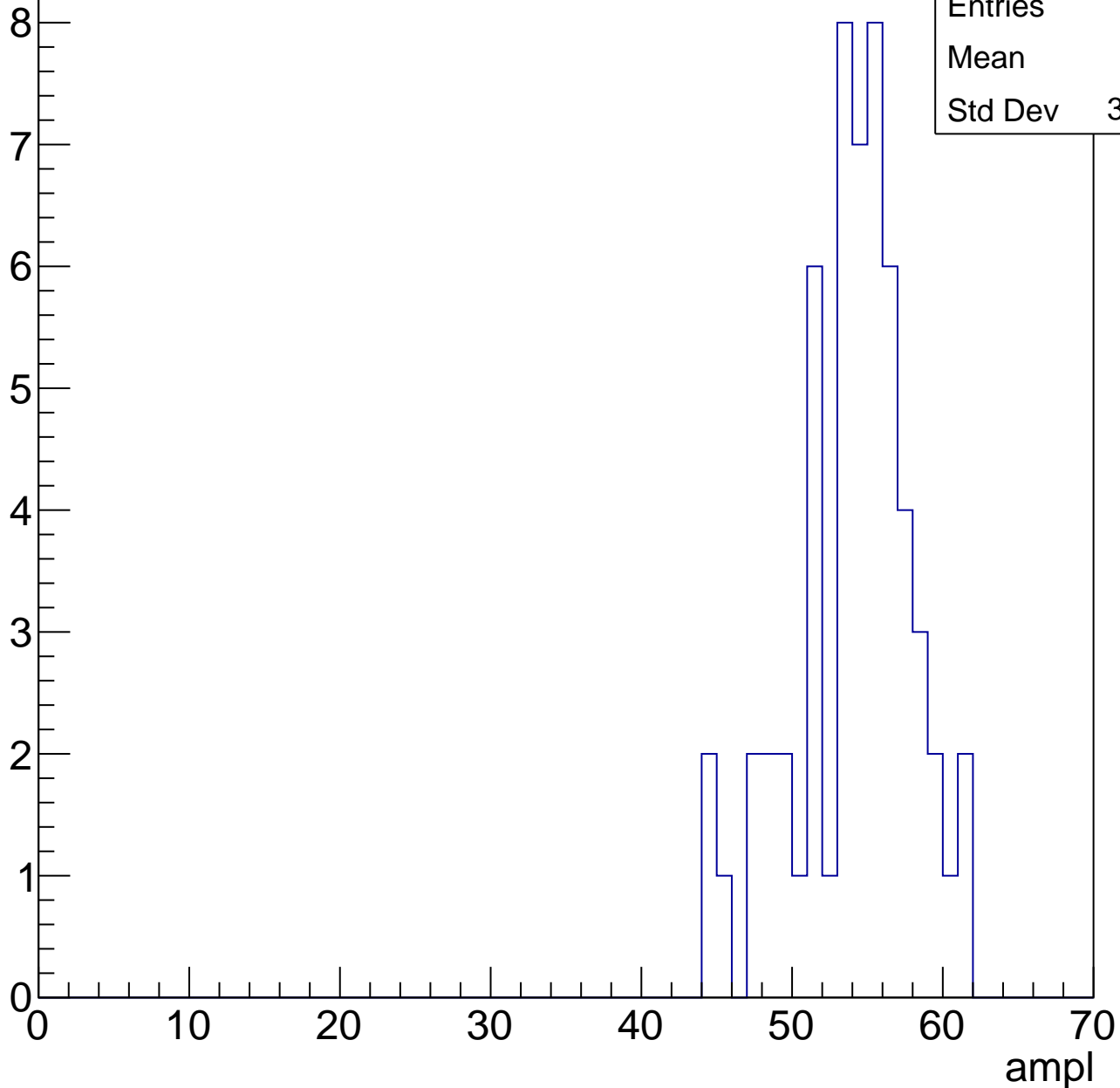
| | |
|---------|-------|
| Entries | 90 |
| Mean | 46.76 |
| Std Dev | 3.973 |



B1L102S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 47 |
| Mean | 58.98 |
| Std Dev | 2.589 |

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

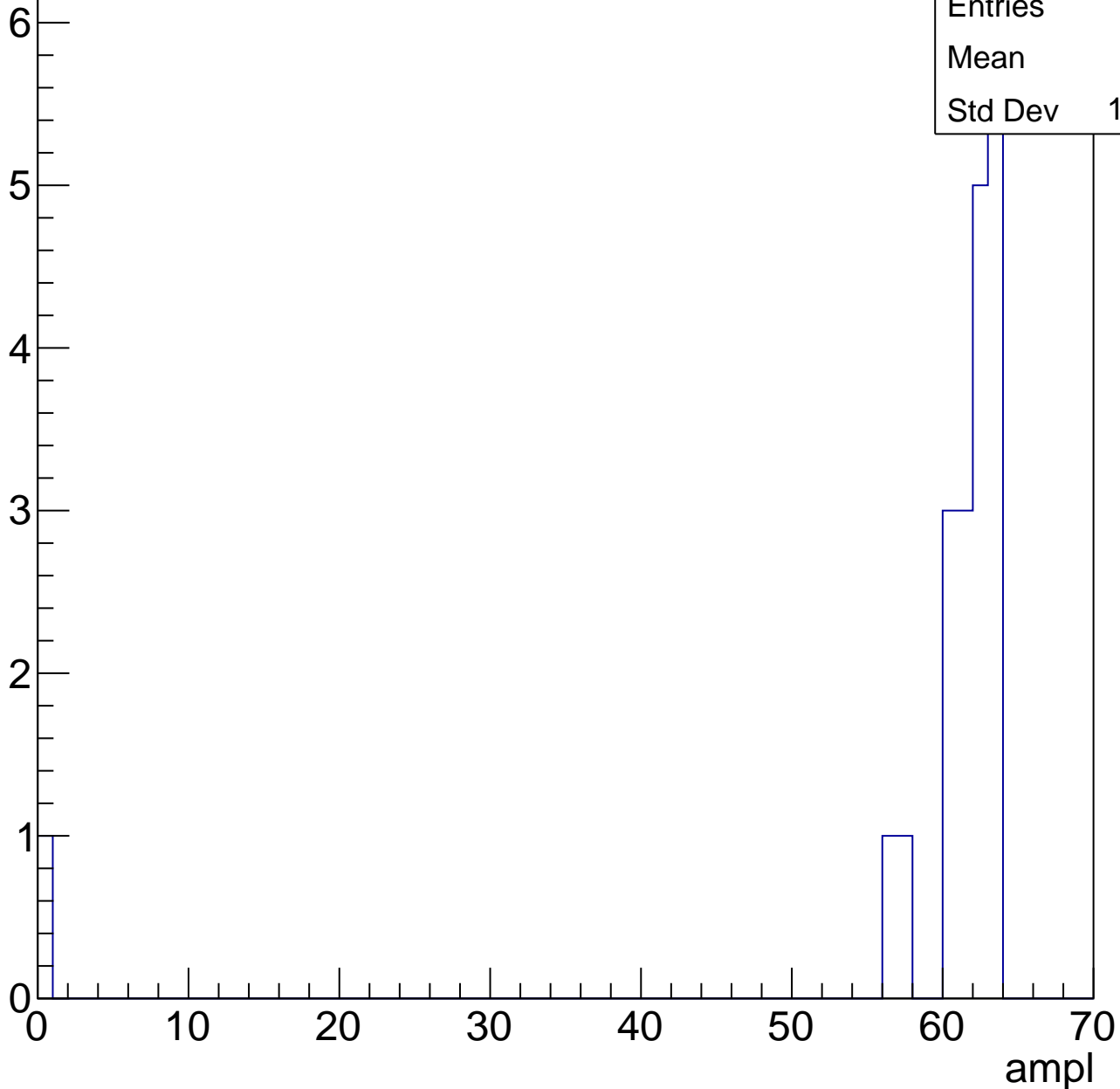
8

B1L102S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 58.2 |
| Std Dev | 13.49 |



B1L102S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch13, adc0

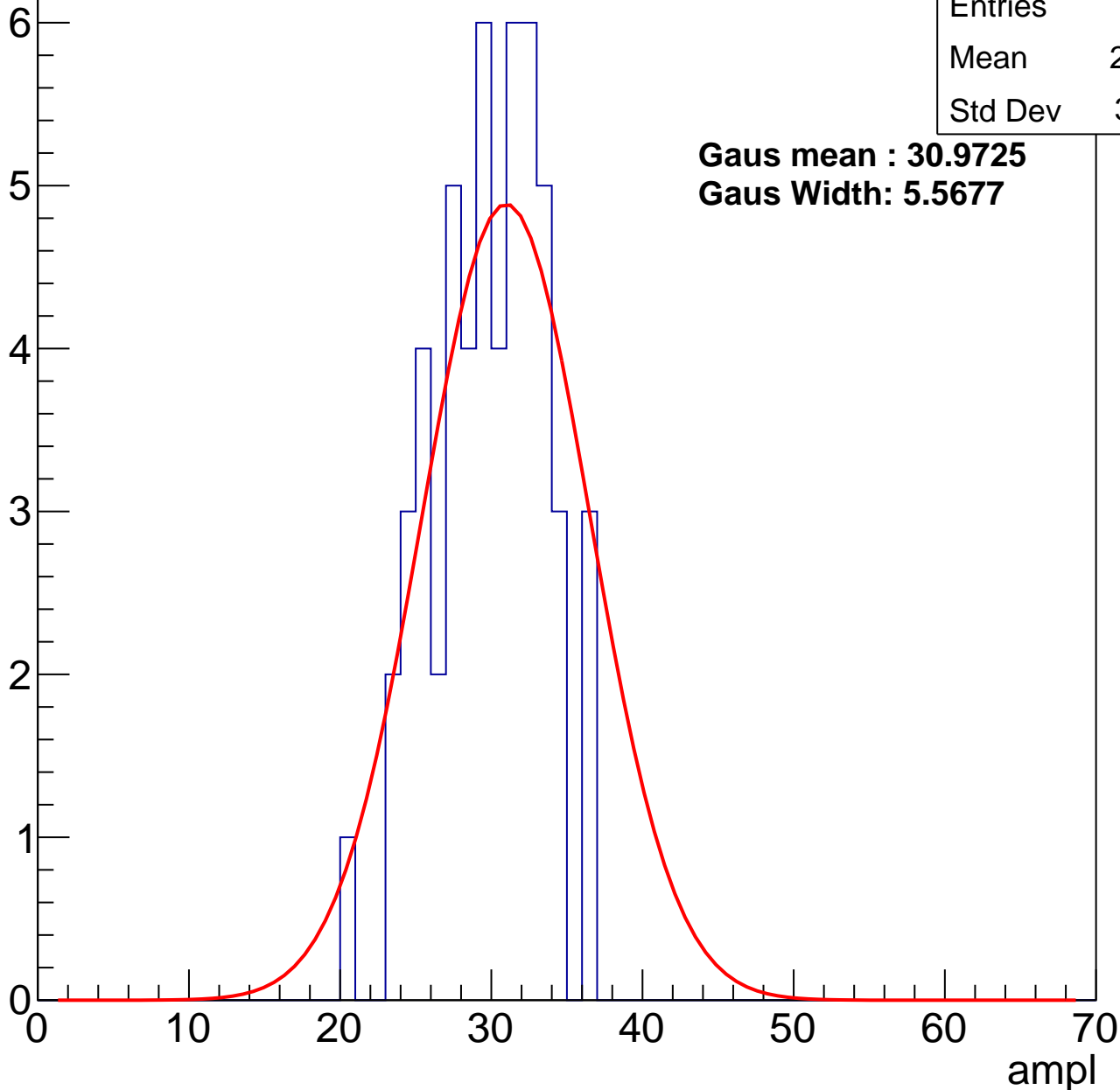
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 29.33 |
| Std Dev | 3.631 |

Gaus mean : 30.9725

Gaus Width: 5.5677



B1L102S, U15-ch13, adc1

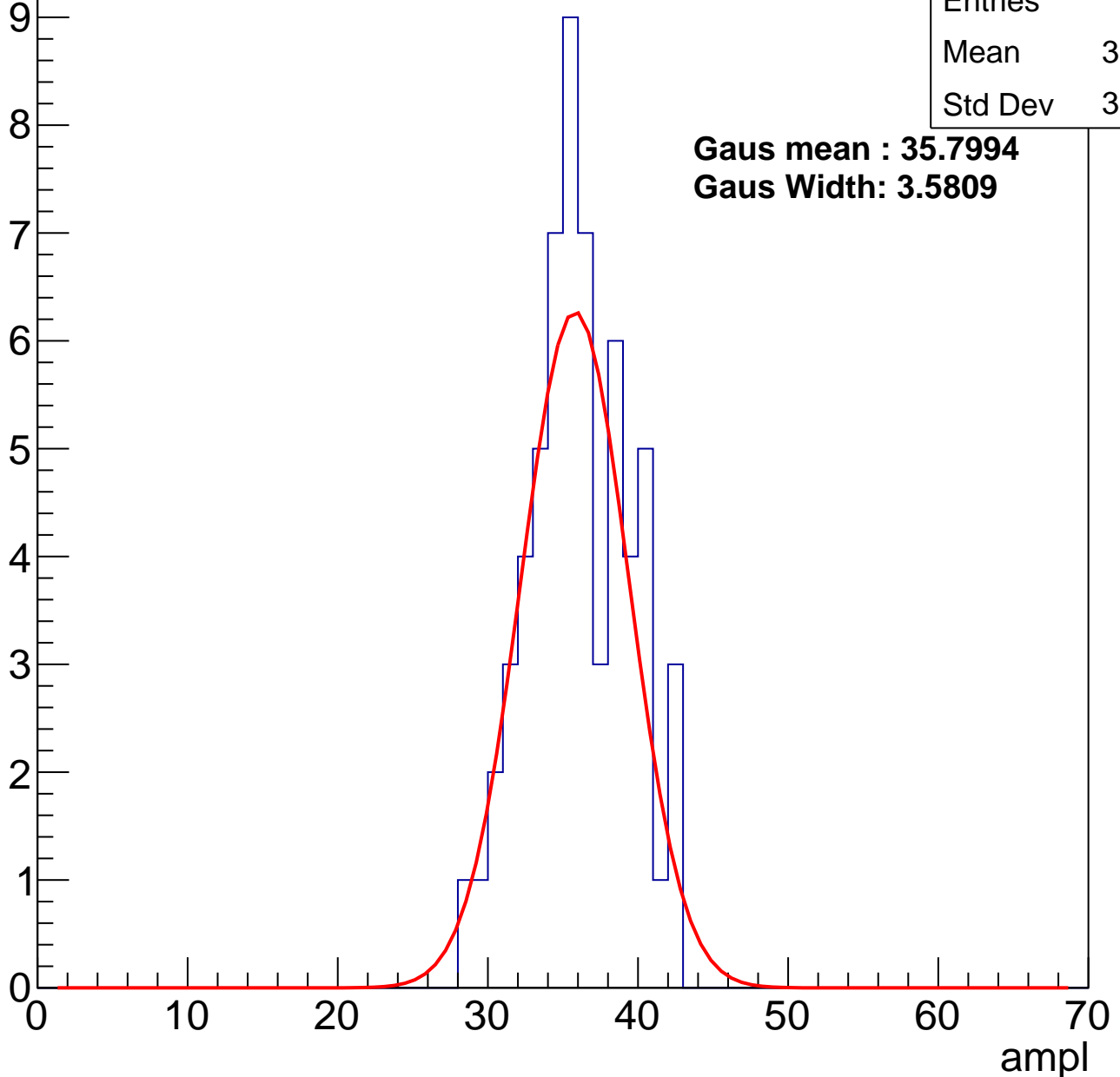
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.57 |
| Std Dev | 3.316 |

Gaus mean : 35.7994

Gaus Width: 3.5809



B1L102S, U15-ch13, adc2

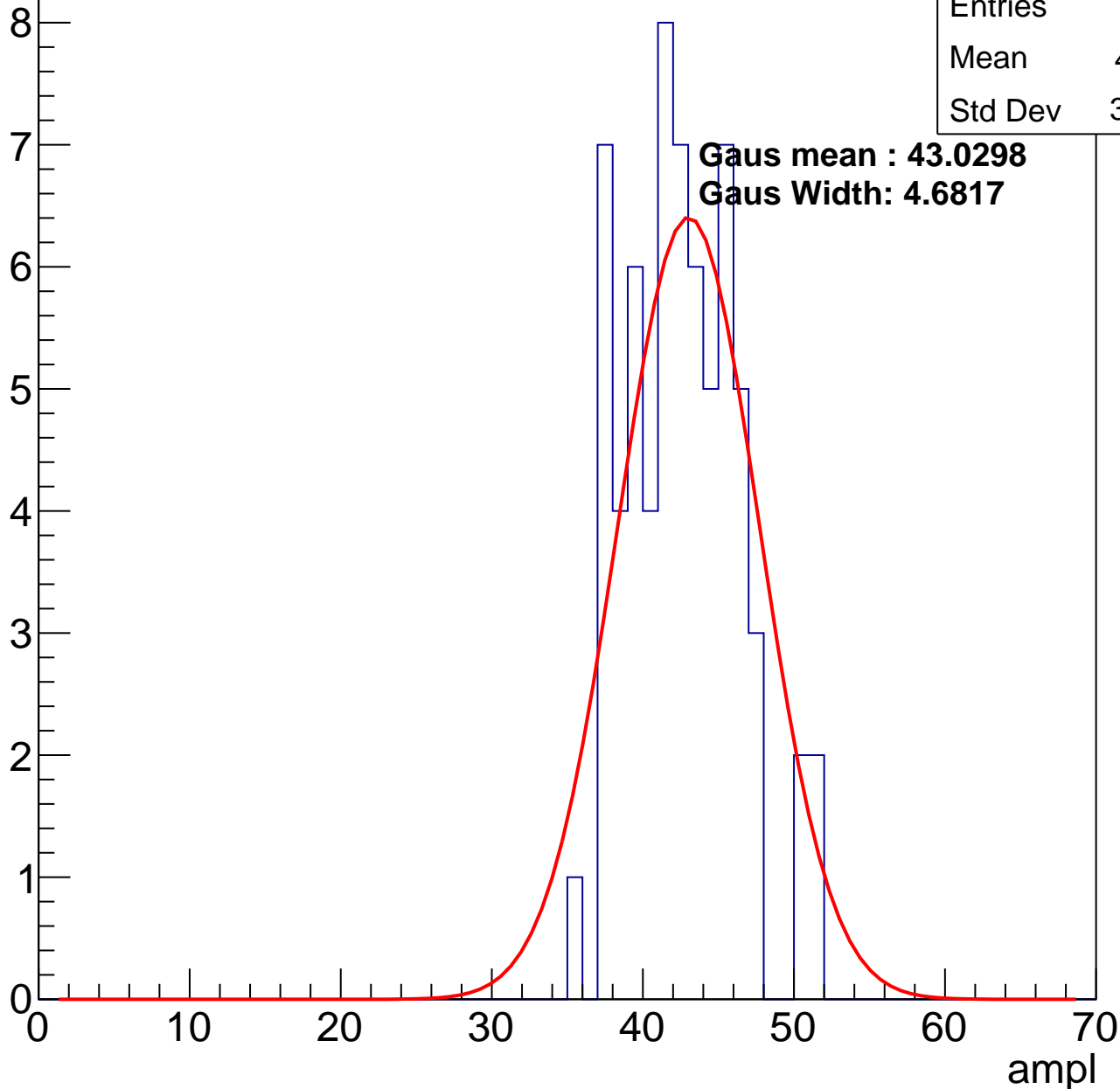
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 42.21 |
| Std Dev | 3.659 |

Gaus mean : 43.0298

Gaus Width: 4.6817



B1L102S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

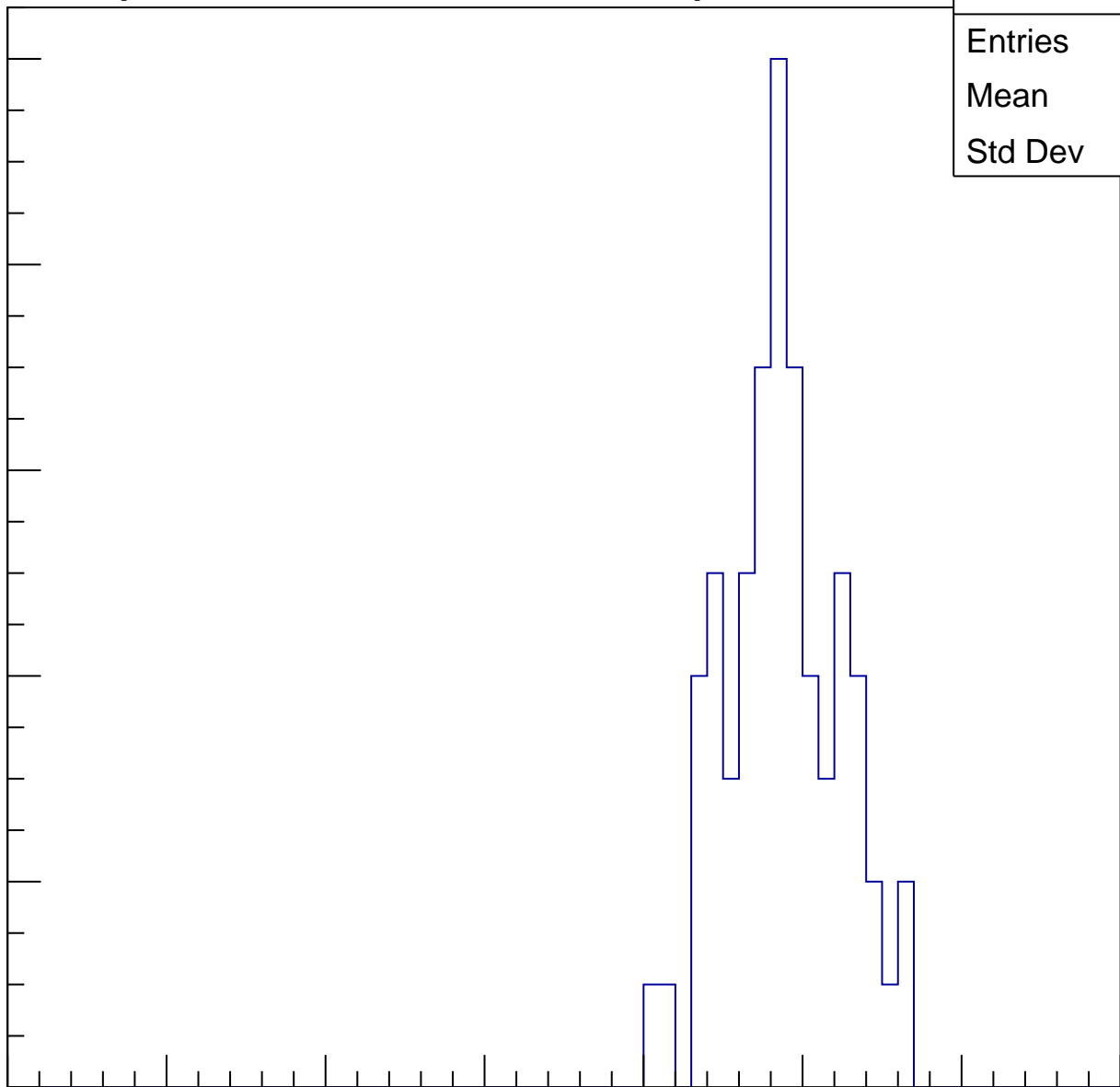
| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.28 |
| Std Dev | 3.582 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

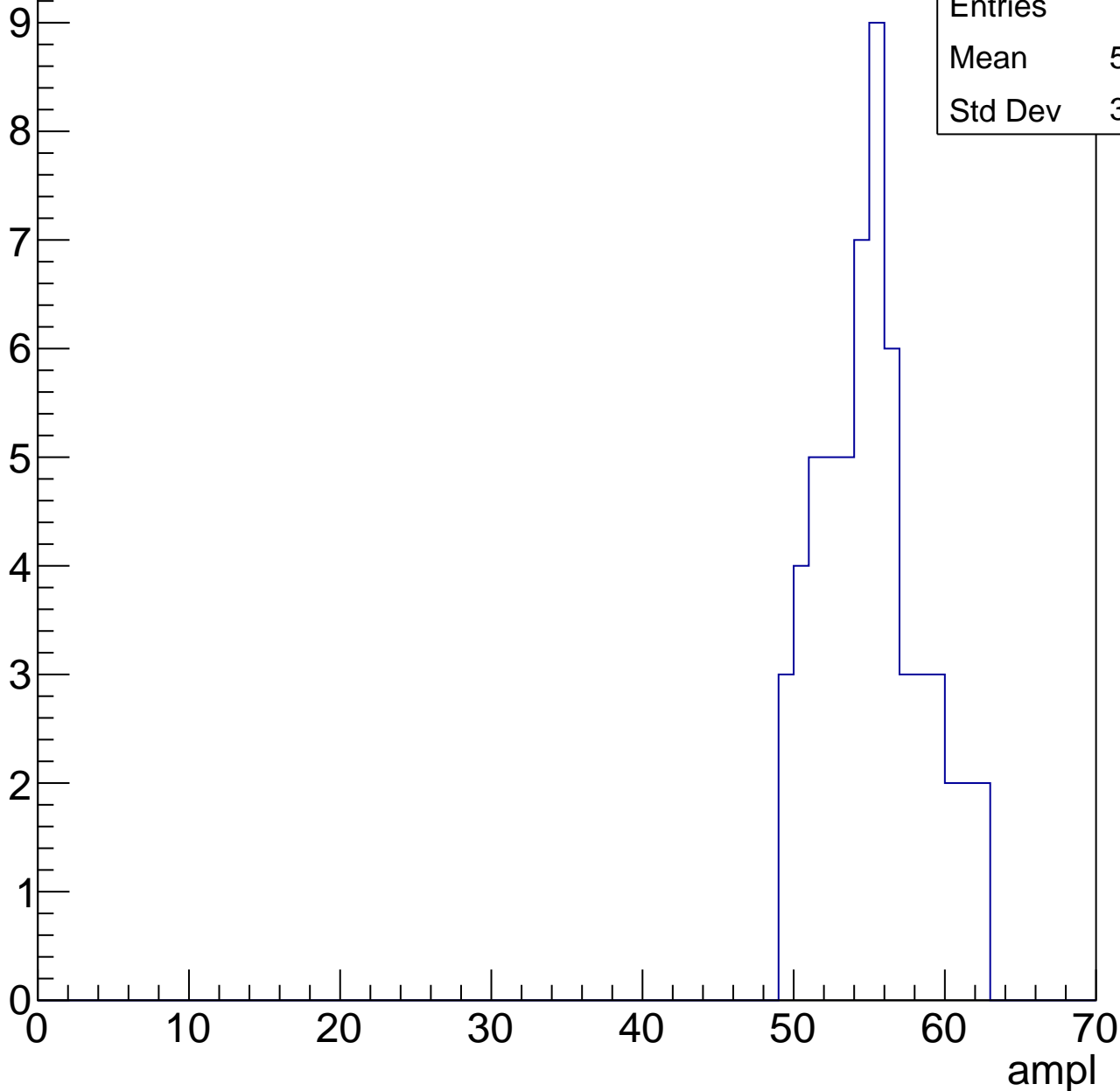


B1L102S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.64 |
| Std Dev | 3.359 |

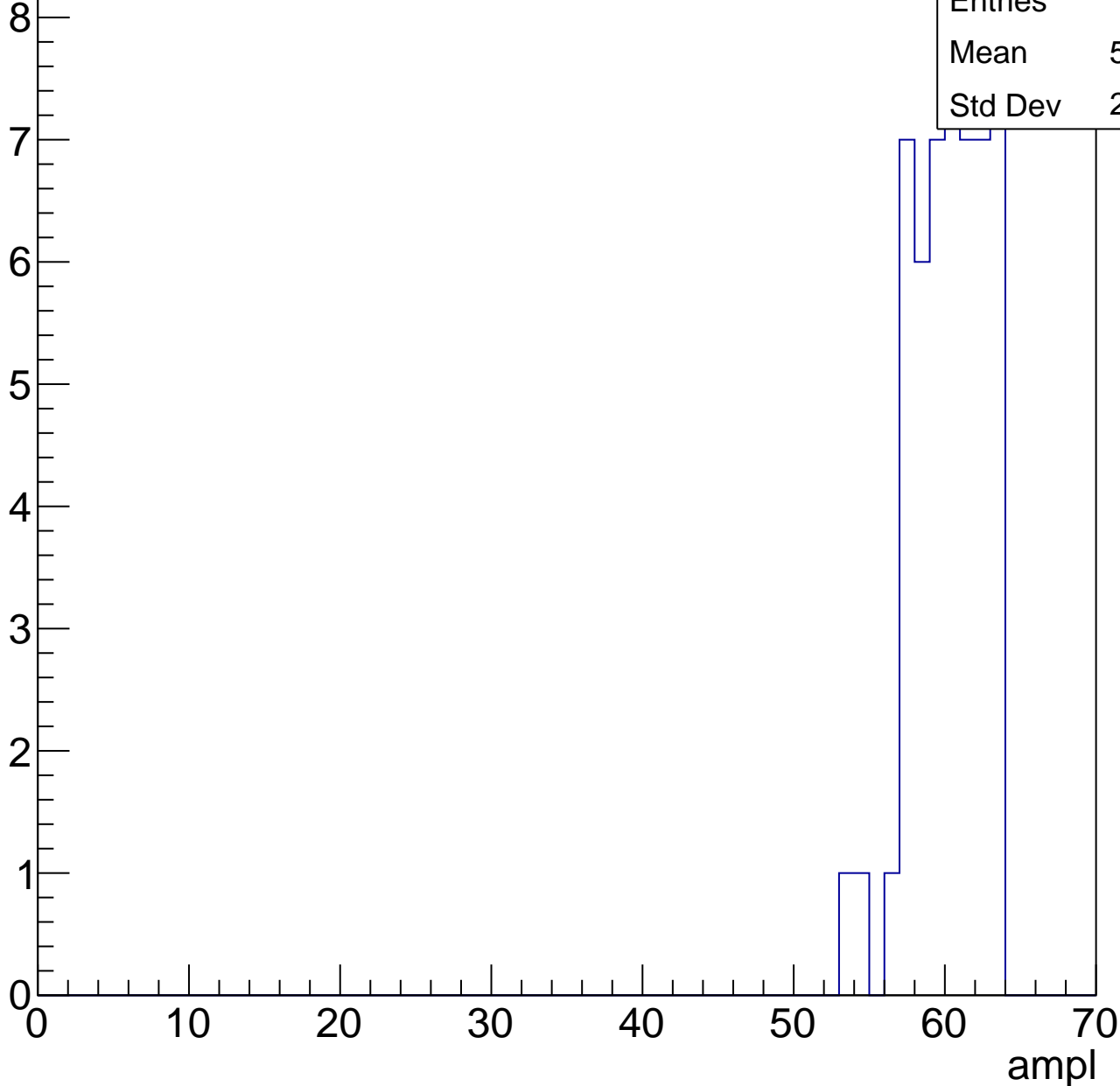


B1L102S, U15-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

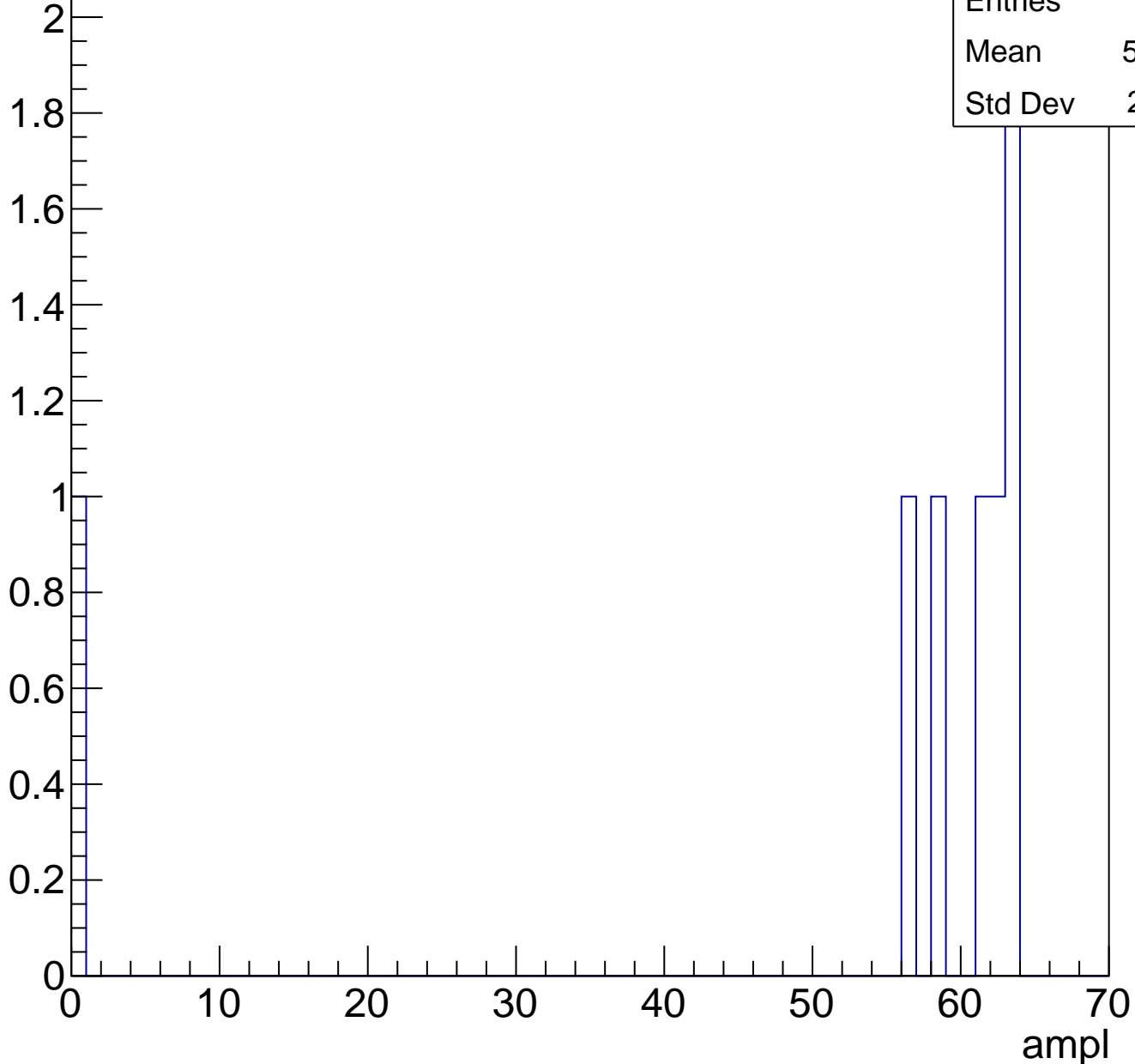
| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.77 |
| Std Dev | 2.376 |



B1L102S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

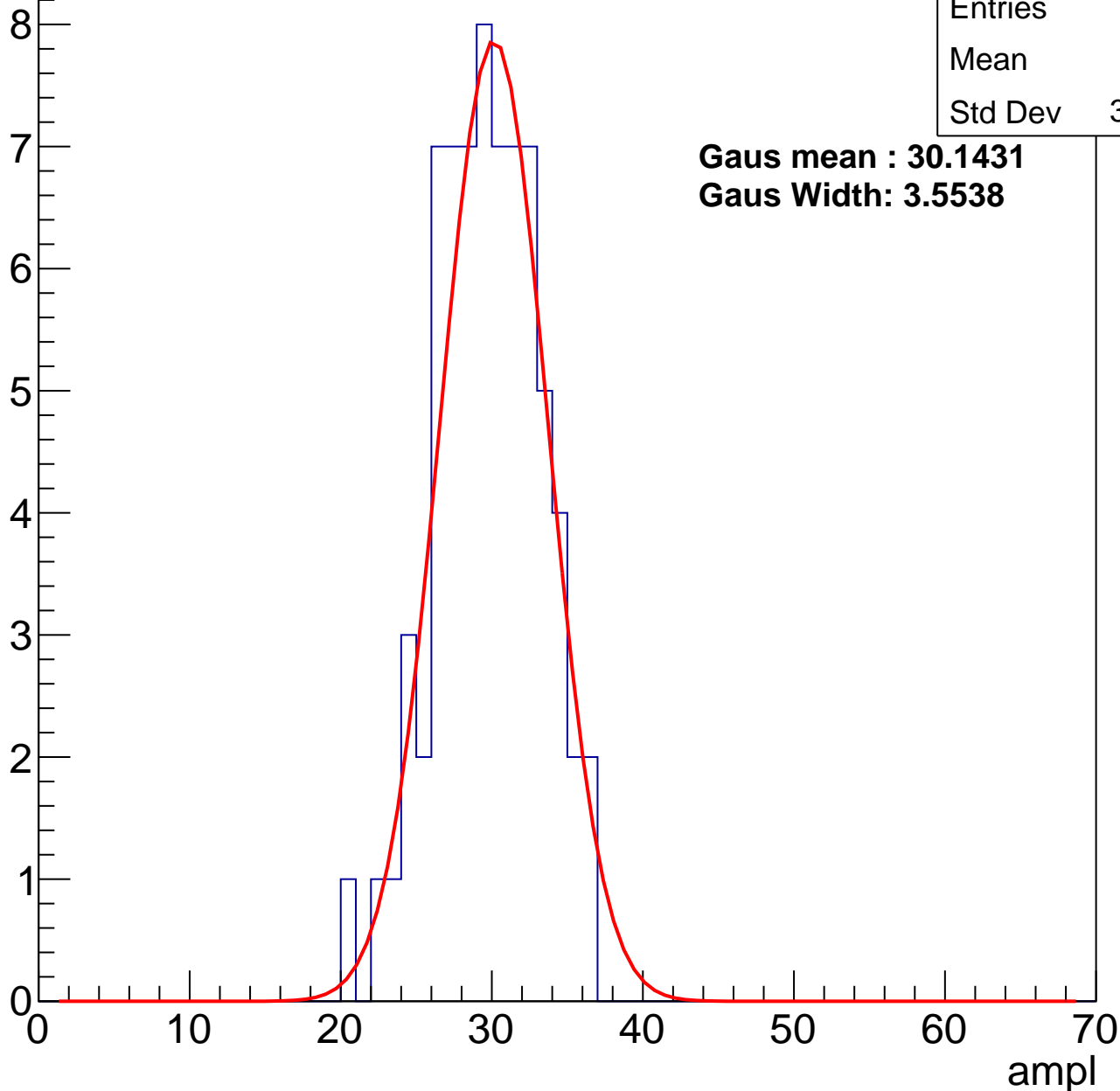
B1L102S, U15-ch14, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 29.3 |
| Std Dev | 3.383 |

Gaus mean : 30.1431
Gaus Width: 3.5538



B1L102S, U15-ch14, adc1

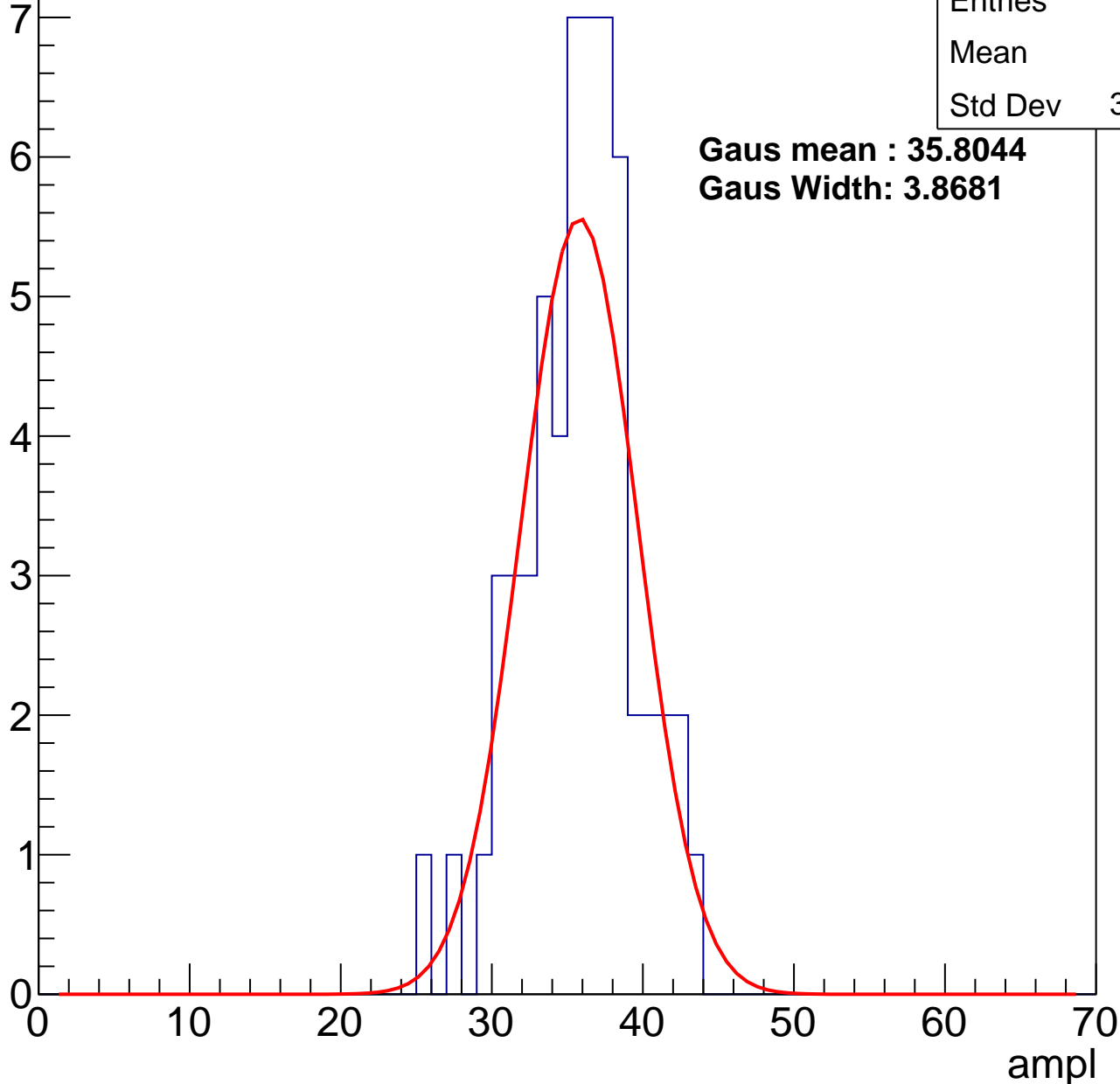
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 35.3 |
| Std Dev | 3.685 |

Gaus mean : 35.8044

Gaus Width: 3.8681



B1L102S, U15-ch14, adc2

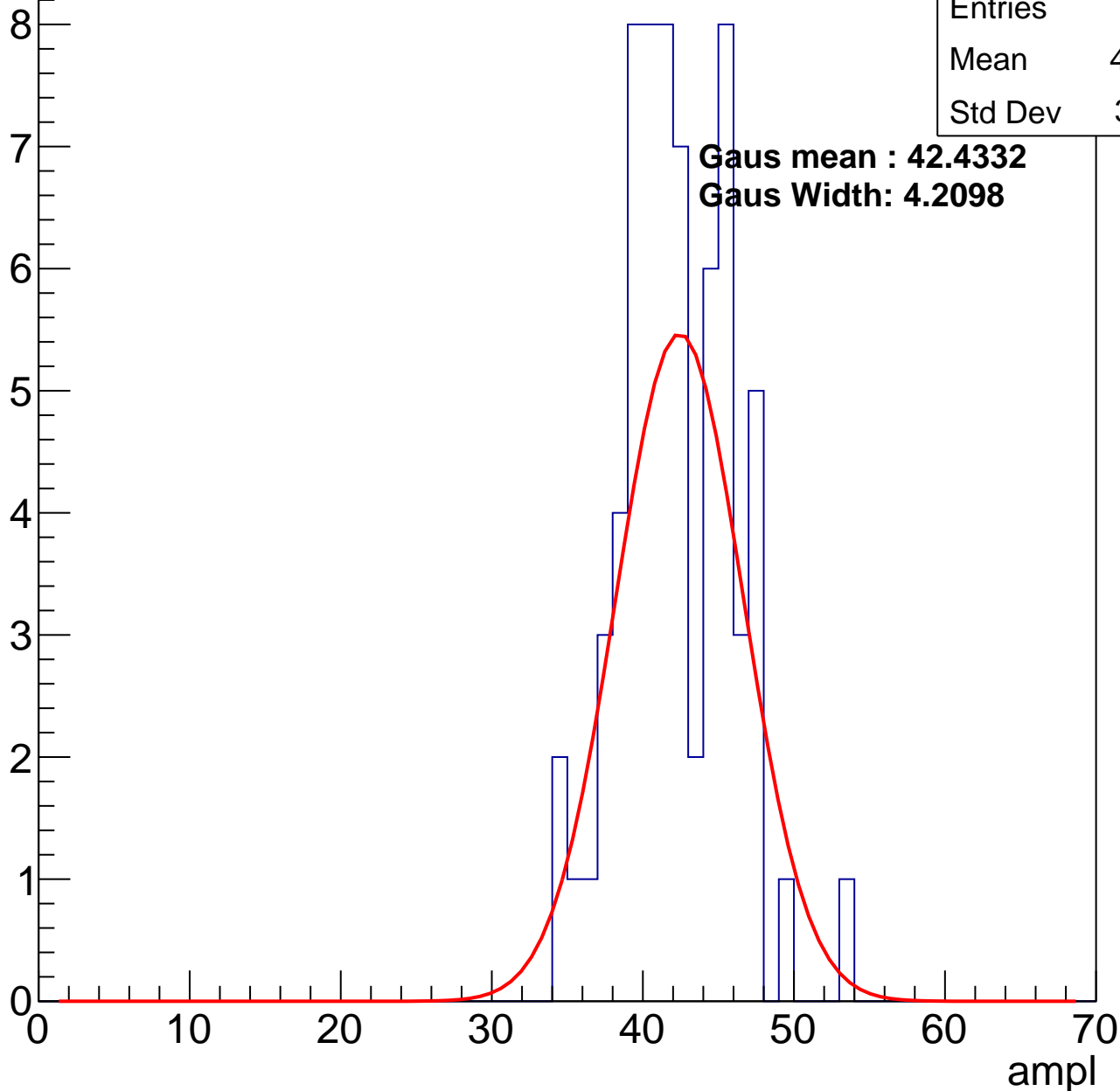
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 41.78 |
| Std Dev | 3.641 |

Gaus mean : 42.4332

Gaus Width: 4.2098

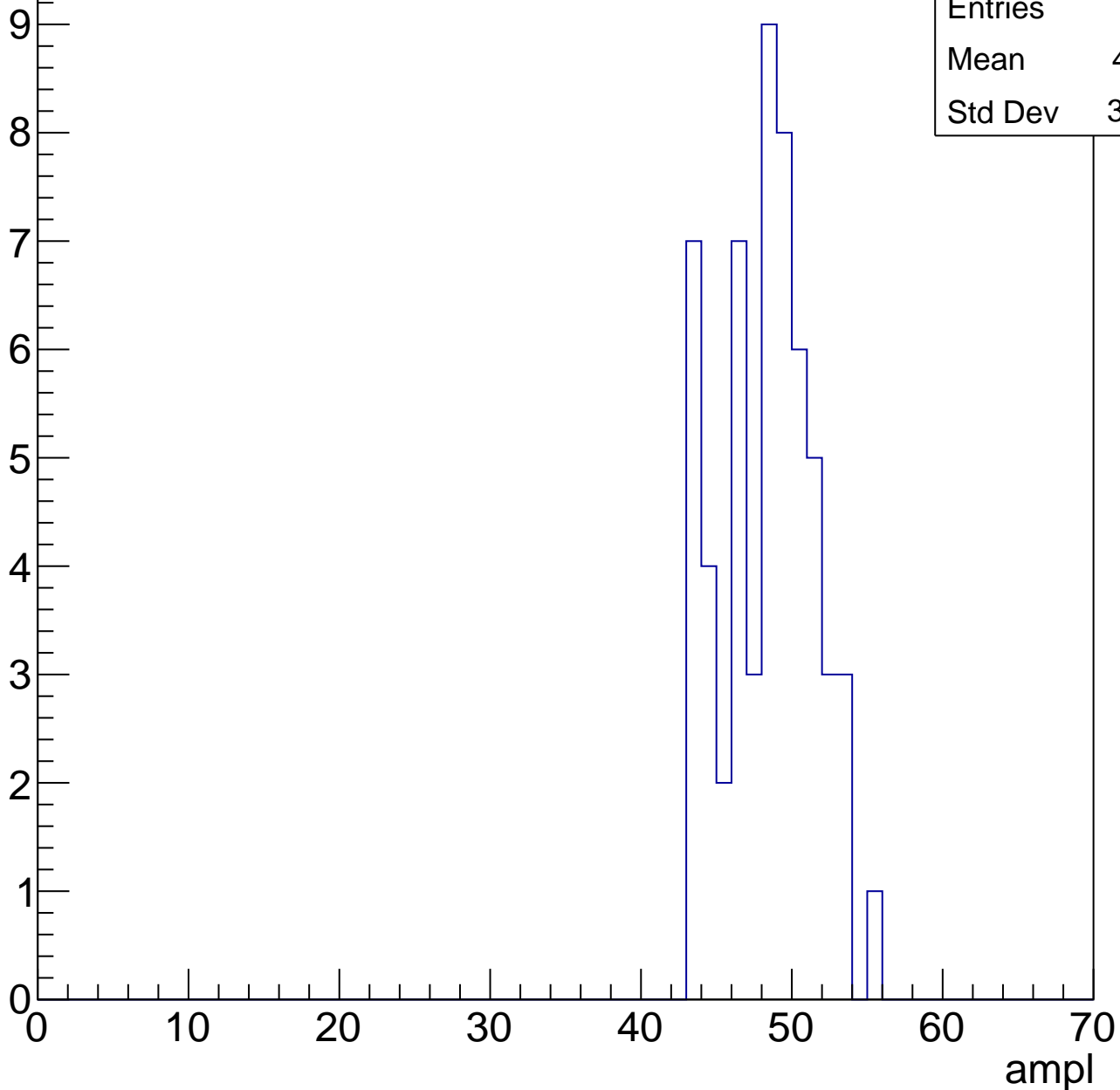


B1L102S, U15-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 47.91 |
| Std Dev | 3.042 |

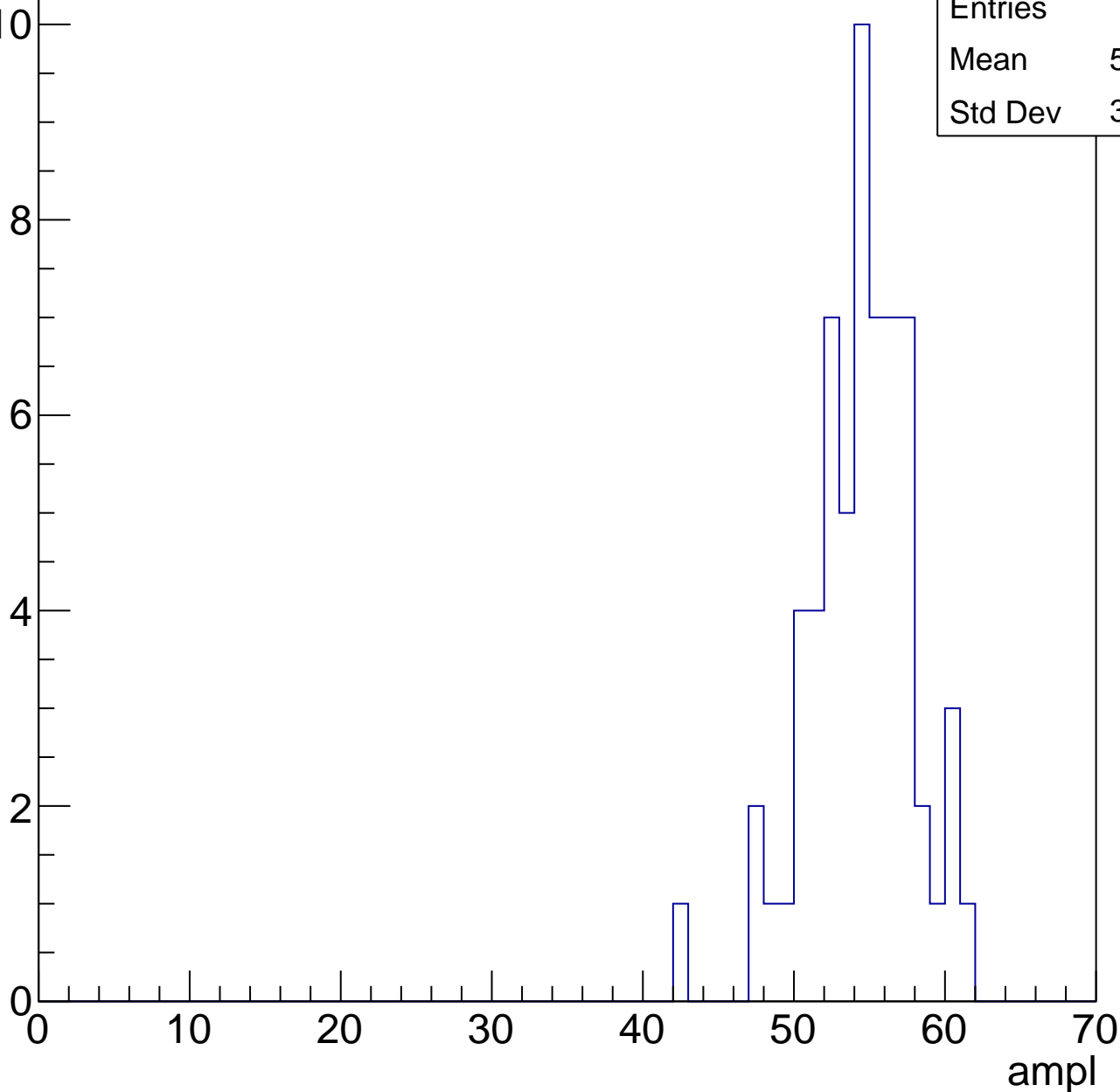


B1L102S, U15-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 53.94 |
| Std Dev | 3.445 |

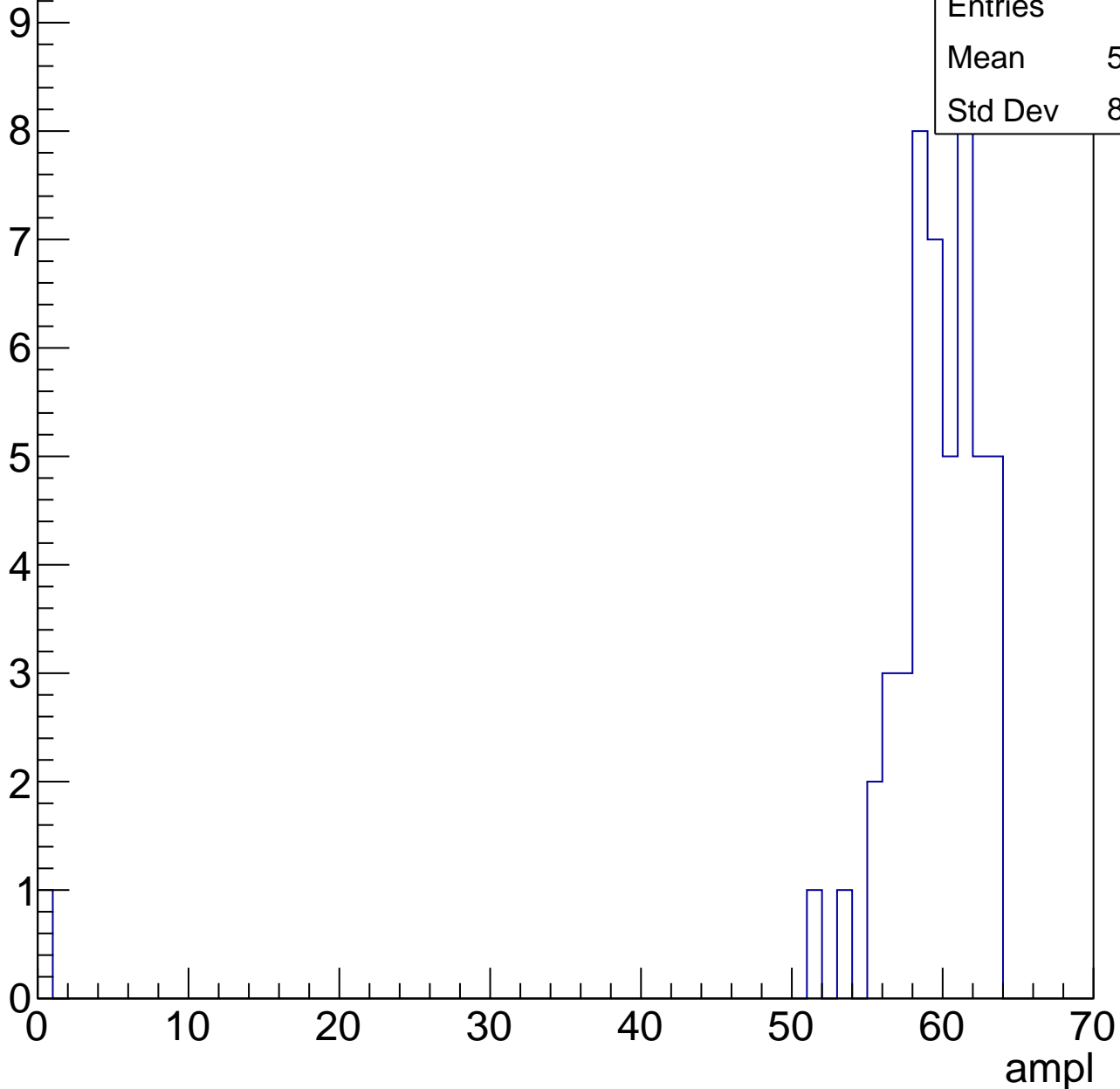


B1L102S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.08 |
| Std Dev | 8.699 |

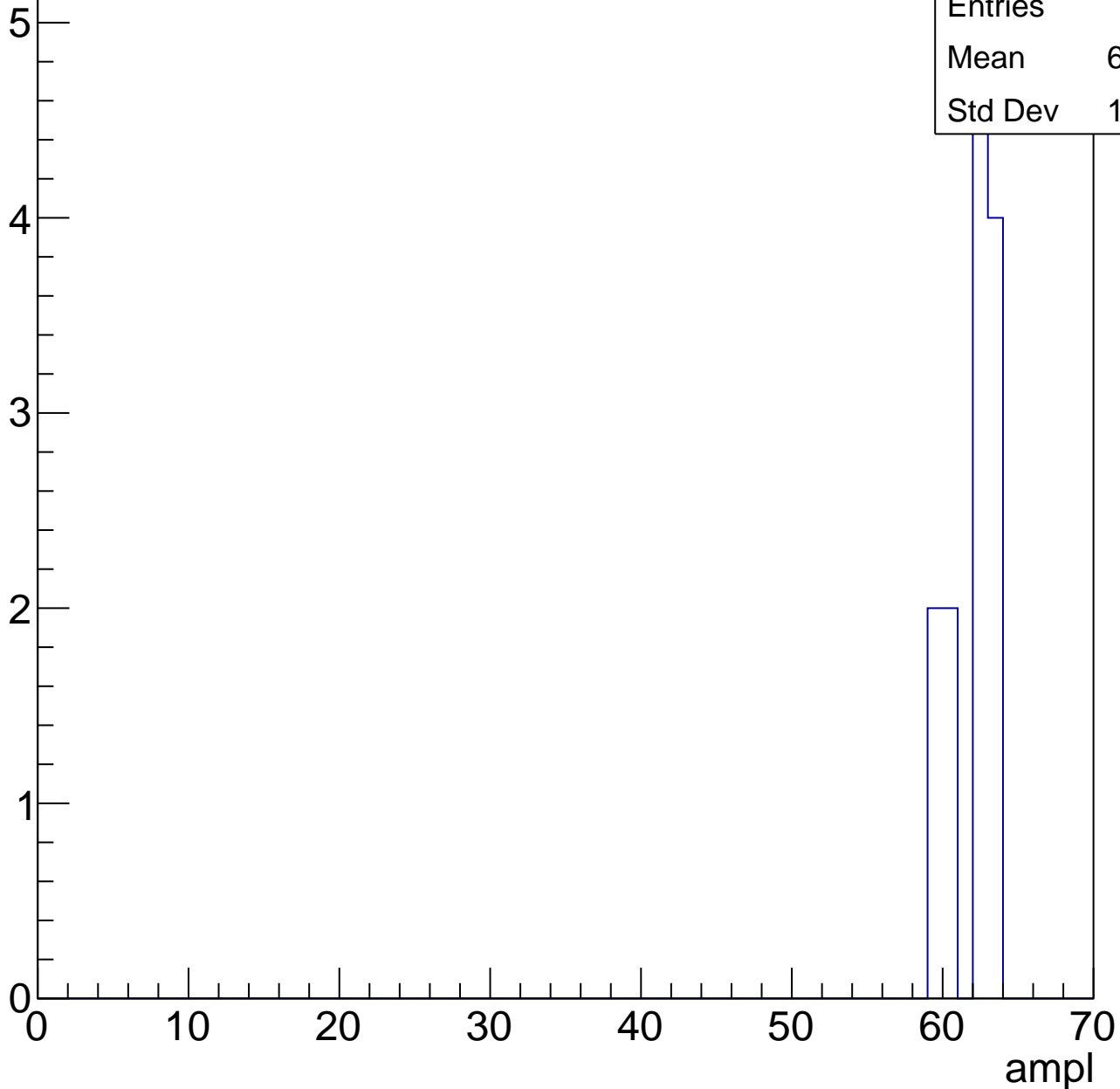


B1L102S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

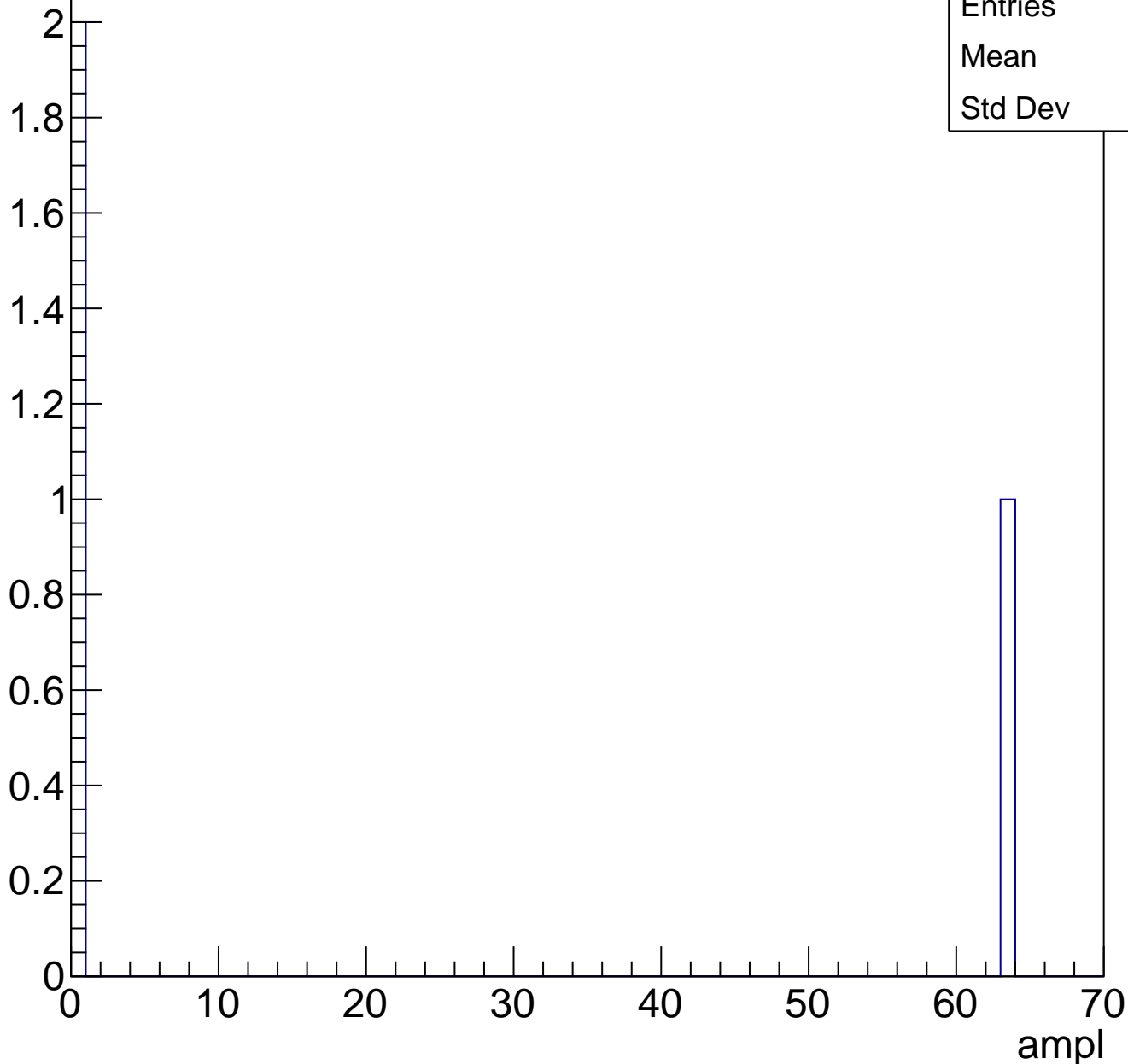
| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.54 |
| Std Dev | 1.447 |



B1L102S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch15, adc0

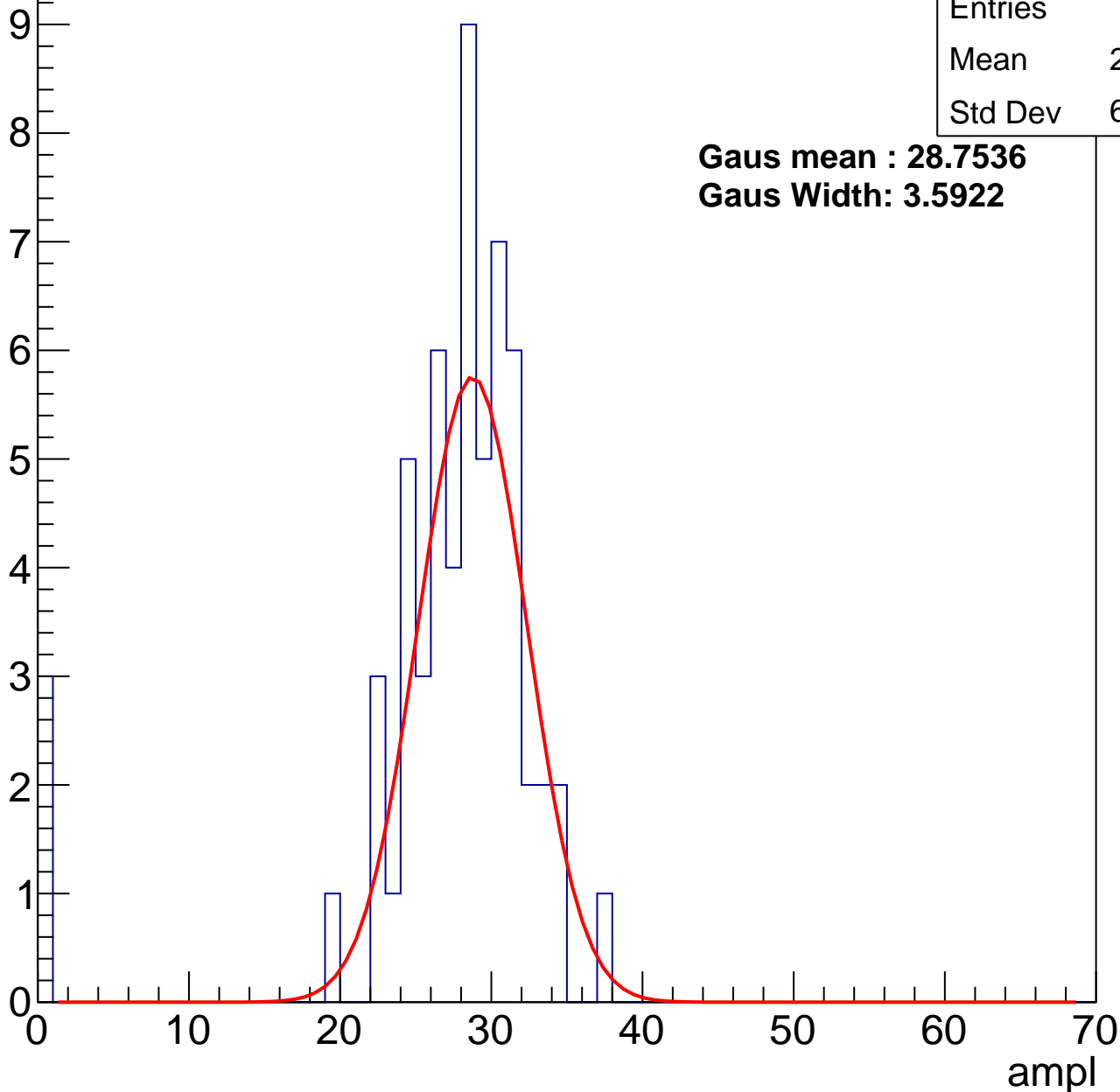
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 26.58 |
| Std Dev | 6.955 |

Gaus mean : 28.7536

Gaus Width: 3.5922



B1L102S, U15-ch15, adc1

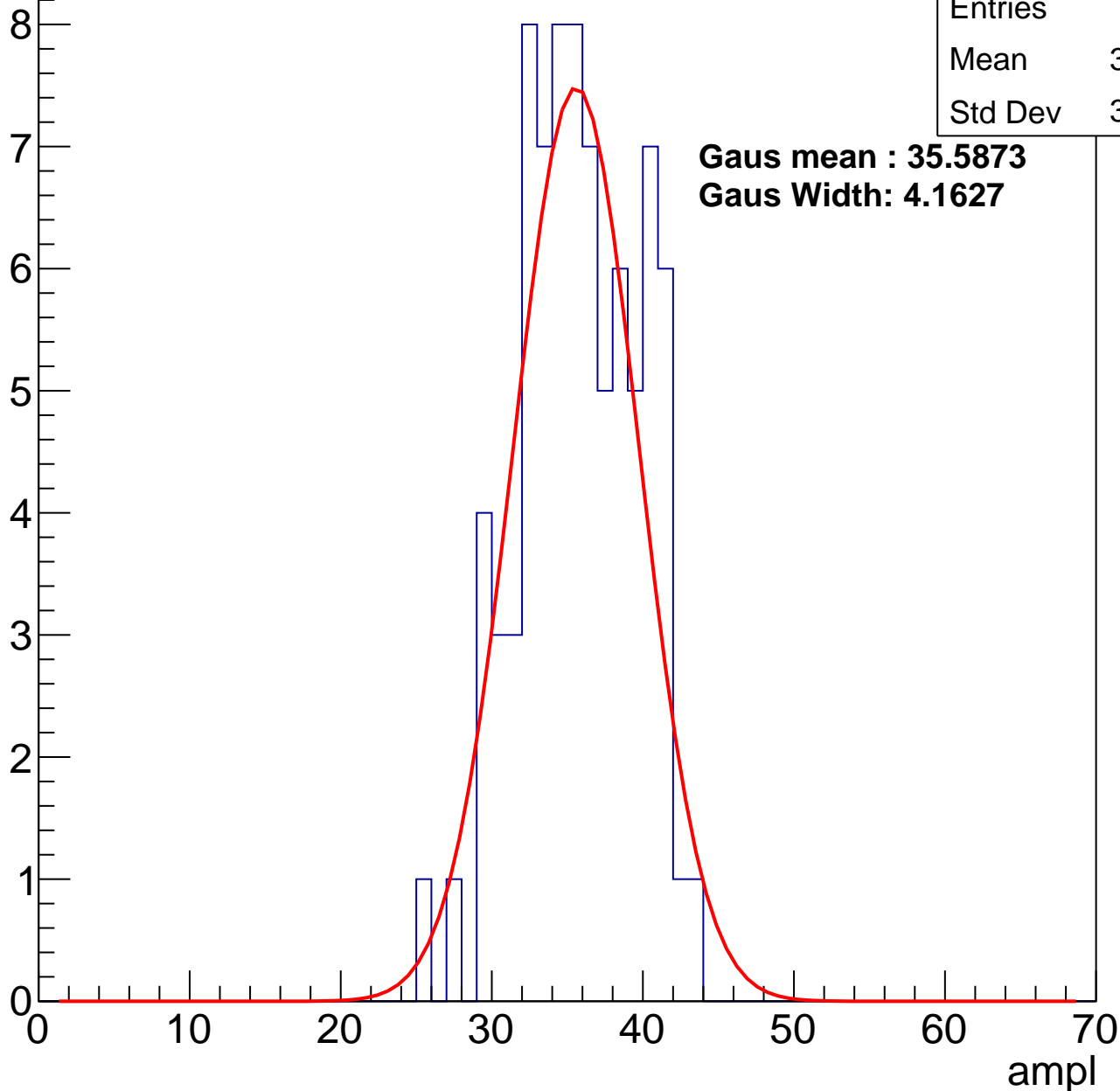
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 35.32 |
| Std Dev | 3.842 |

Gaus mean : 35.5873

Gaus Width: 4.1627



B1L102S, U15-ch15, adc2

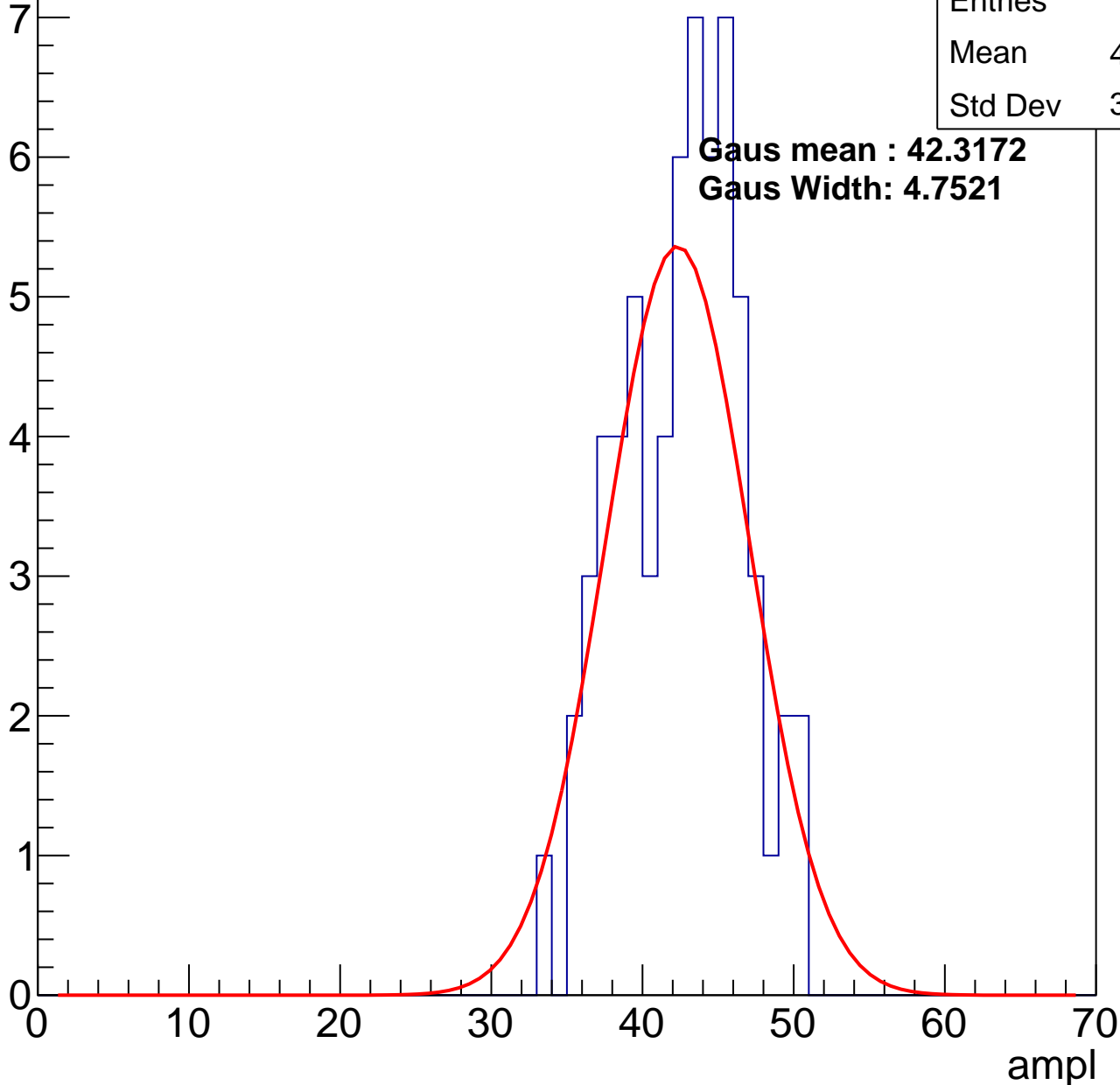
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 42.14 |
| Std Dev | 3.965 |

Gaus mean : 42.3172

Gaus Width: 4.7521

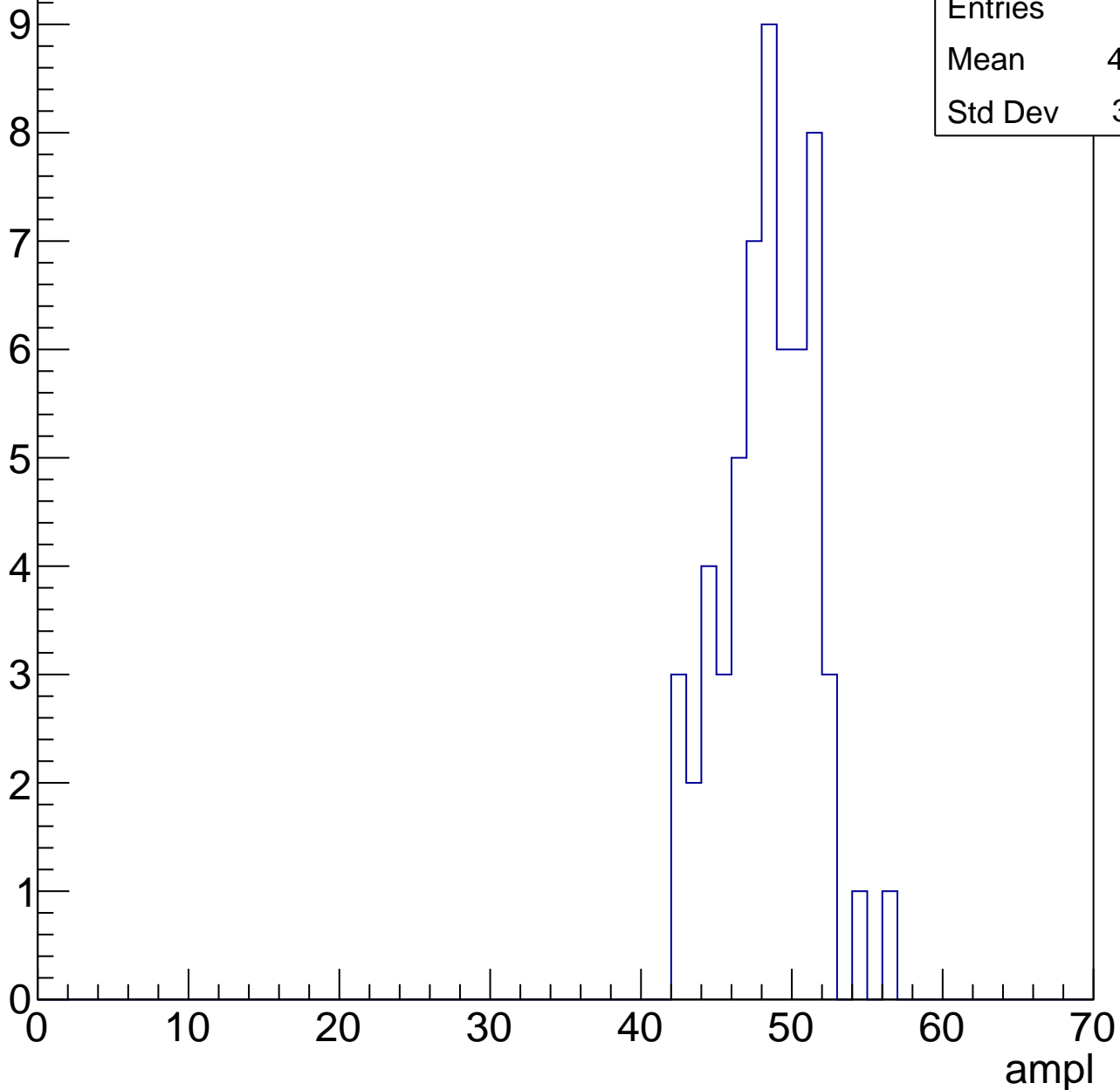


B1L102S, U15-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

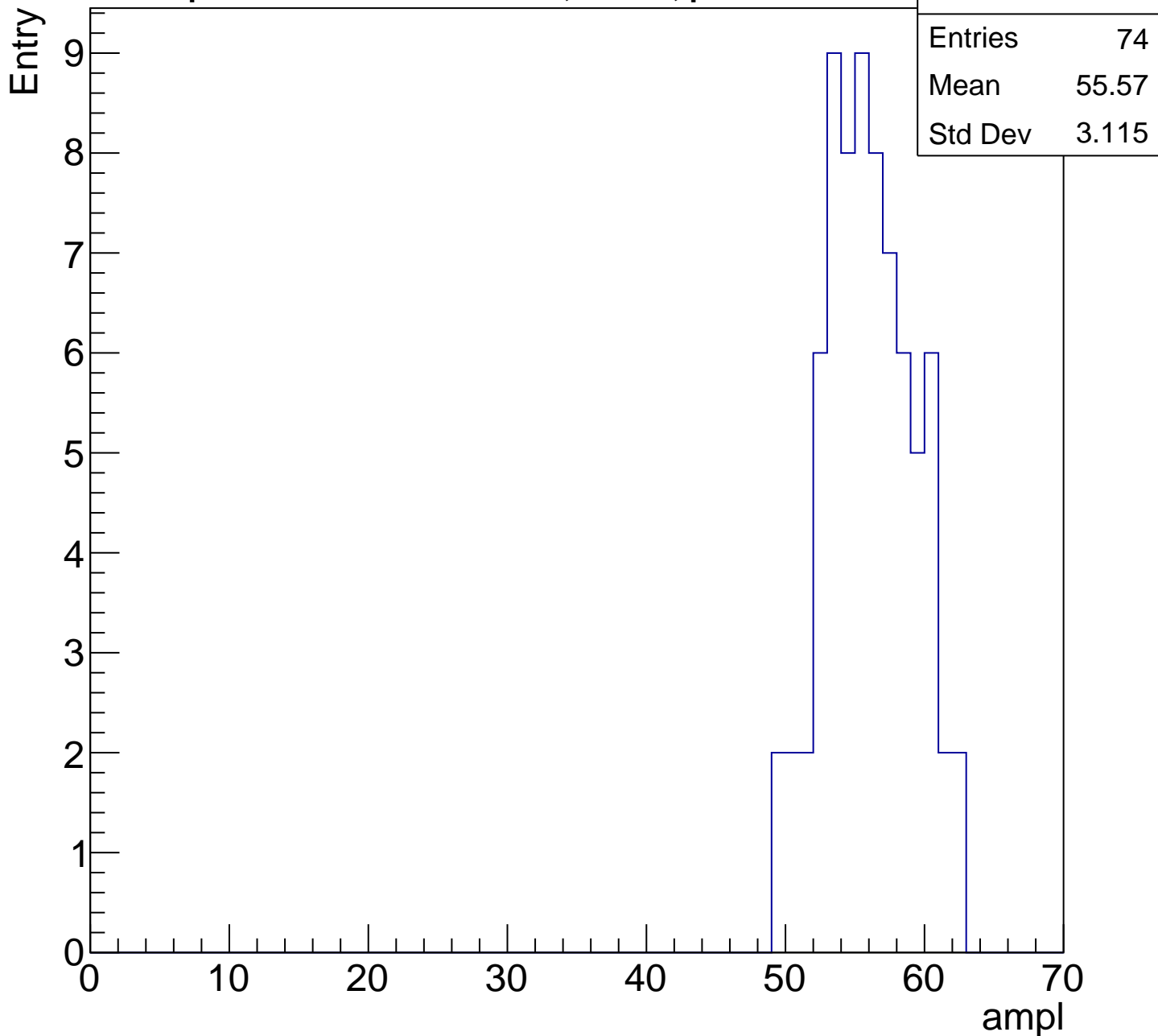
Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 47.97 |
| Std Dev | 3.011 |



B1L102S, U15-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

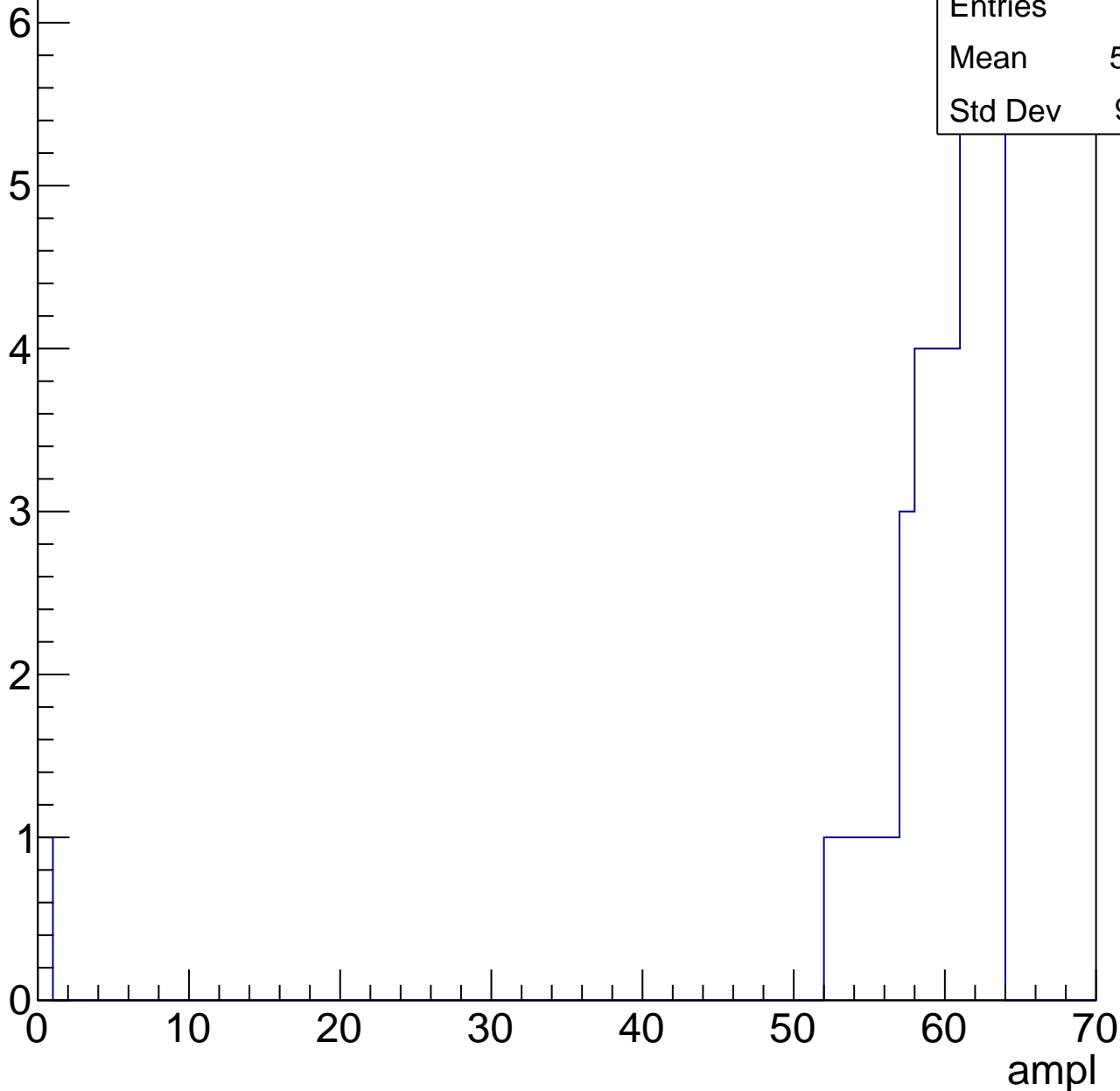


B1L102S, U15-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

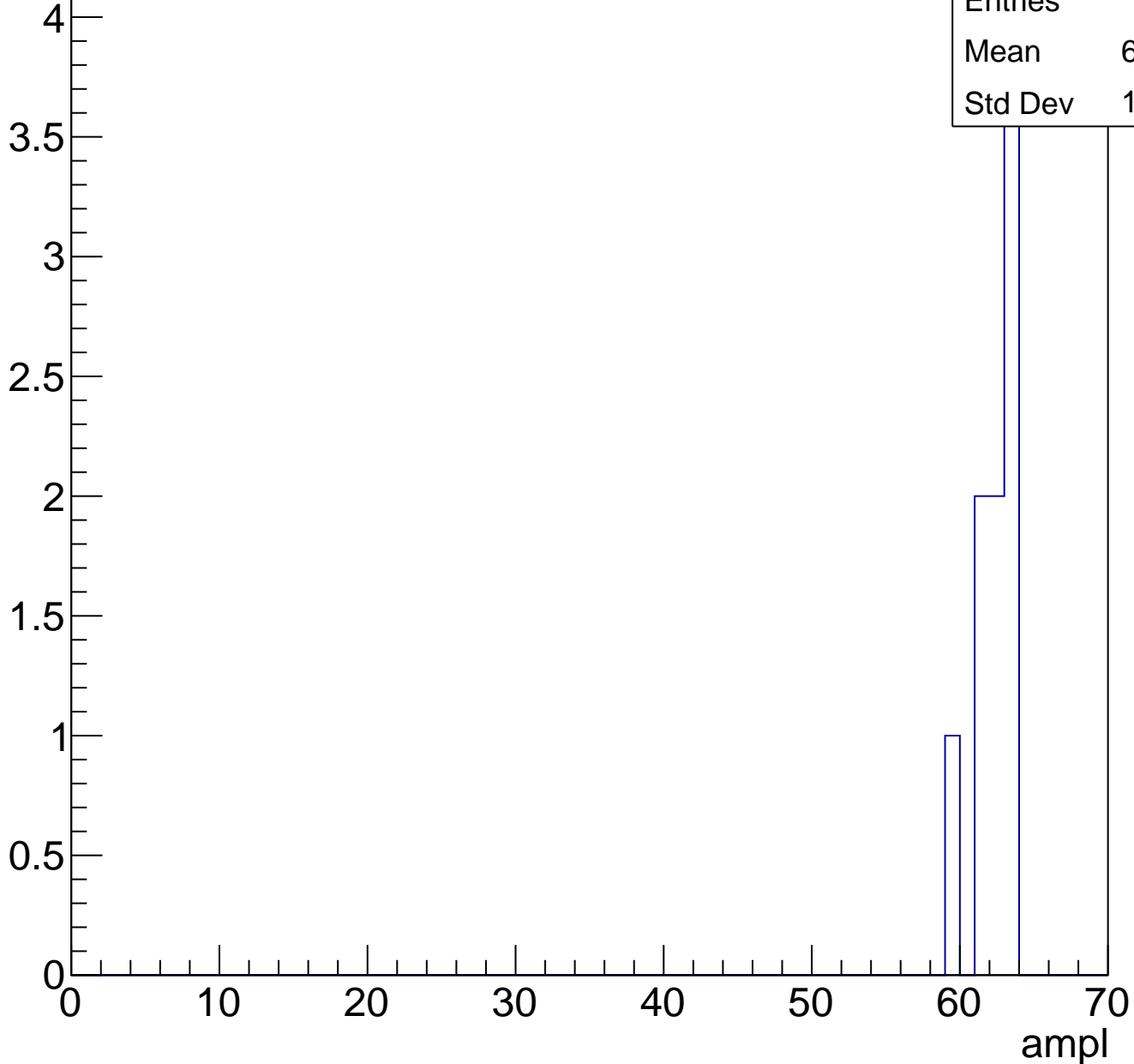
| | |
|---------|-------|
| Entries | 39 |
| Mean | 58.08 |
| Std Dev | 9.841 |



B1L102S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch16, adc0

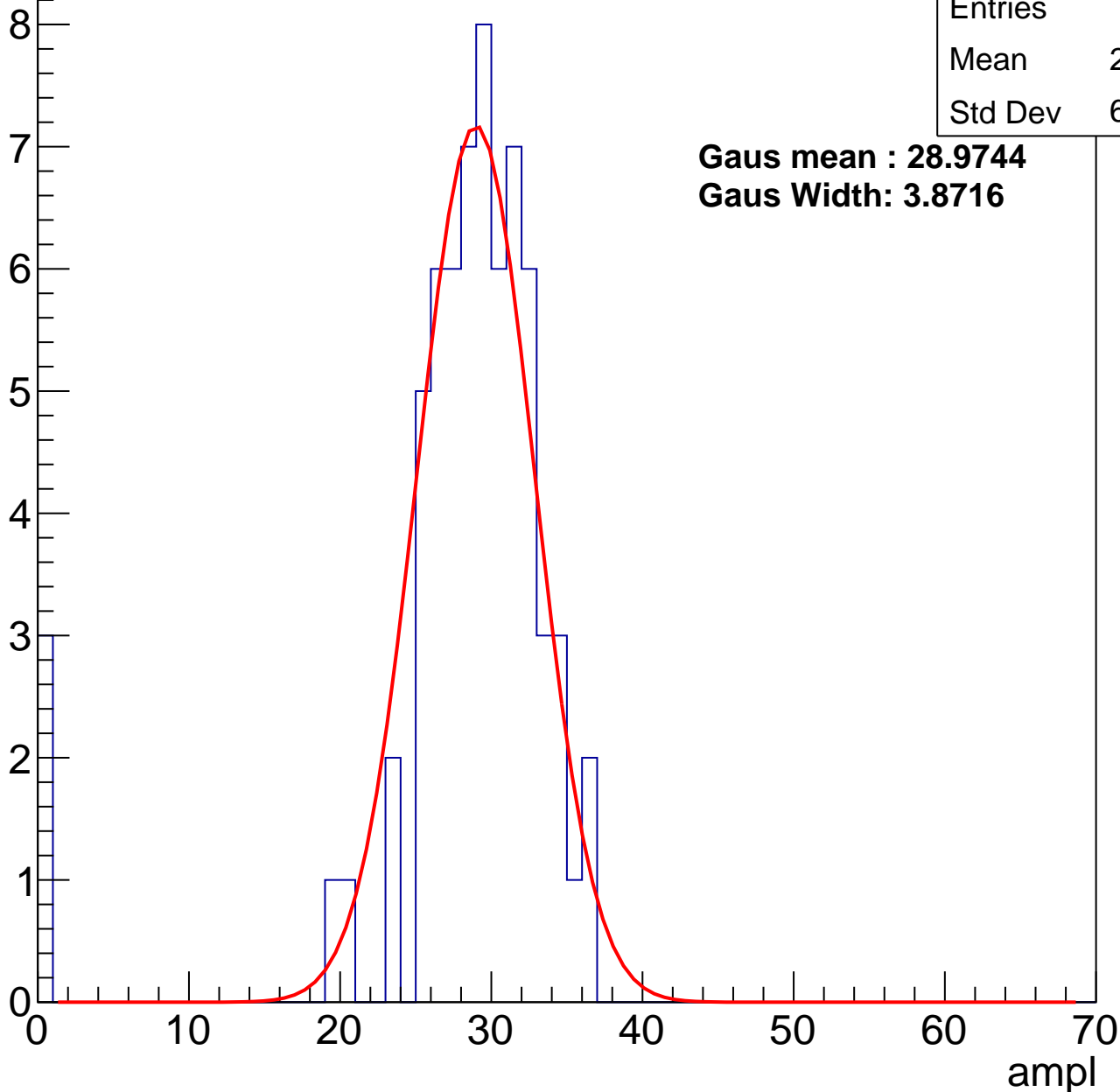
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.66 |
| Std Dev | 6.873 |

Gaus mean : 28.9744

Gaus Width: 3.8716



B1L102S, U15-ch16, adc1

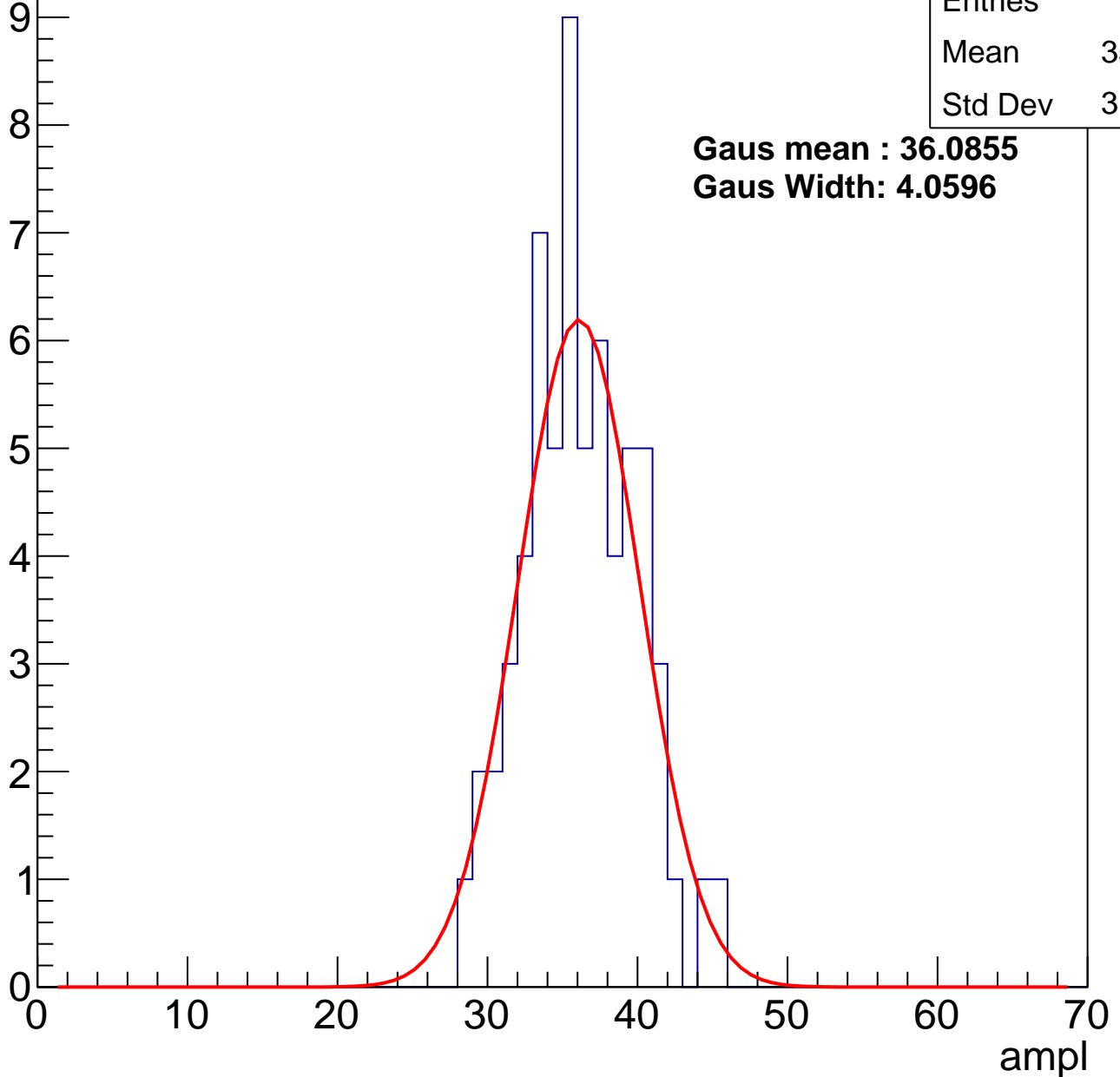
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.72 |
| Std Dev | 3.672 |

Gaus mean : 36.0855

Gaus Width: 4.0596



B1L102S, U15-ch16, adc2

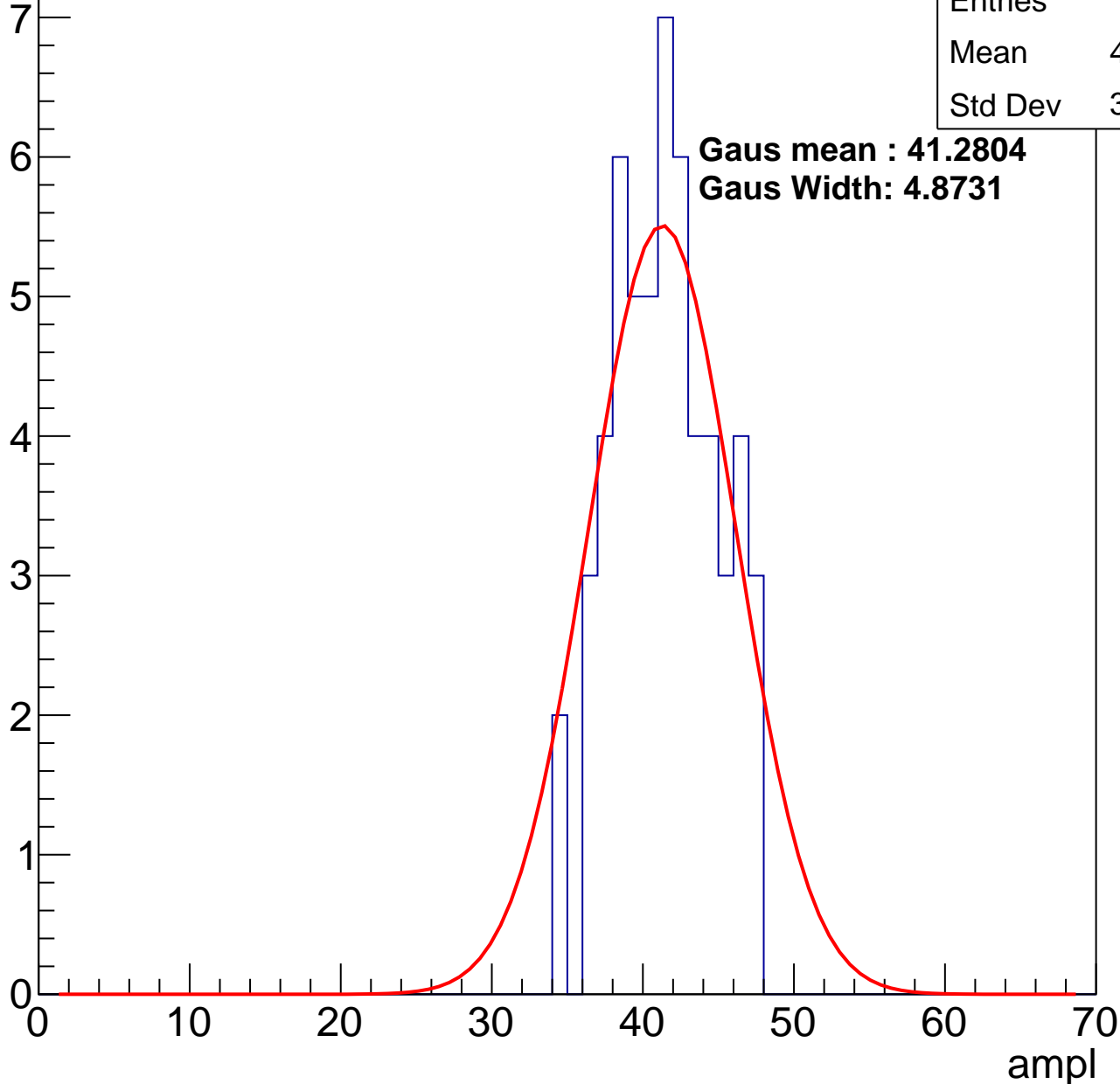
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 40.96 |
| Std Dev | 3.354 |

Gaus mean : 41.2804

Gaus Width: 4.8731

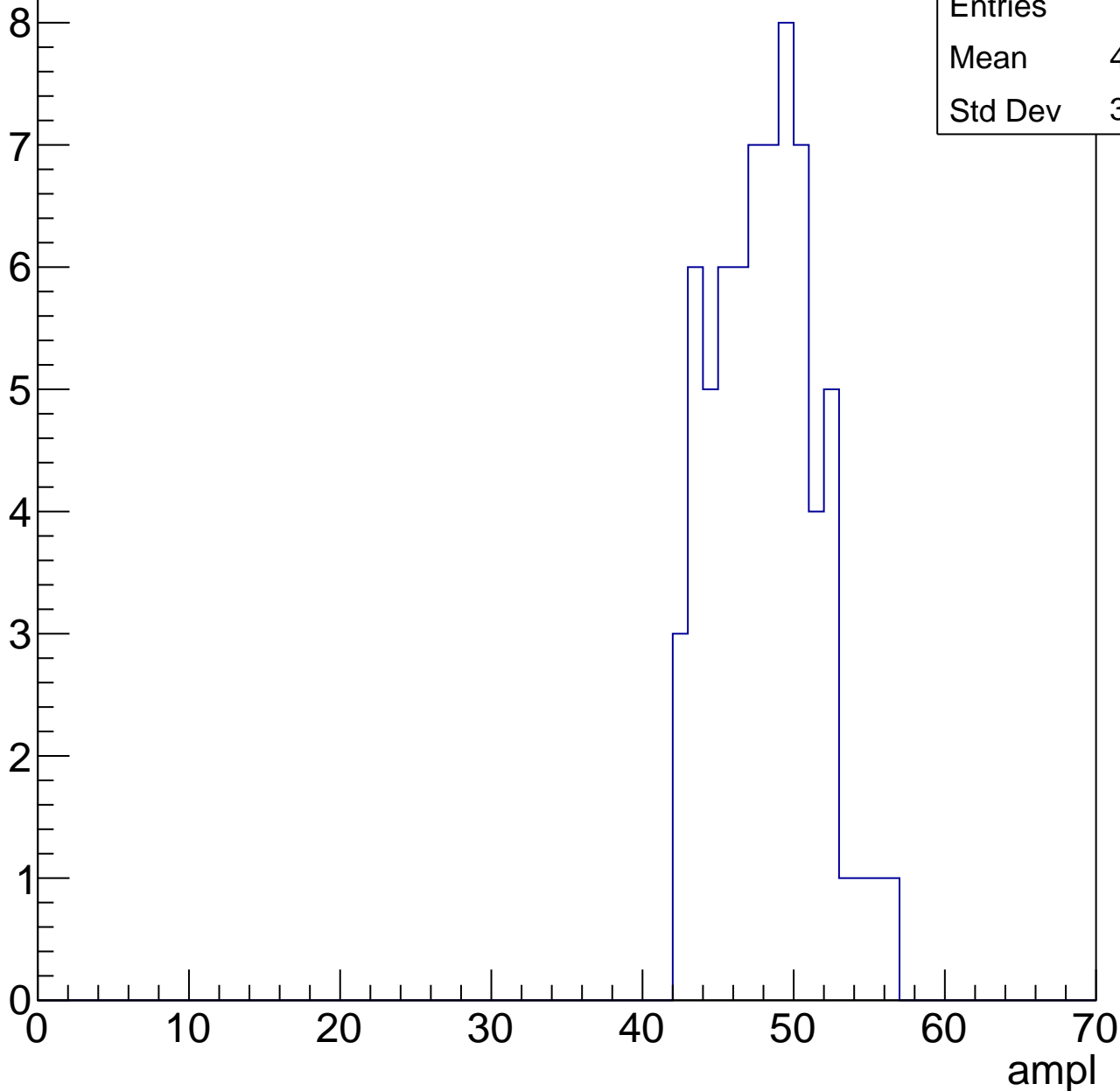


B1L102S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 47.63 |
| Std Dev | 3.298 |

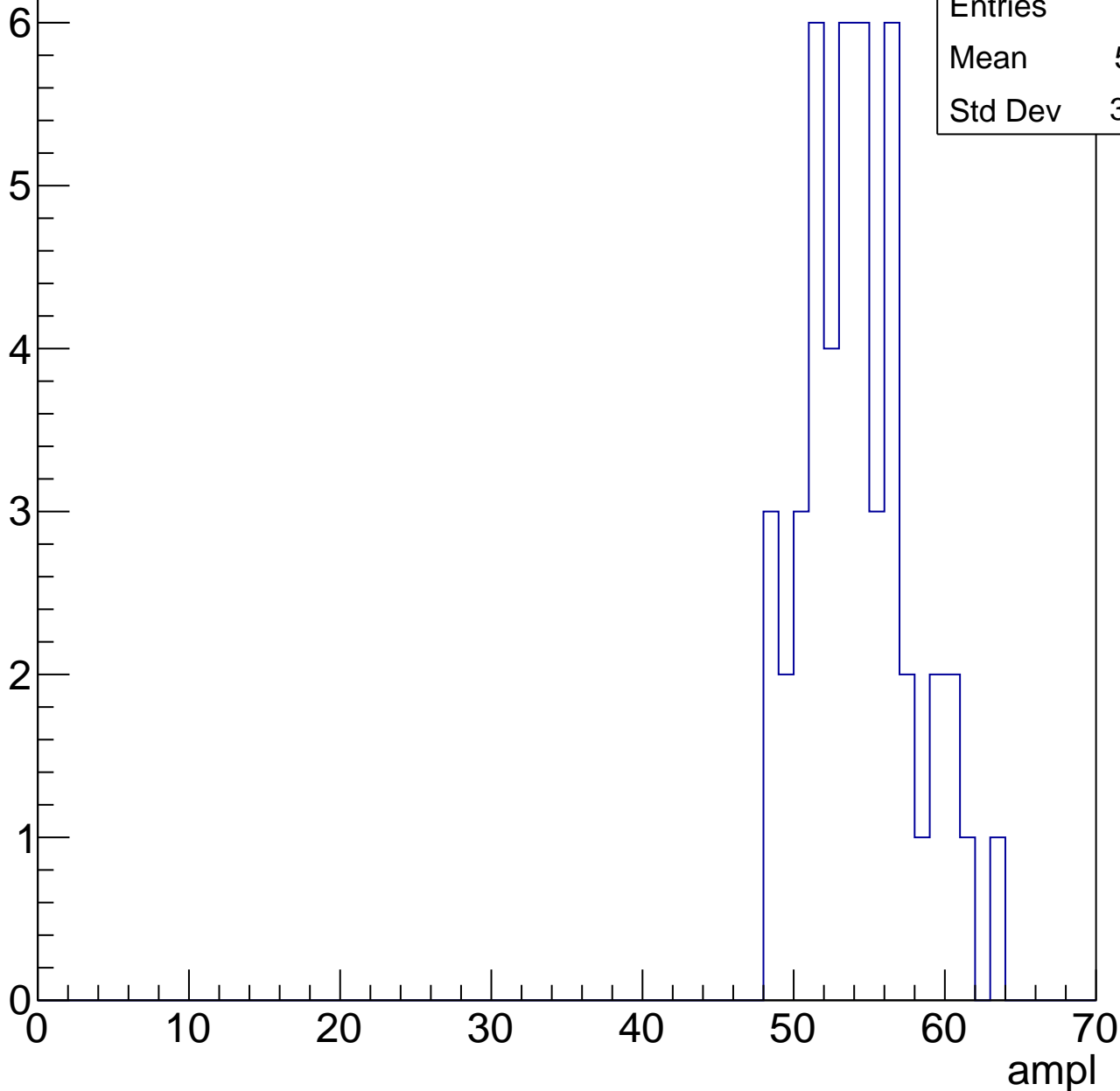


B1L102S, U15-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 53.81 |
| Std Dev | 3.516 |

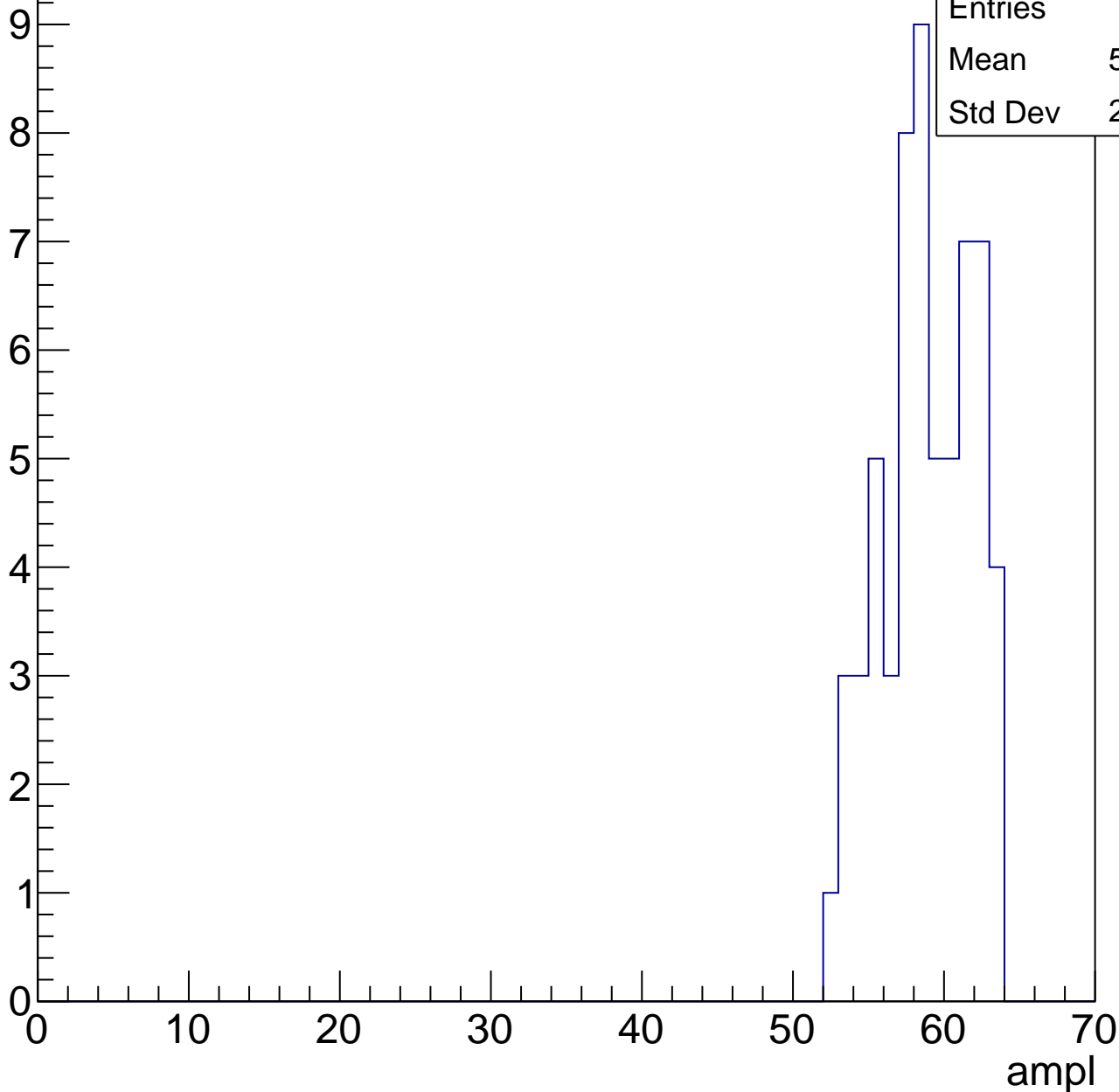


B1L102S, U15-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 58.37 |
| Std Dev | 2.932 |

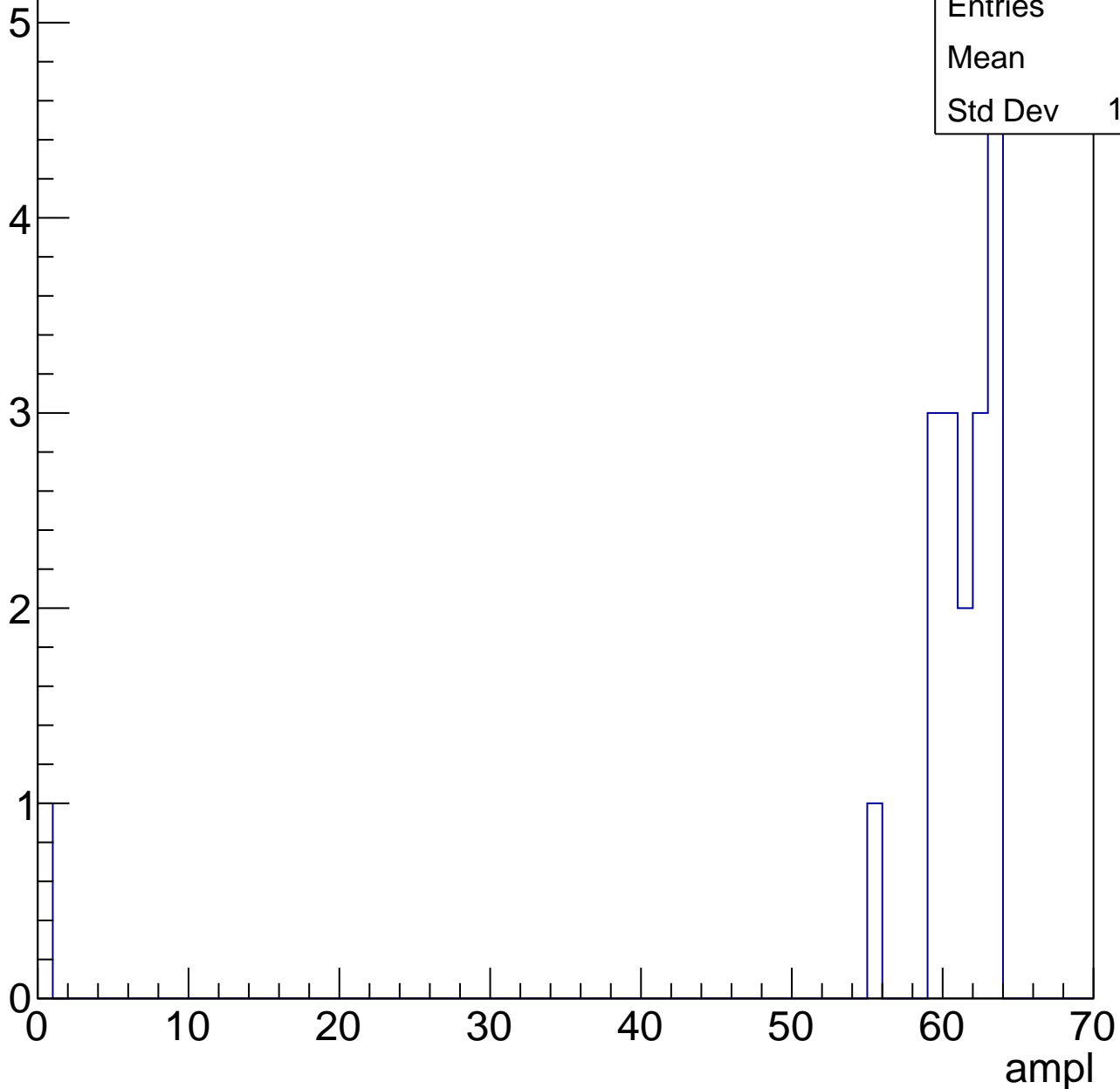


B1L102S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

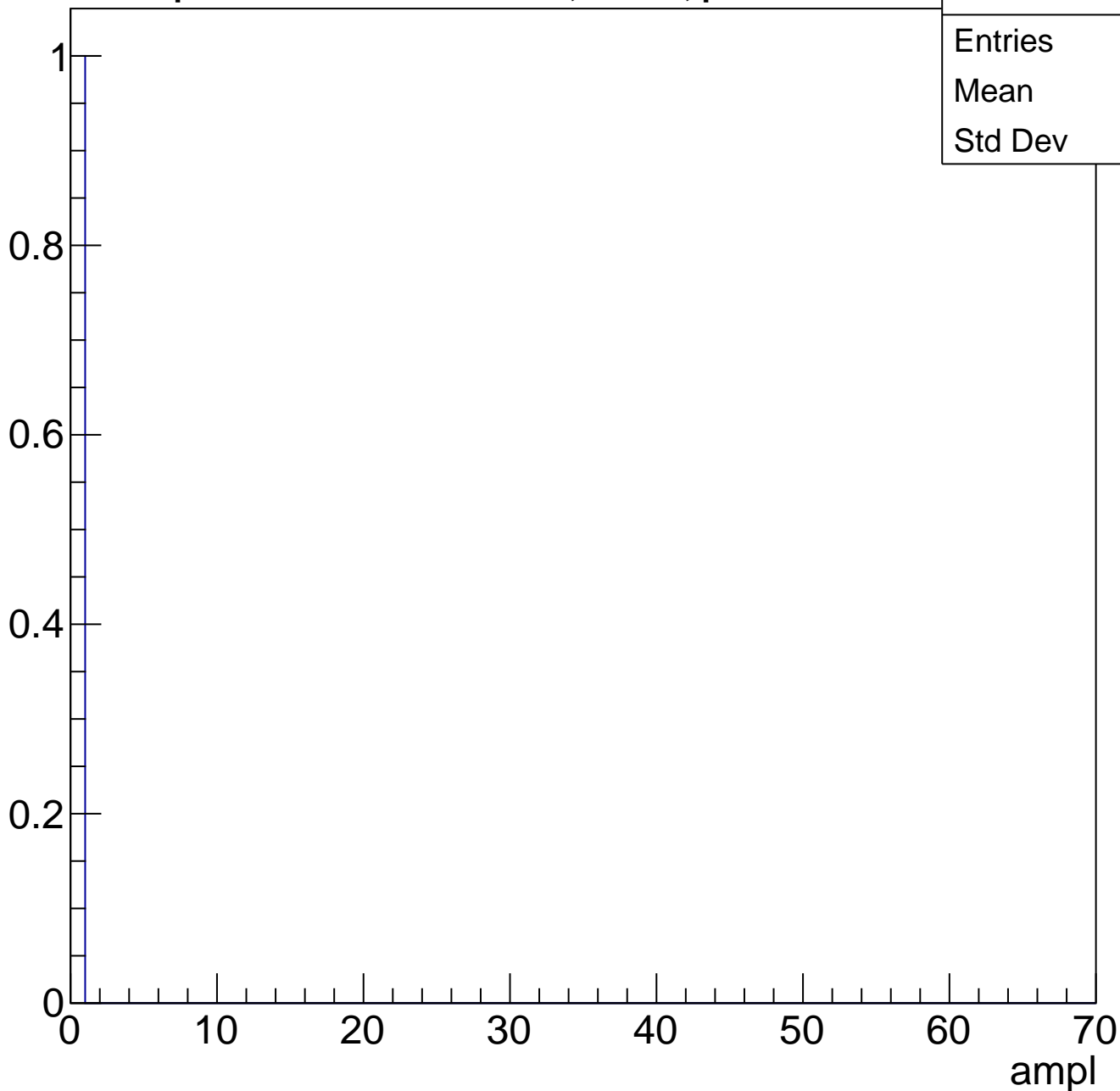
| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.5 |
| Std Dev | 14.09 |



B1L102S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch17, adc0

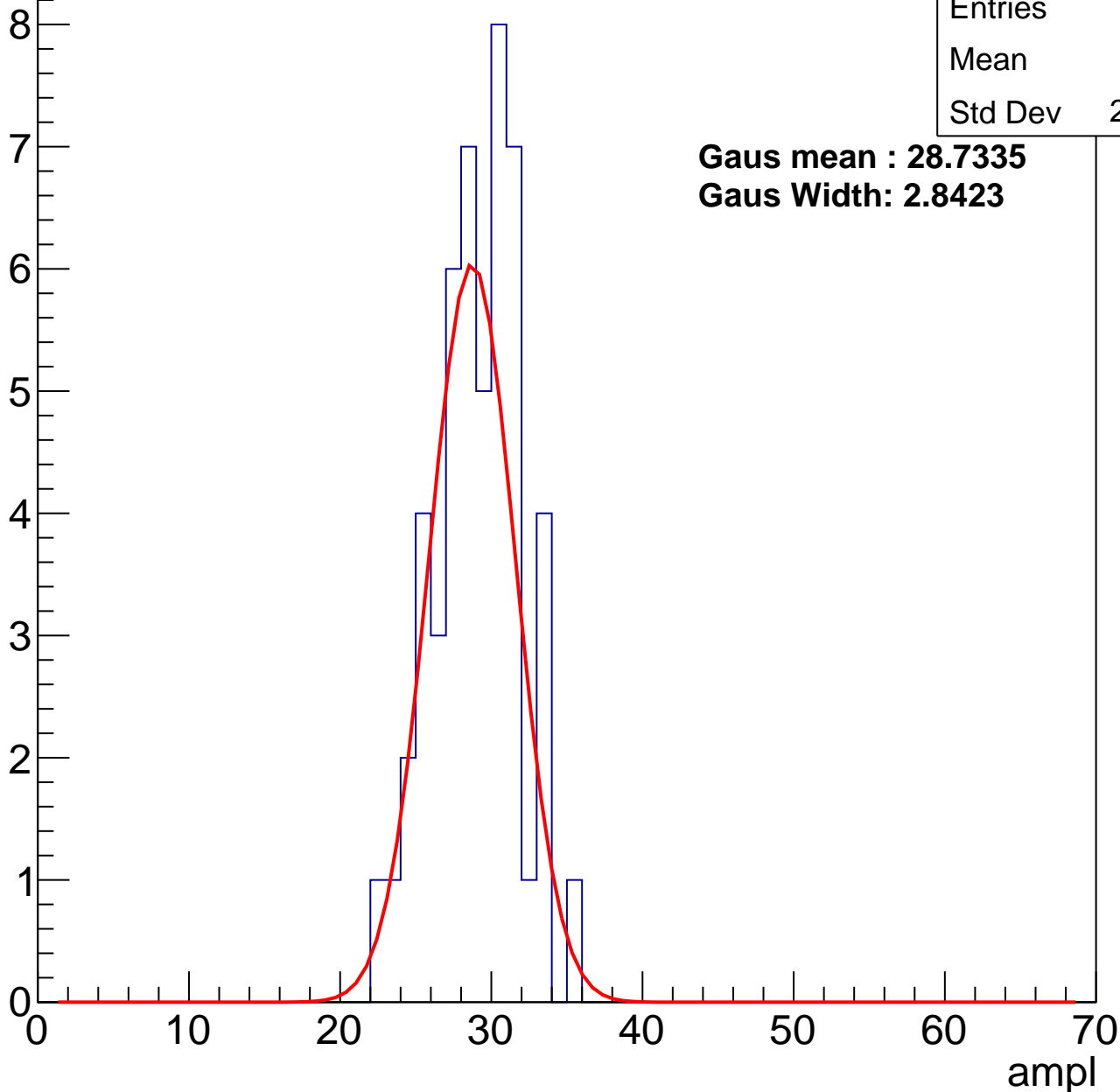
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 28.6 |
| Std Dev | 2.807 |

Gaus mean : 28.7335

Gaus Width: 2.8423



B1L102S, U15-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 35.63 |
| Std Dev | 3.594 |

Gaus mean : 36.1026

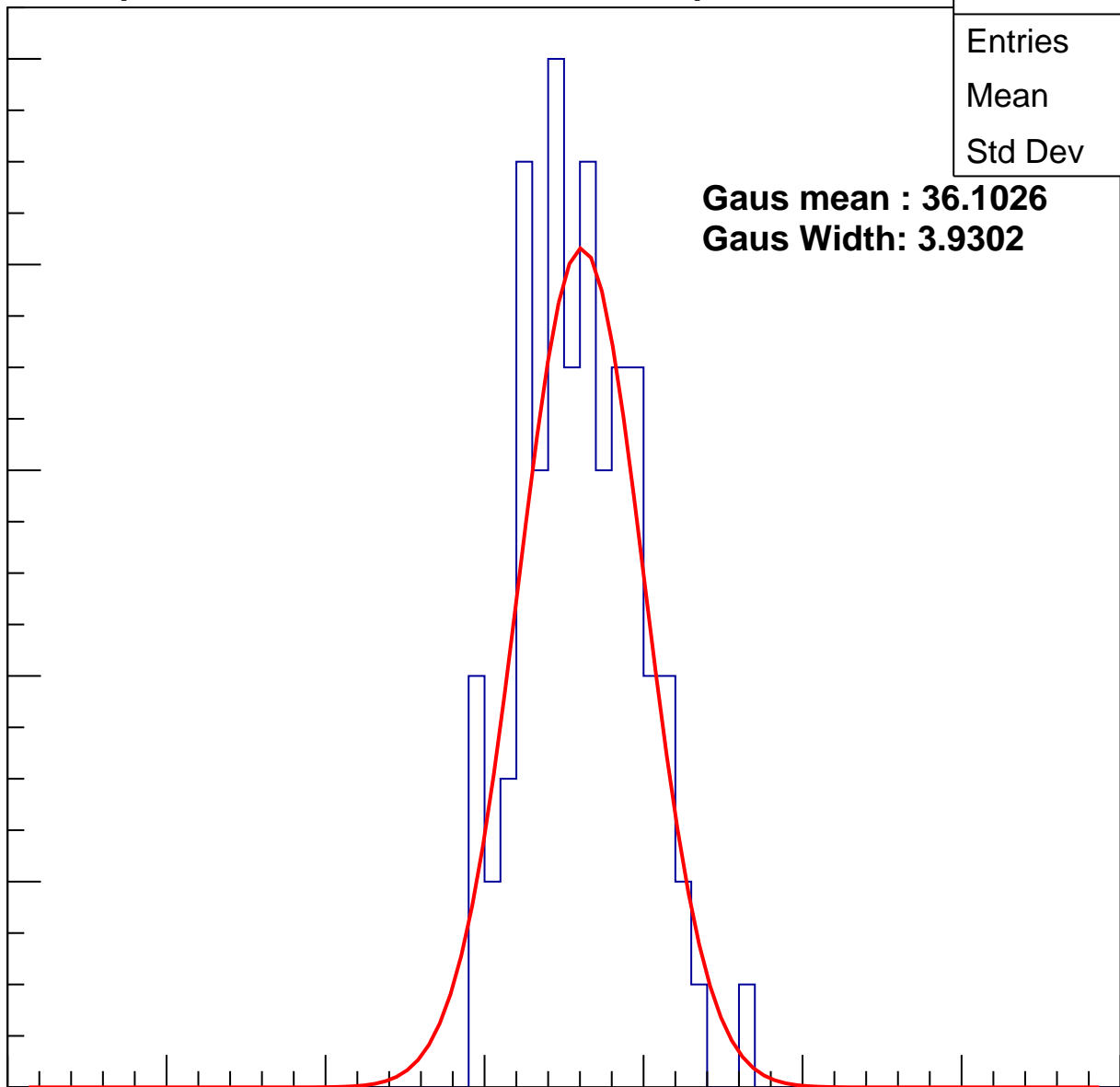
Gaus Width: 3.9302

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch17, adc2

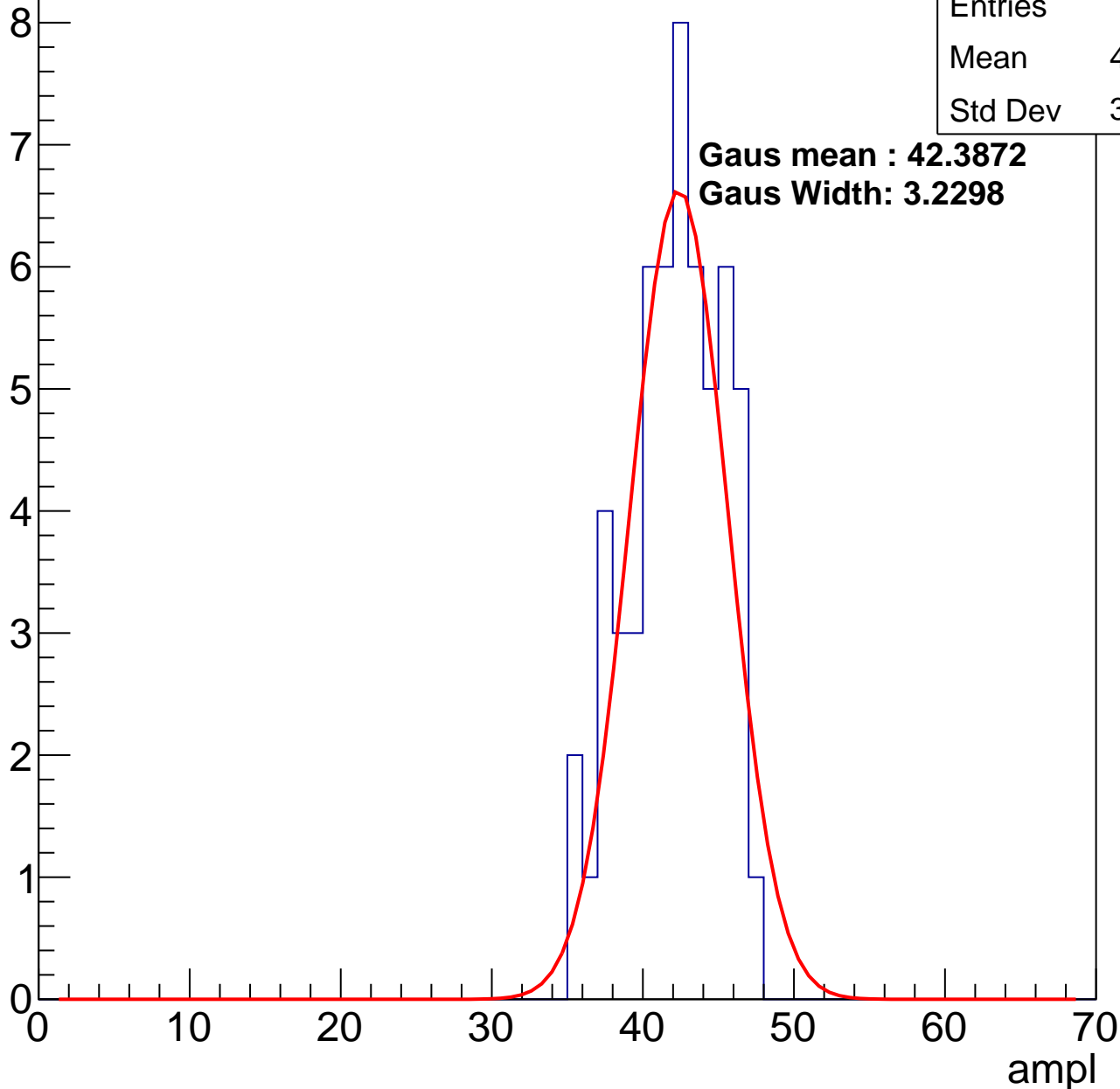
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 41.64 |
| Std Dev | 3.038 |

Gaus mean : 42.3872

Gaus Width: 3.2298

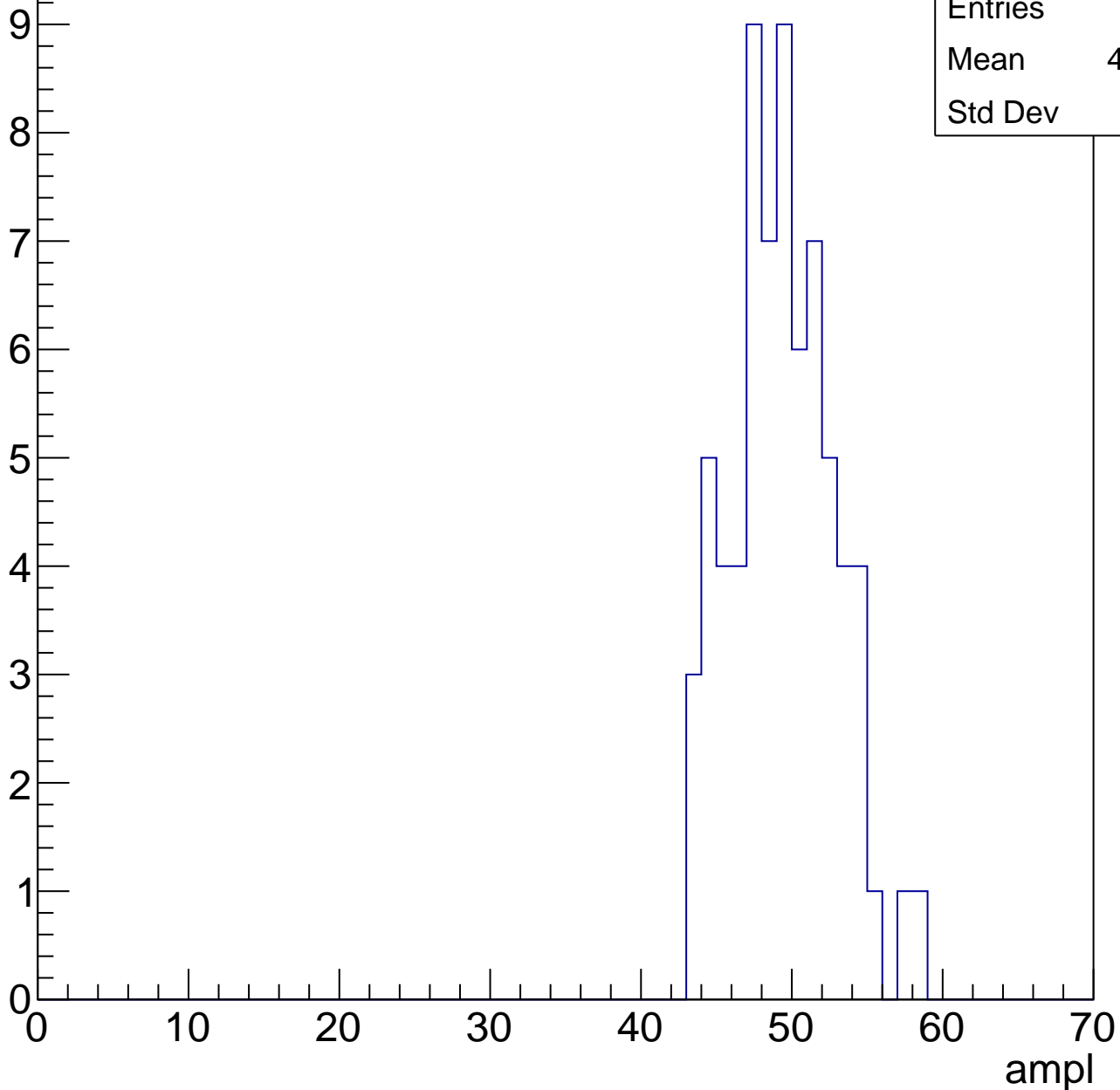


B1L102S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 48.97 |
| Std Dev | 3.38 |

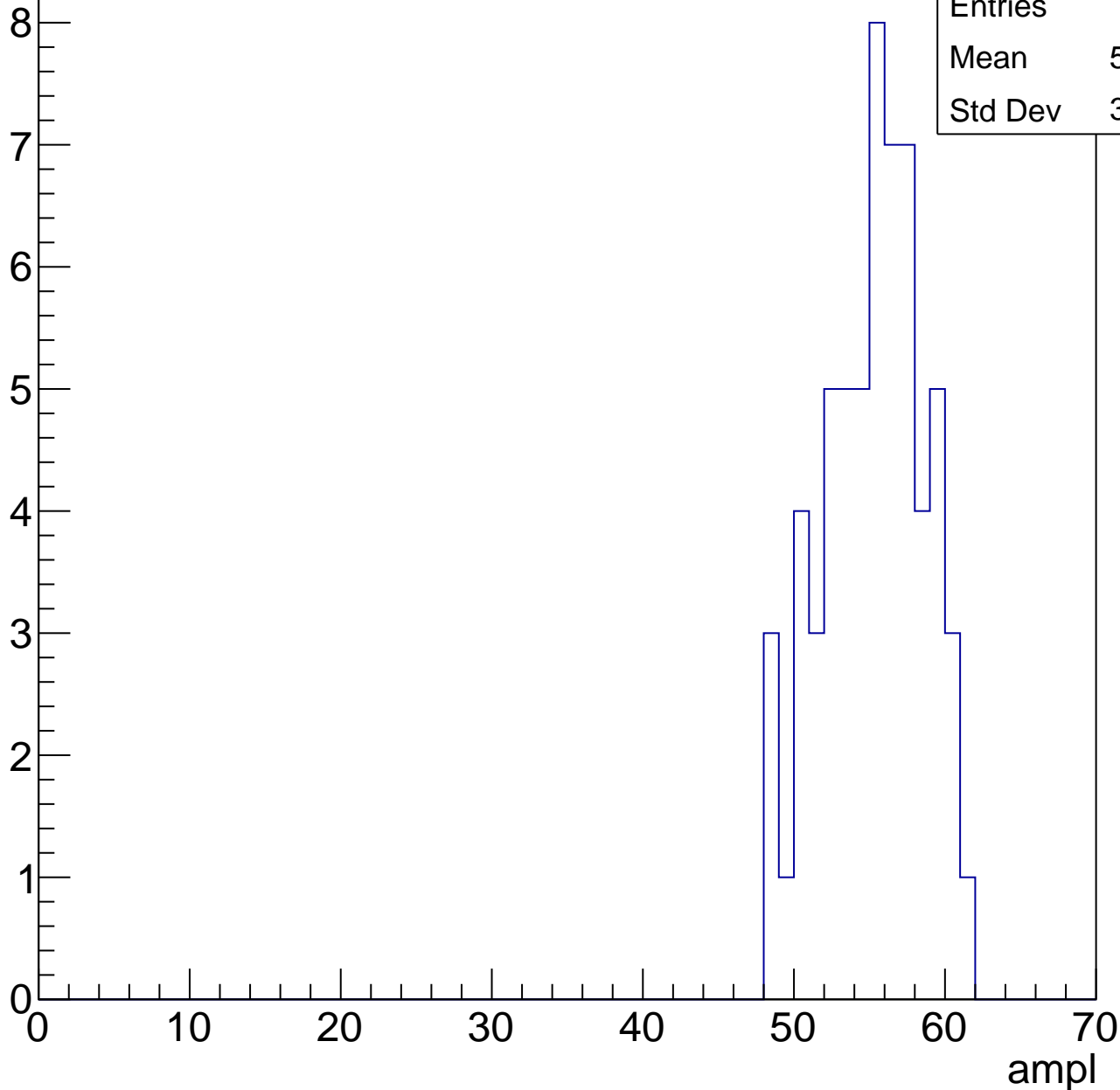


B1L102S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 54.75 |
| Std Dev | 3.288 |

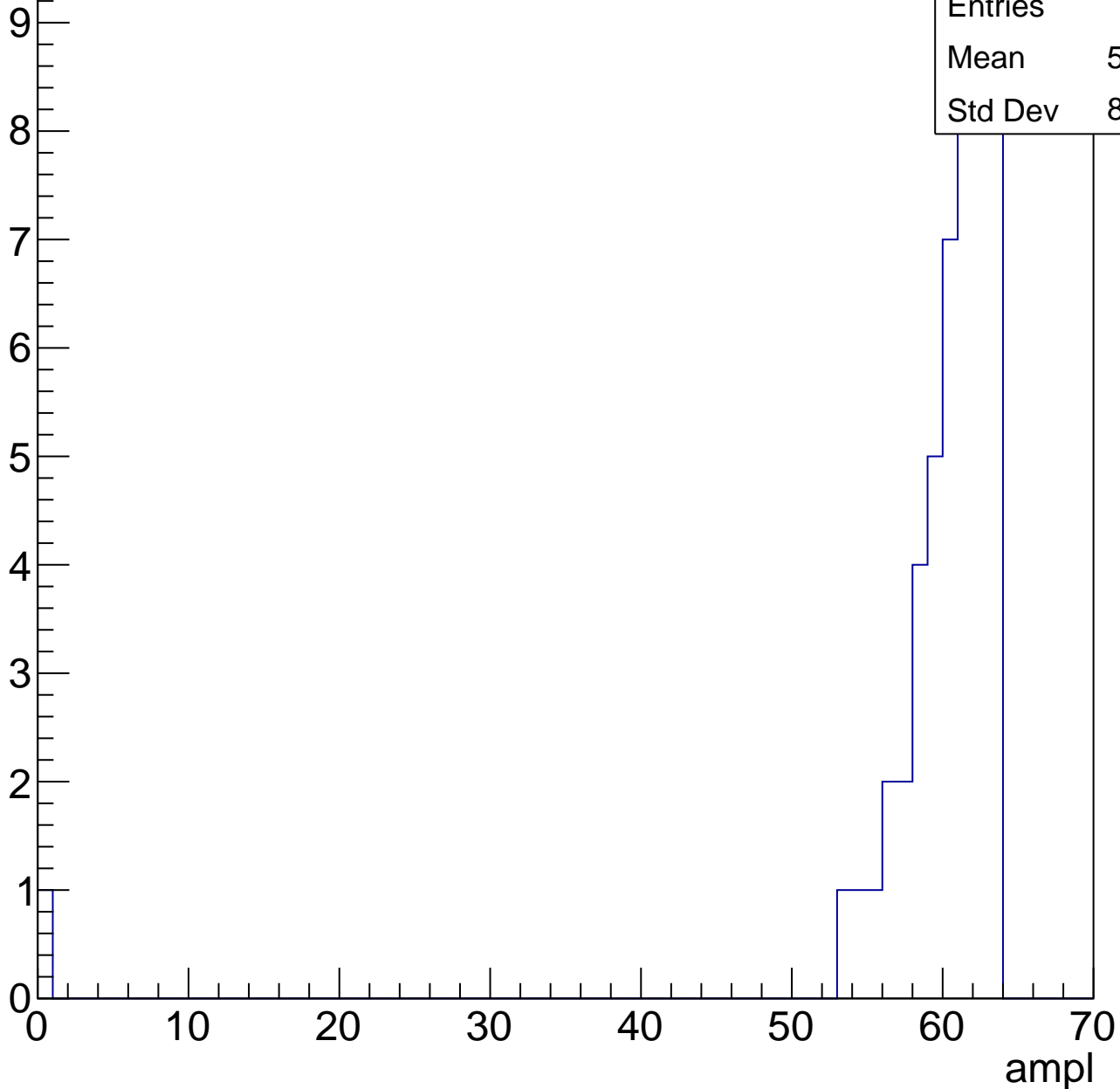


B1L102S, U15-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

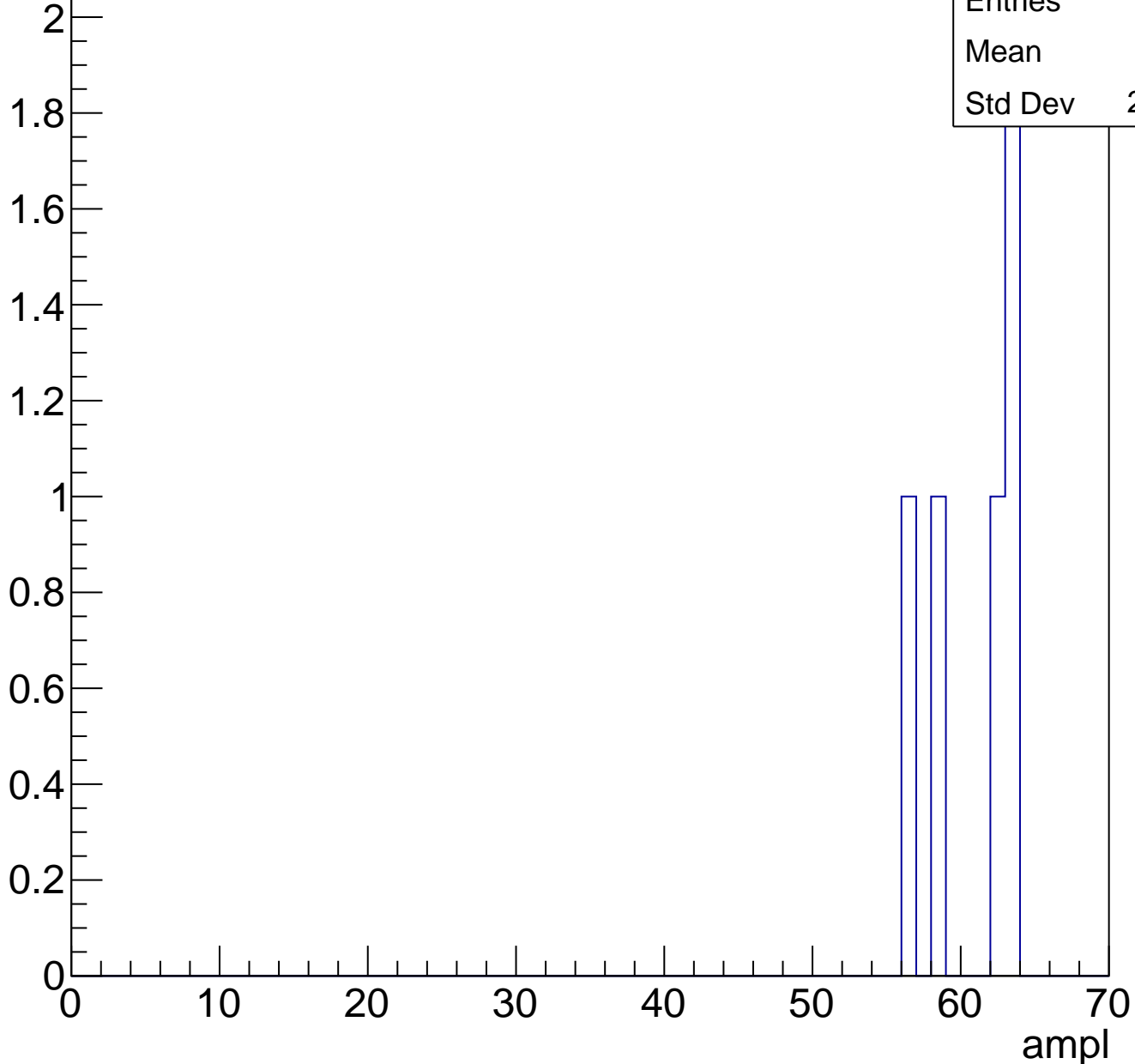
| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.92 |
| Std Dev | 8.763 |



B1L102S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl

B1L102S, U15-ch18, adc0

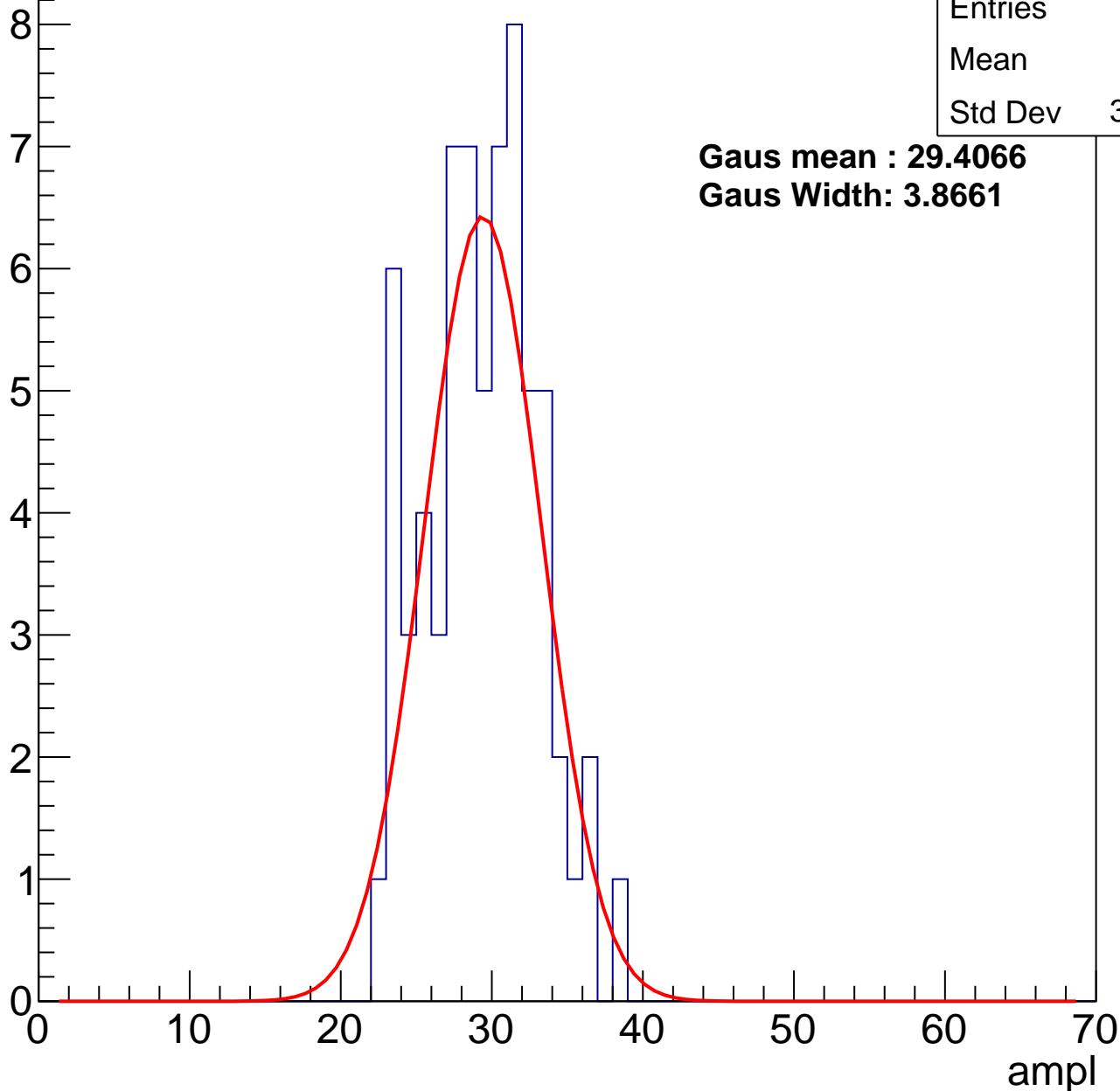
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 28.9 |
| Std Dev | 3.653 |

Gaus mean : 29.4066

Gaus Width: 3.8661



B1L102S, U15-ch18, adc1

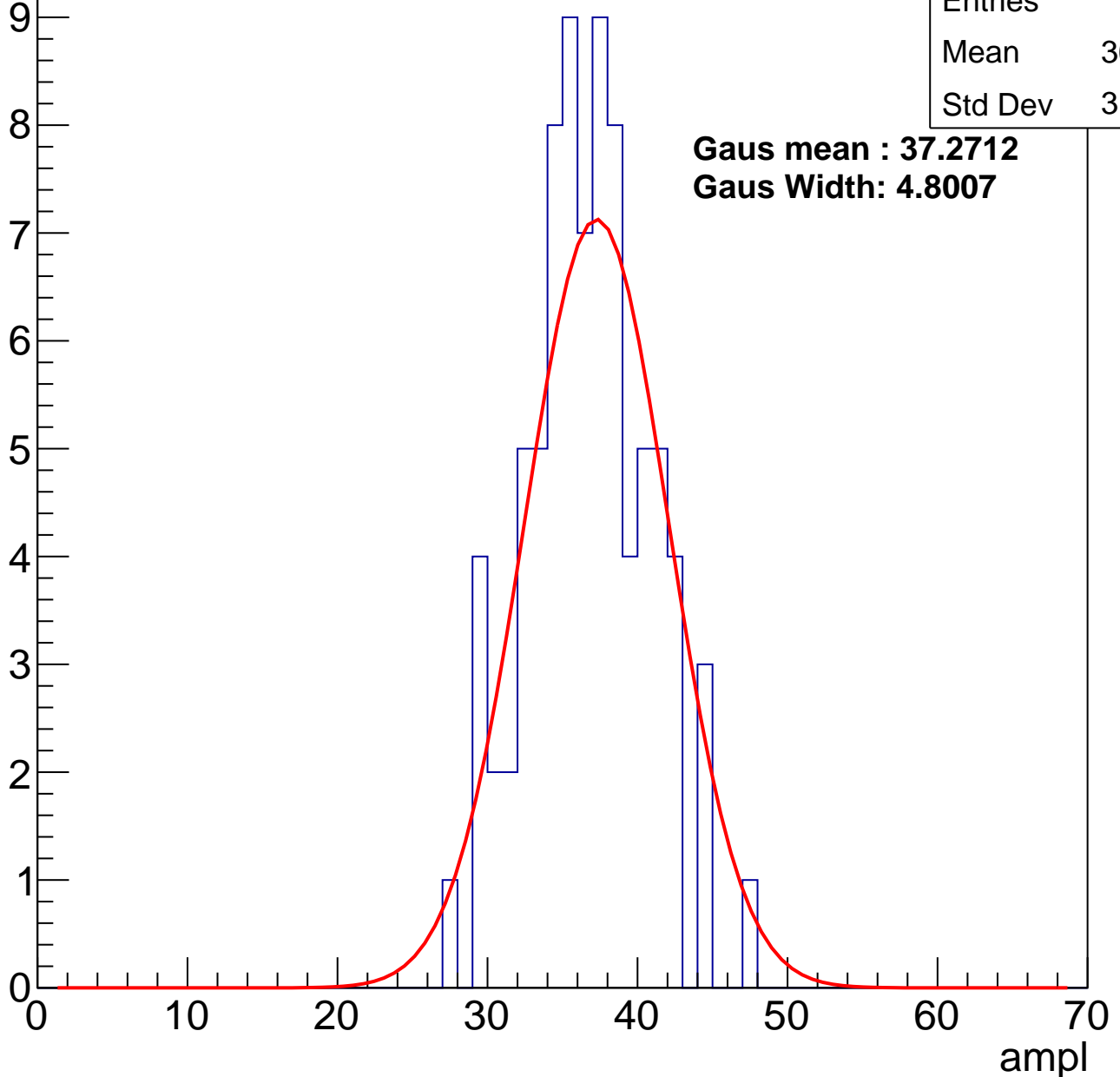
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 36.27 |
| Std Dev | 3.979 |

Gaus mean : 37.2712

Gaus Width: 4.8007



B1L102S, U15-ch18, adc2

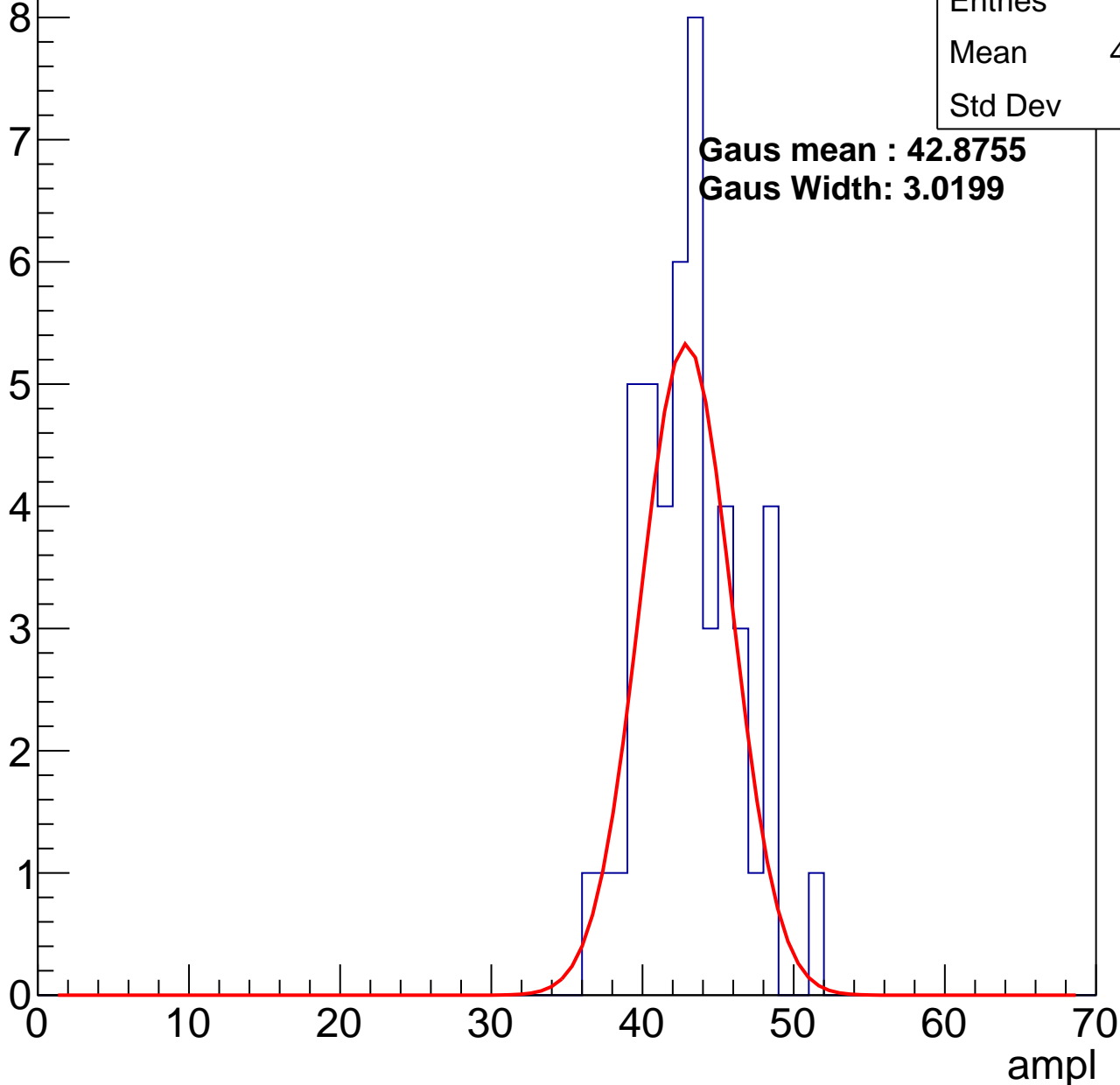
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 42.68 |
| Std Dev | 3.19 |

Gaus mean : 42.8755

Gaus Width: 3.0199

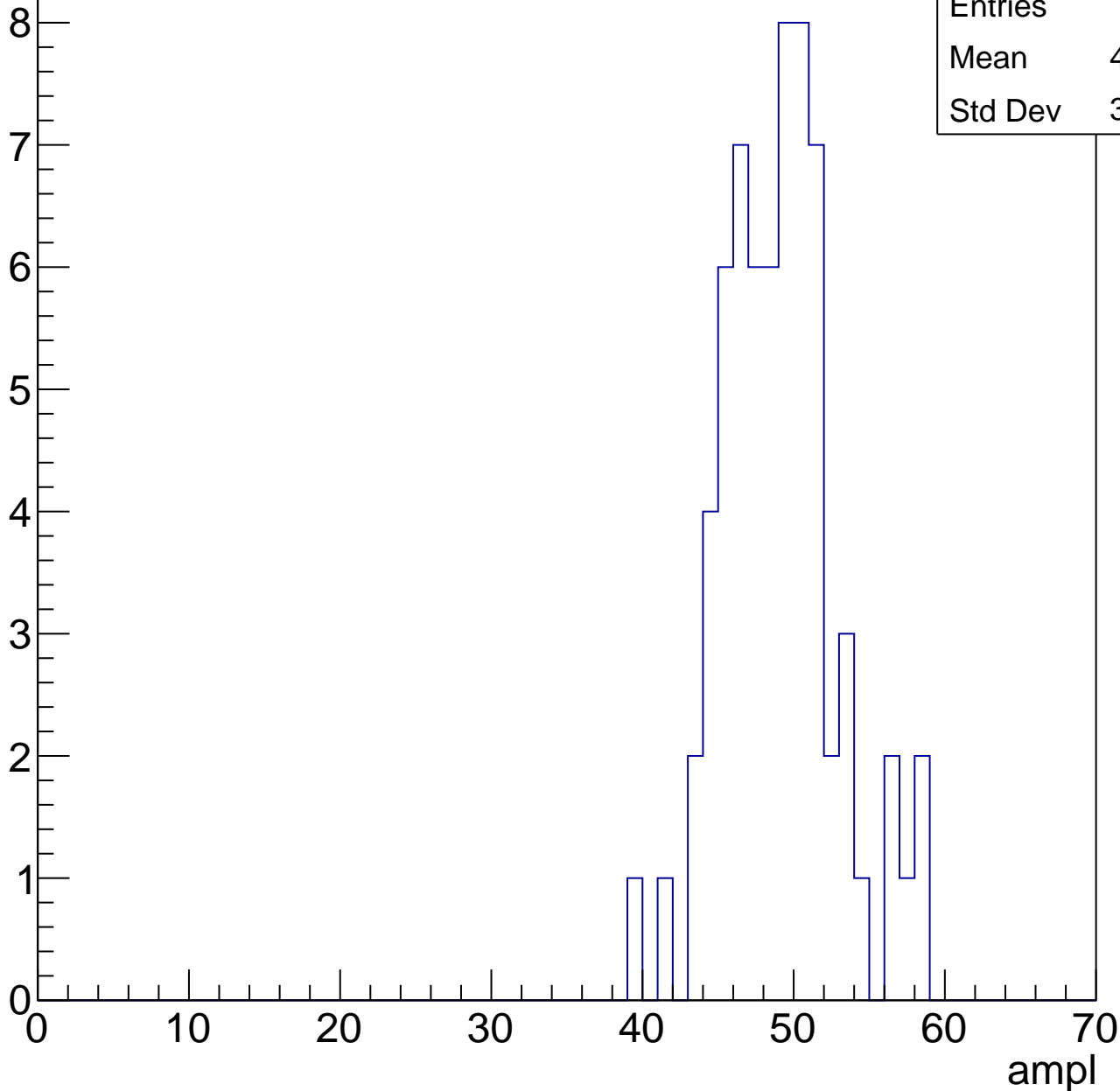


B1L102S, U15-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

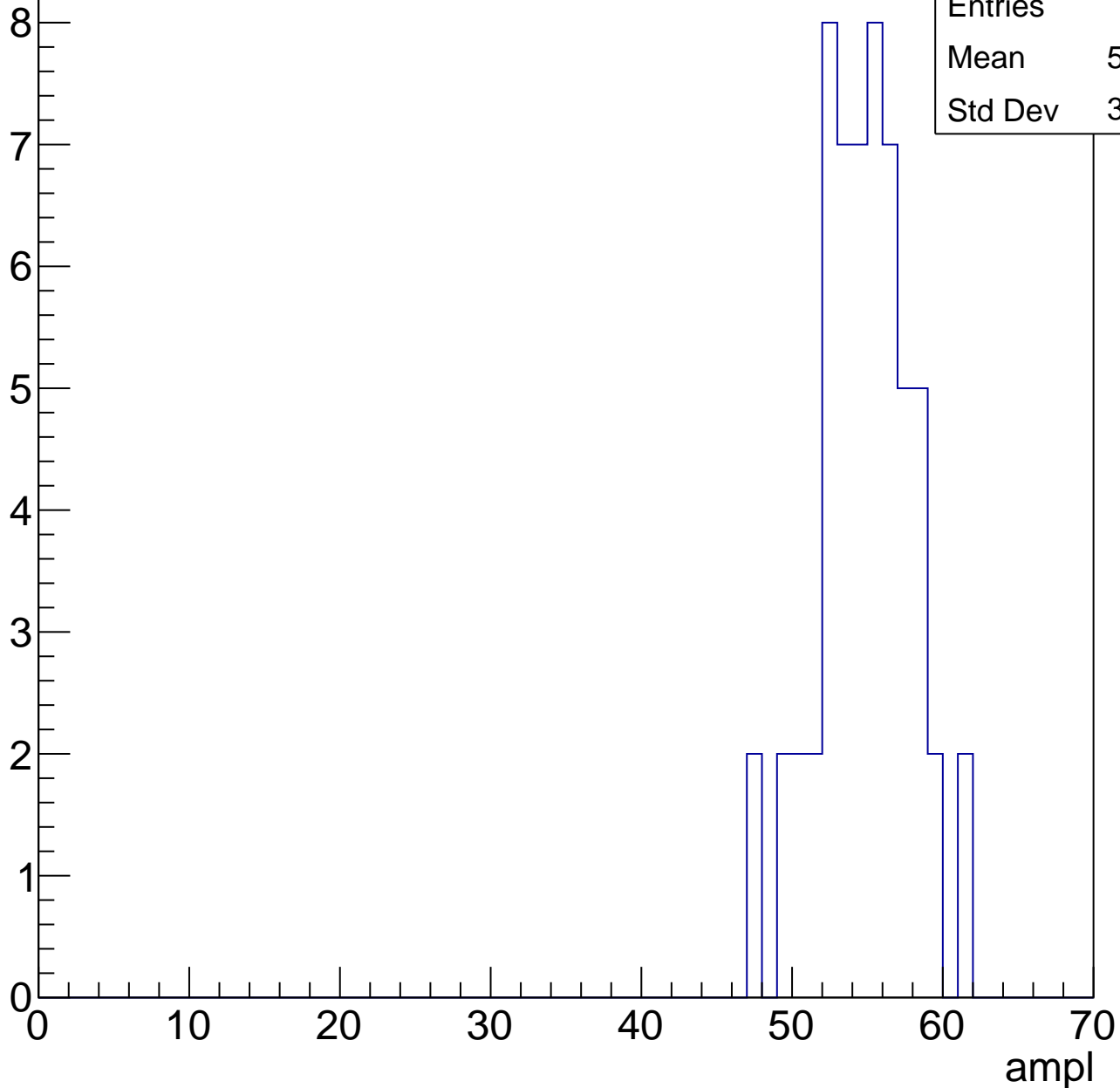
| | |
|---------|-------|
| Entries | 67 |
| Mean | 48.58 |
| Std Dev | 3.794 |



B1L102S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

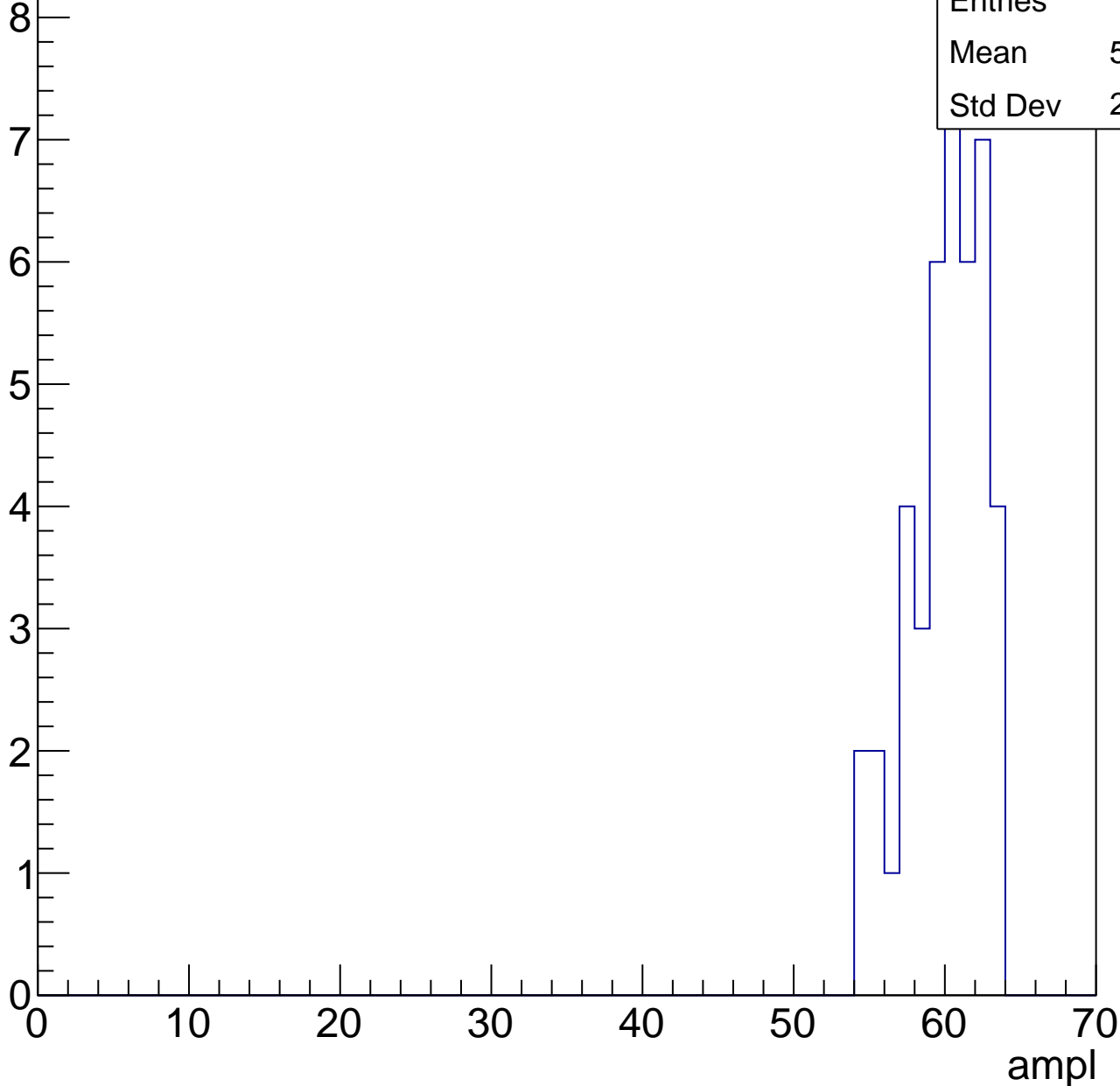


B1L102S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 59.58 |
| Std Dev | 2.433 |

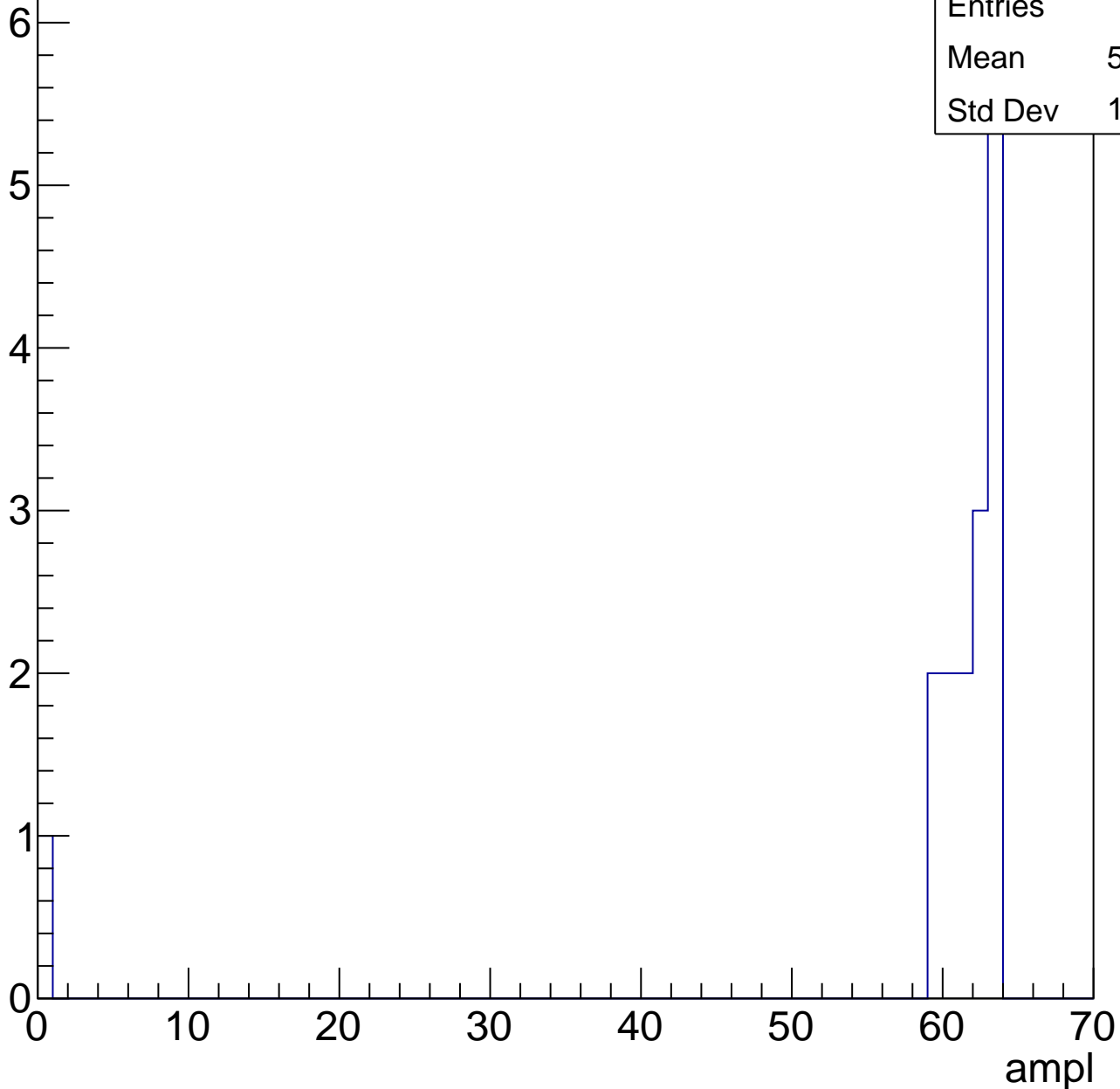


B1L102S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 16 |
| Mean | 57.75 |
| Std Dev | 14.98 |



B1L102S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch19, adc0

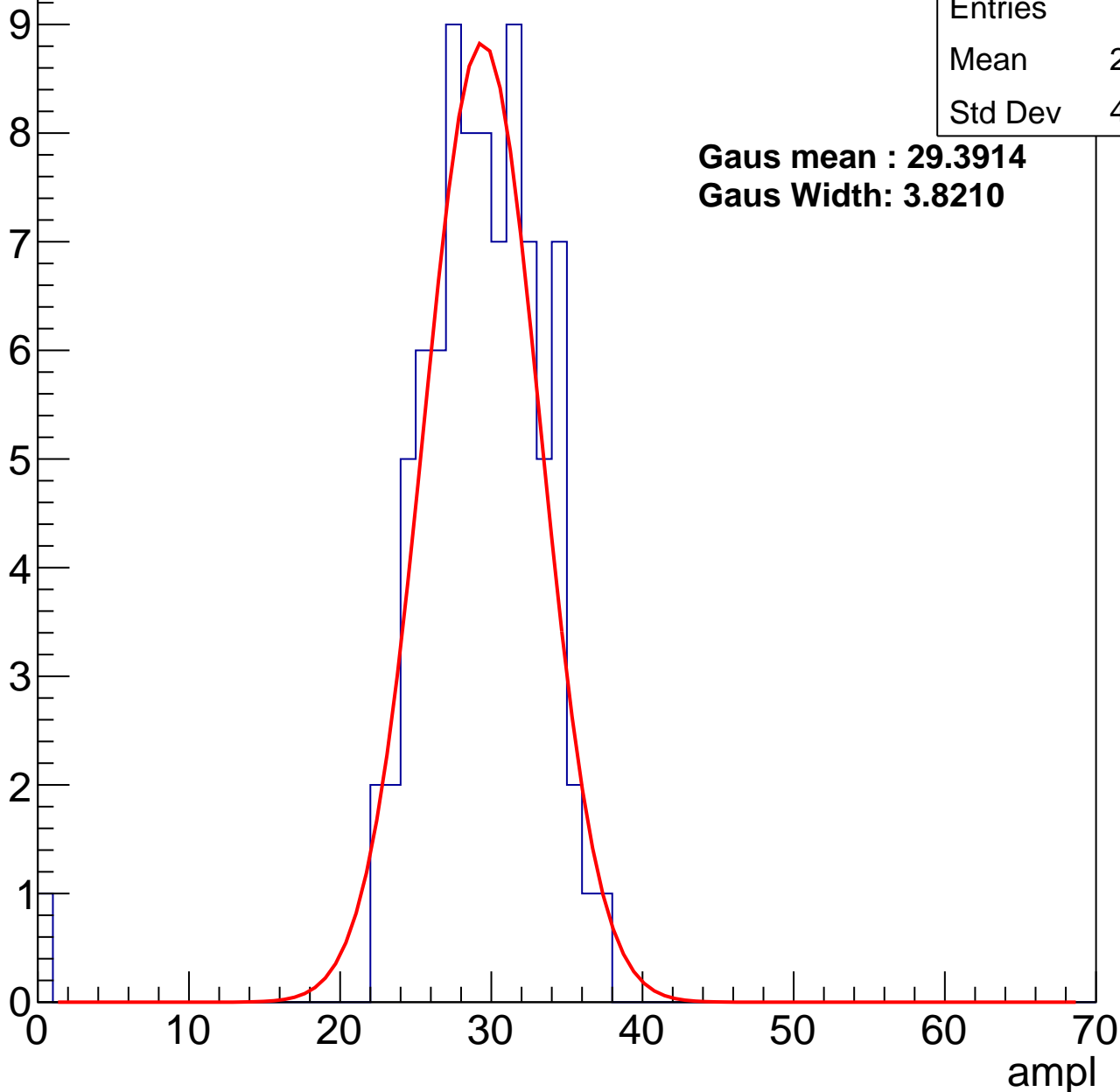
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 86 |
| Mean | 28.77 |
| Std Dev | 4.665 |

Gaus mean : 29.3914

Gaus Width: 3.8210



B1L102S, U15-ch19, adc1

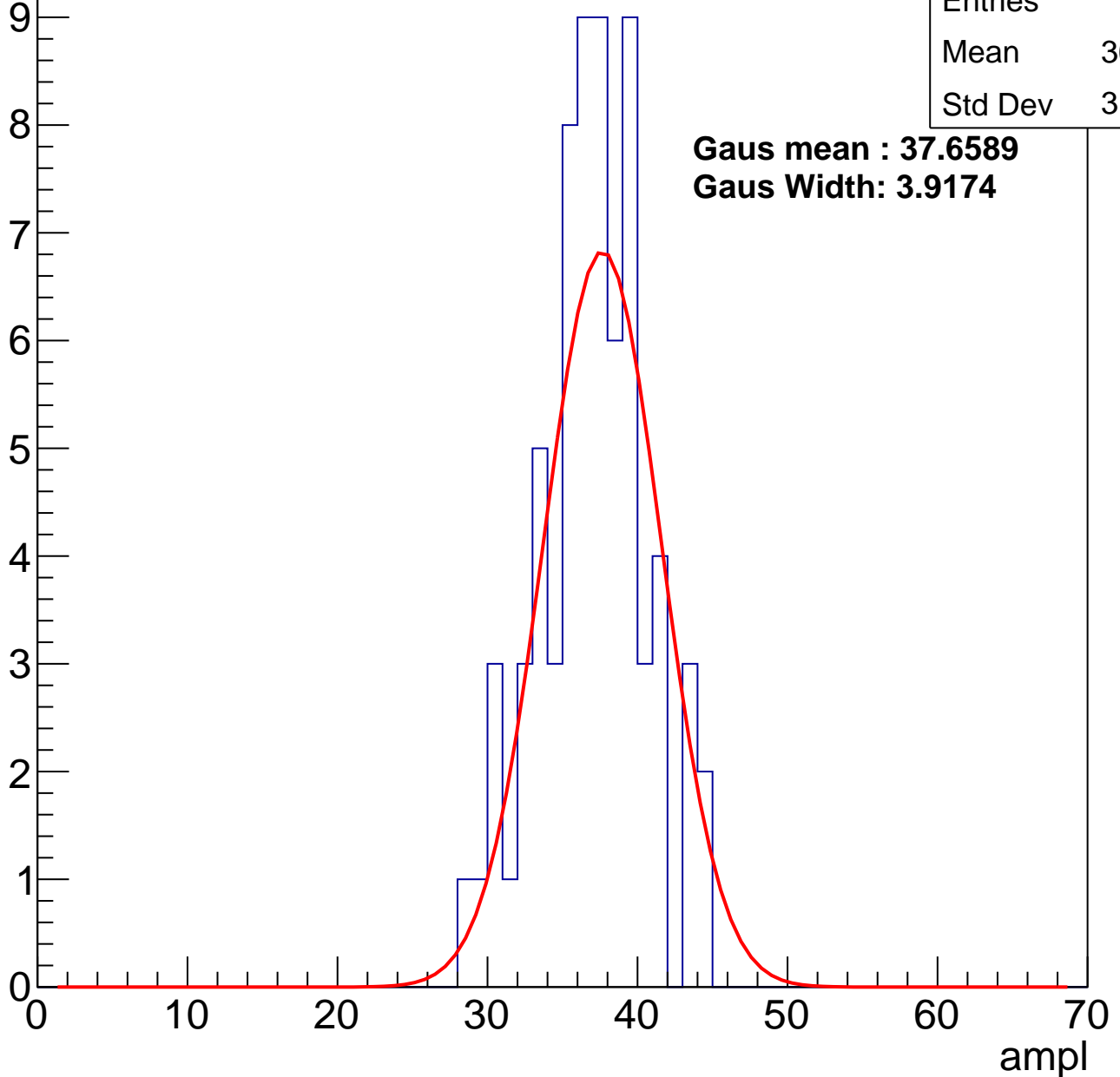
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 36.54 |
| Std Dev | 3.536 |

Gaus mean : 37.6589

Gaus Width: 3.9174



B1L102S, U15-ch19, adc2

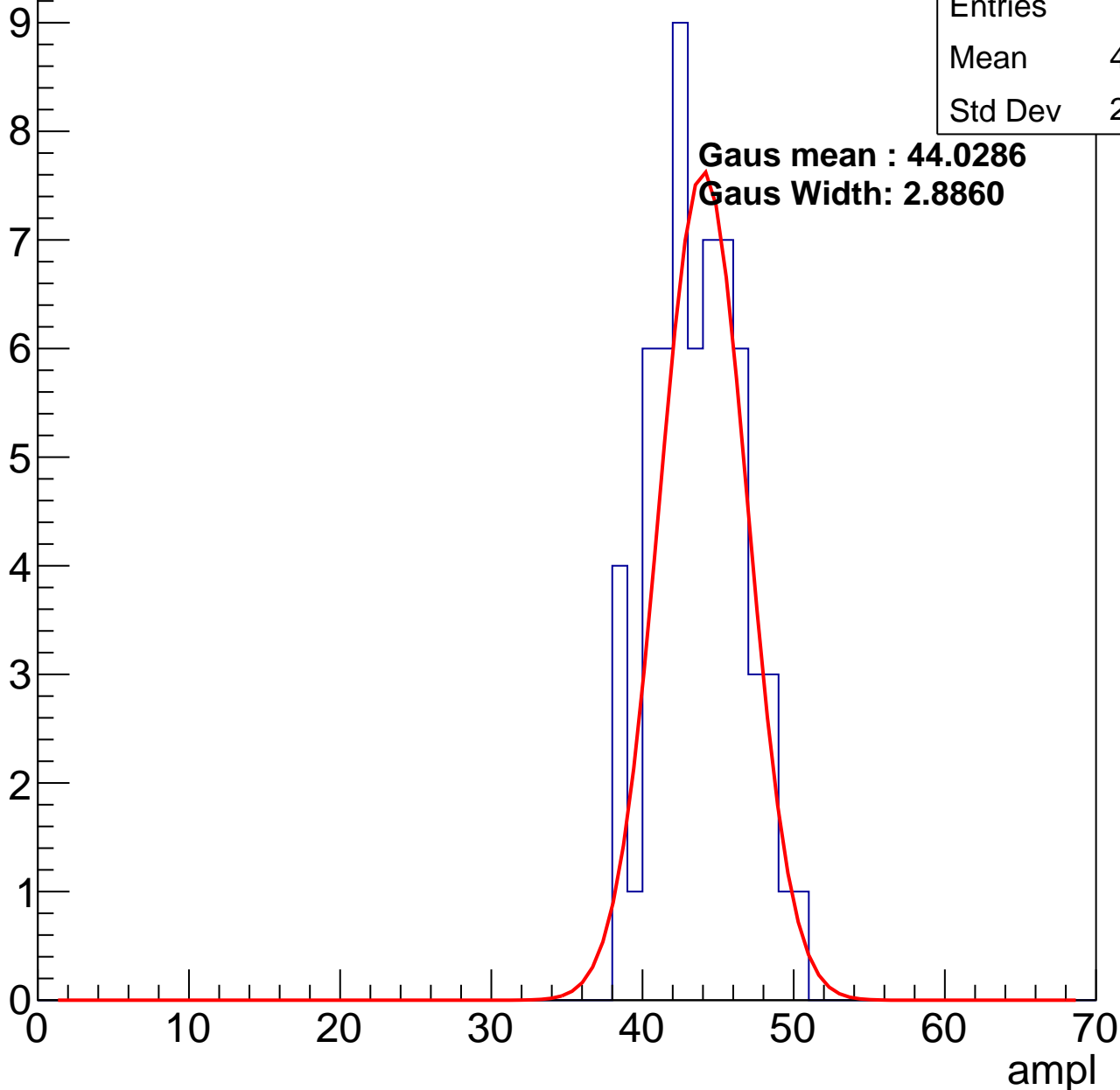
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 43.27 |
| Std Dev | 2.874 |

Gaus mean : 44.0286

Gaus Width: 2.8860

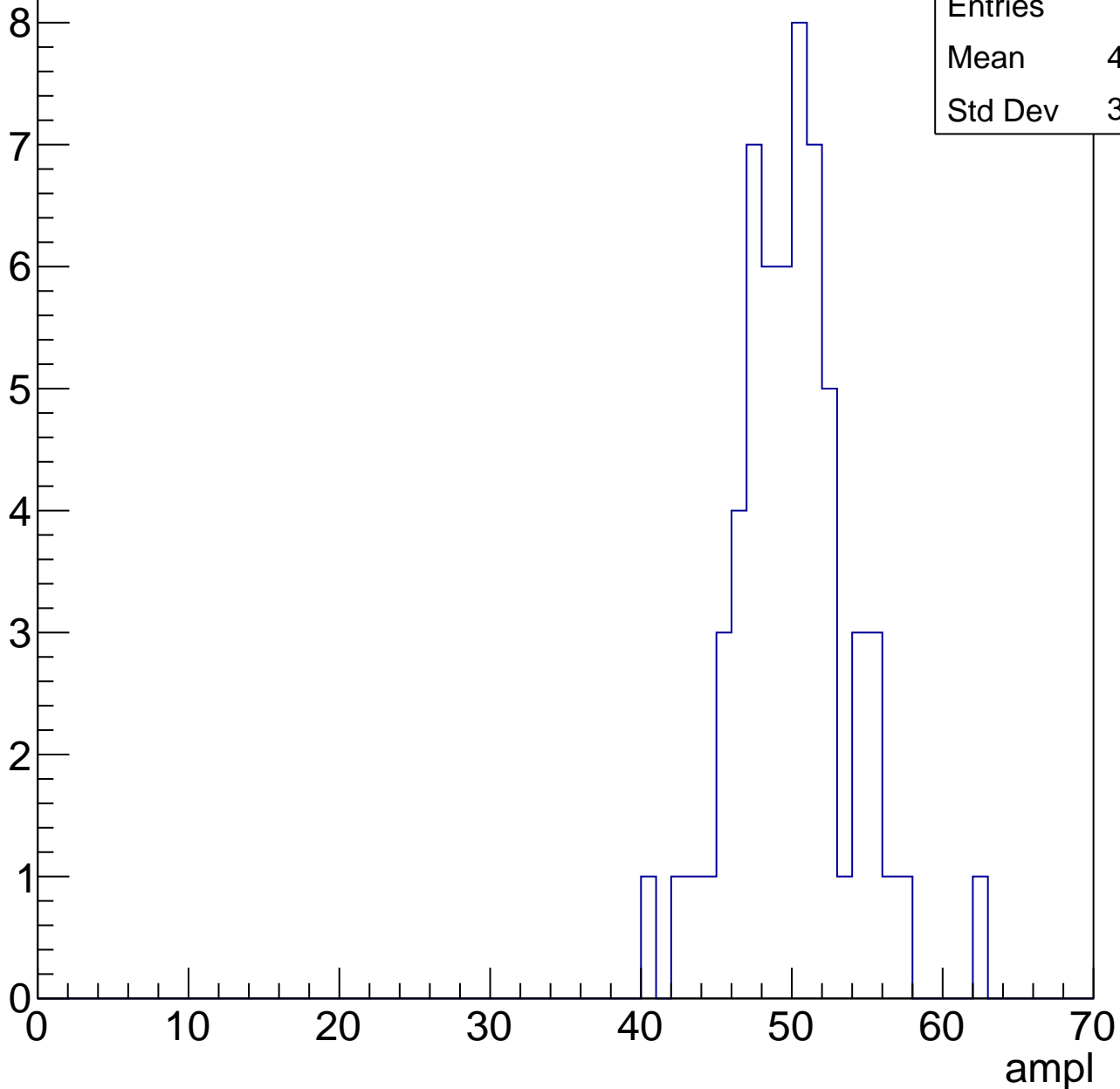


B1L102S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 49.52 |
| Std Dev | 3.775 |

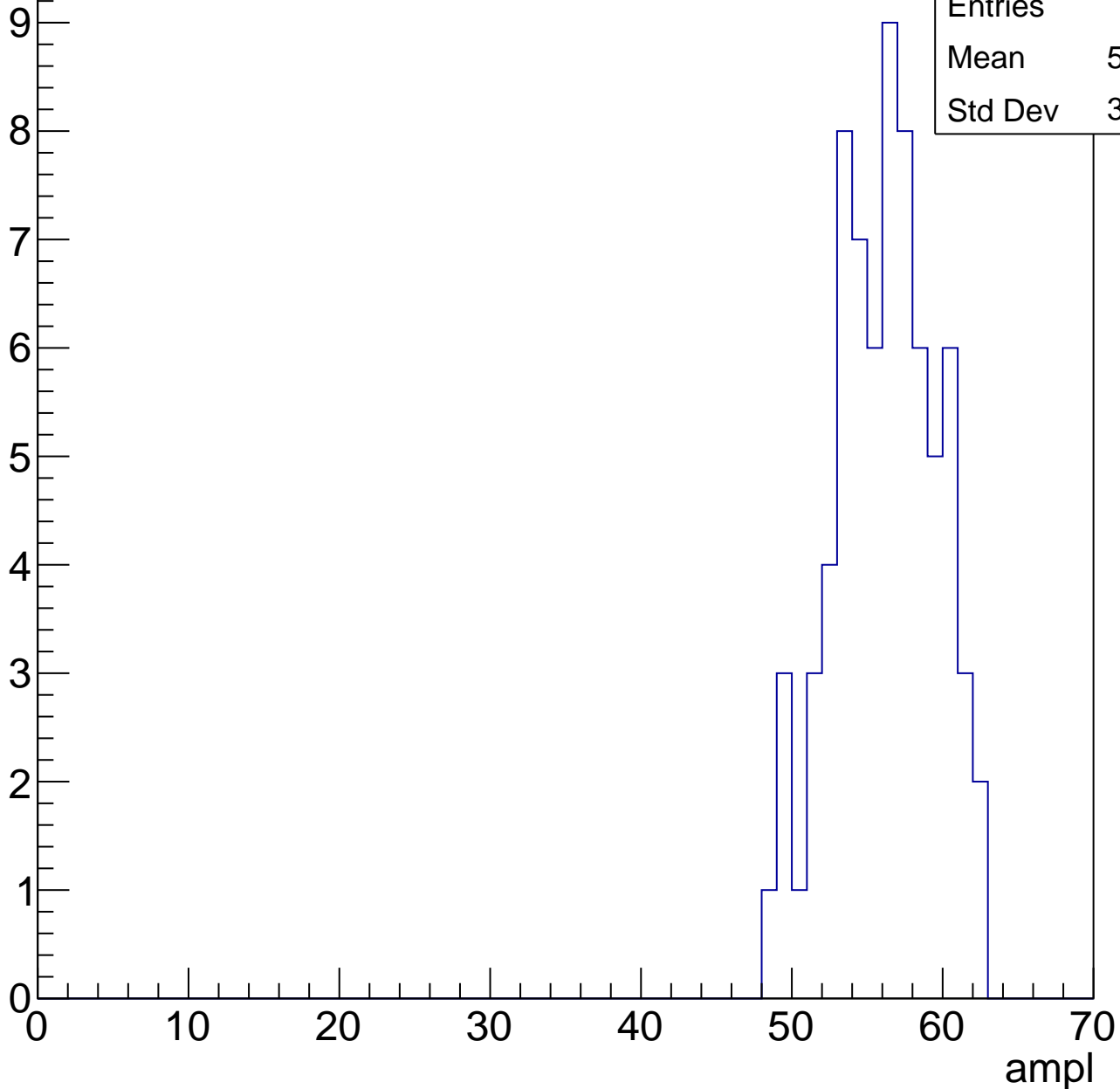


B1L102S, U15-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 55.67 |
| Std Dev | 3.342 |

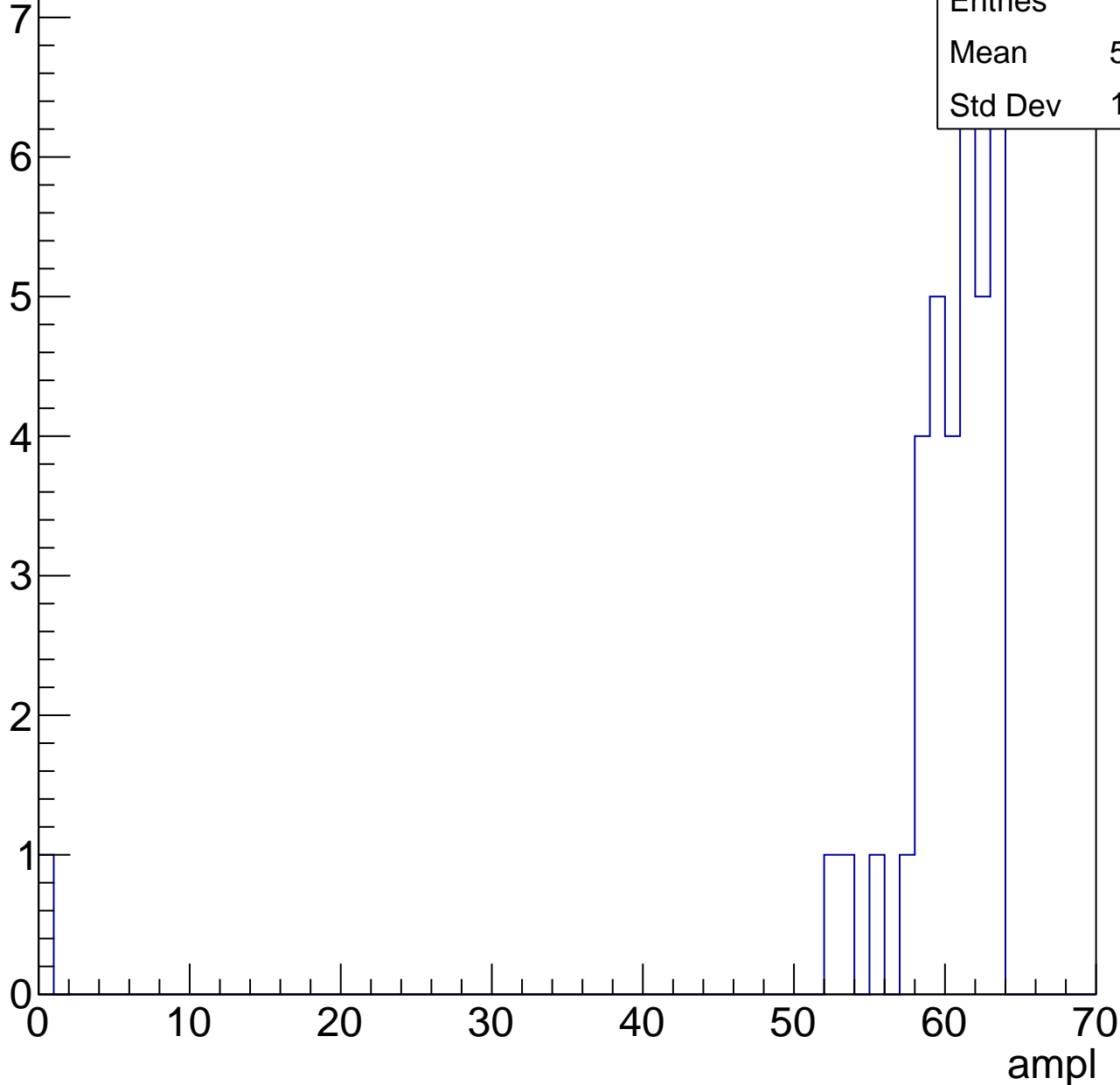


B1L102S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 58.43 |
| Std Dev | 10.09 |



B1L102S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.6

Std Dev

0.4899

B1L102S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

| | |
|---------|----|
| Entries | 1 |
| Mean | 21 |
| Std Dev | 0 |

ampl

B1L102S, U15-ch20, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 28.16 |
| Std Dev | 3.964 |

Gaus mean : 28.4887

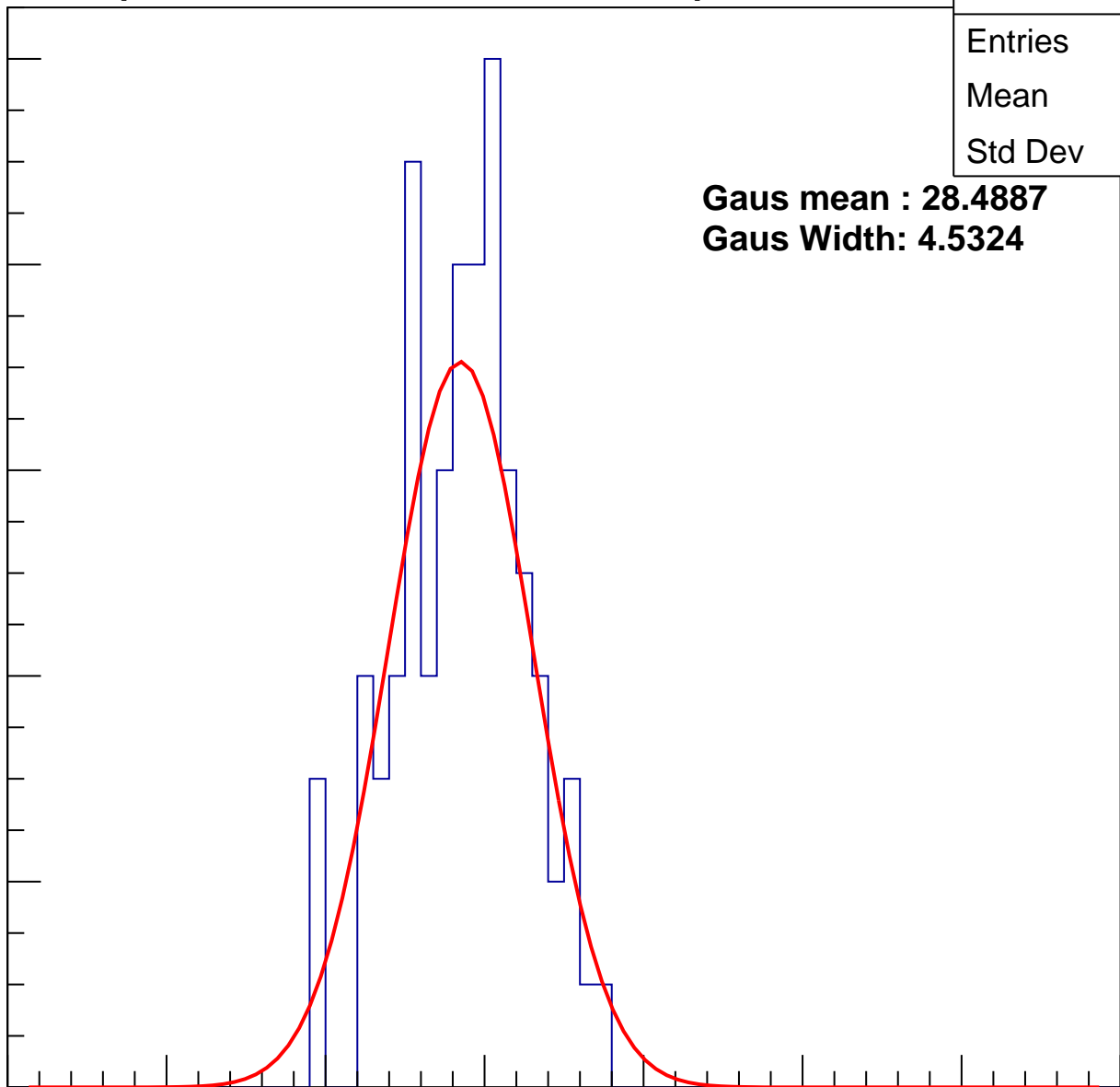
Gaus Width: 4.5324

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch20, adc1

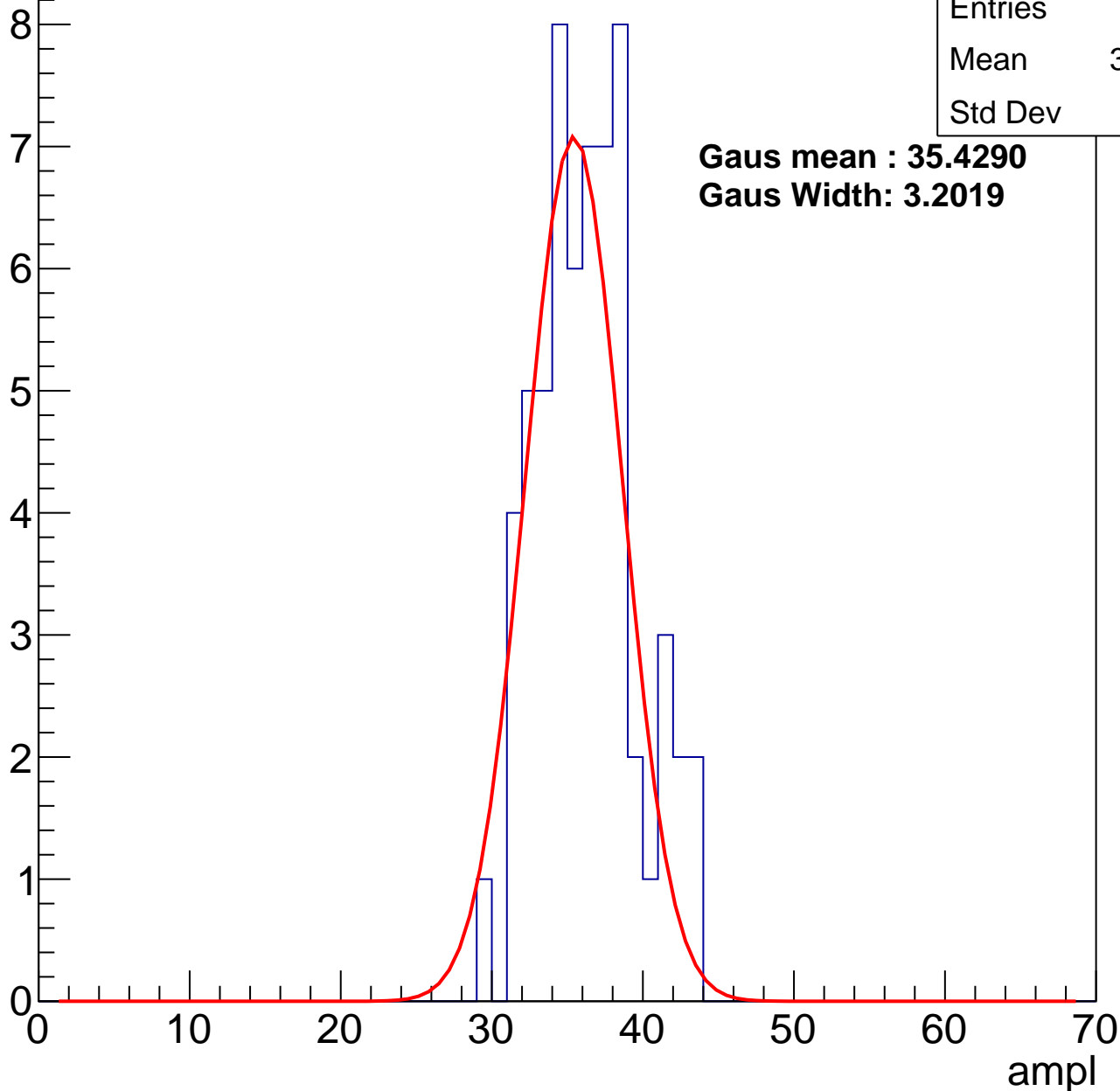
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 35.84 |
| Std Dev | 3.21 |

Gaus mean : 35.4290

Gaus Width: 3.2019



B1L102S, U15-ch20, adc2

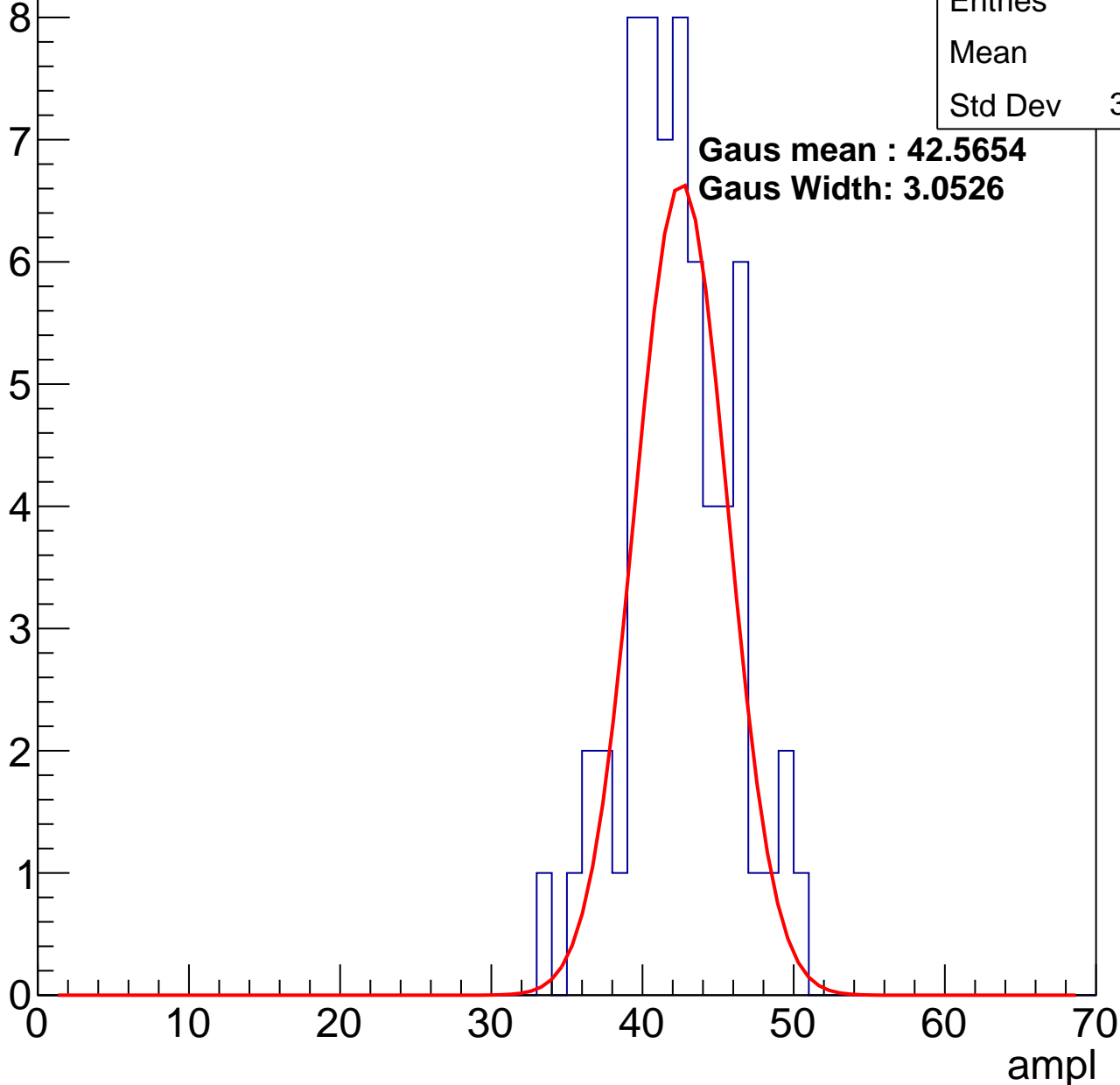
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.9 |
| Std Dev | 3.463 |

Gaus mean : 42.5654

Gaus Width: 3.0526

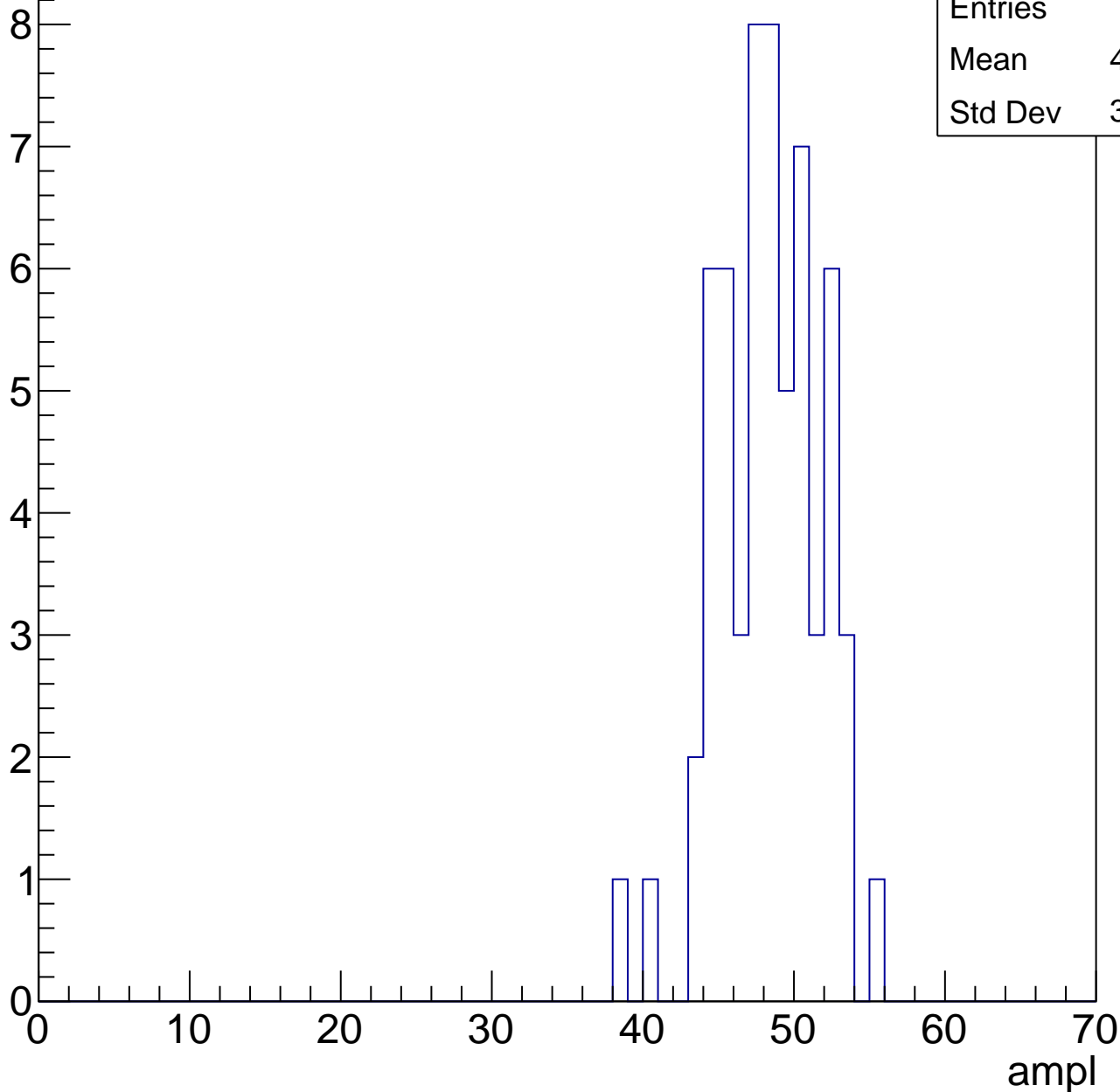


B1L102S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.83 |
| Std Dev | 3.322 |

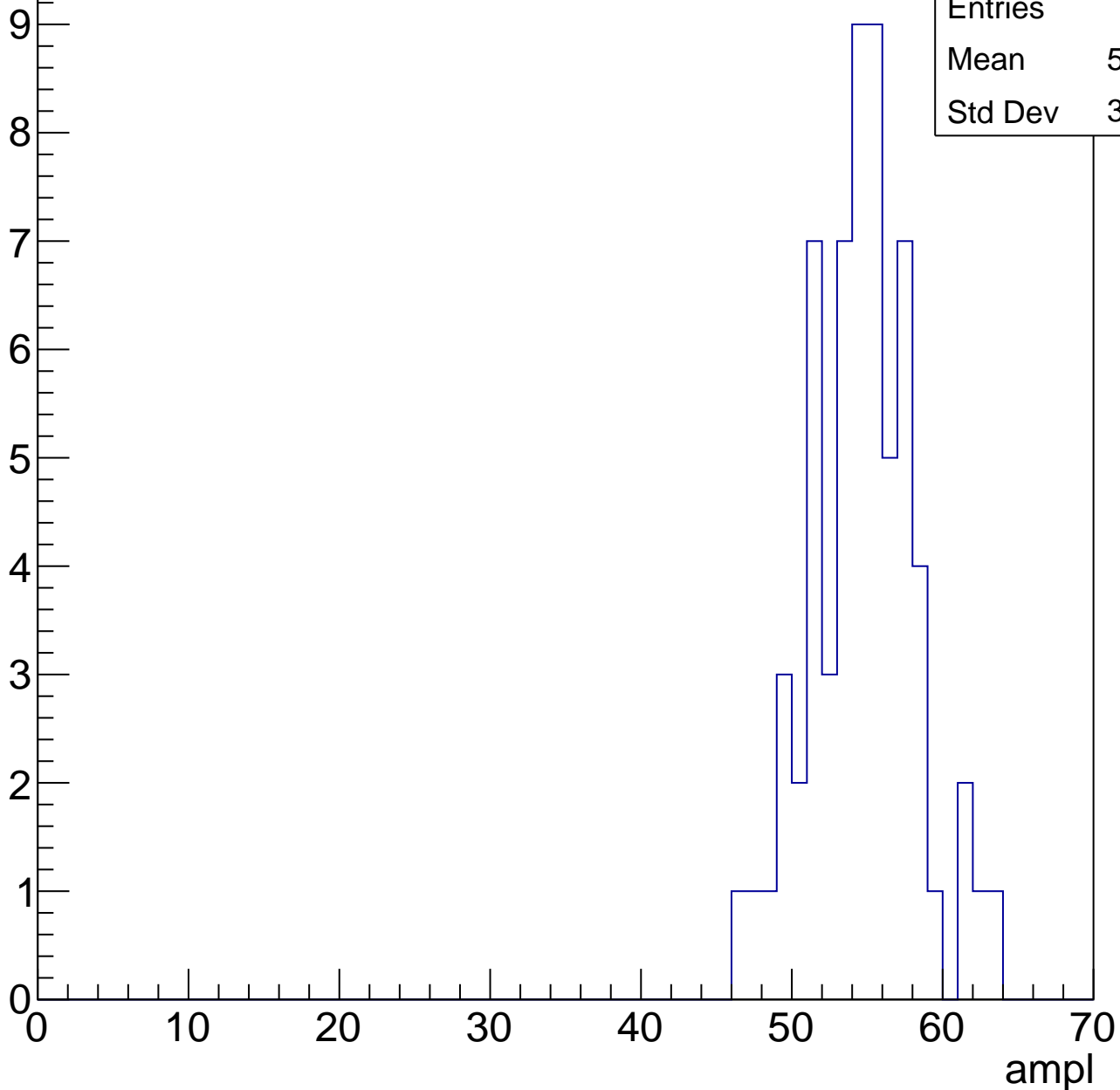


B1L102S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 54.22 |
| Std Dev | 3.444 |

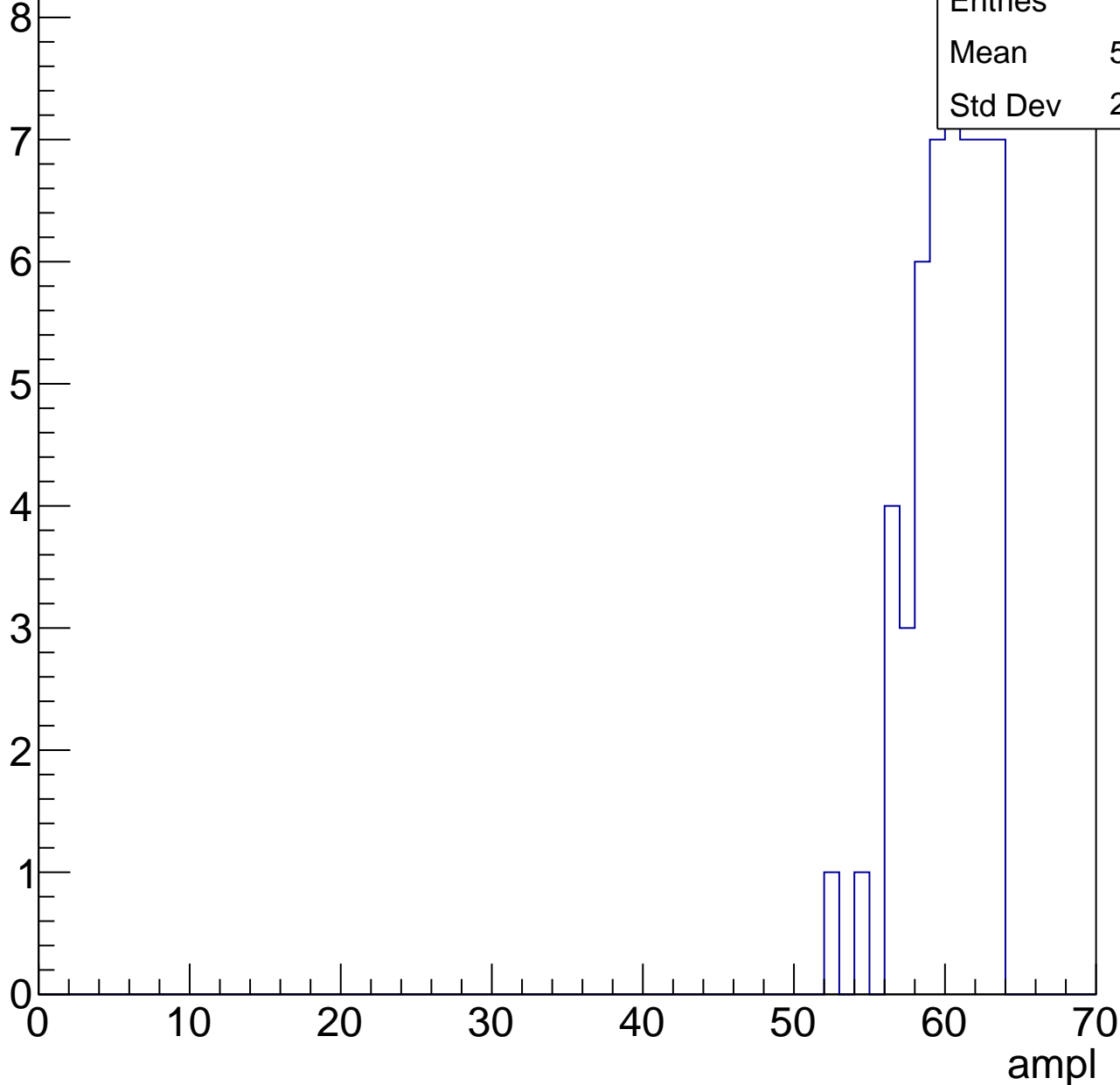


B1L102S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.69 |
| Std Dev | 2.485 |



B1L102S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

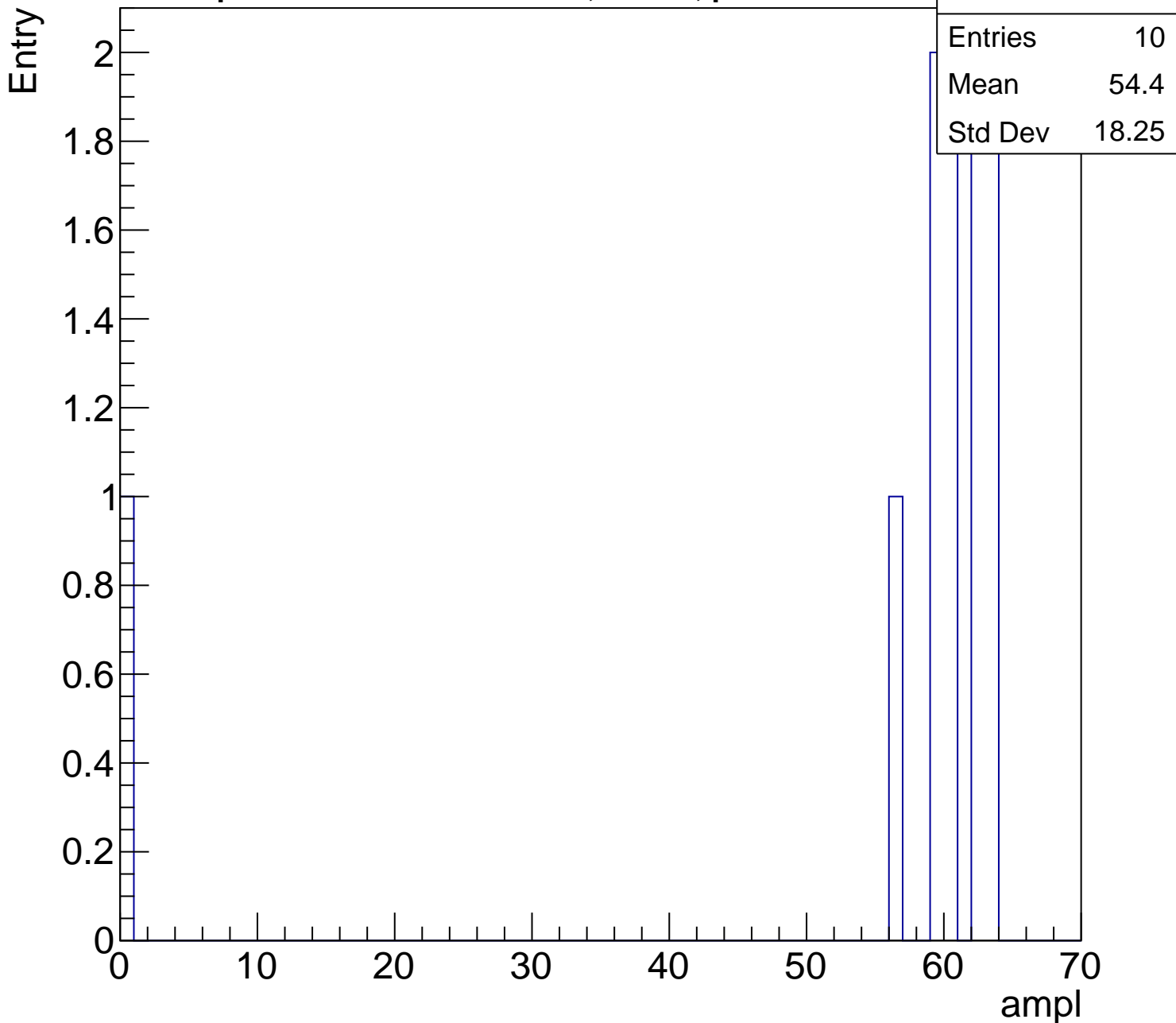
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 10 |
| Mean | 54.4 |
| Std Dev | 18.25 |

0 10 20 30 40 50 60 70

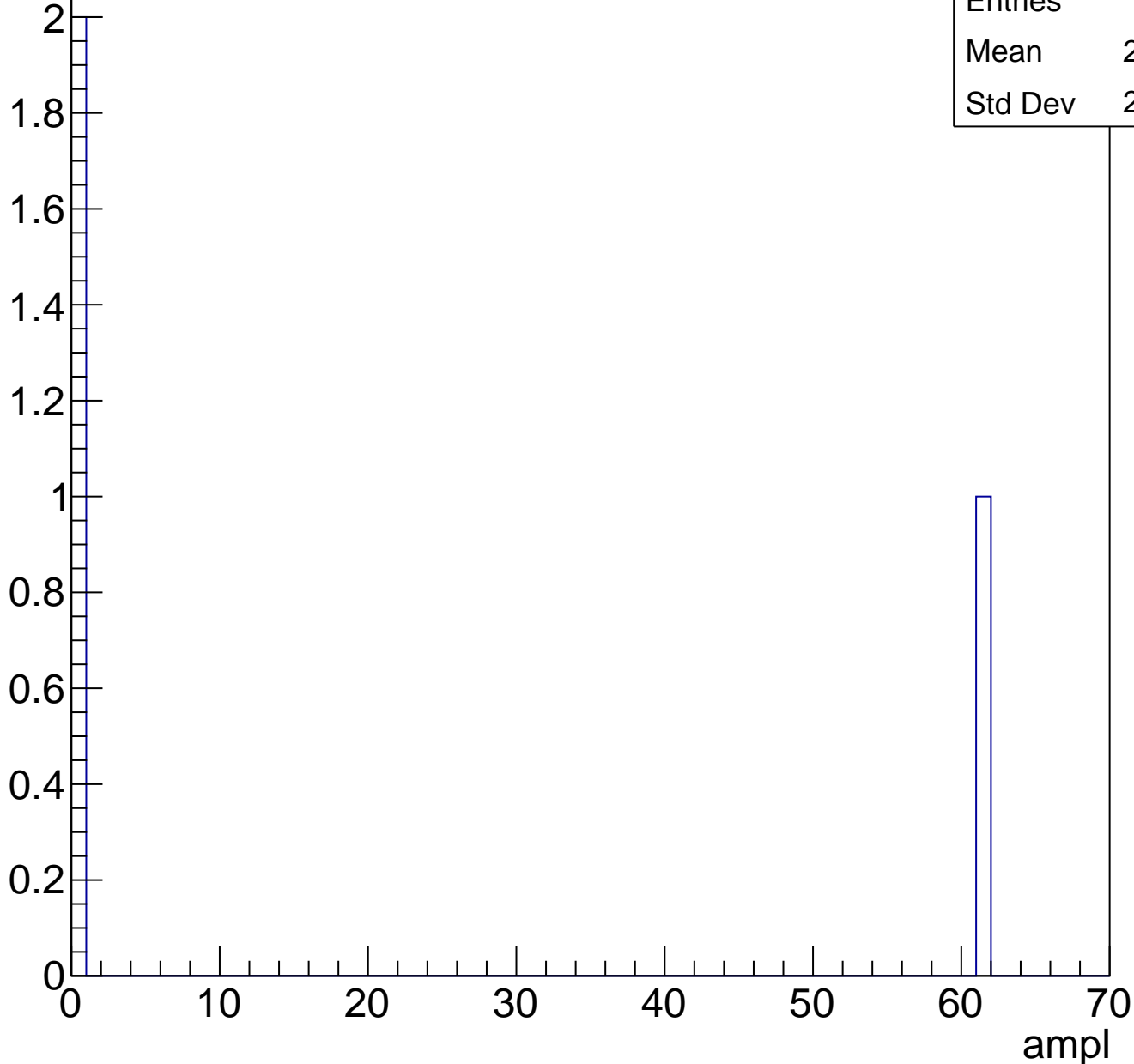
ampl



B1L102S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch21, adc0

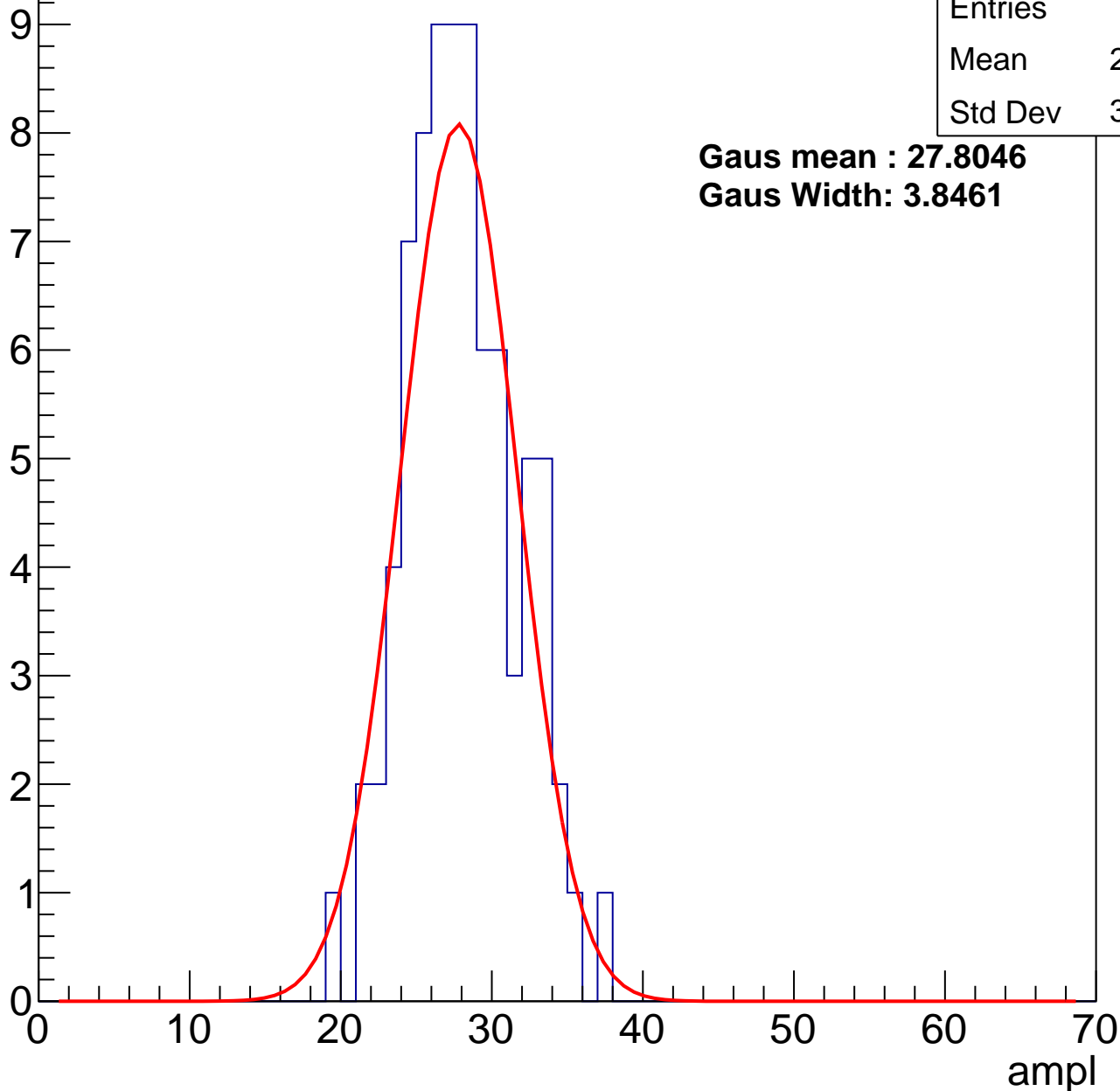
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 27.57 |
| Std Dev | 3.587 |

Gaus mean : 27.8046

Gaus Width: 3.8461



B1L102S, U15-ch21, adc1

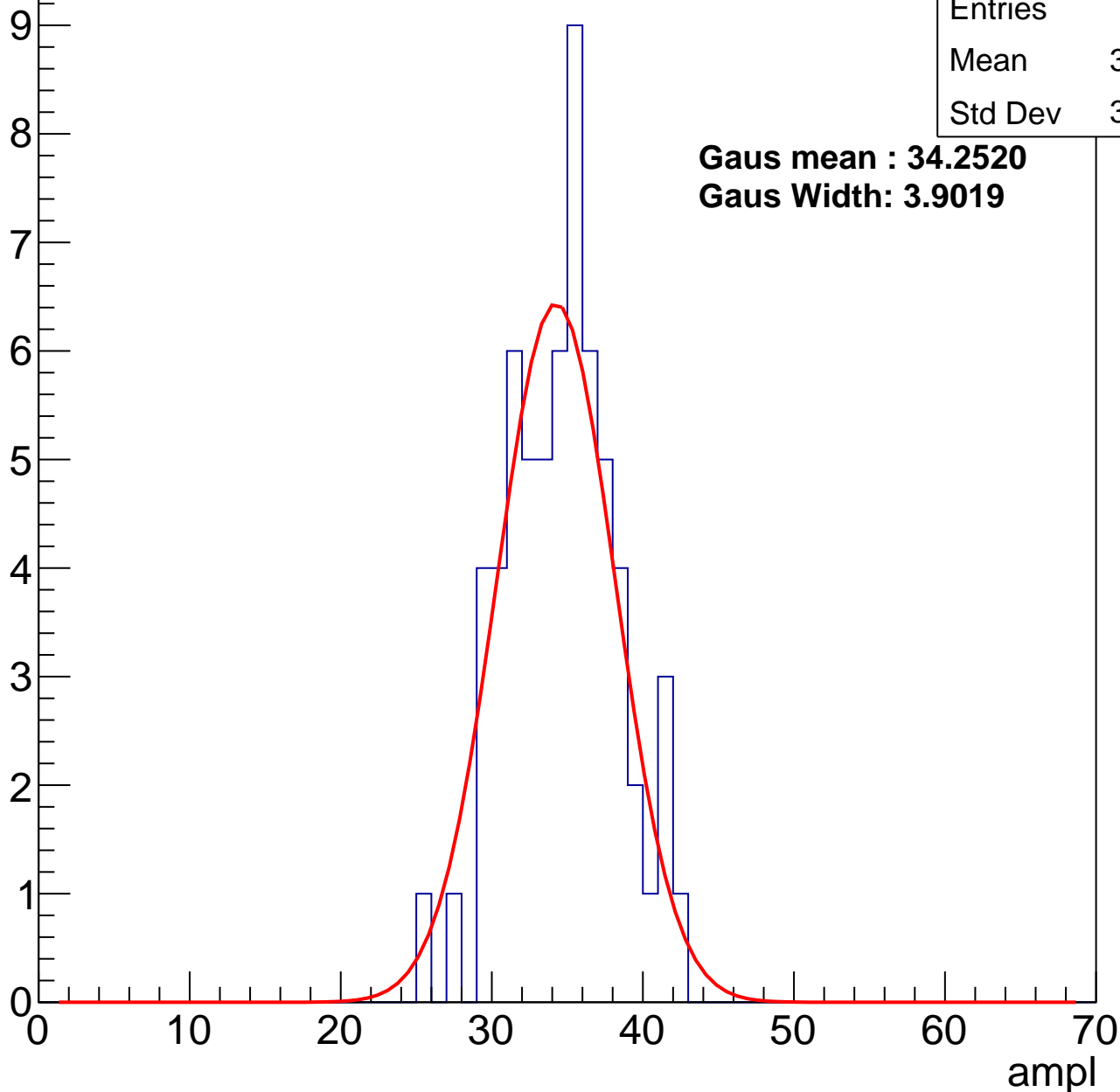
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 34.19 |
| Std Dev | 3.585 |

Gaus mean : 34.2520

Gaus Width: 3.9019



B1L102S, U15-ch21, adc2

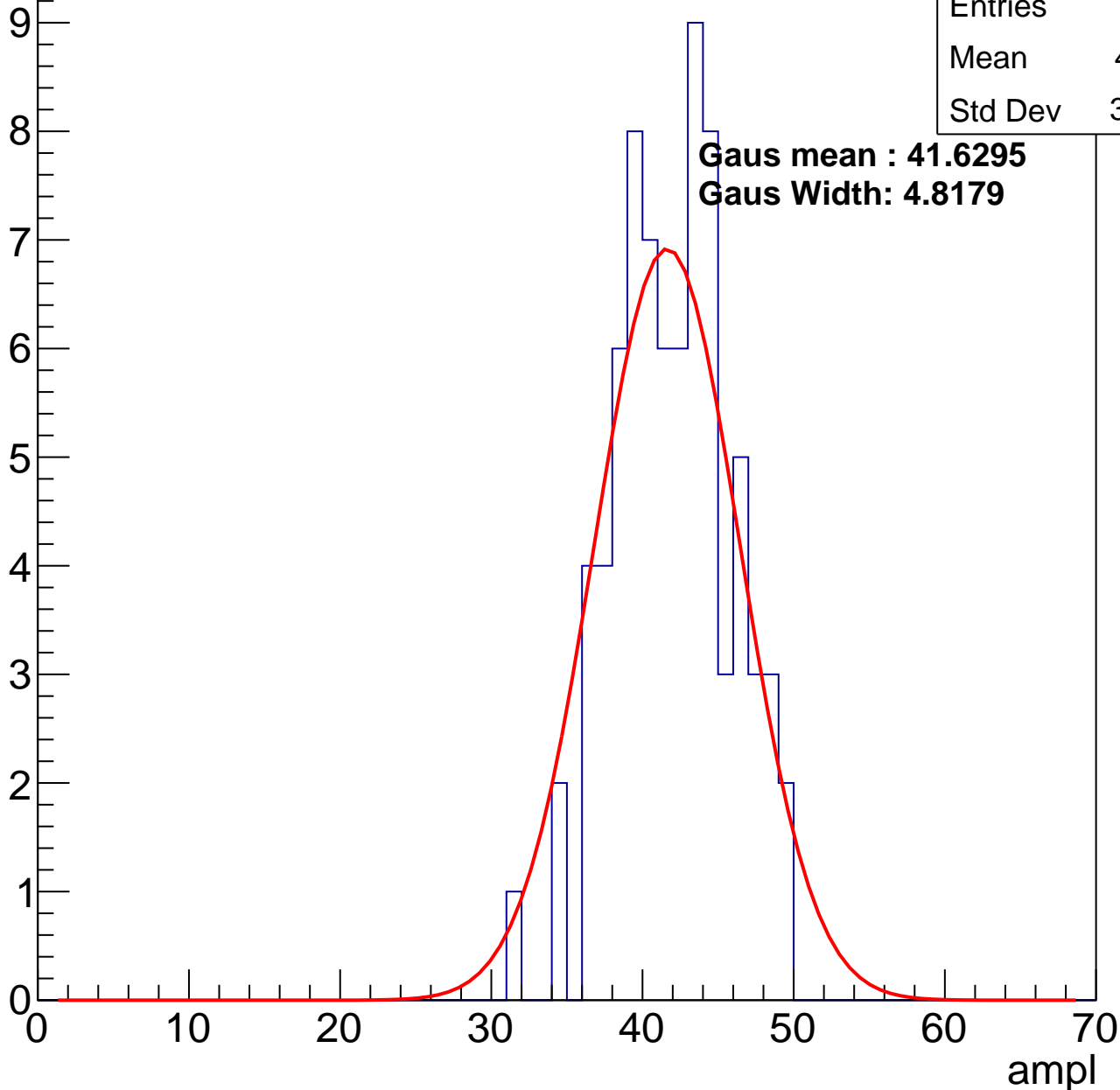
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 41.51 |
| Std Dev | 3.789 |

Gaus mean : 41.6295

Gaus Width: 4.8179

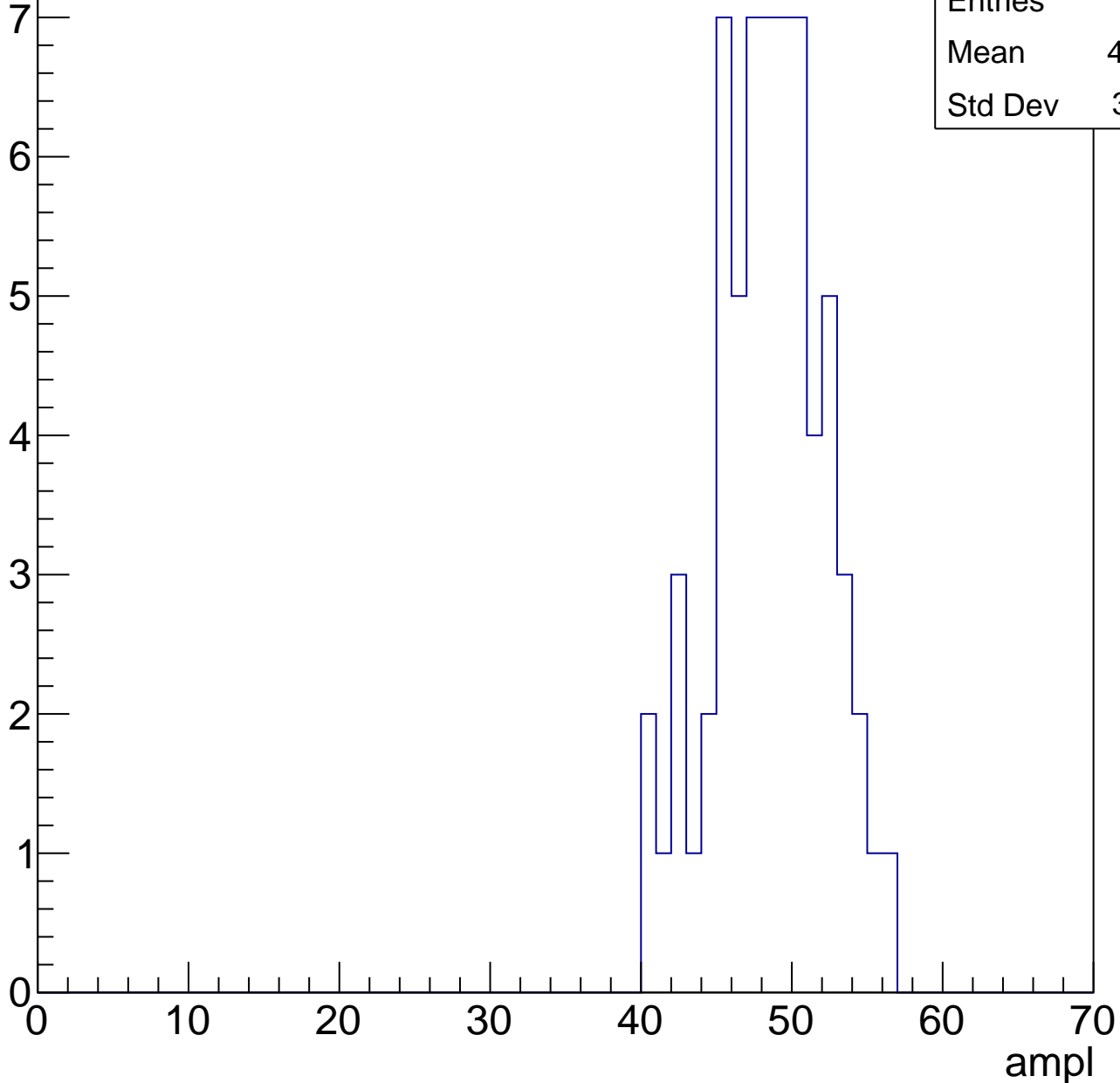


B1L102S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 48.05 |
| Std Dev | 3.601 |

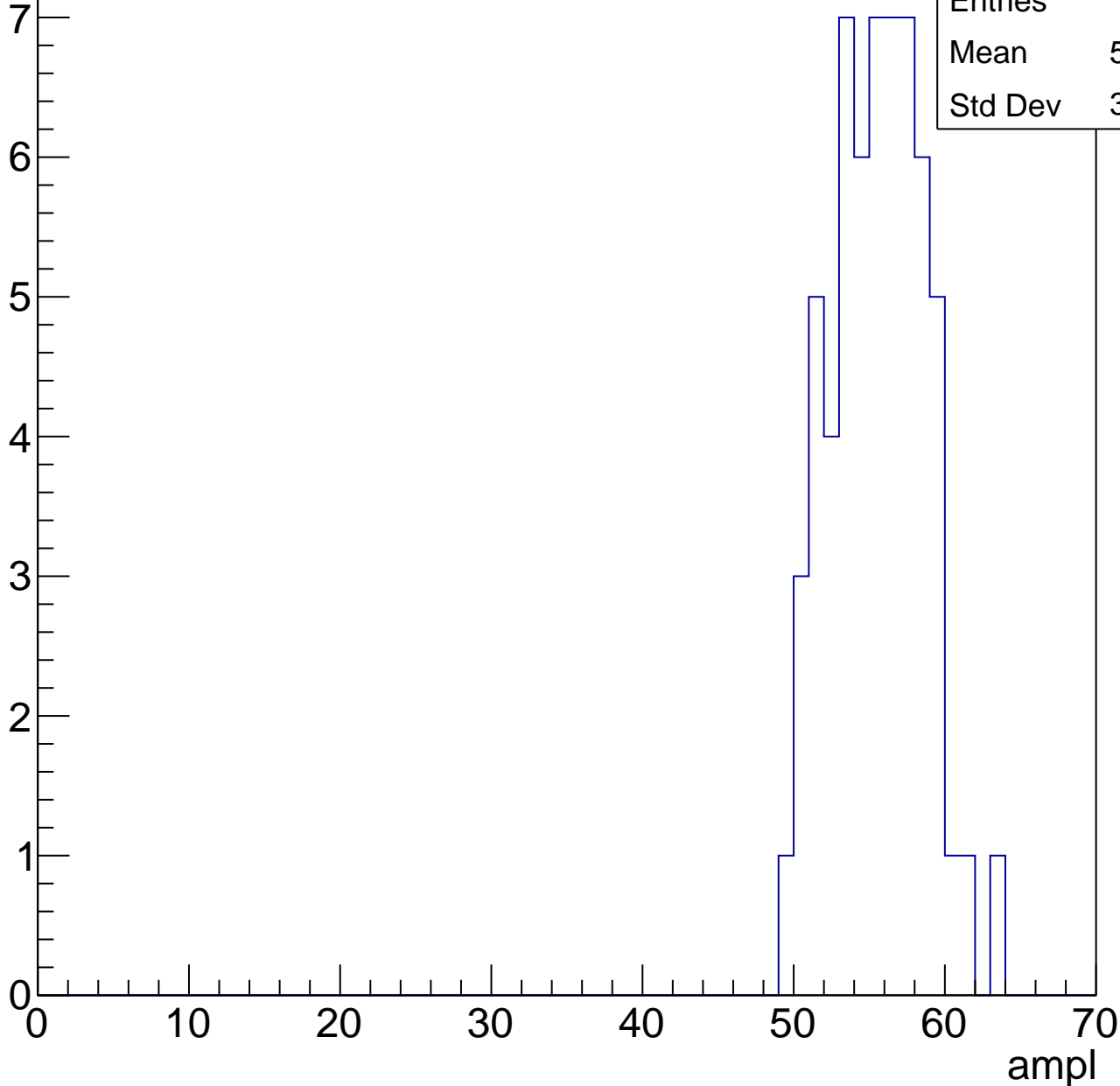


B1L102S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

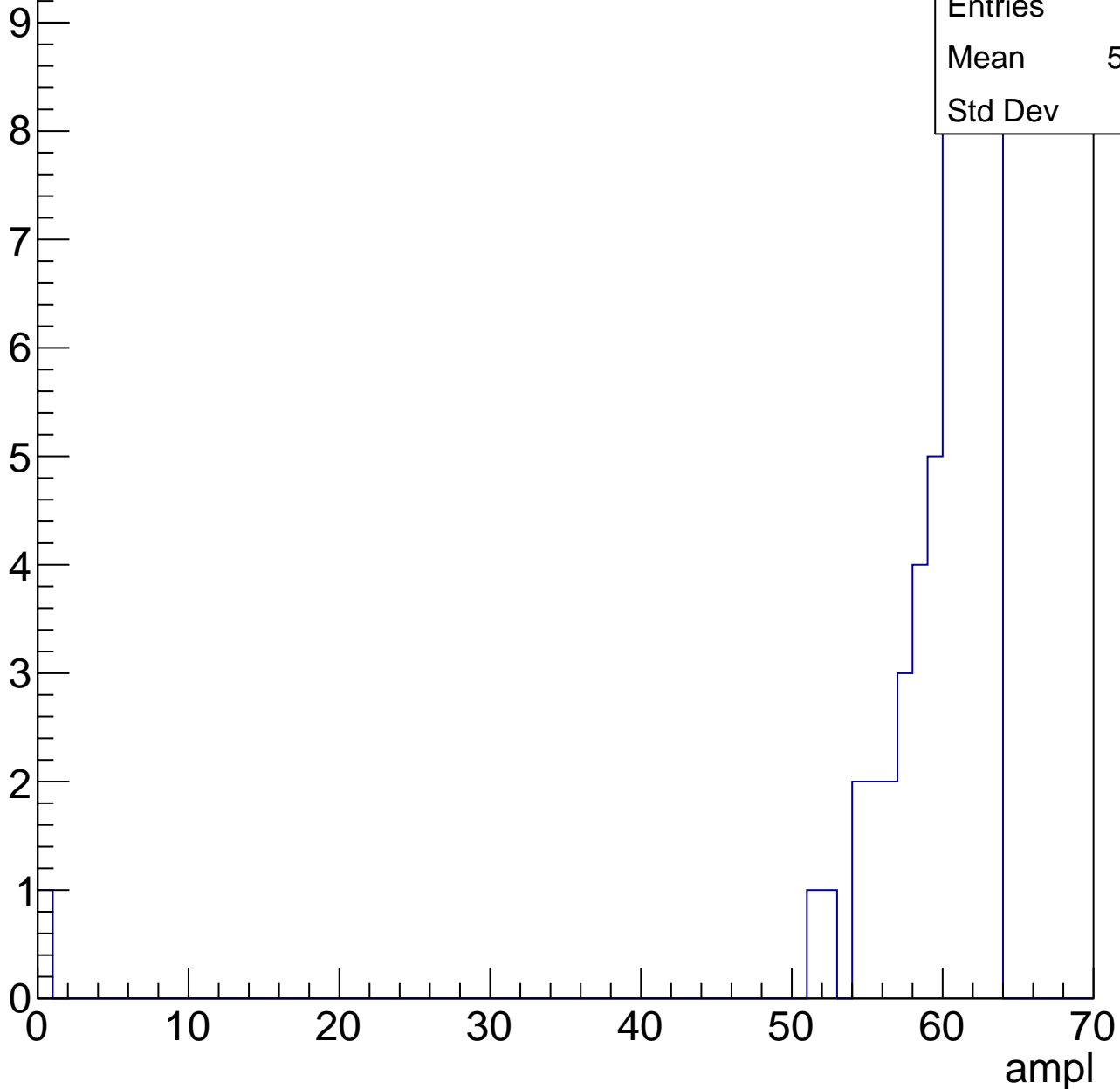
| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.08 |
| Std Dev | 3.015 |



B1L102S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch22, adc0

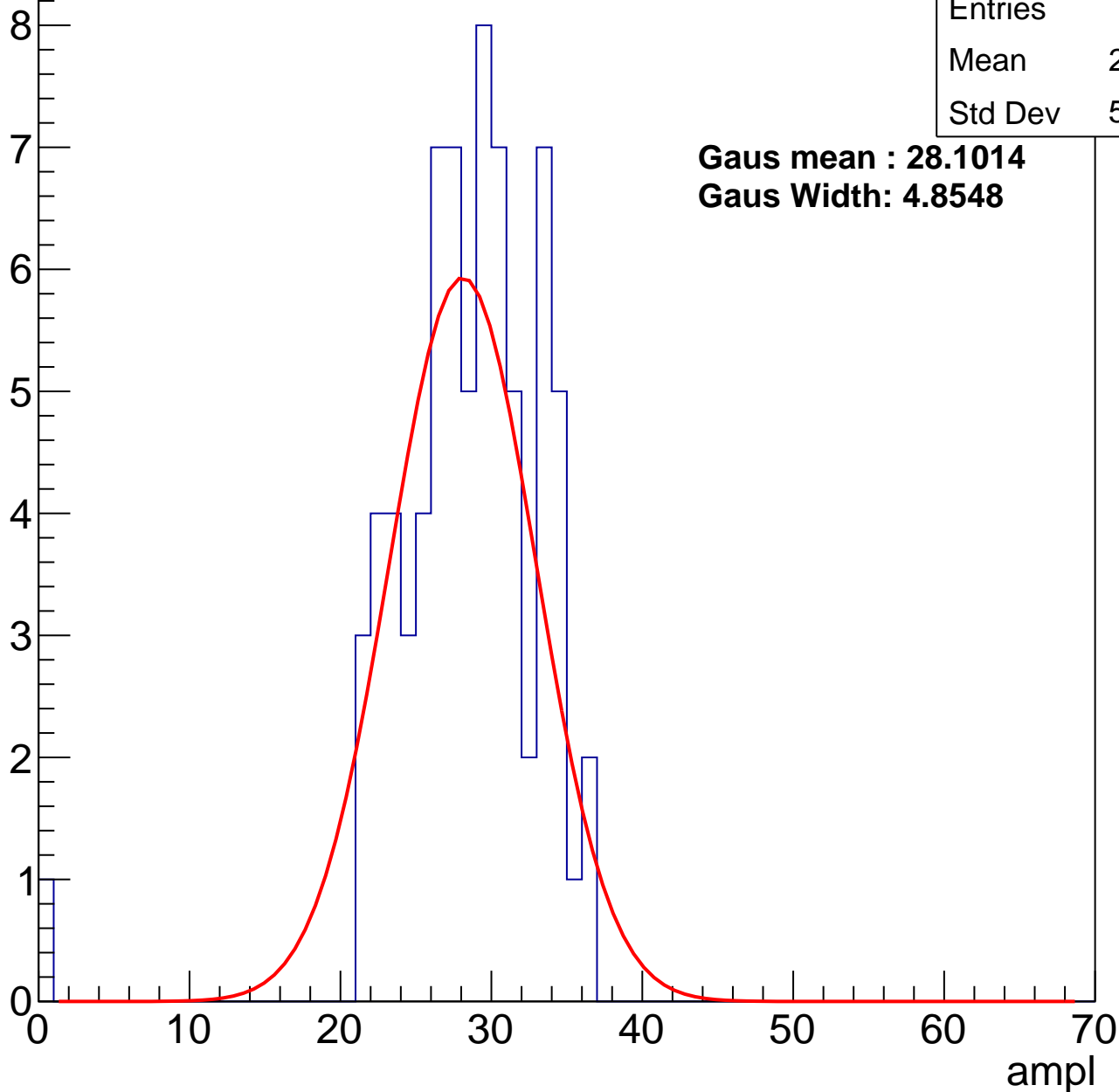
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 27.93 |
| Std Dev | 5.084 |

Gaus mean : 28.1014

Gaus Width: 4.8548



B1L102S, U15-ch22, adc1

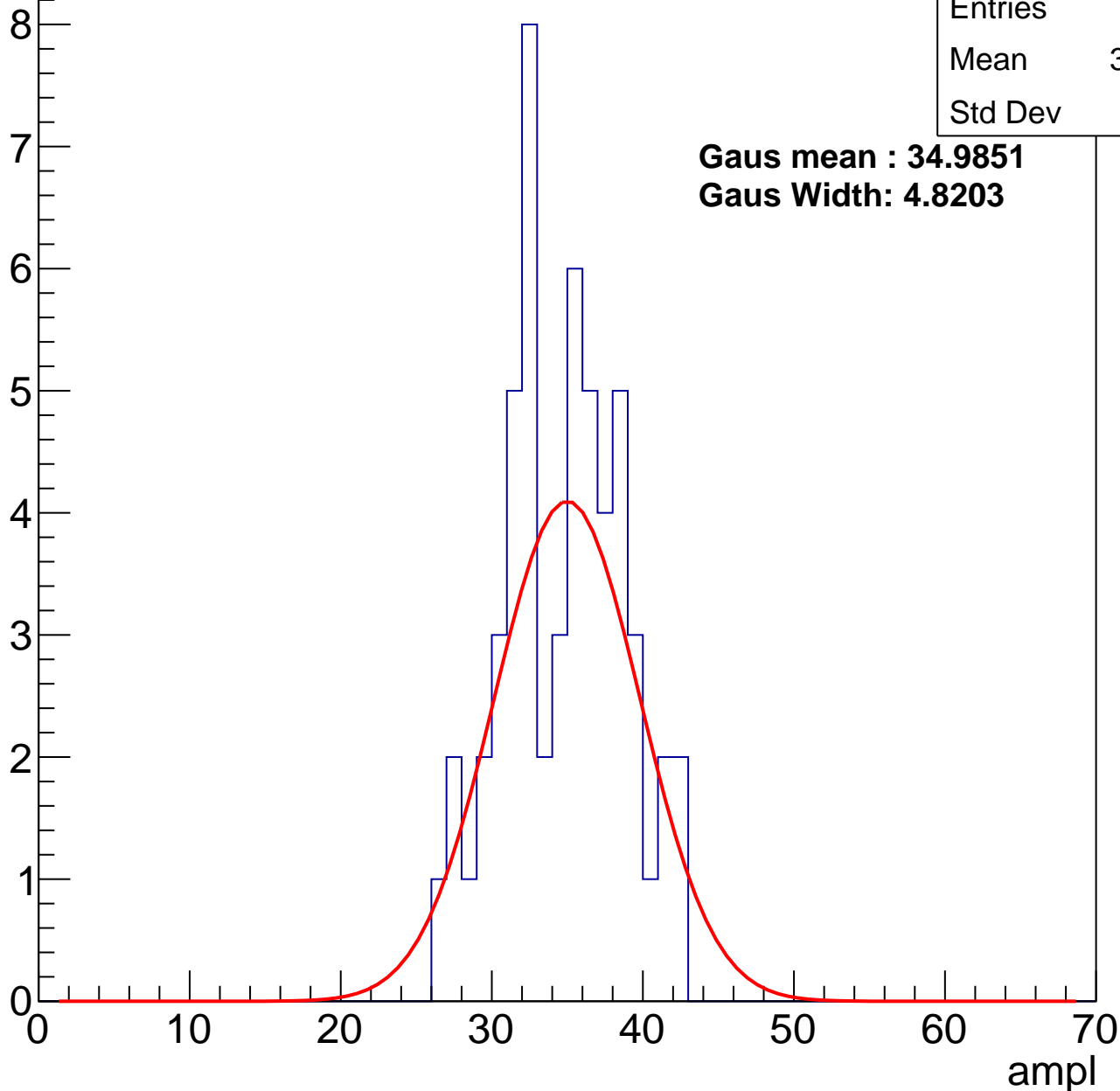
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 34.29 |
| Std Dev | 3.93 |

Gaus mean : 34.9851

Gaus Width: 4.8203



B1L102S, U15-ch22, adc2

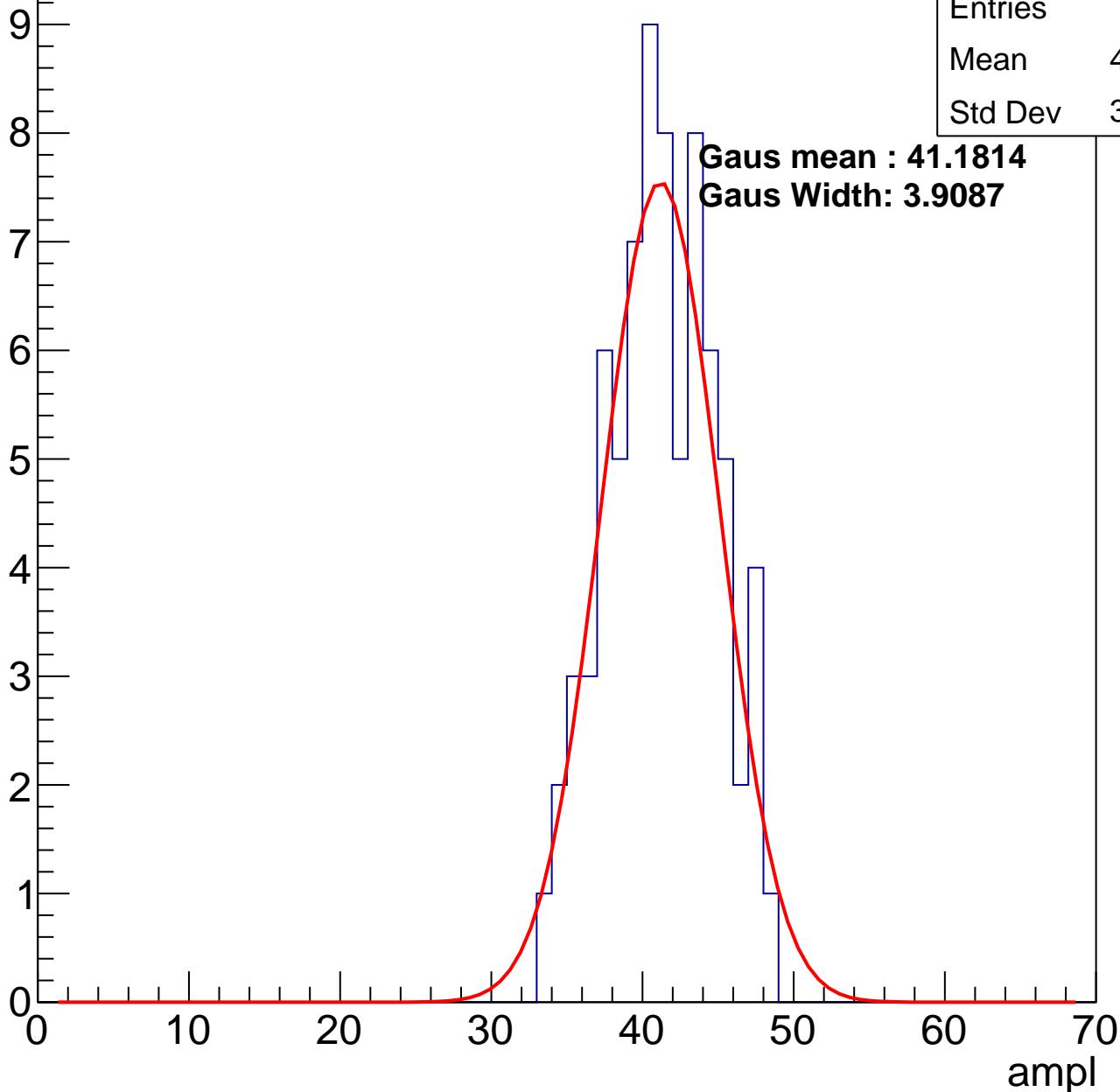
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 40.77 |
| Std Dev | 3.527 |

Gaus mean : 41.1814

Gaus Width: 3.9087

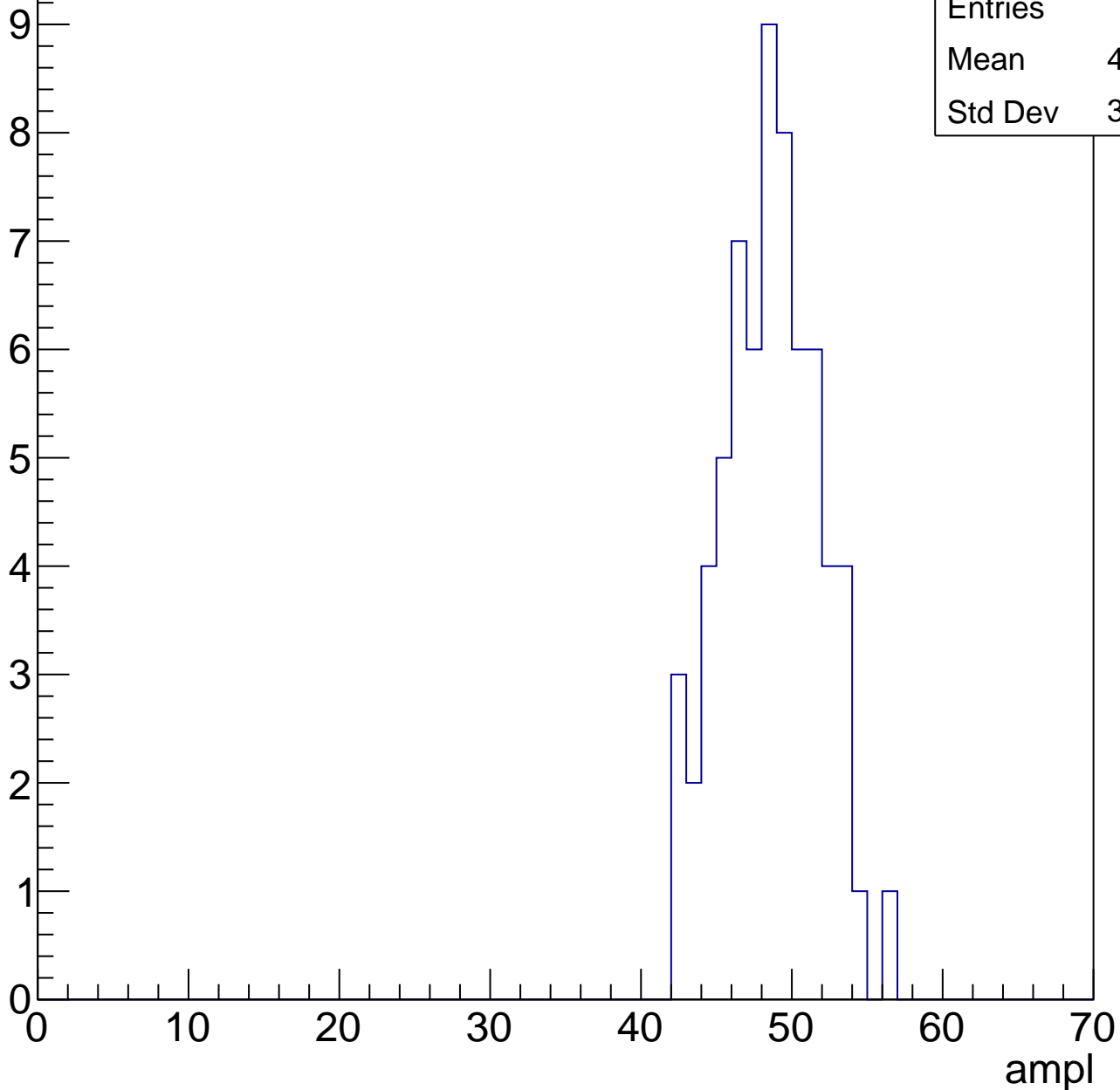


B1L102S, U15-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.14 |
| Std Dev | 3.138 |

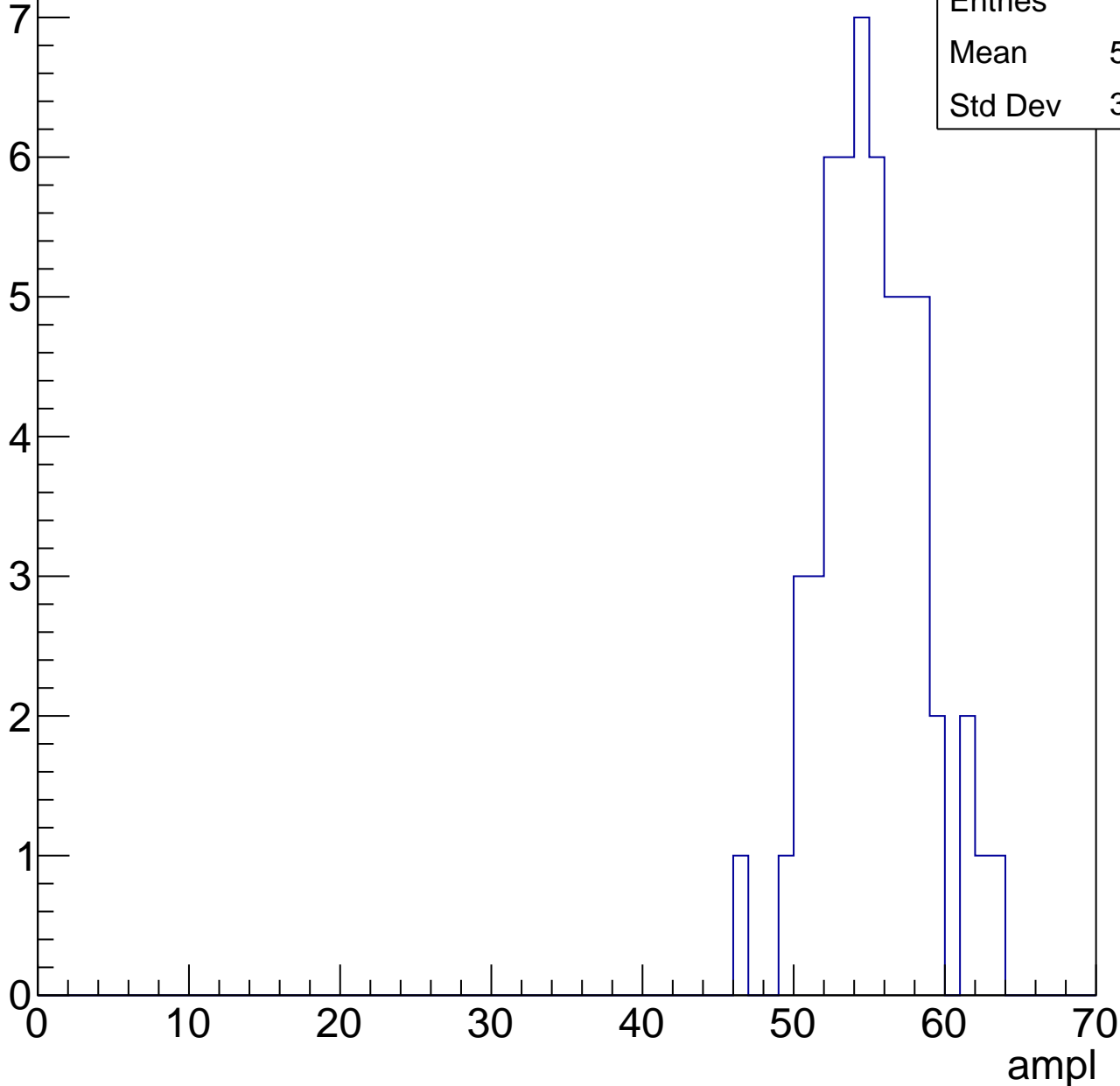


B1L102S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 54.74 |
| Std Dev | 3.357 |

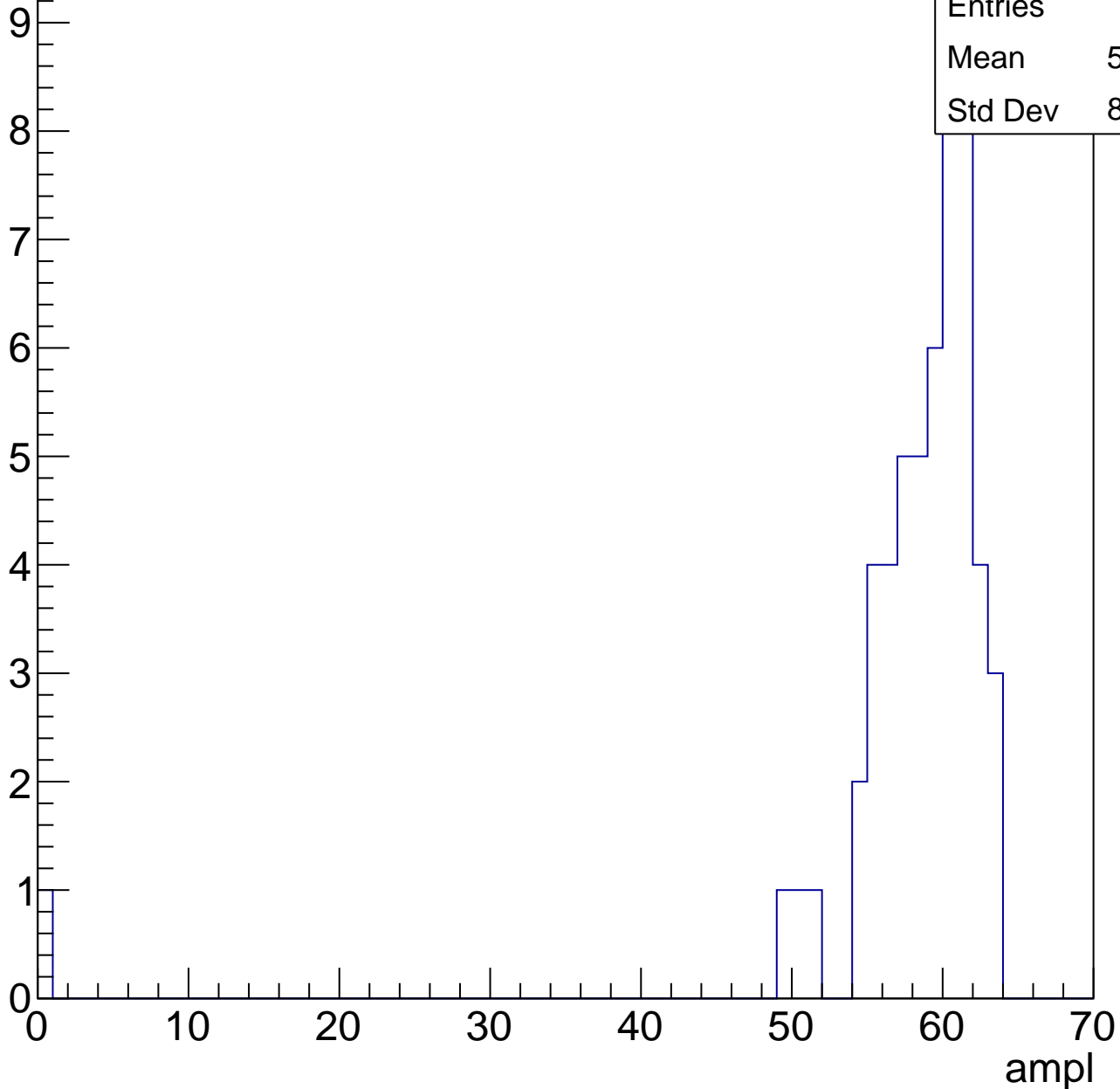


B1L102S, U15-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 57.33 |
| Std Dev | 8.472 |

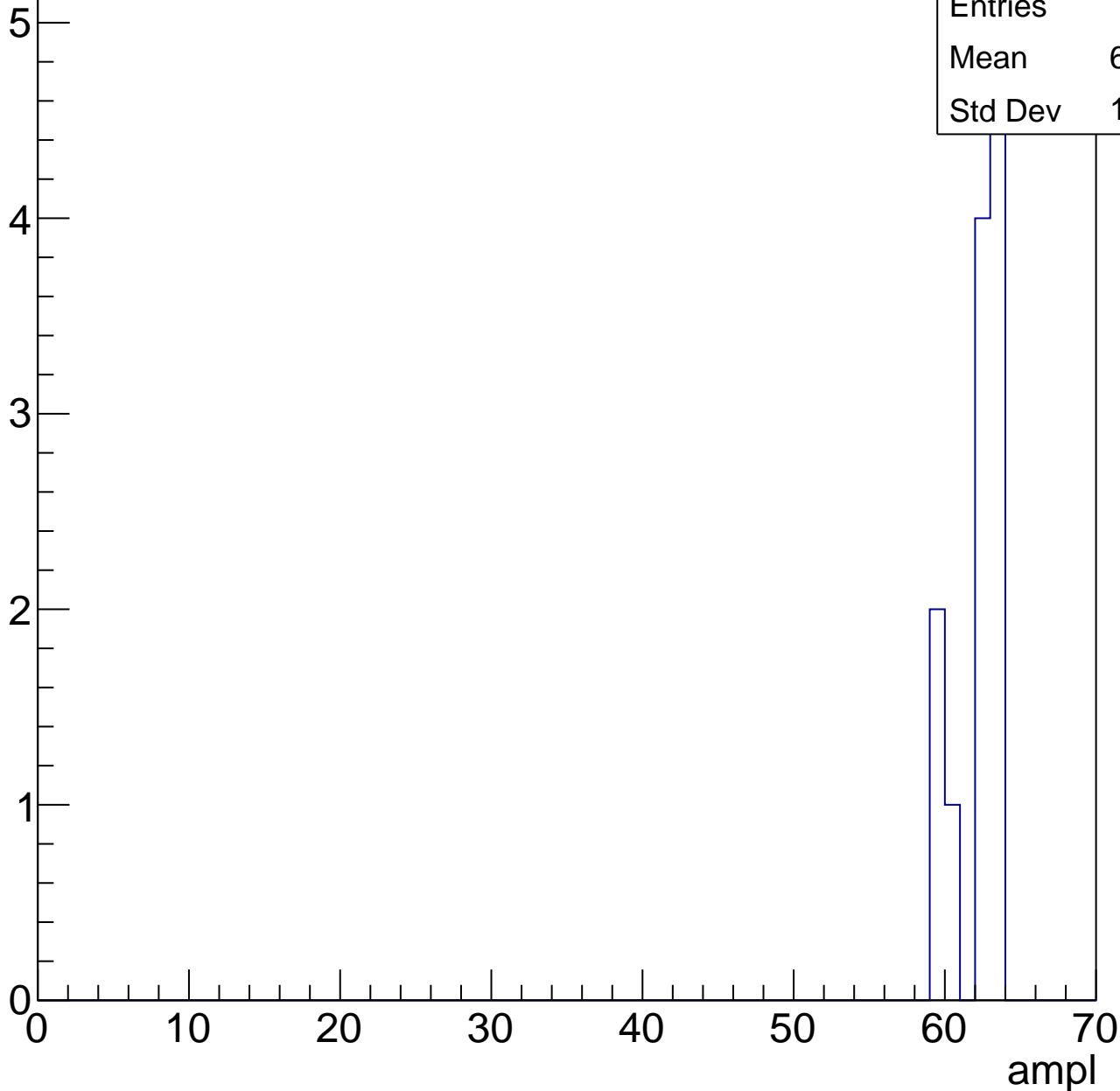


B1L102S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 12 |
| Mean | 61.75 |
| Std Dev | 1.479 |



B1L102S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch23, adc0

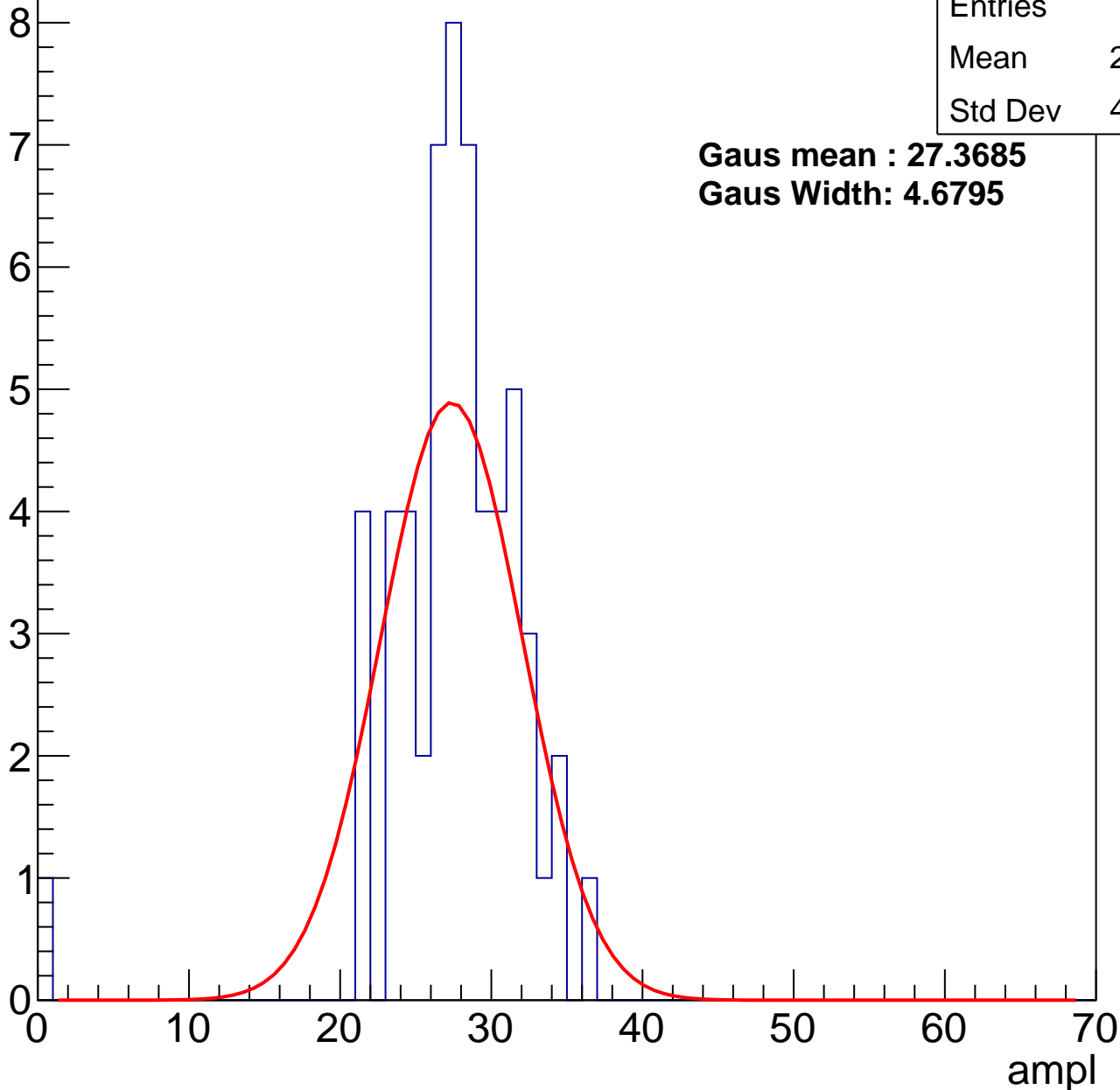
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 27.02 |
| Std Dev | 4.986 |

Gaus mean : 27.3685

Gaus Width: 4.6795



B1L102S, U15-ch23, adc1

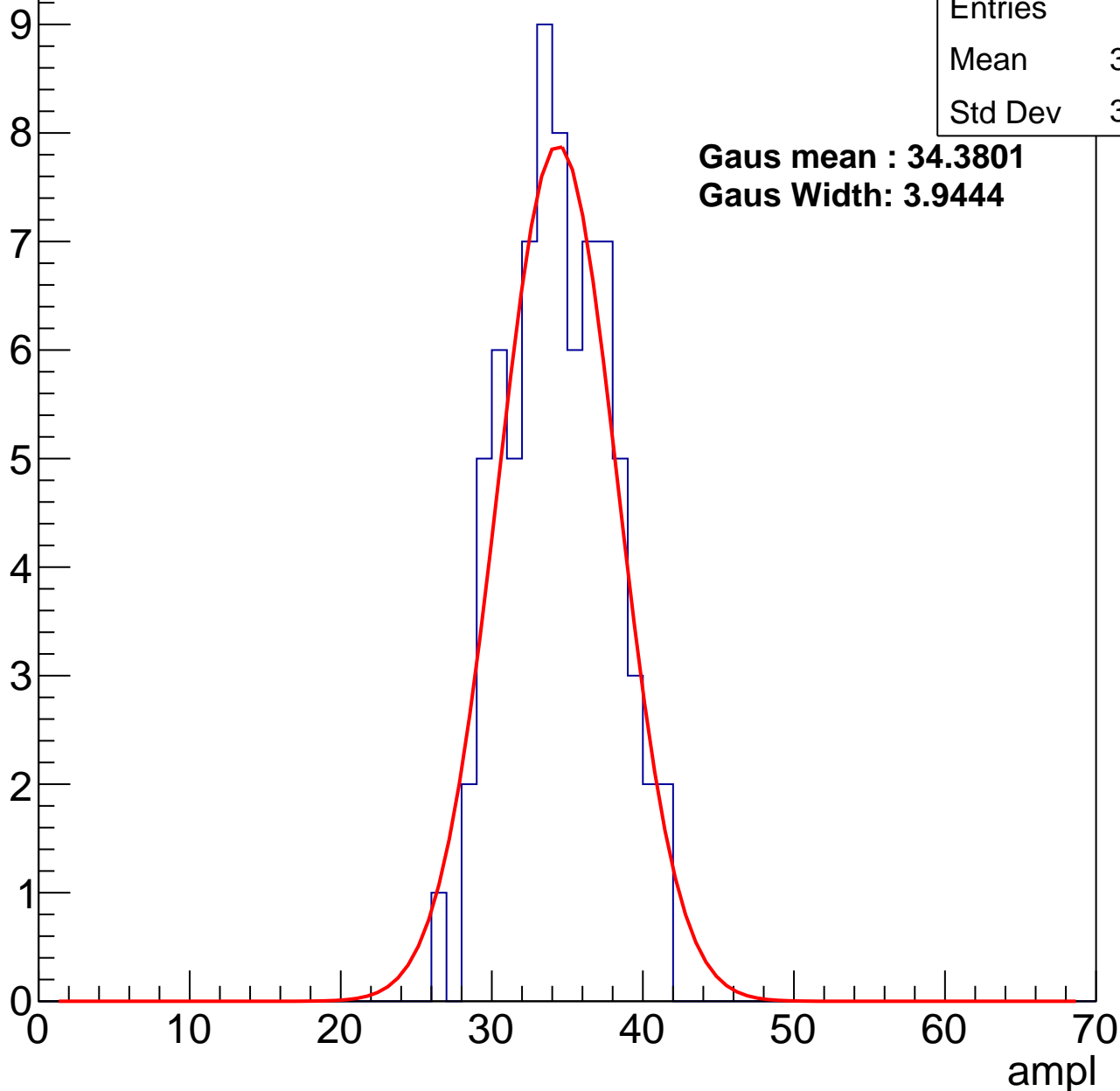
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 33.93 |
| Std Dev | 3.388 |

Gaus mean : 34.3801

Gaus Width: 3.9444



B1L102S, U15-ch23, adc2

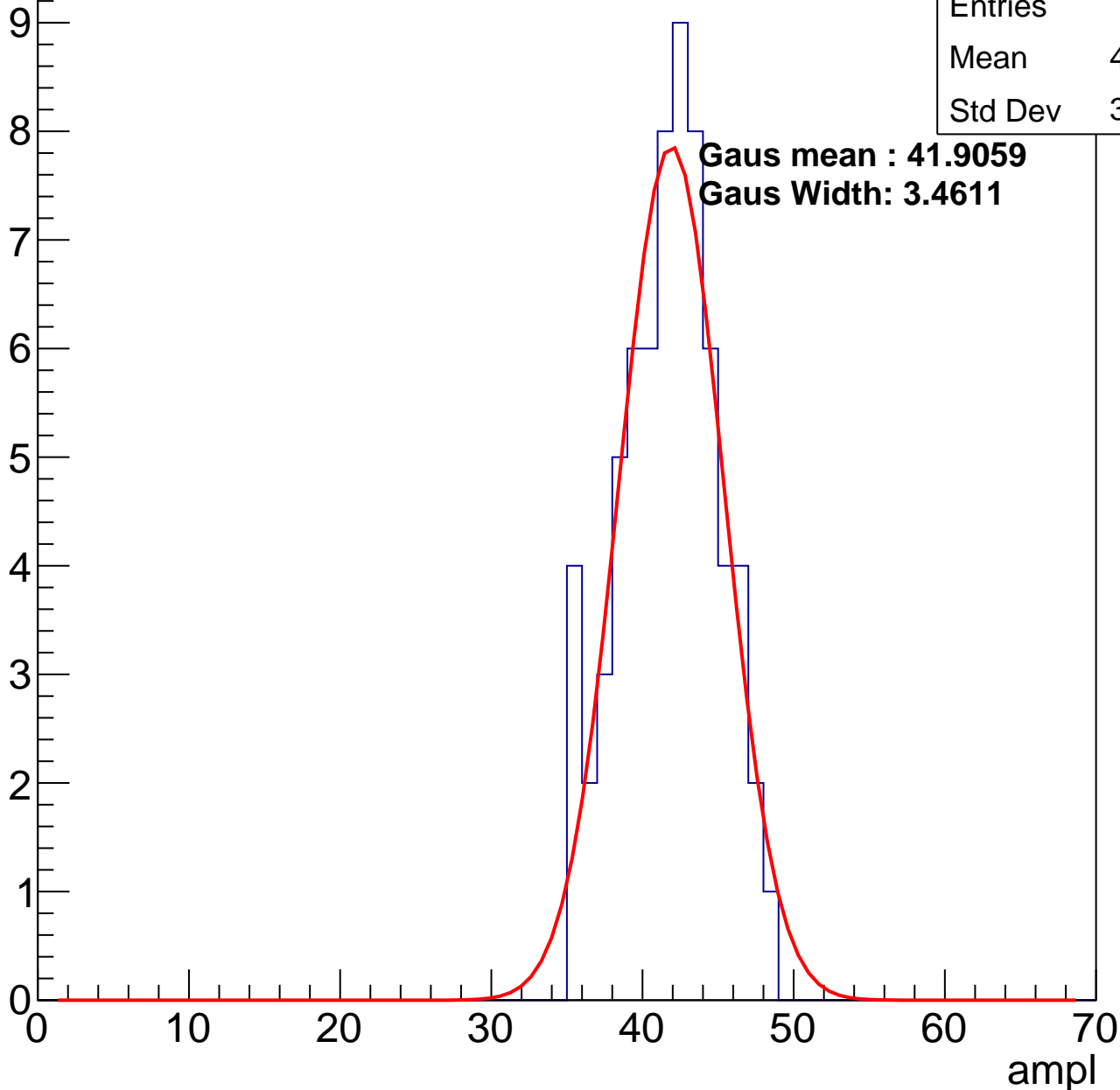
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 41.28 |
| Std Dev | 3.189 |

Gaus mean : 41.9059

Gaus Width: 3.4611

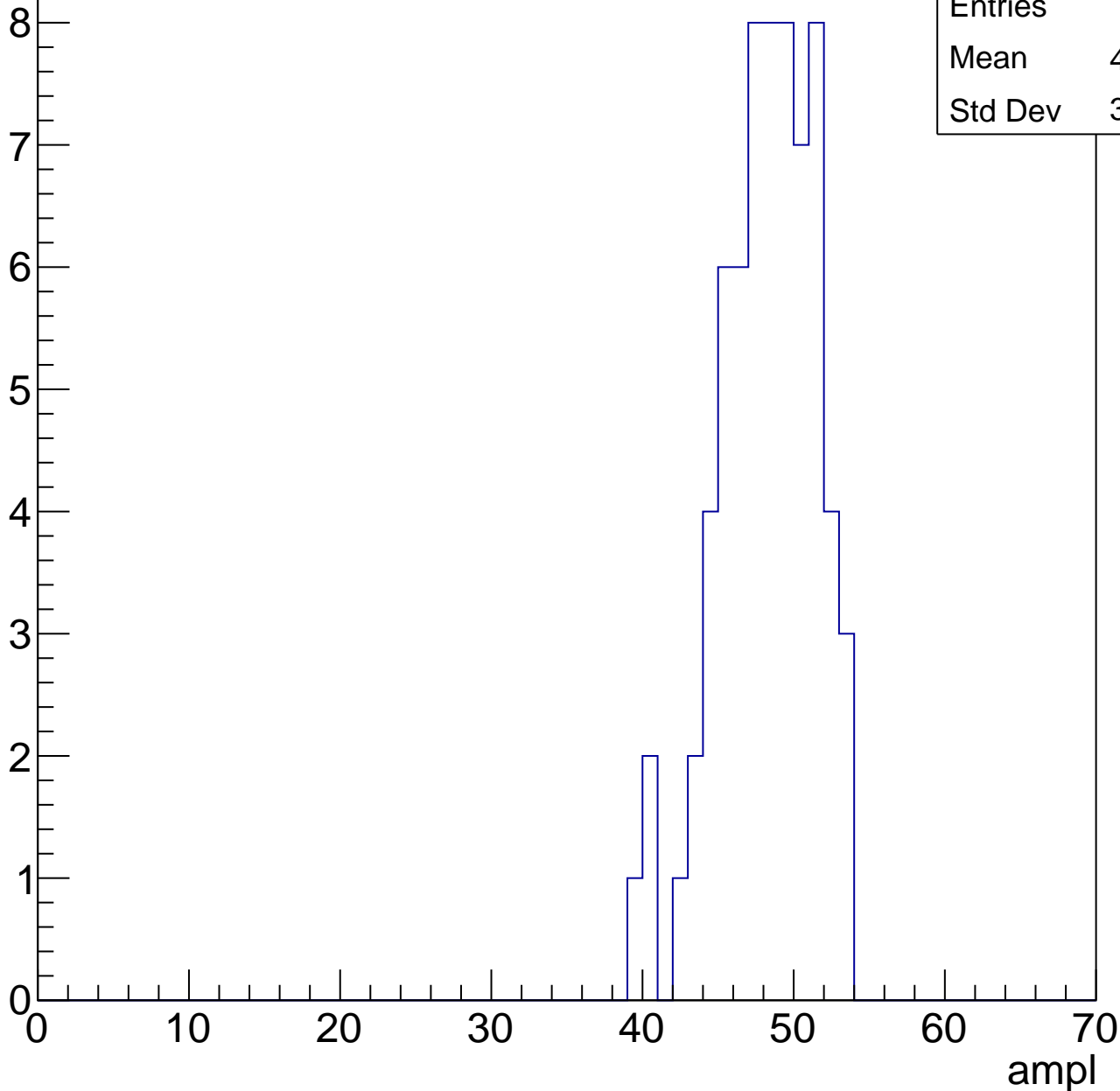


B1L102S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

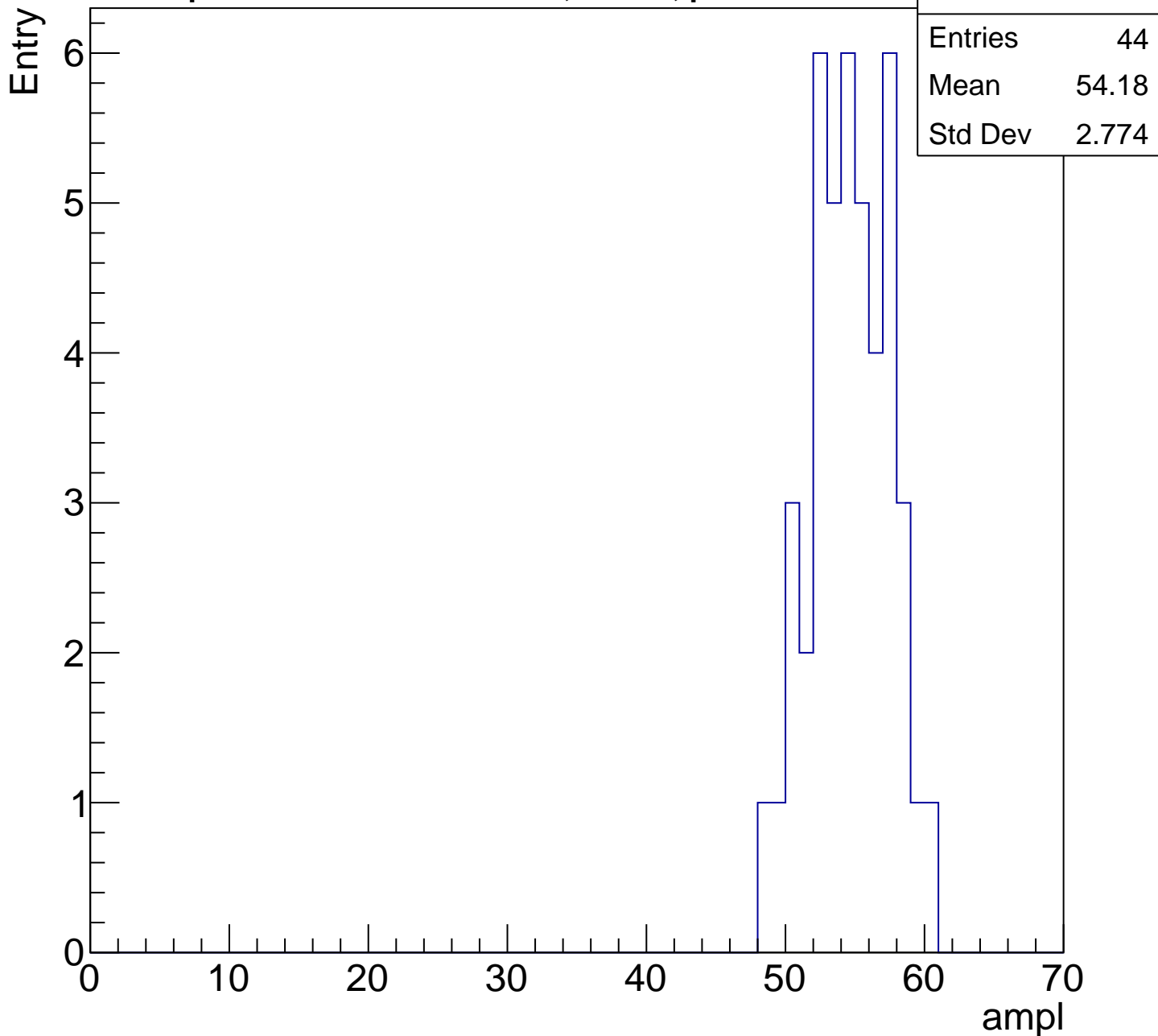
Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 47.74 |
| Std Dev | 3.179 |



B1L102S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

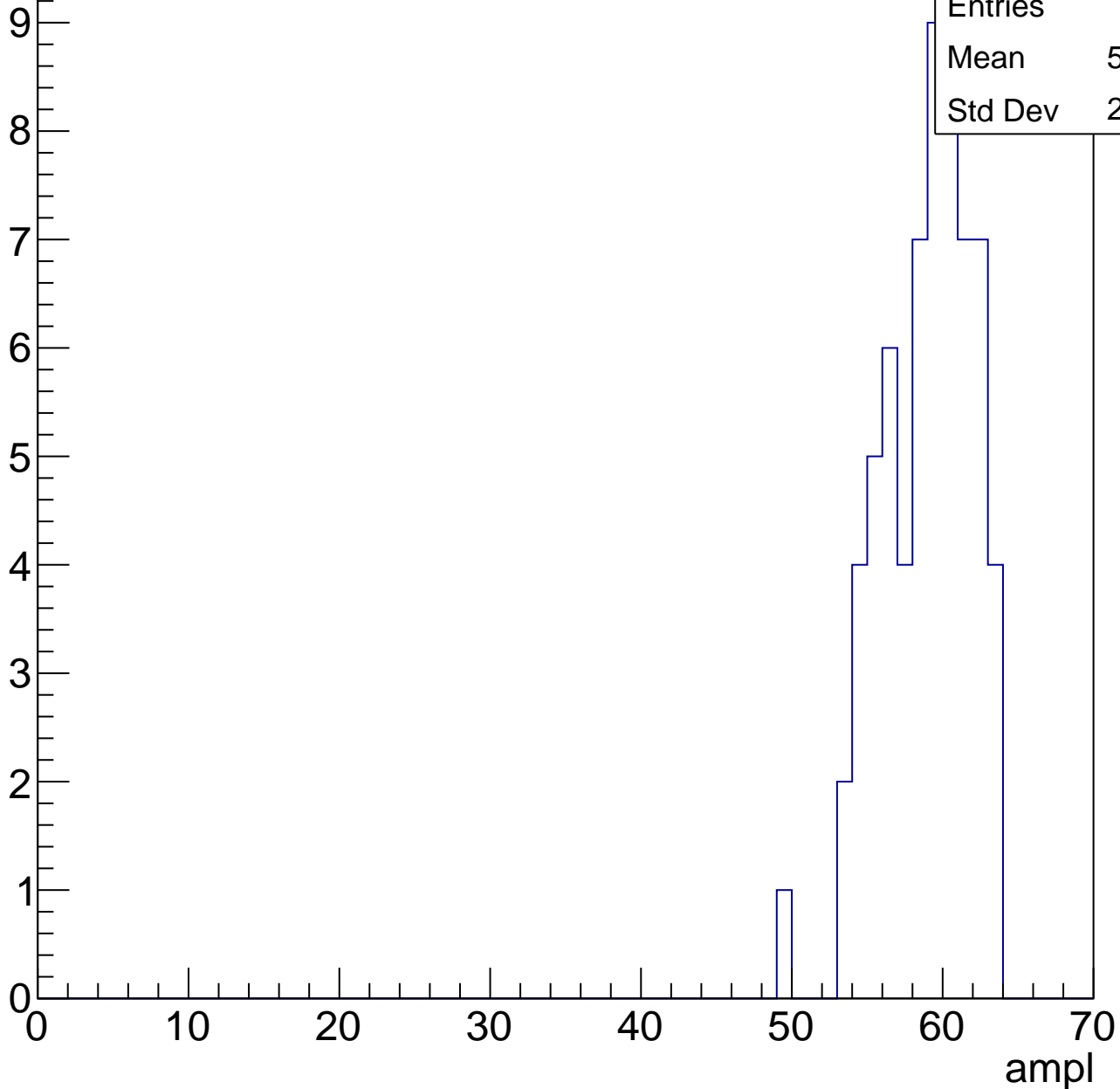


B1L102S, U15-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 58.44 |
| Std Dev | 2.989 |

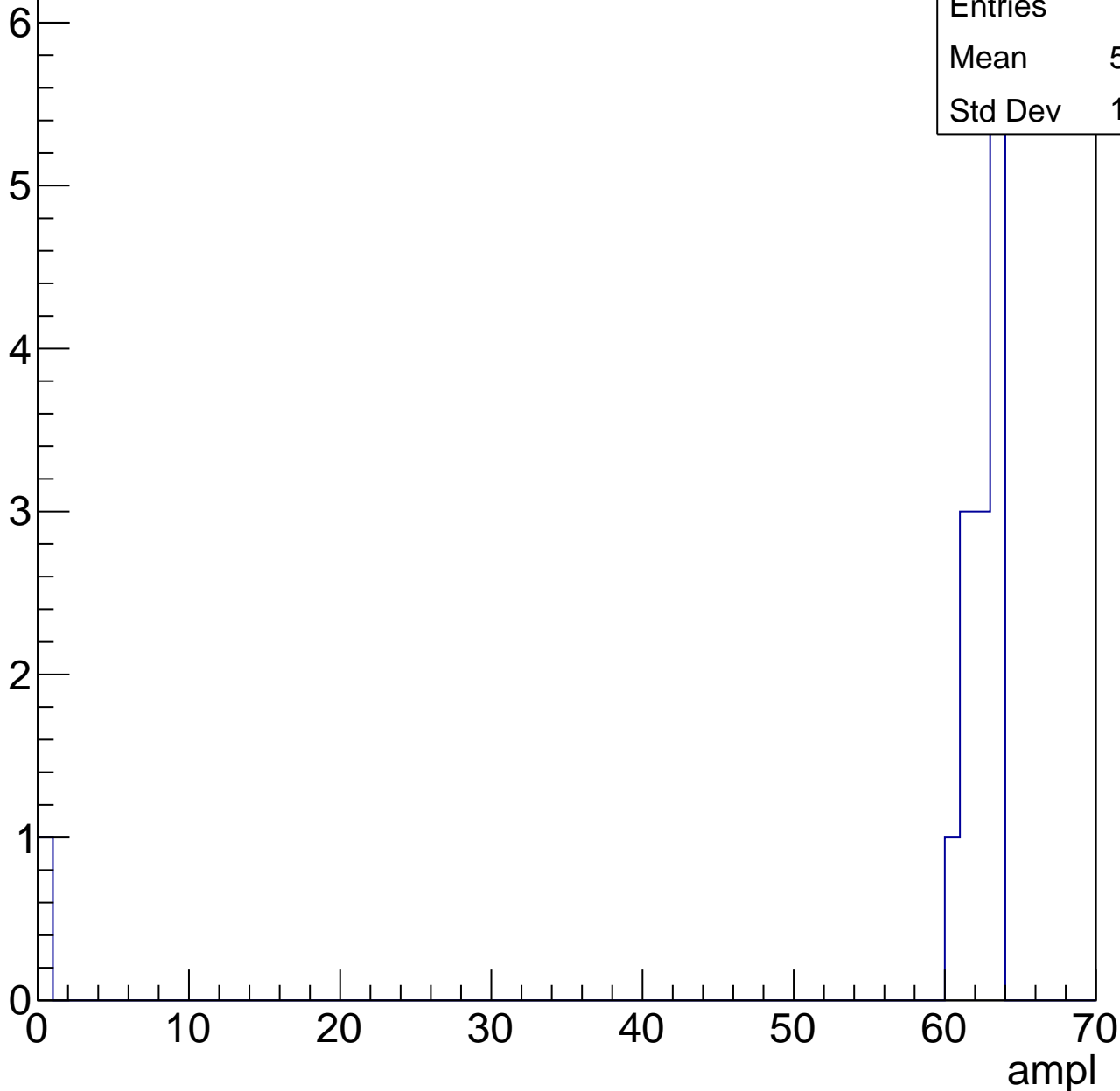


B1L102S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

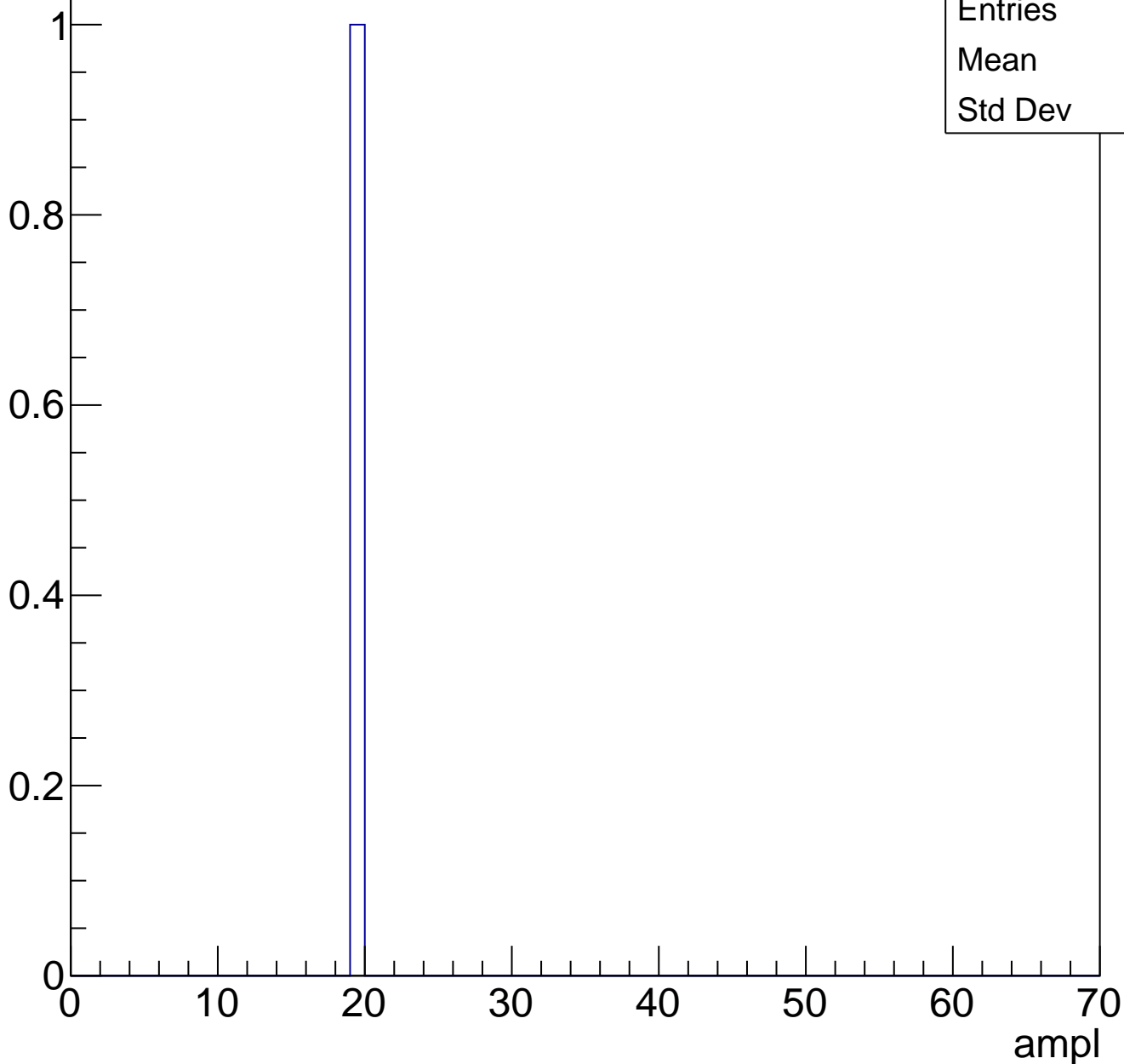
| | |
|---------|-------|
| Entries | 14 |
| Mean | 57.64 |
| Std Dev | 16.02 |



B1L102S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch24, adc0

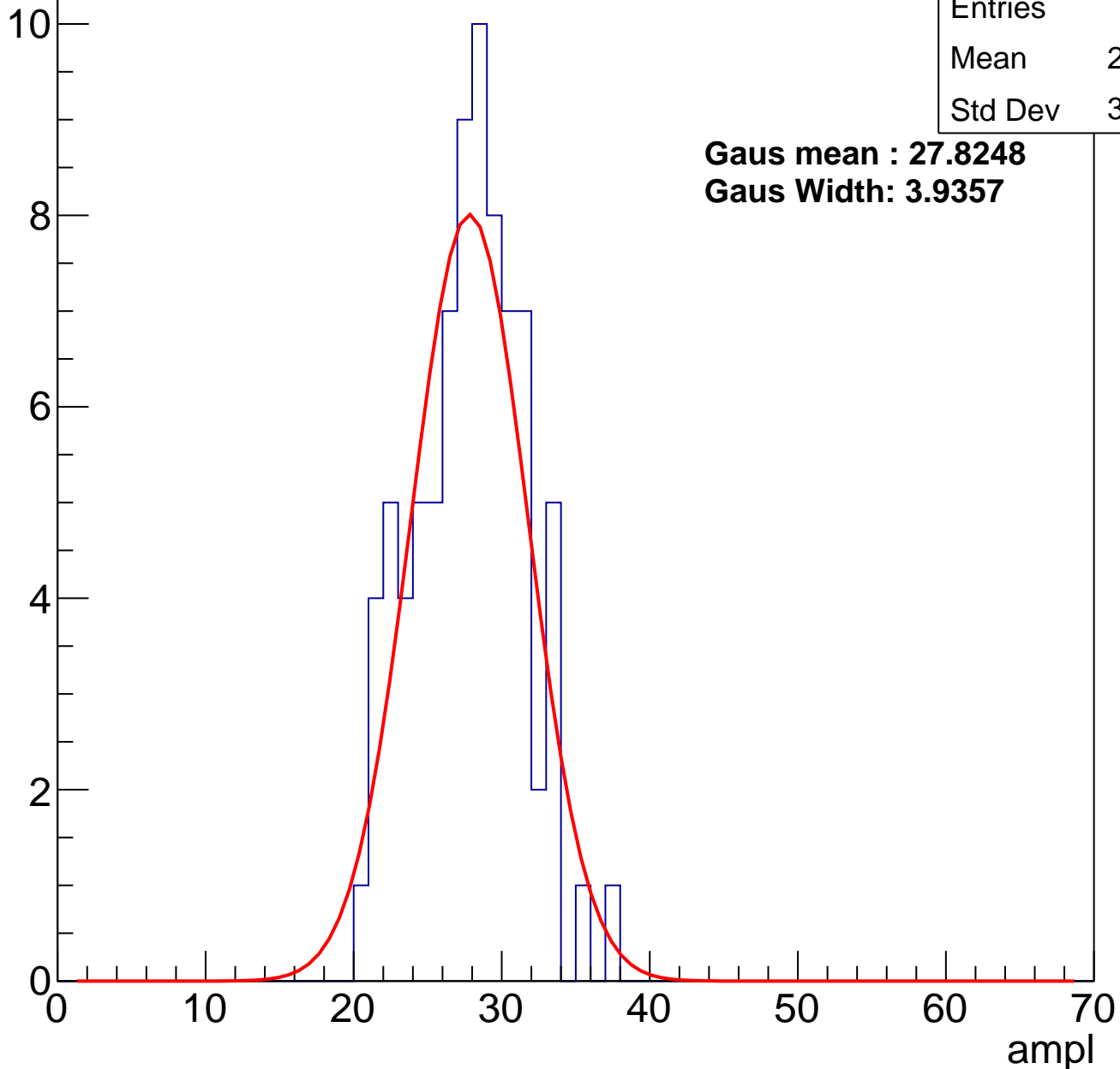
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 27.36 |
| Std Dev | 3.608 |

Gaus mean : 27.8248

Gaus Width: 3.9357

Entry



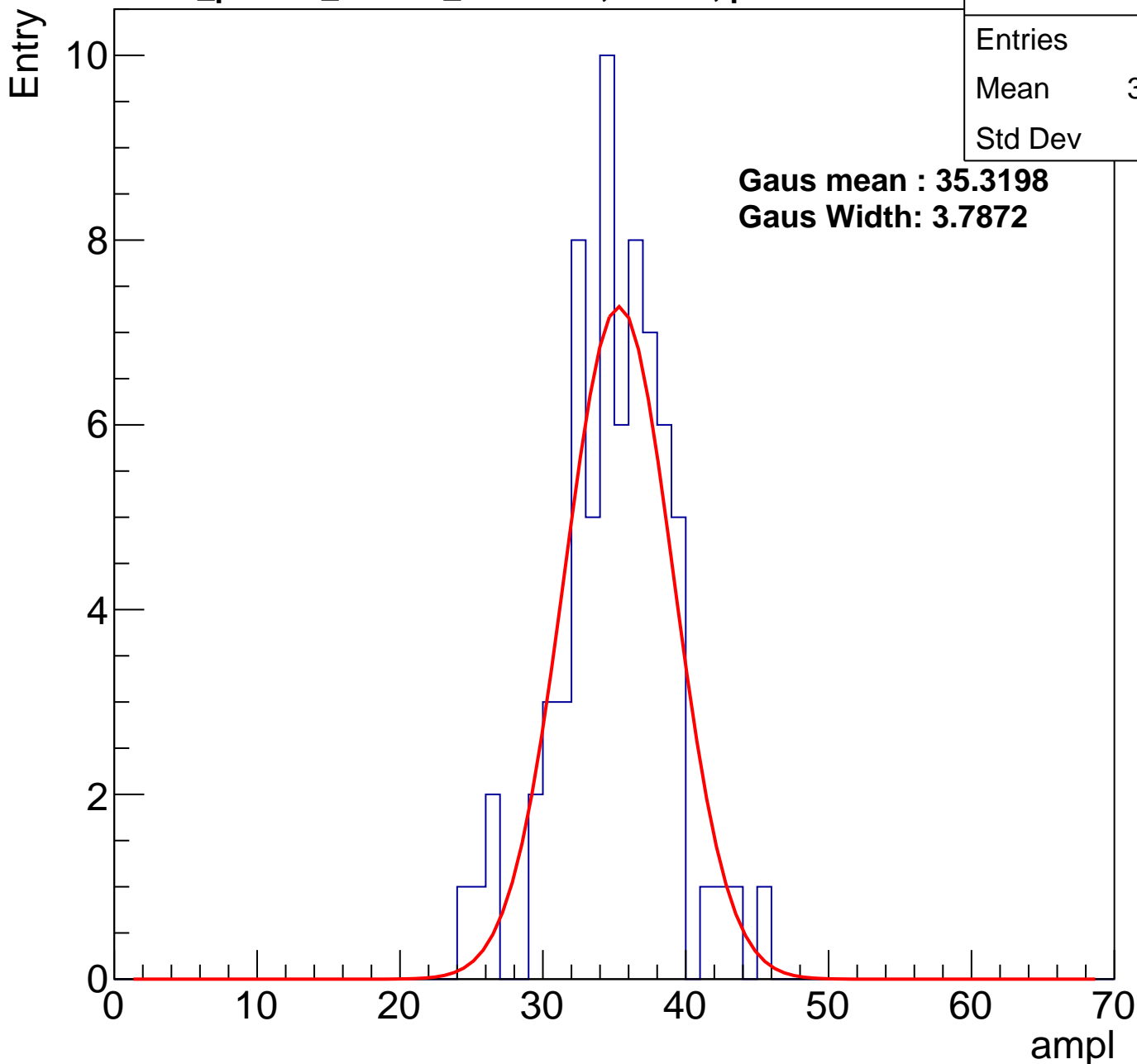
B1L102S, U15-ch24, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 34.56 |
| Std Dev | 3.91 |

Gaus mean : 35.3198

Gaus Width: 3.7872



B1L102S, U15-ch24, adc2

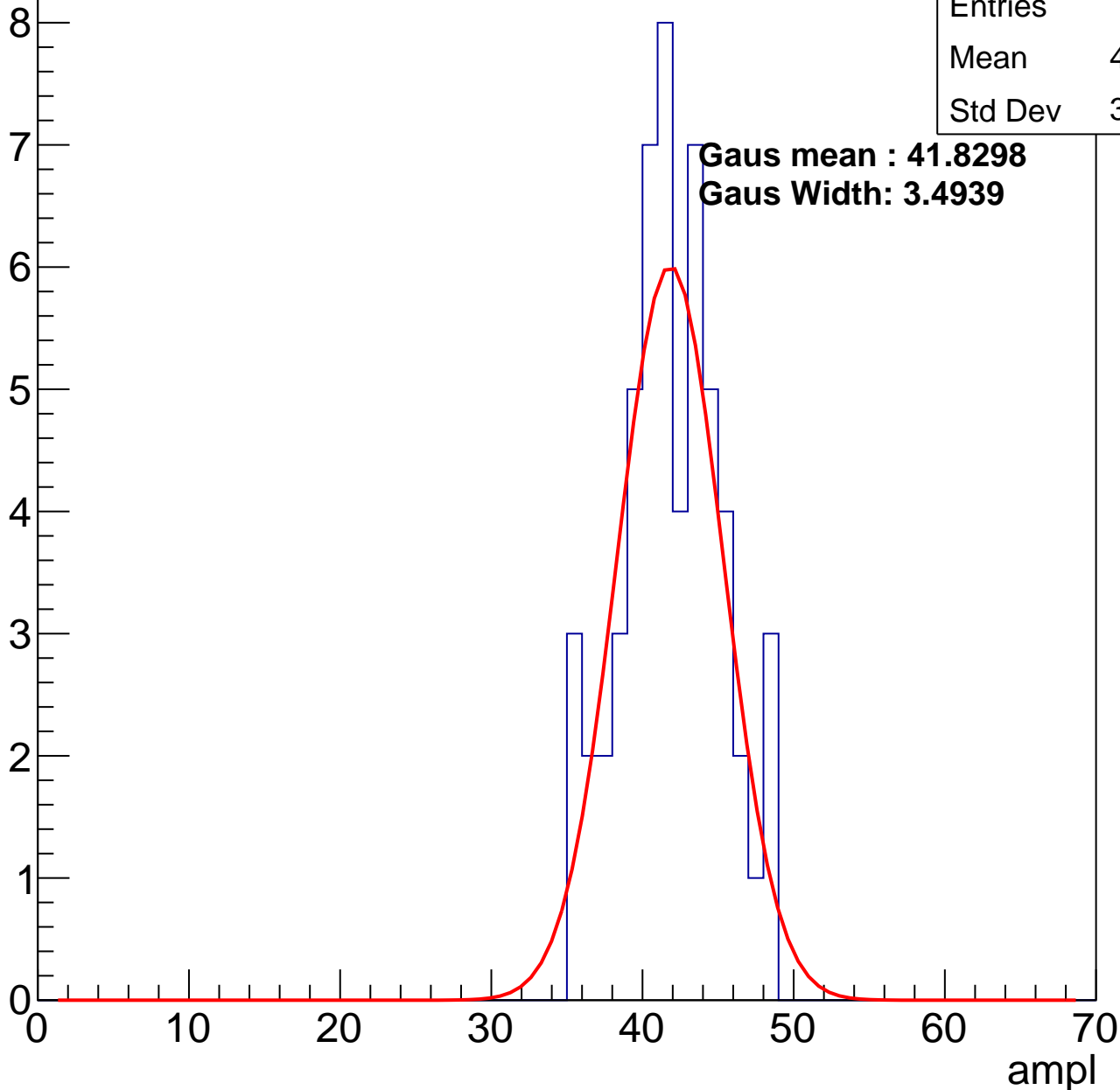
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 41.43 |
| Std Dev | 3.294 |

Gaus mean : 41.8298

Gaus Width: 3.4939

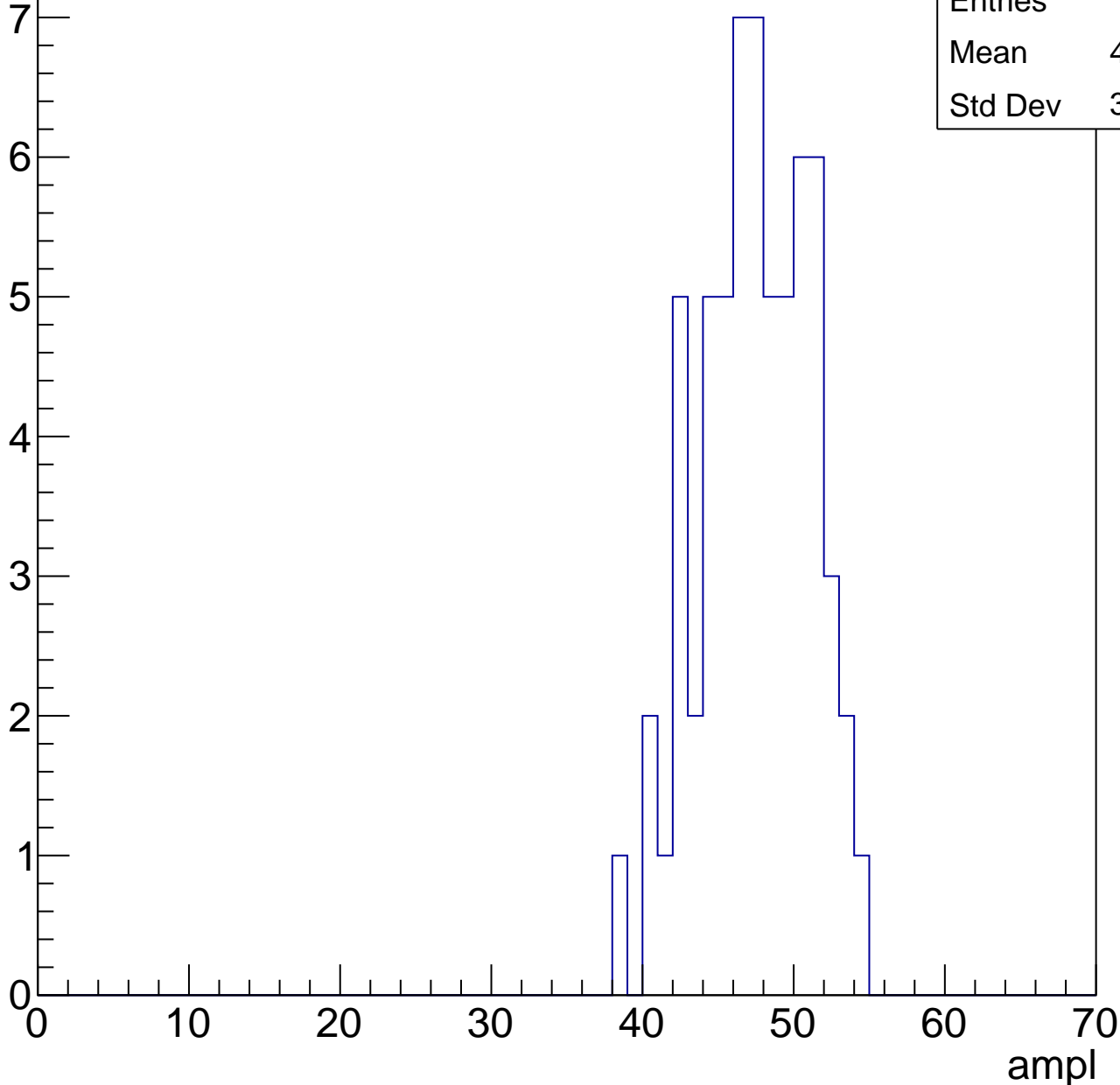


B1L102S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 46.95 |
| Std Dev | 3.596 |

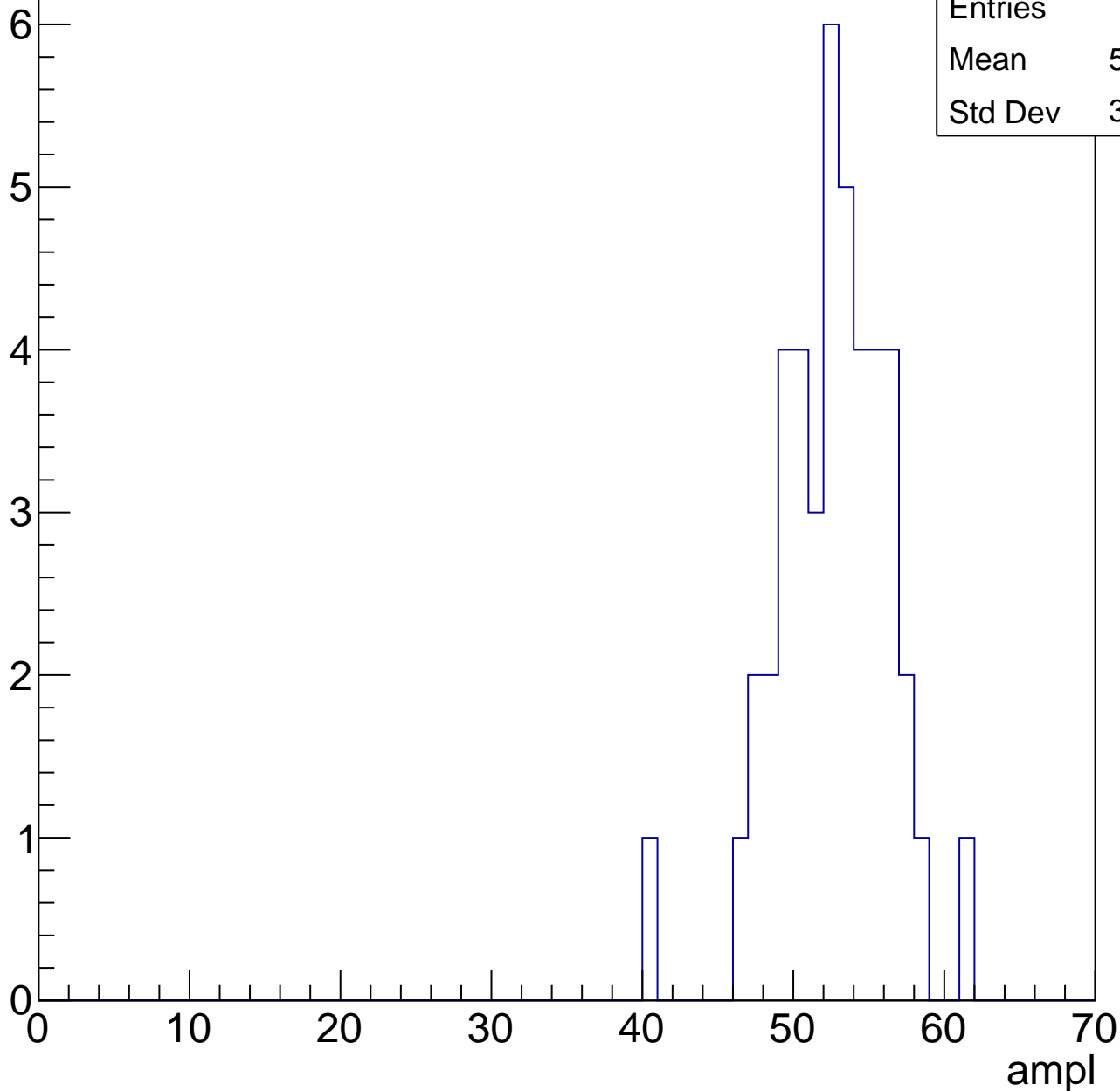


B1L102S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 52.16 |
| Std Dev | 3.705 |

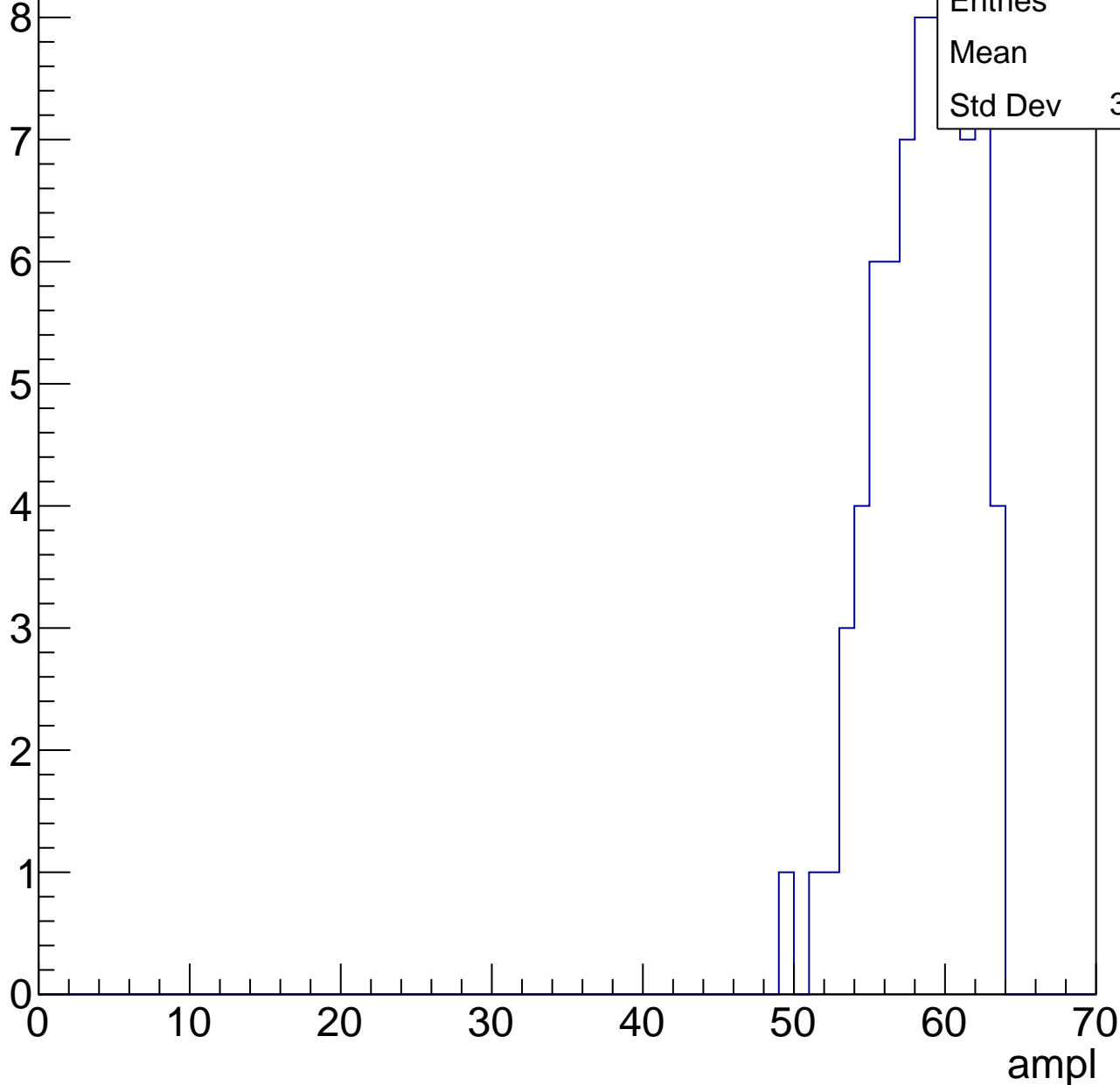


B1L102S, U15-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

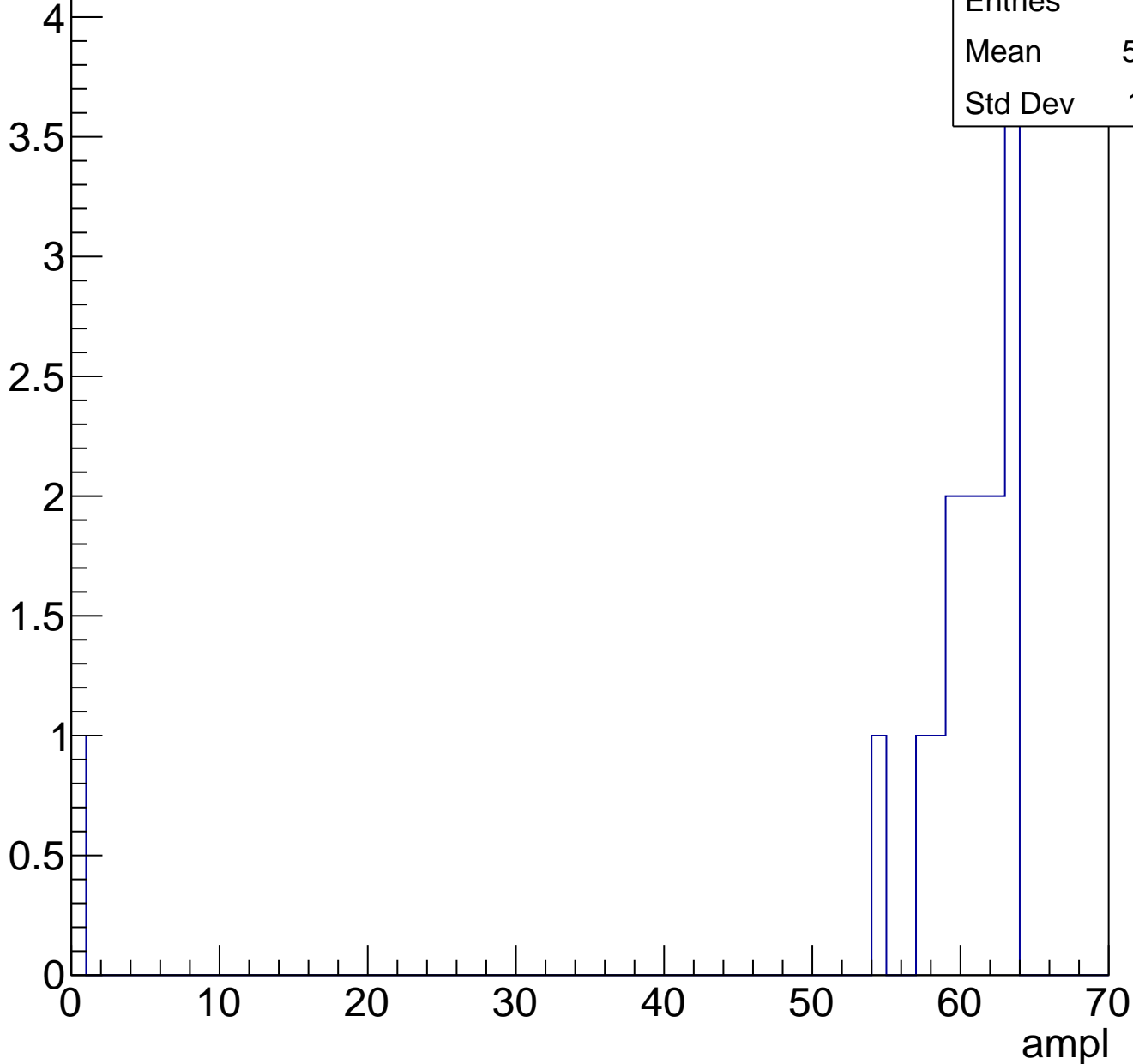
| | |
|---------|-------|
| Entries | 72 |
| Mean | 58.1 |
| Std Dev | 3.163 |



B1L102S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch25, adc0

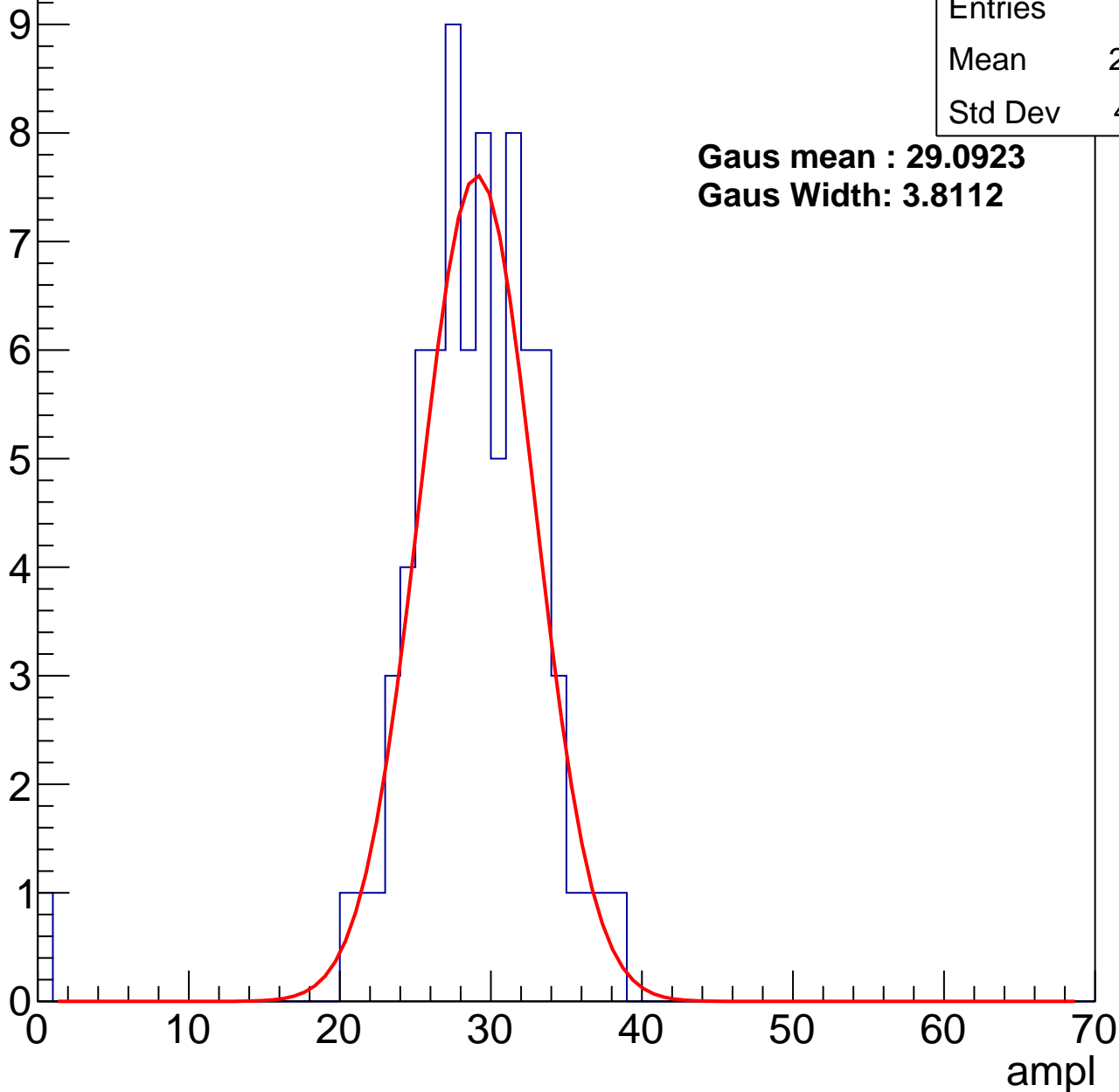
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 28.37 |
| Std Dev | 4.931 |

Gaus mean : 29.0923

Gaus Width: 3.8112



B1L102S, U15-ch25, adc1

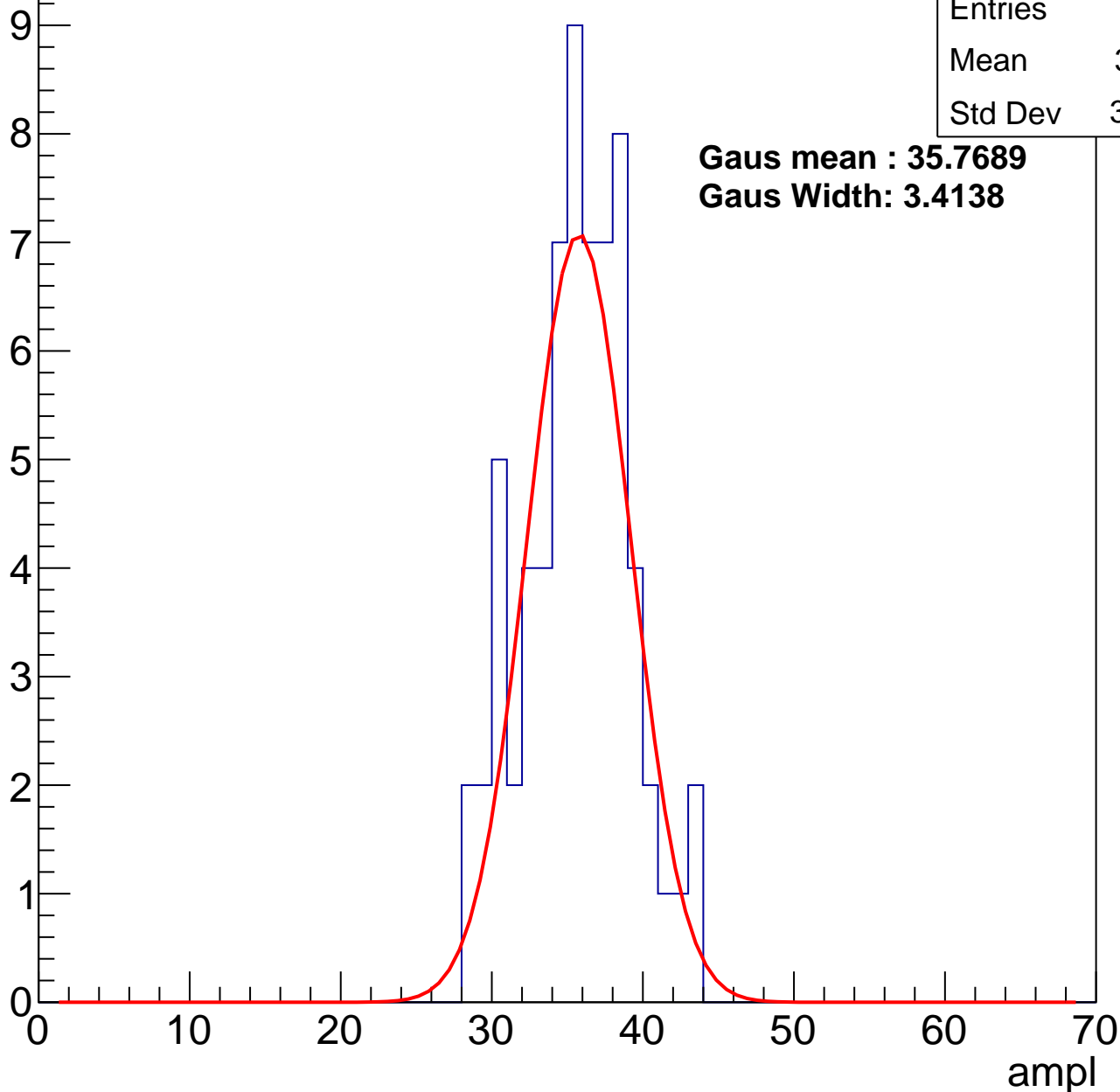
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 35.21 |
| Std Dev | 3.492 |

Gaus mean : 35.7689

Gaus Width: 3.4138



B1L102S, U15-ch25, adc2

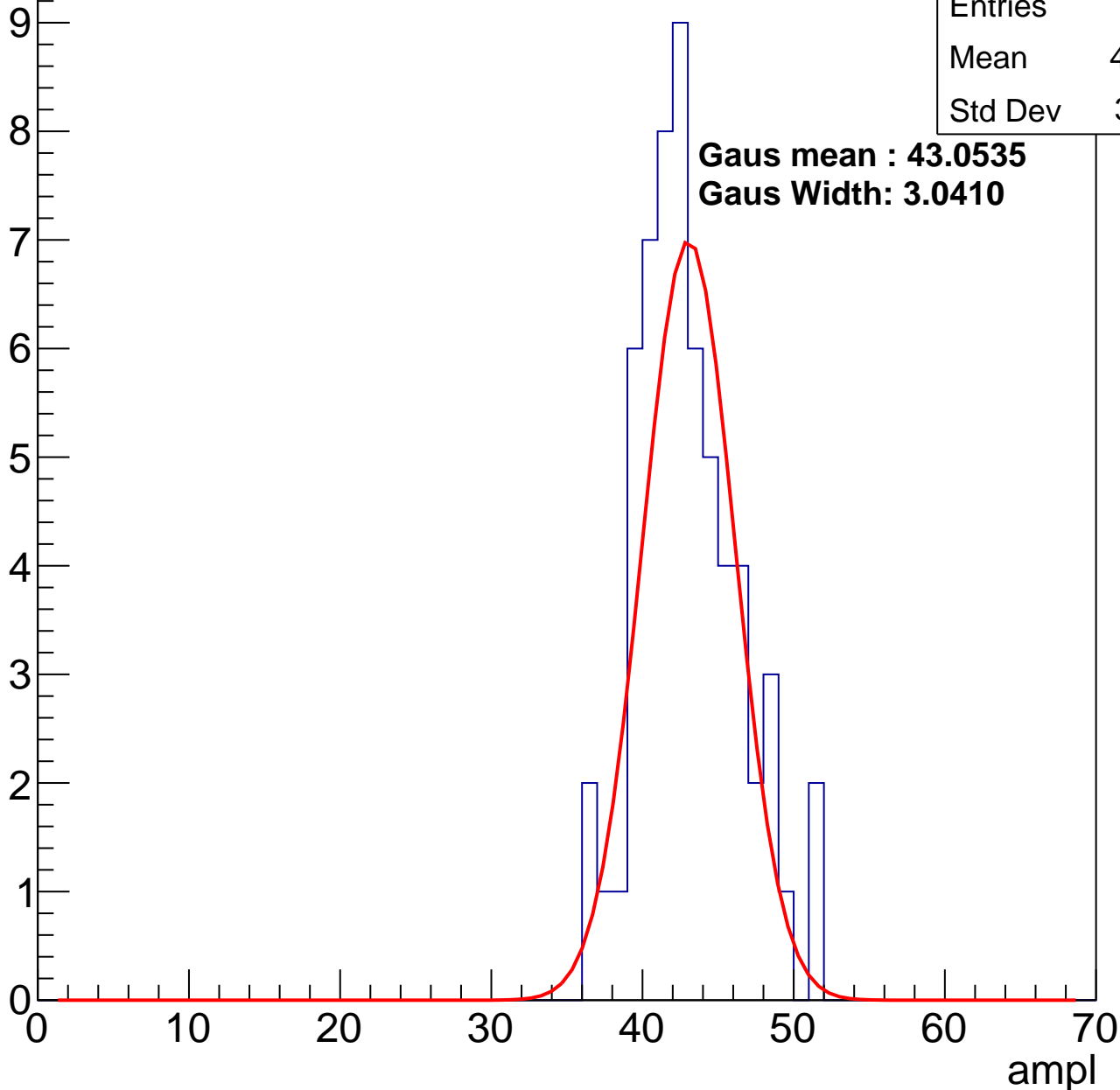
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.59 |
| Std Dev | 3.331 |

Gaus mean : 43.0535

Gaus Width: 3.0410

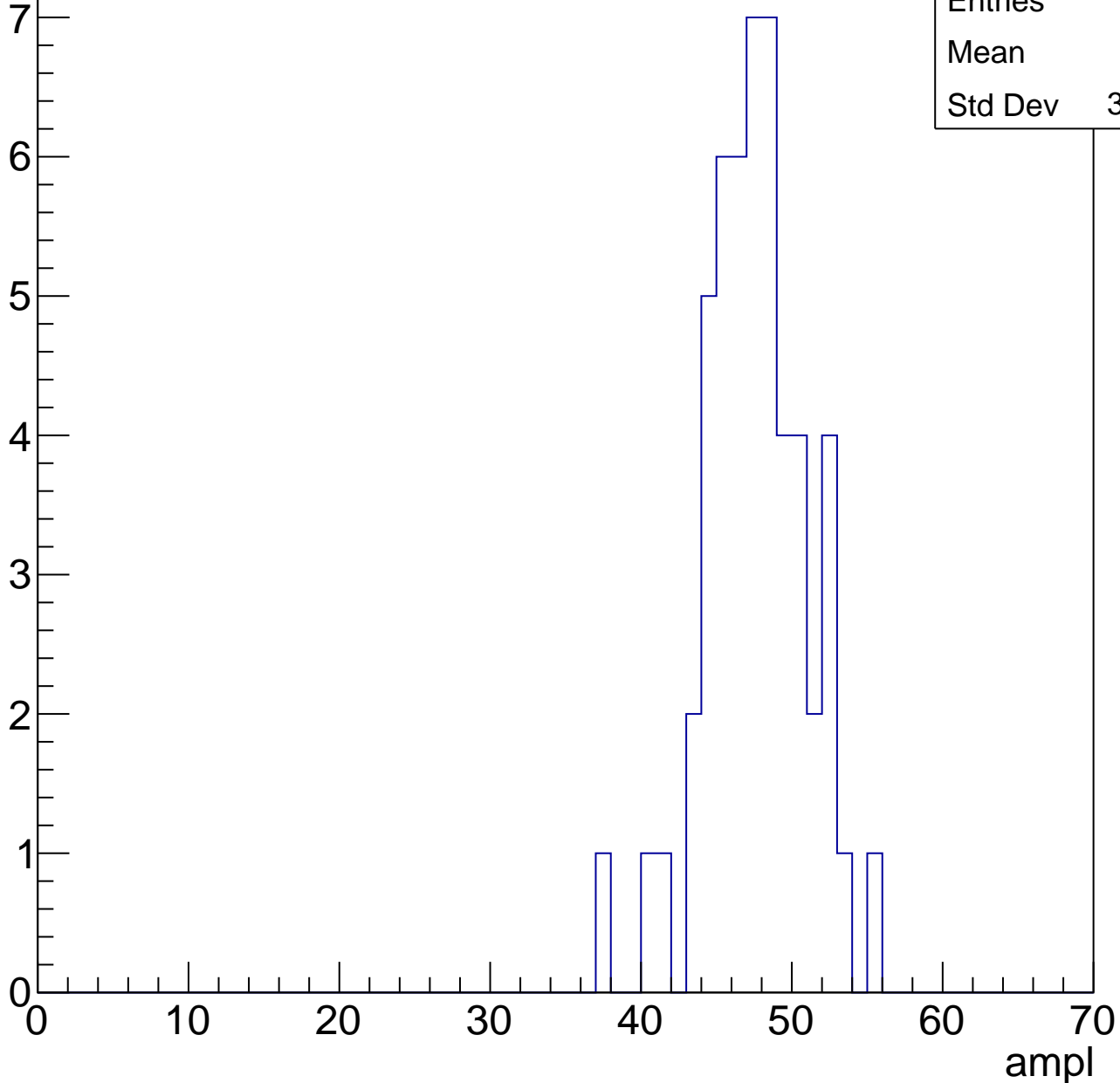


B1L102S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 47.1 |
| Std Dev | 3.347 |

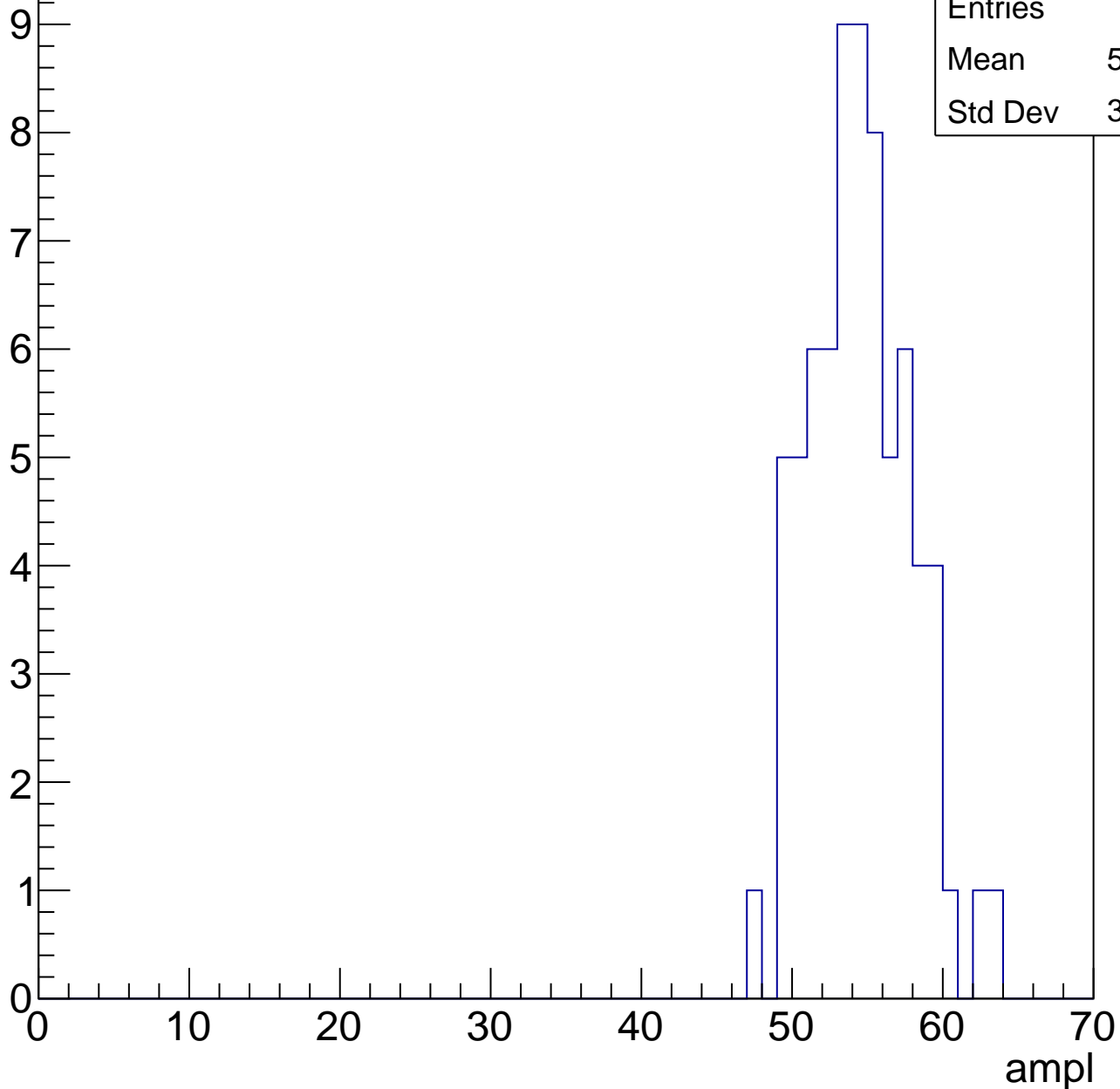


B1L102S, U15-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 54.06 |
| Std Dev | 3.288 |

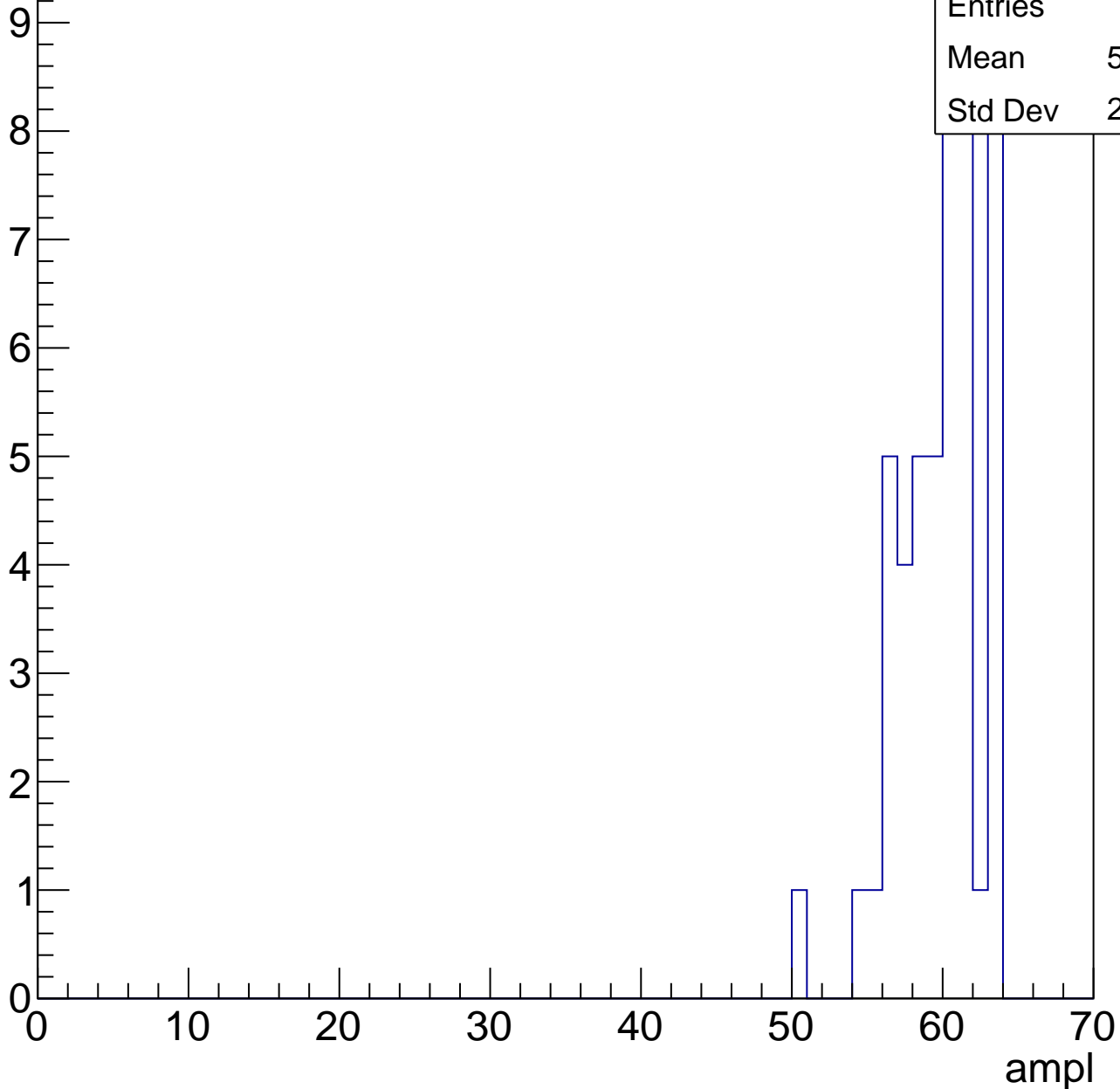


B1L102S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.35 |
| Std Dev | 2.795 |

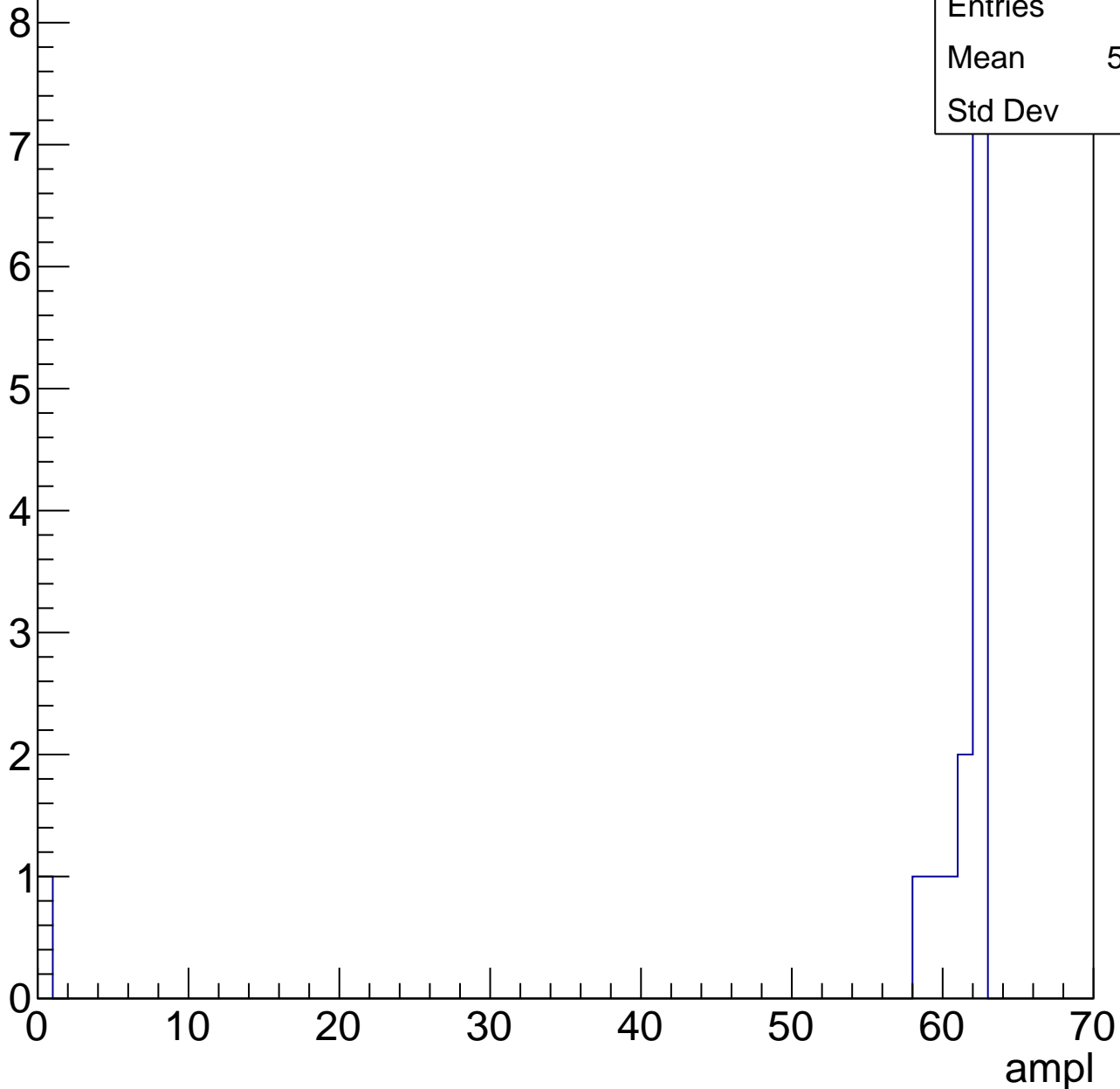


B1L102S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 14 |
| Mean | 56.79 |
| Std Dev | 15.8 |



B1L102S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch26, adc0

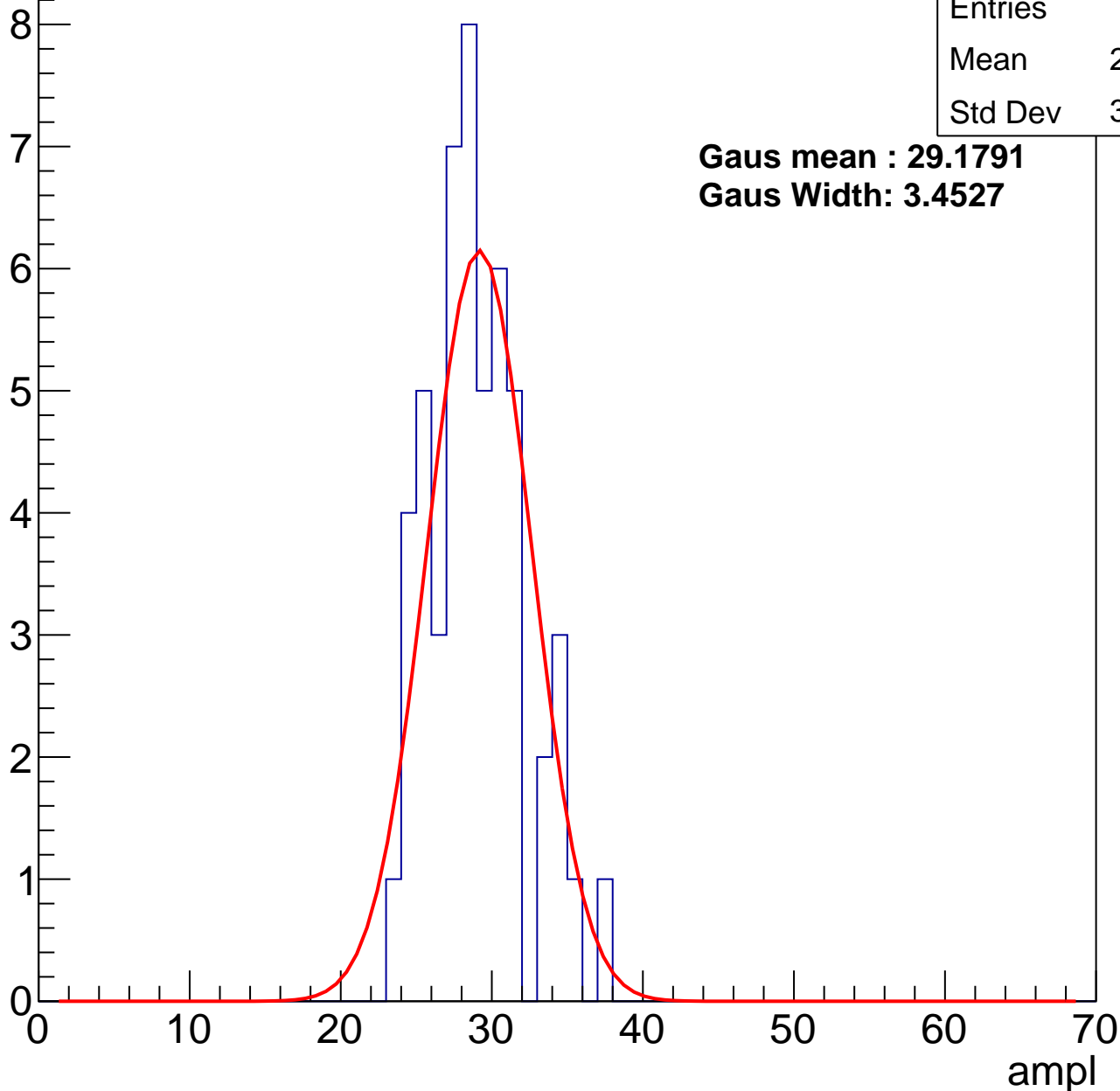
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 28.53 |
| Std Dev | 3.133 |

Gaus mean : 29.1791

Gaus Width: 3.4527



B1L102S, U15-ch26, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.64 |
| Std Dev | 3.439 |

Gaus mean : 34.6944

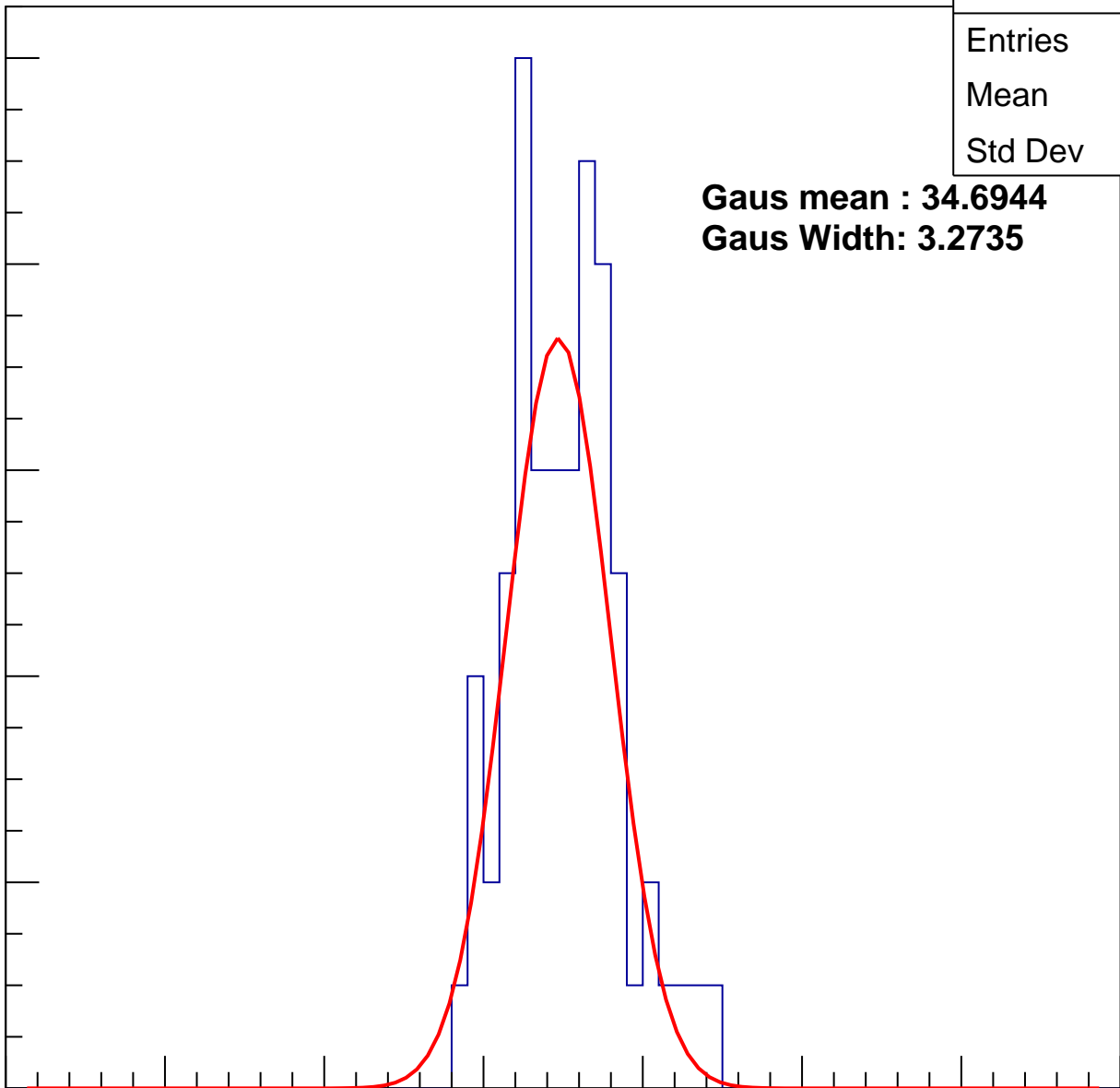
Gaus Width: 3.2735

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch26, adc2

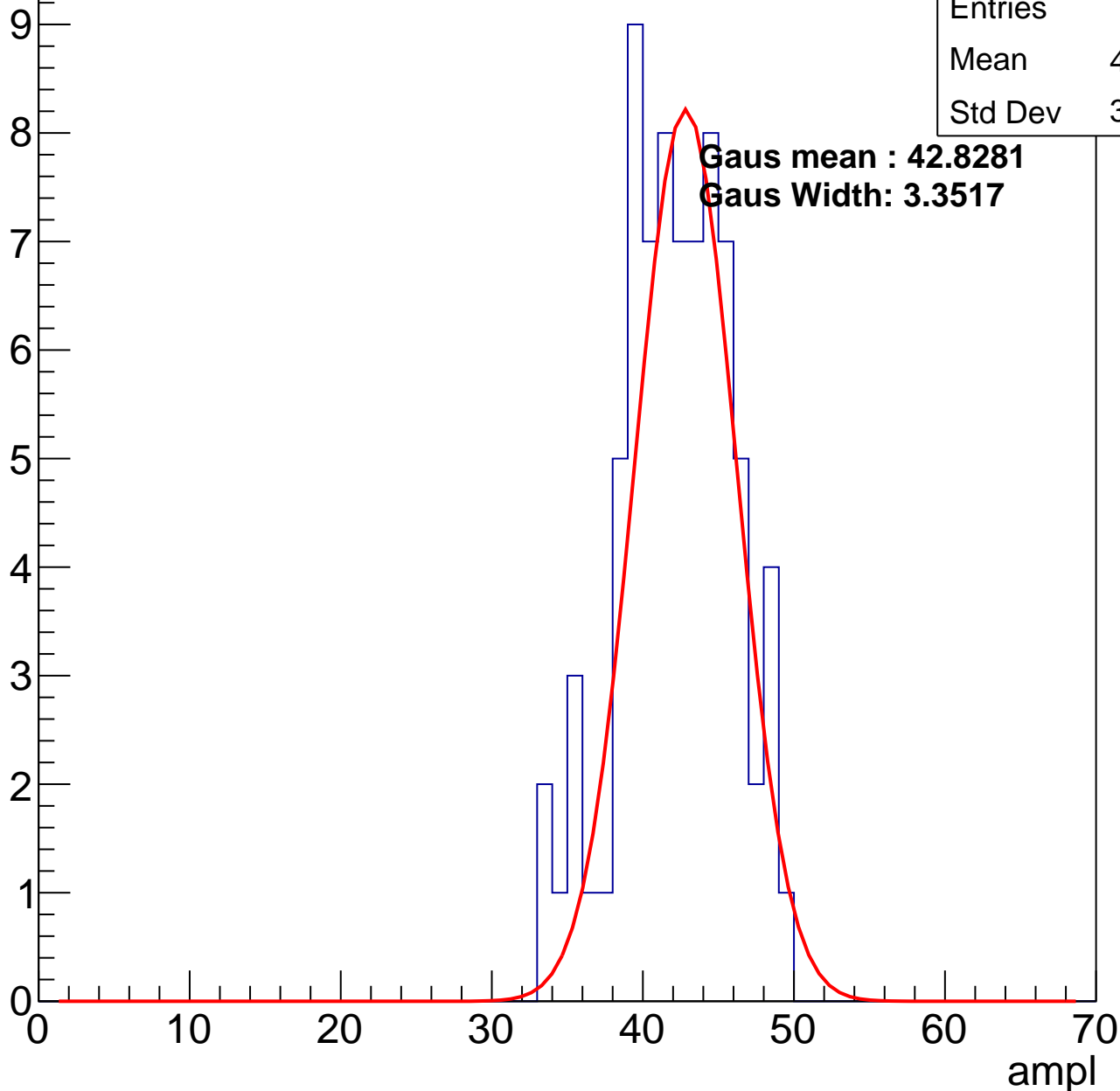
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 41.72 |
| Std Dev | 3.669 |

Gaus mean : 42.8281

Gaus Width: 3.3517

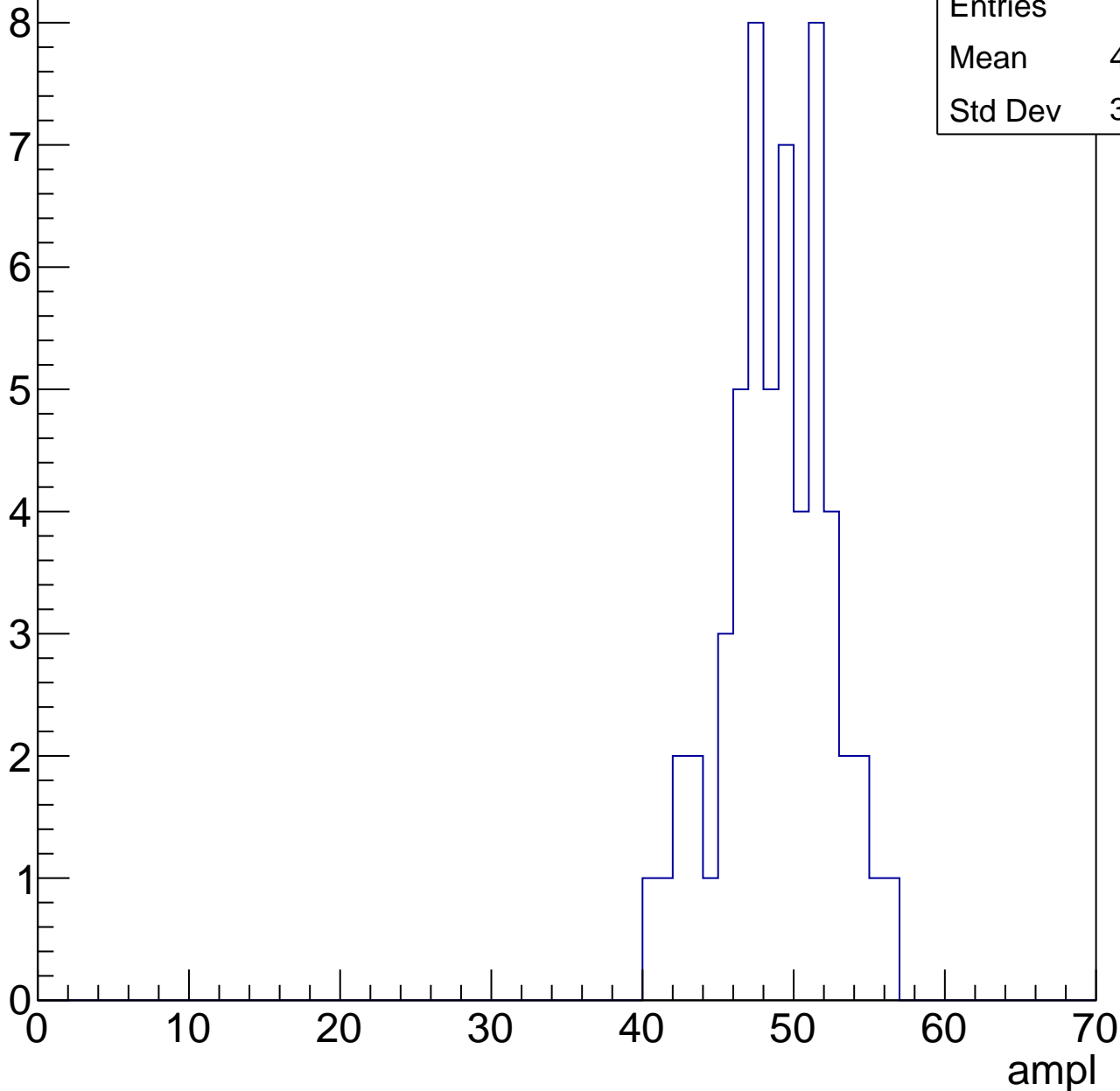


B1L102S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

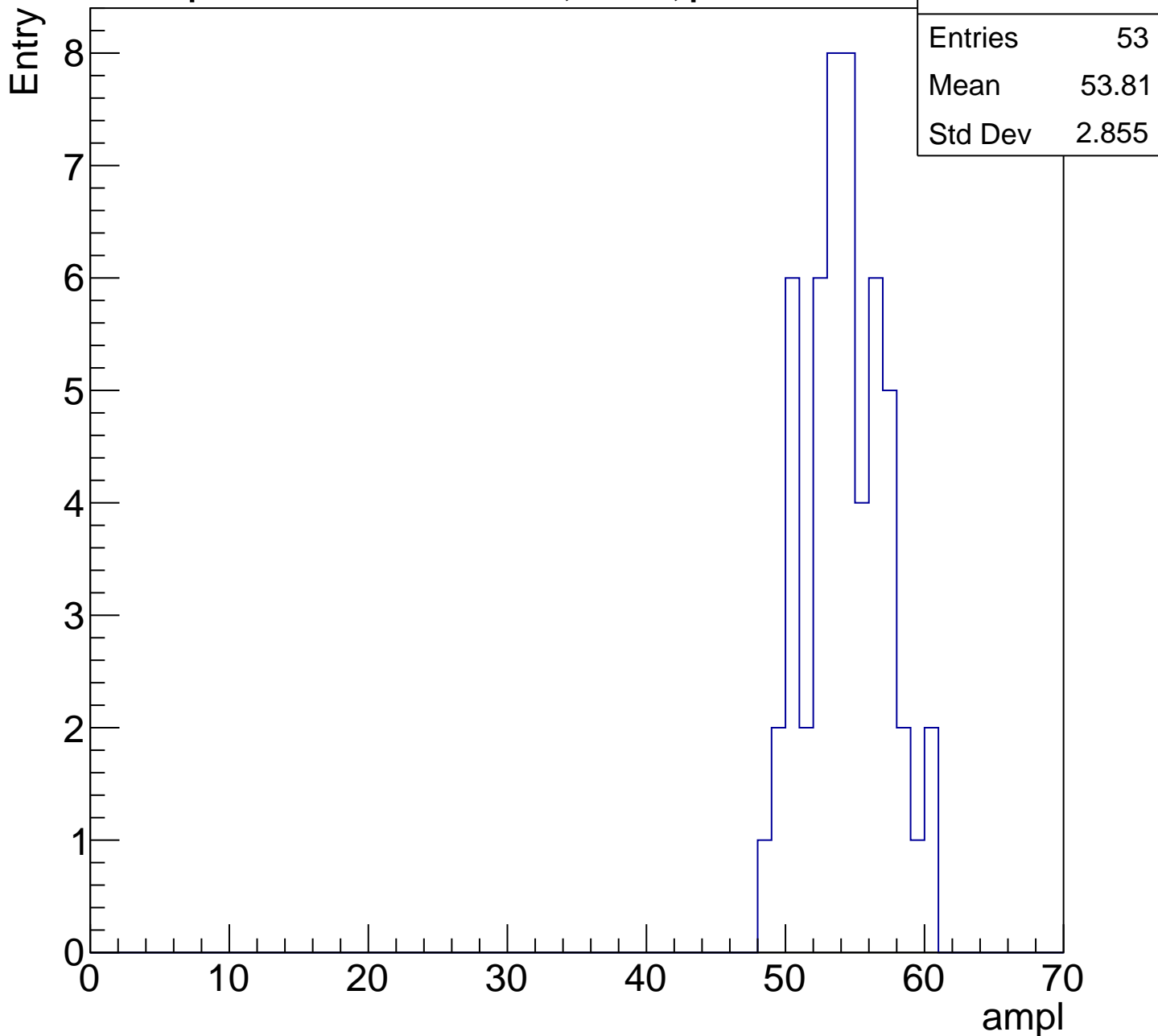
Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 48.42 |
| Std Dev | 3.479 |



B1L102S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

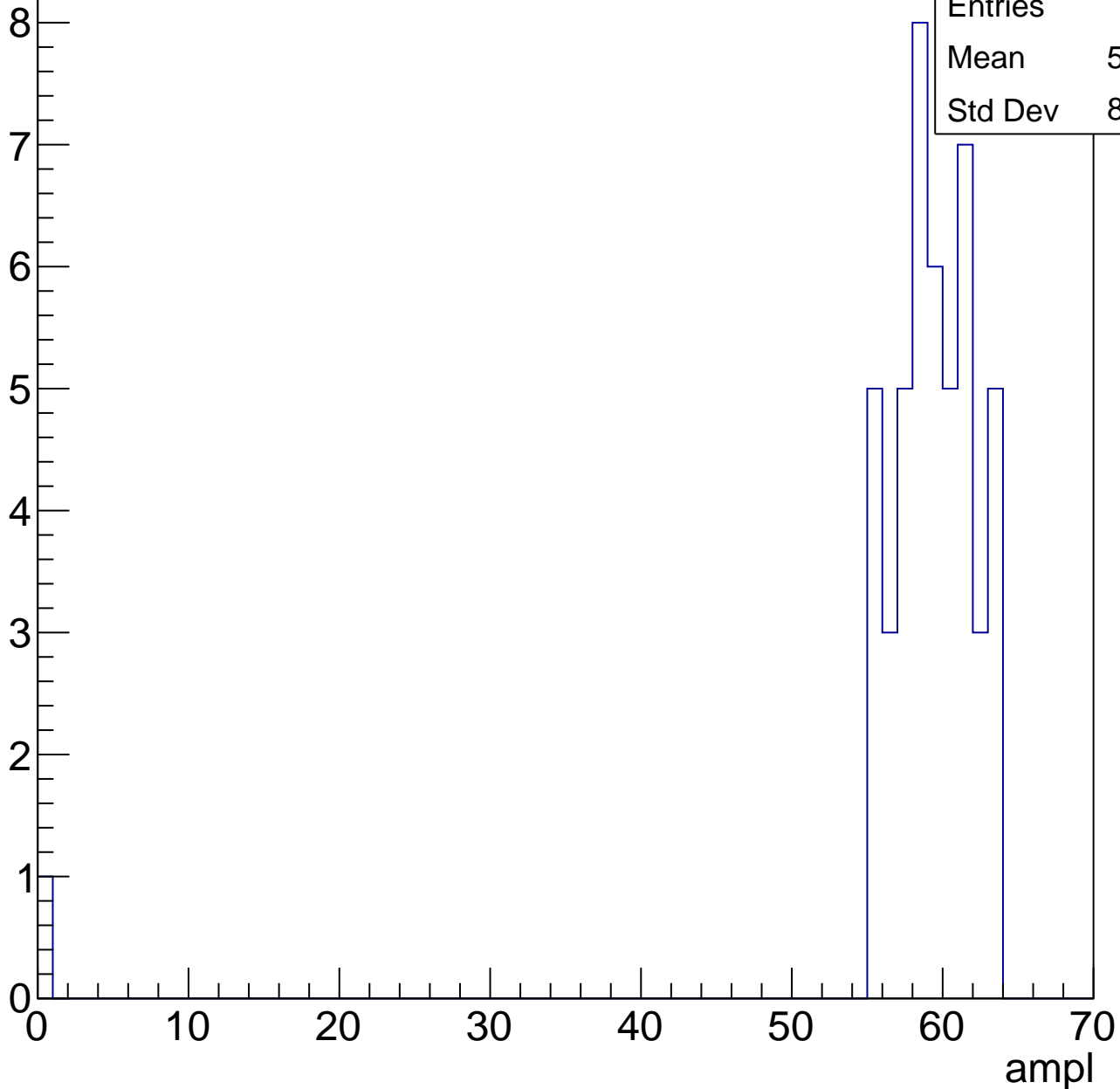


B1L102S, U15-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 57.79 |
| Std Dev | 8.763 |

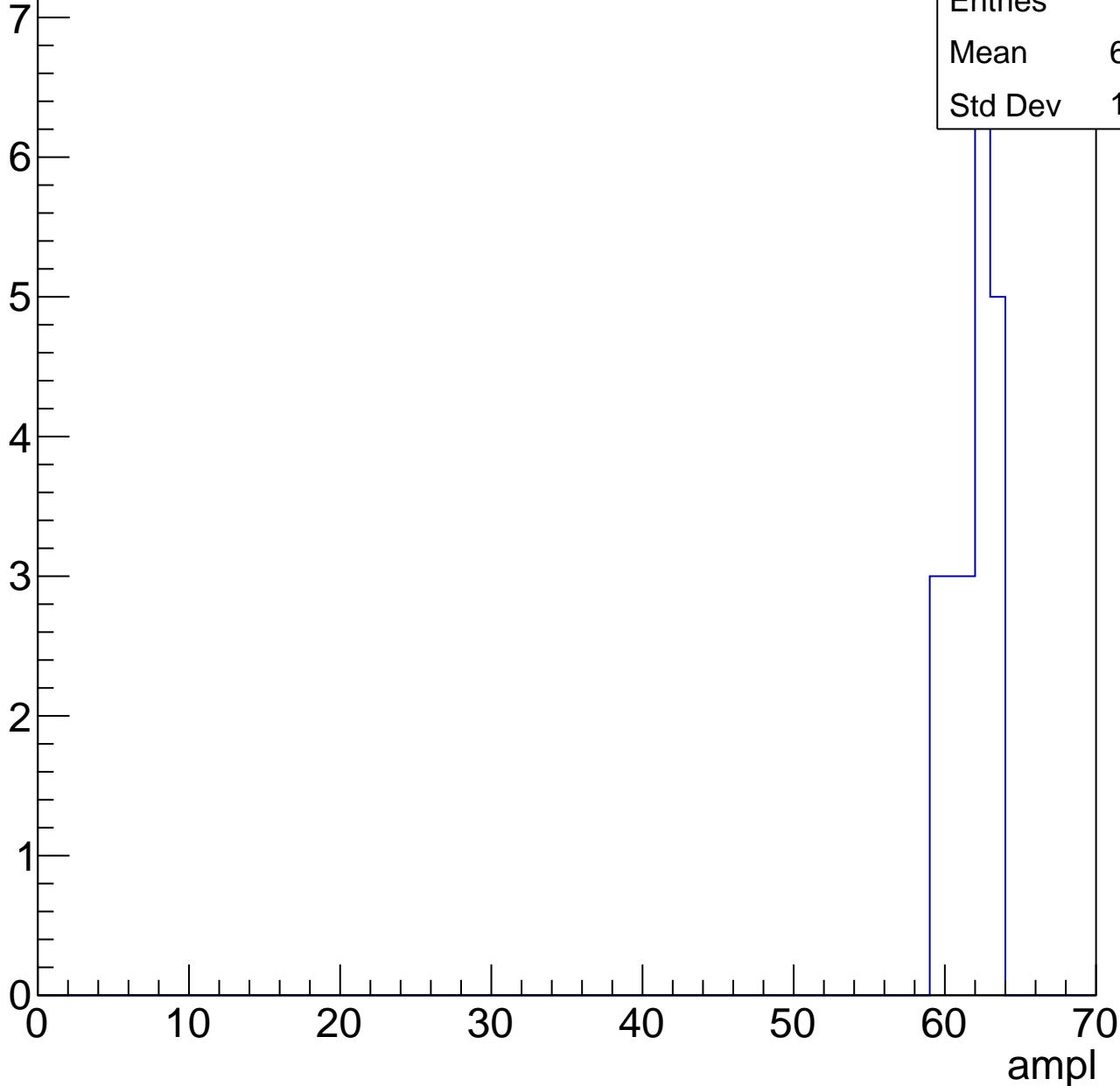


B1L102S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 21 |
| Mean | 61.38 |
| Std Dev | 1.362 |



B1L102S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch27, adc0

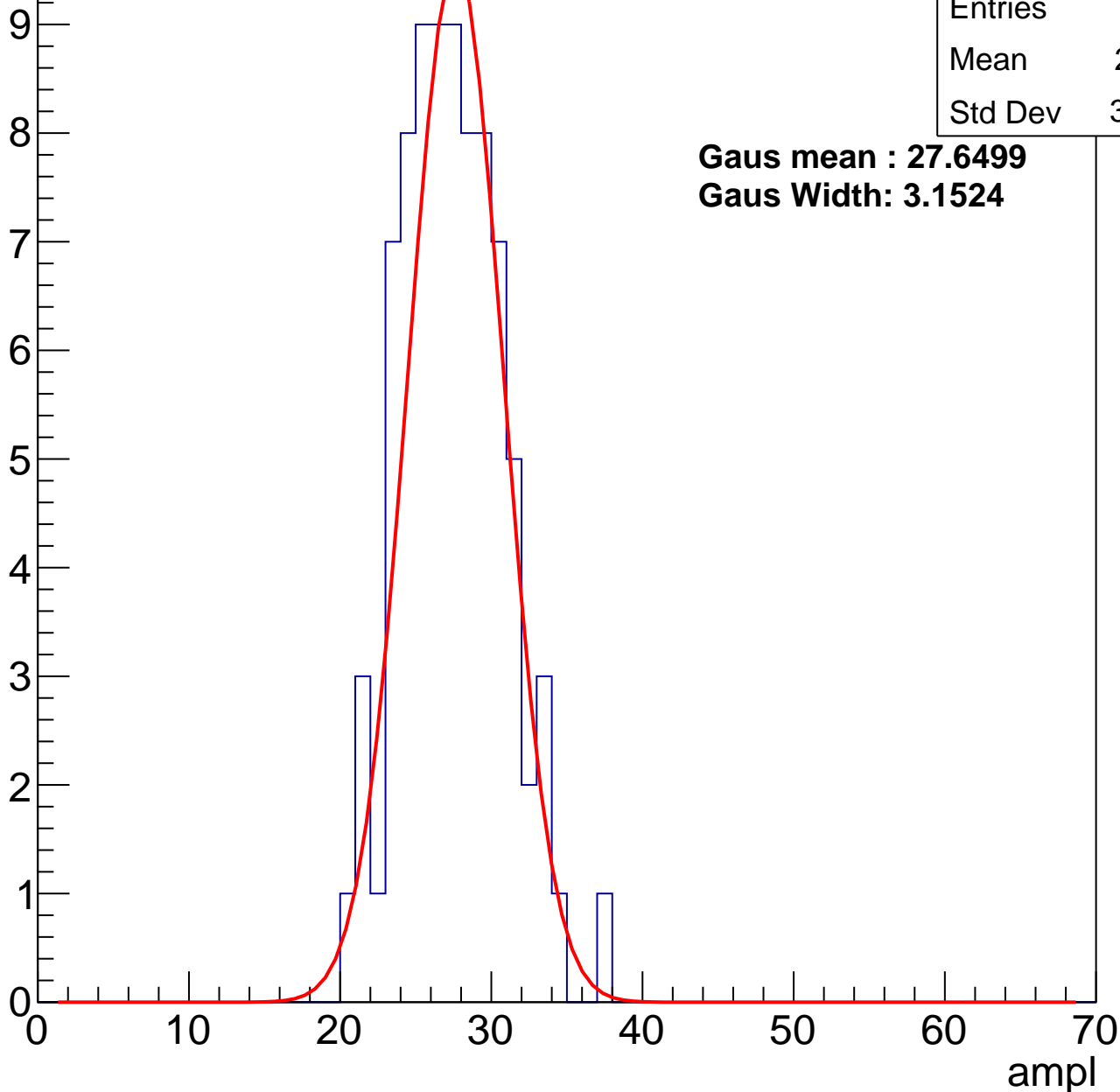
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 27.01 |
| Std Dev | 3.315 |

Gaus mean : 27.6499

Gaus Width: 3.1524



B1L102S, U15-ch27, adc1

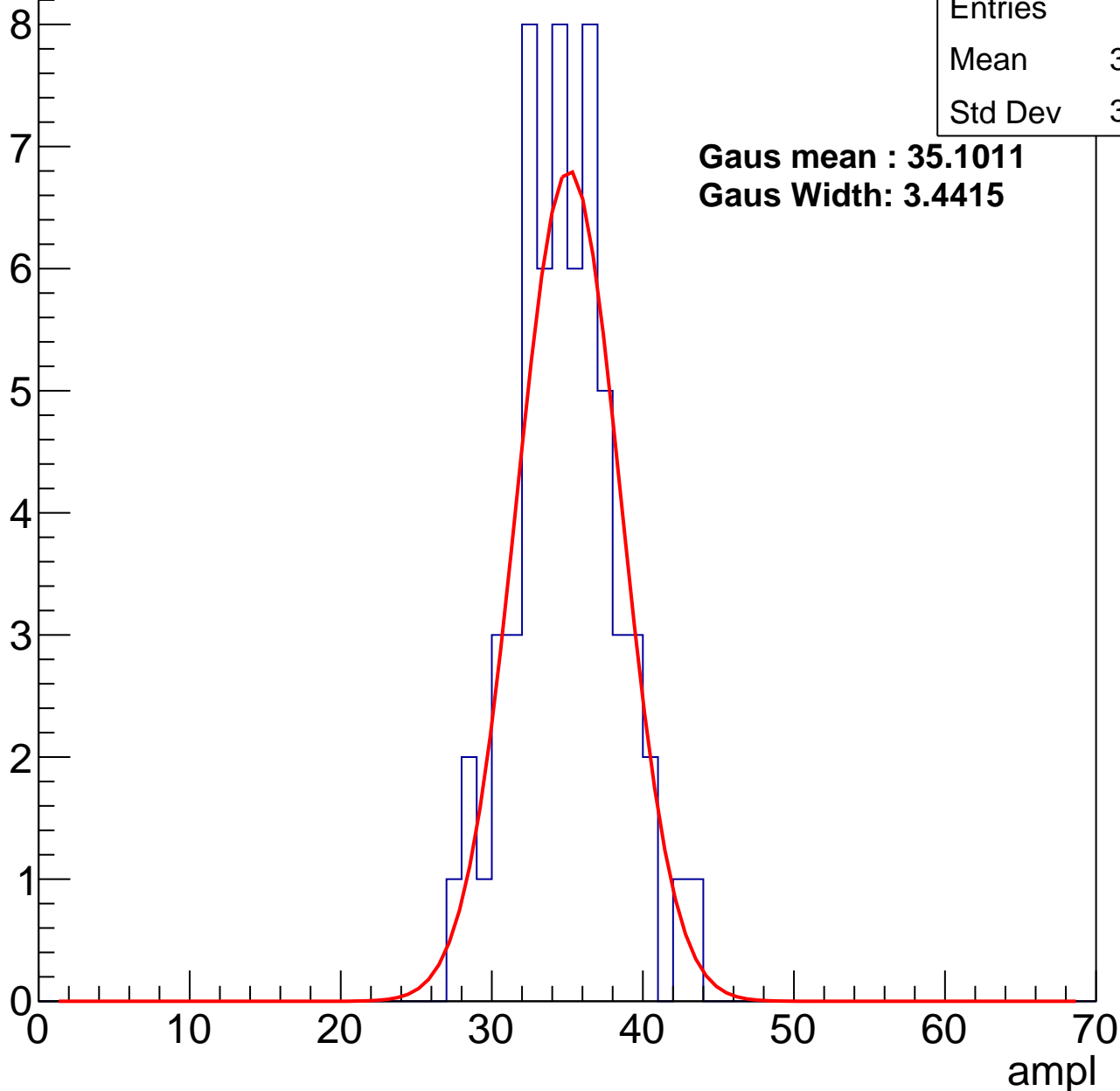
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 34.43 |
| Std Dev | 3.316 |

Gaus mean : 35.1011

Gaus Width: 3.4415



B1L102S, U15-ch27, adc2

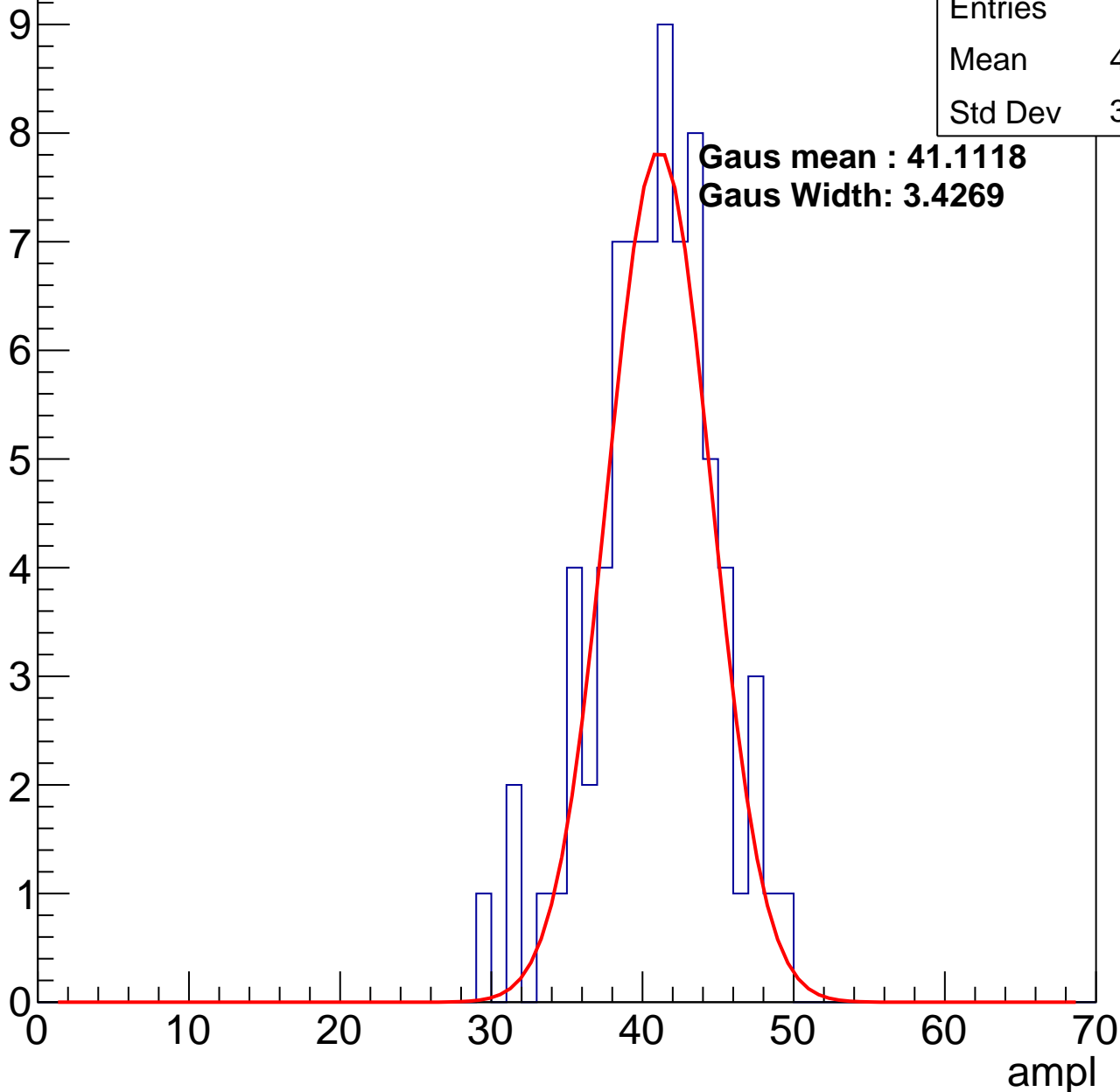
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 40.37 |
| Std Dev | 3.962 |

Gaus mean : 41.1118

Gaus Width: 3.4269

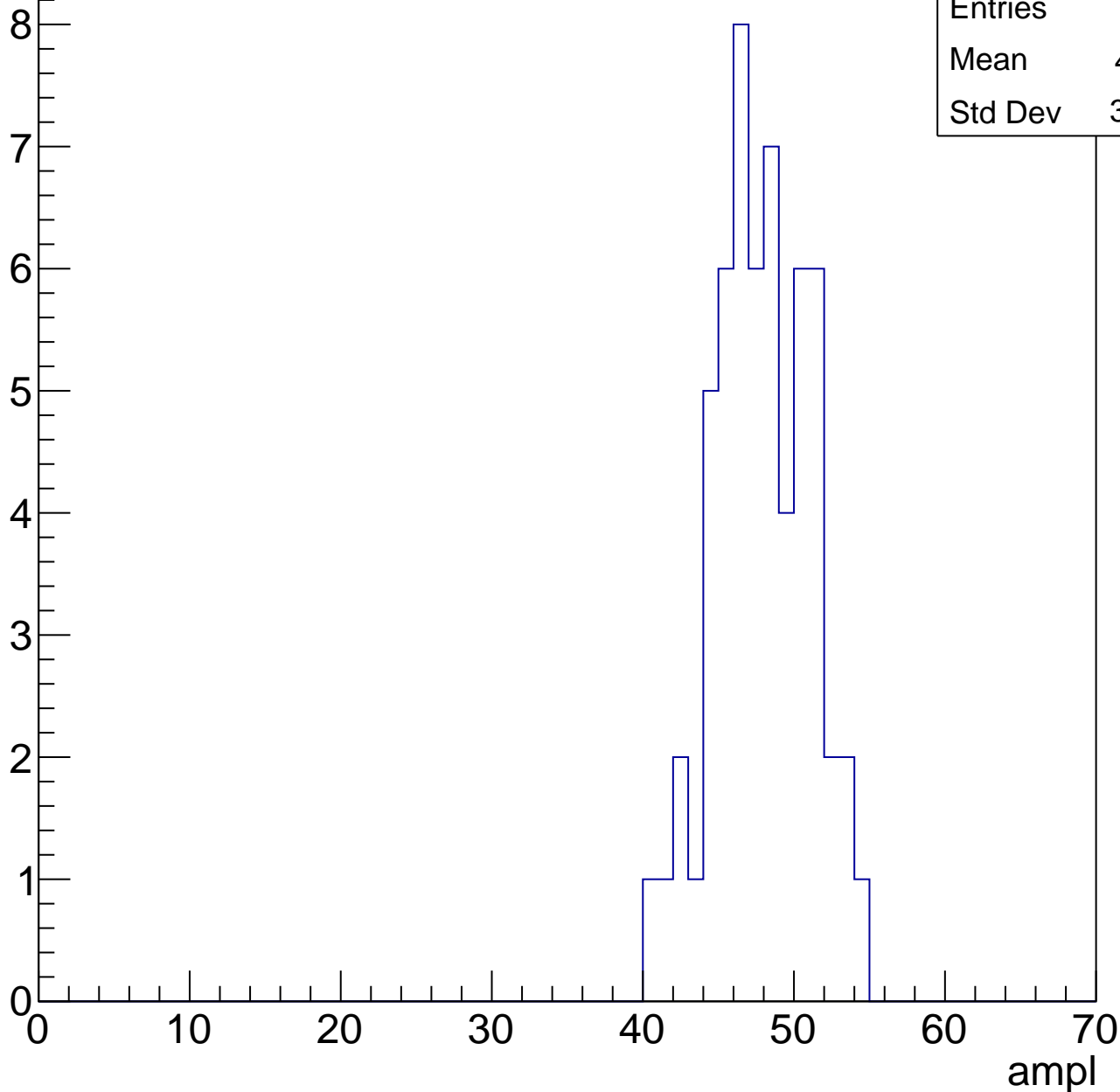


B1L102S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 47.41 |
| Std Dev | 3.113 |

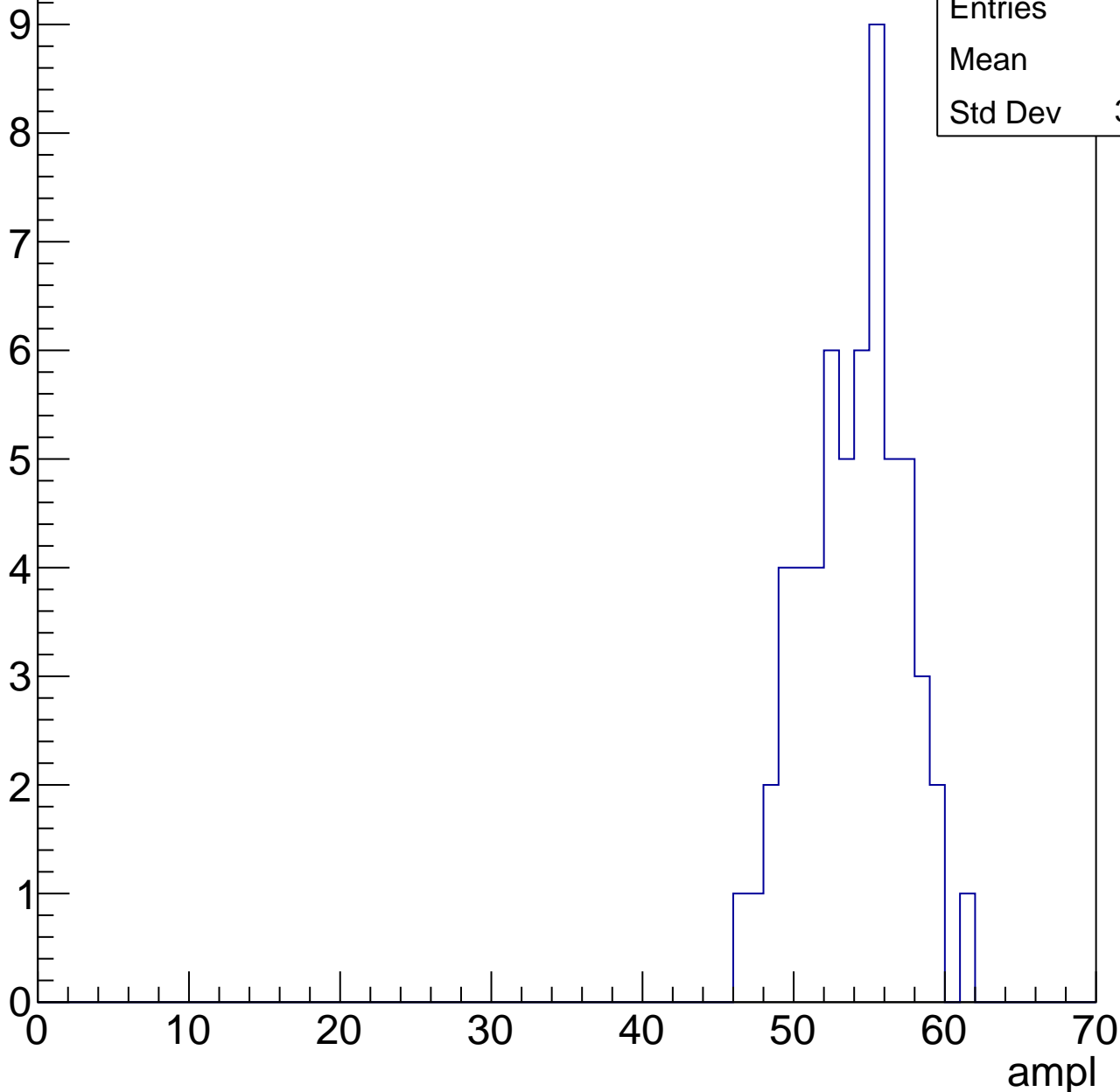


B1L102S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 53.5 |
| Std Dev | 3.271 |



B1L102S, U15-ch27, adc5

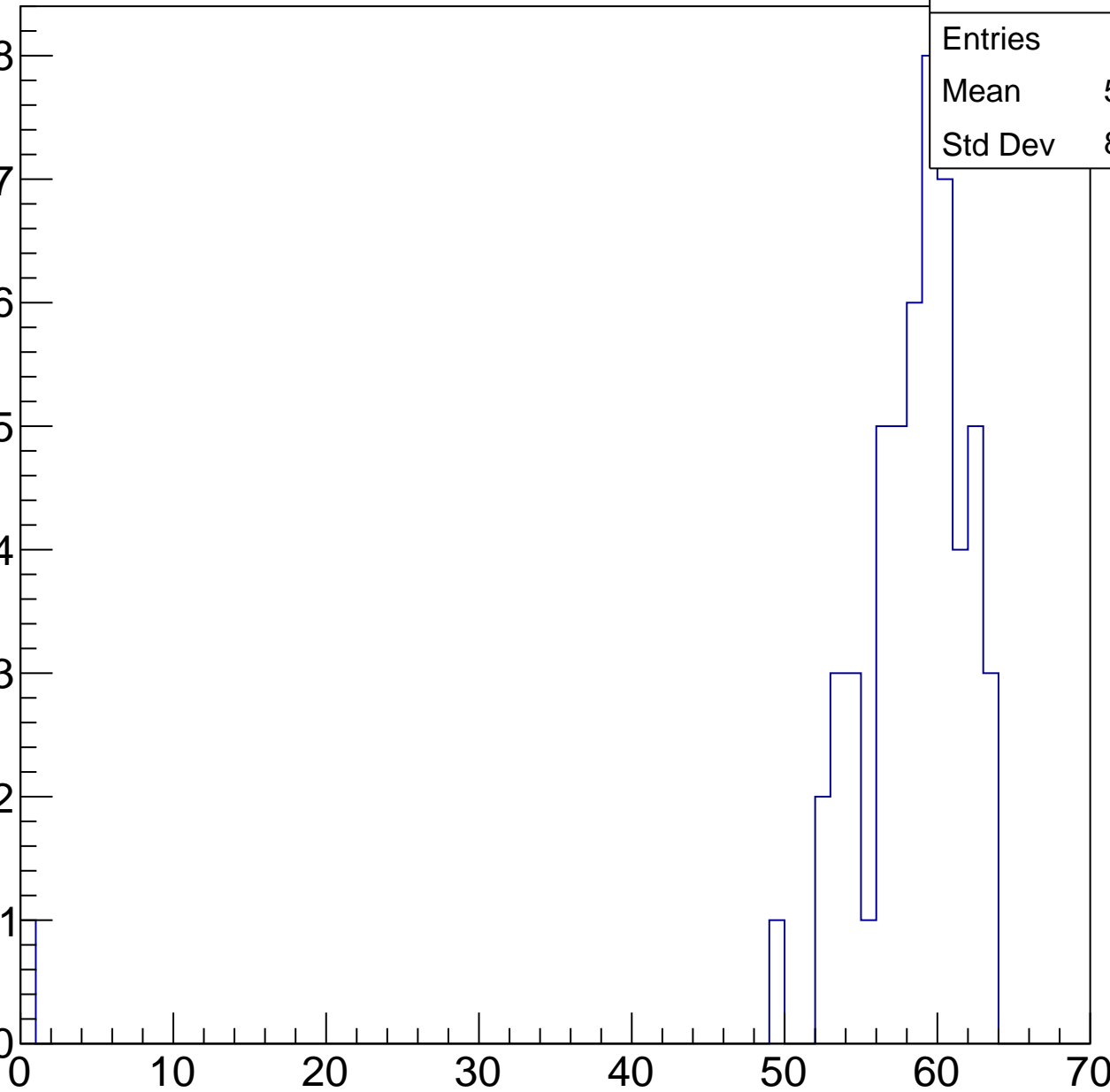
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 56.98 |
| Std Dev | 8.438 |

8
7
6
5
4
3
2
1
0

ampl

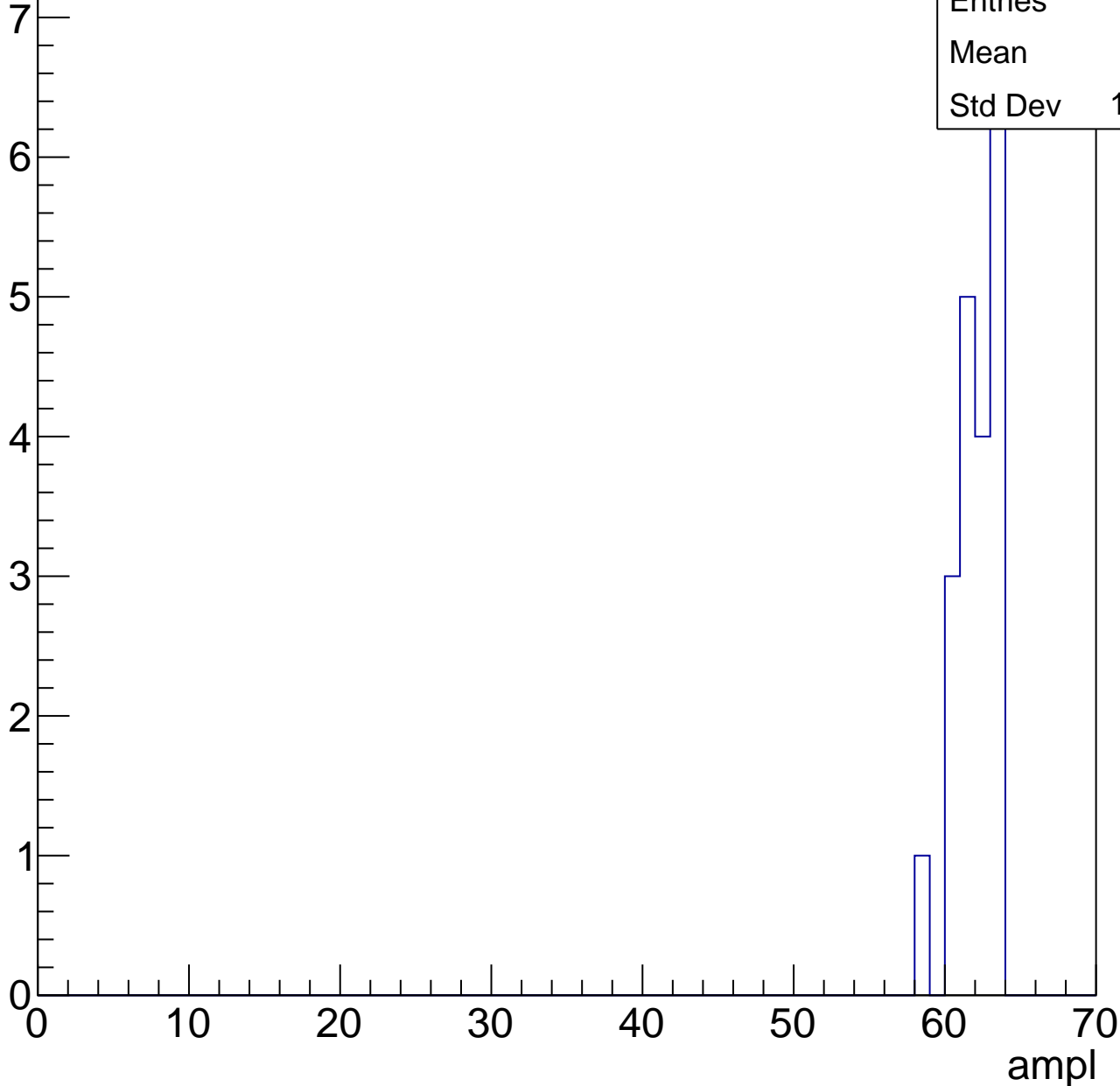


B1L102S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 61.6 |
| Std Dev | 1.356 |



B1L102S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|----|
| Entries | 1 |
| Mean | 62 |
| Std Dev | 0 |

B1L102S, U15-ch28, adc0

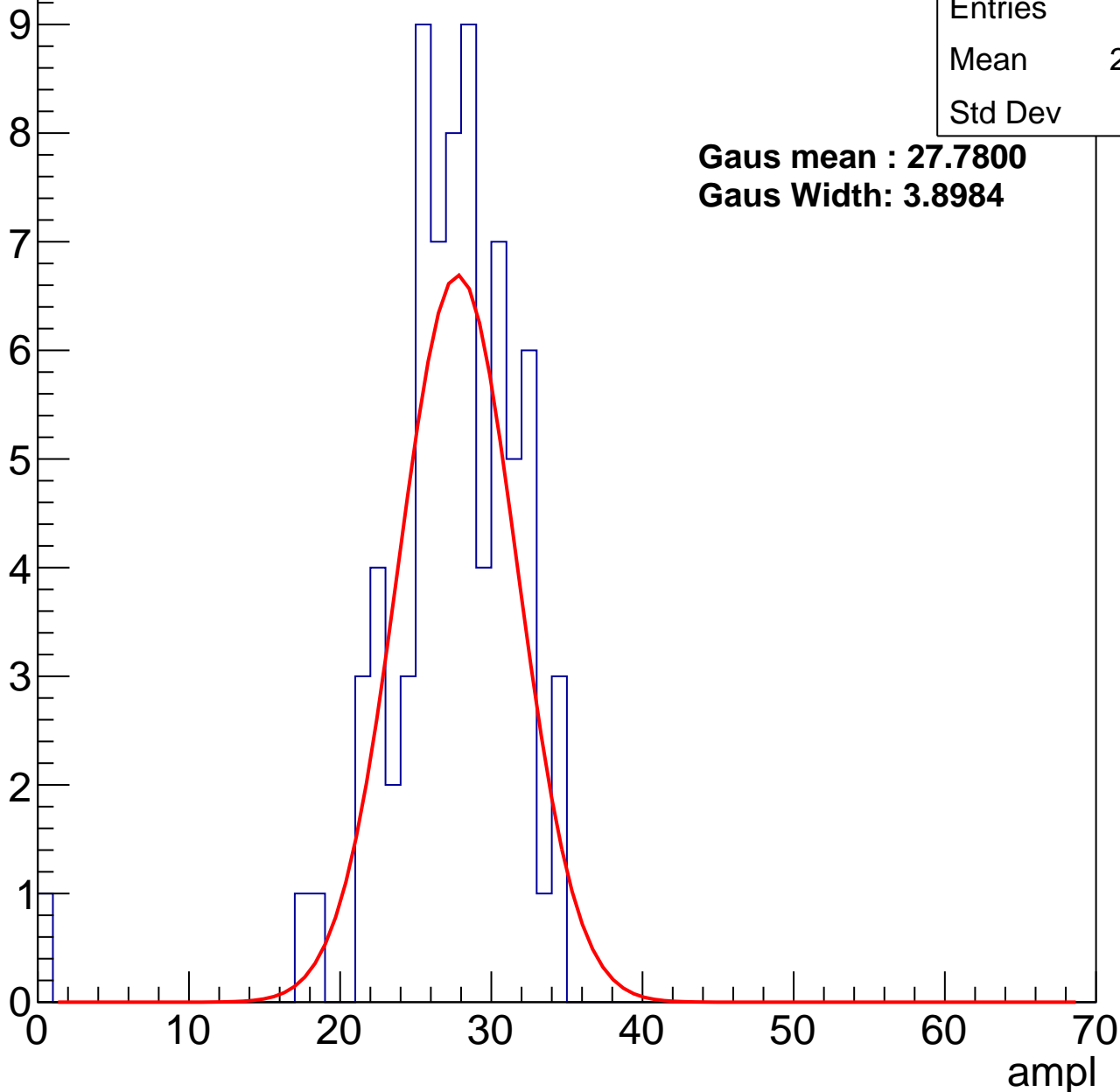
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 26.85 |
| Std Dev | 4.82 |

Gaus mean : 27.7800

Gaus Width: 3.8984



B1L102S, U15-ch28, adc1

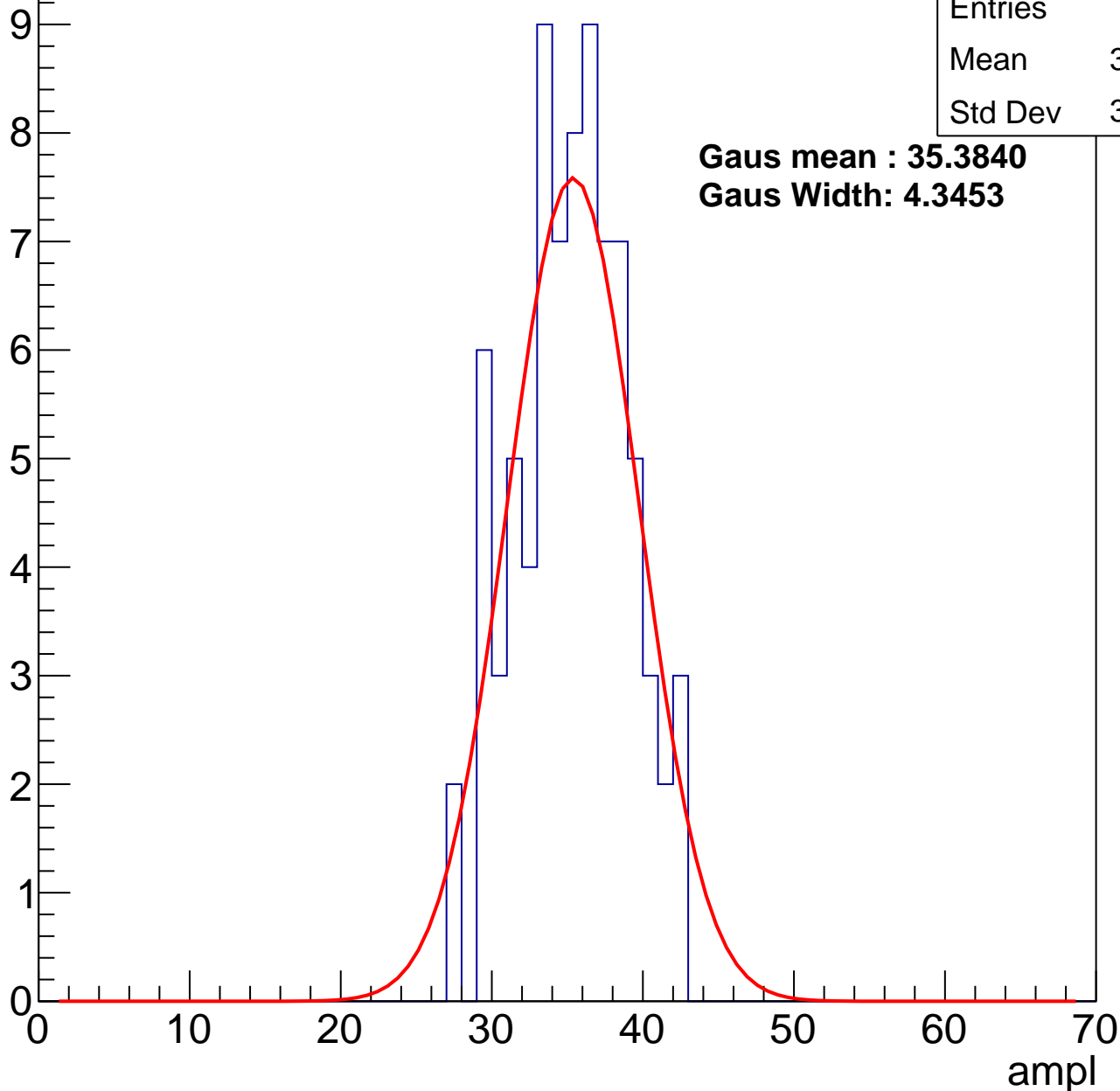
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 34.85 |
| Std Dev | 3.623 |

Gaus mean : 35.3840

Gaus Width: 4.3453



B1L102S, U15-ch28, adc2

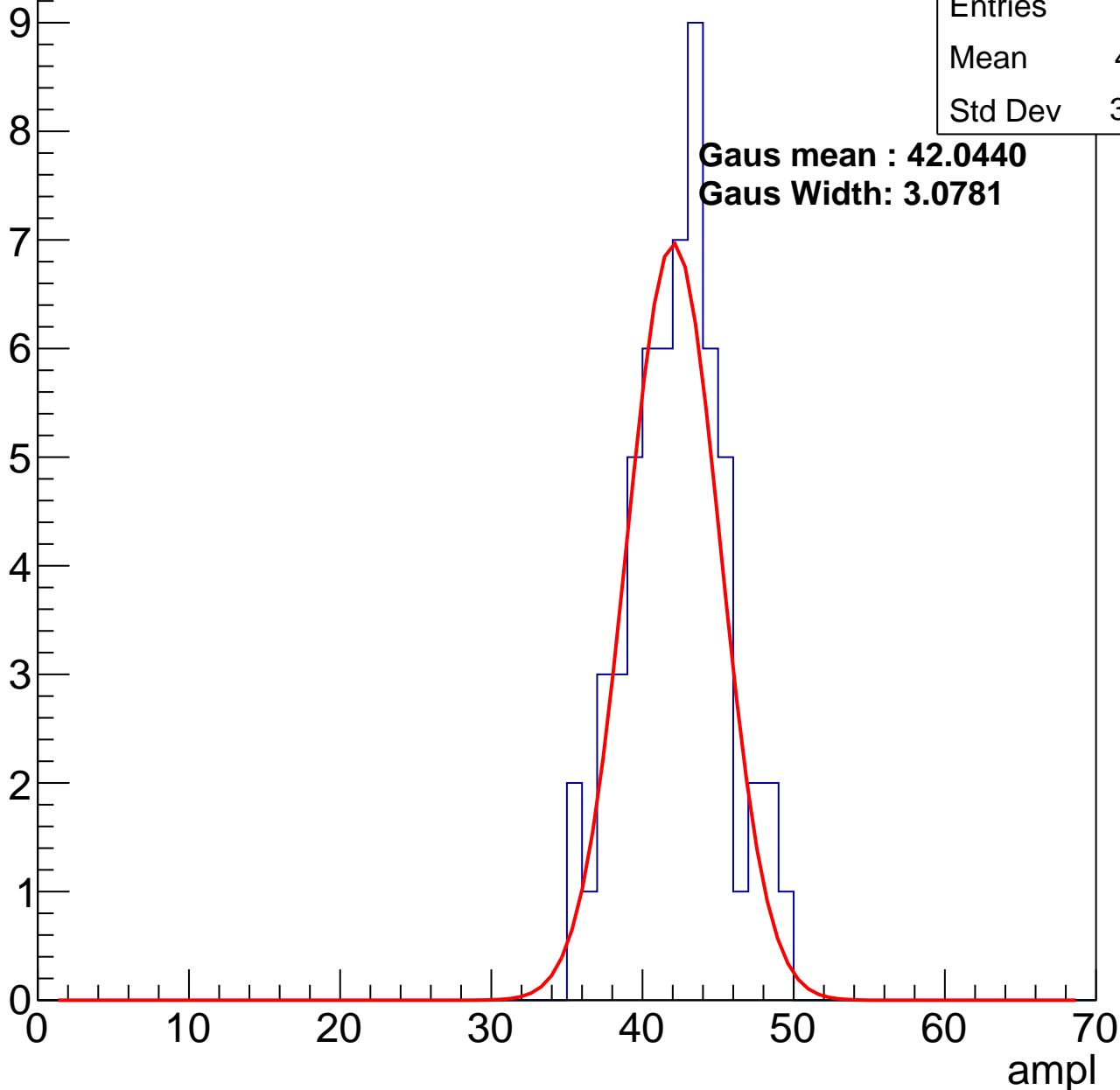
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 41.81 |
| Std Dev | 3.176 |

Gaus mean : 42.0440

Gaus Width: 3.0781

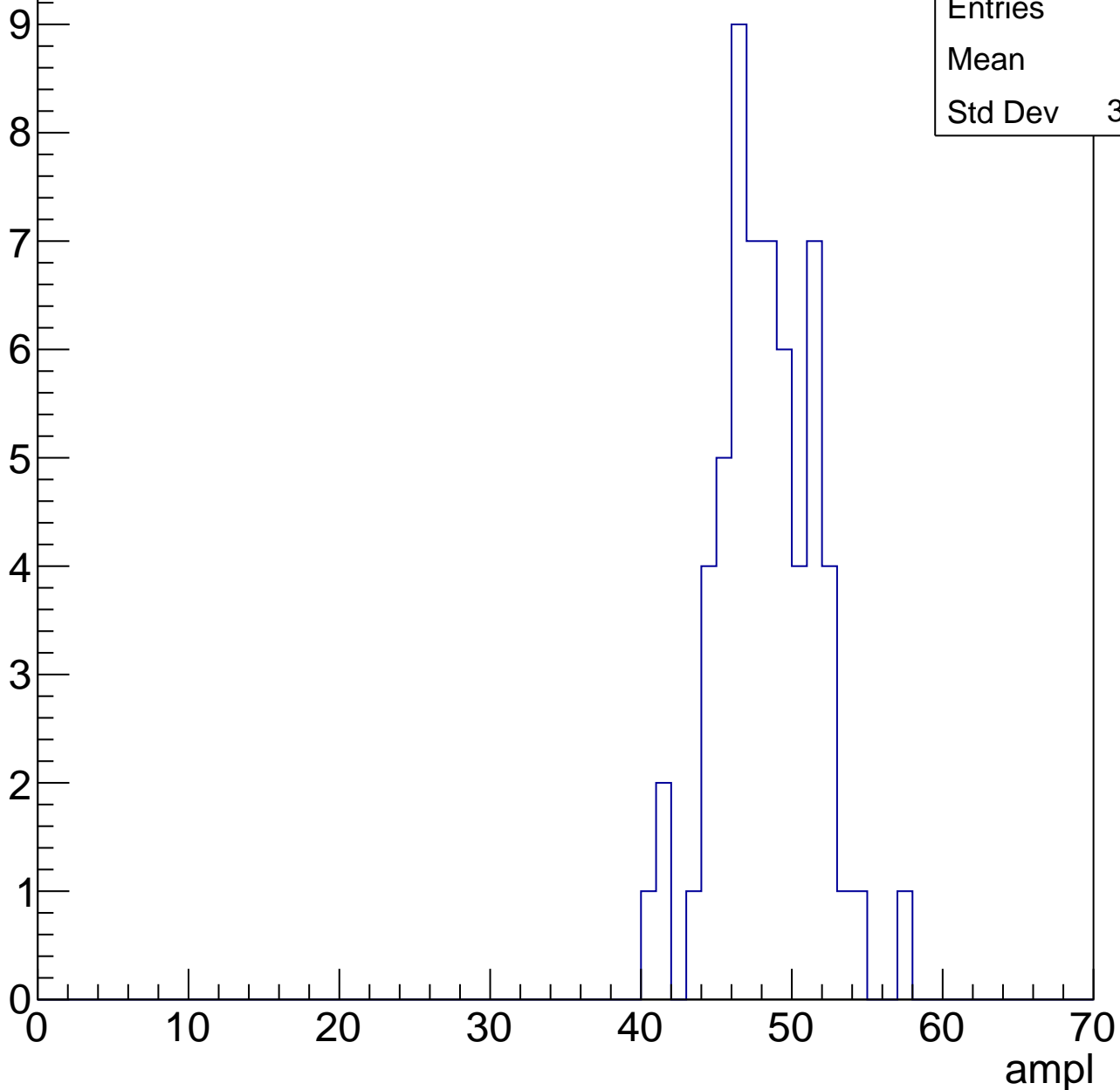


B1L102S, U15-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.8 |
| Std Dev | 3.234 |

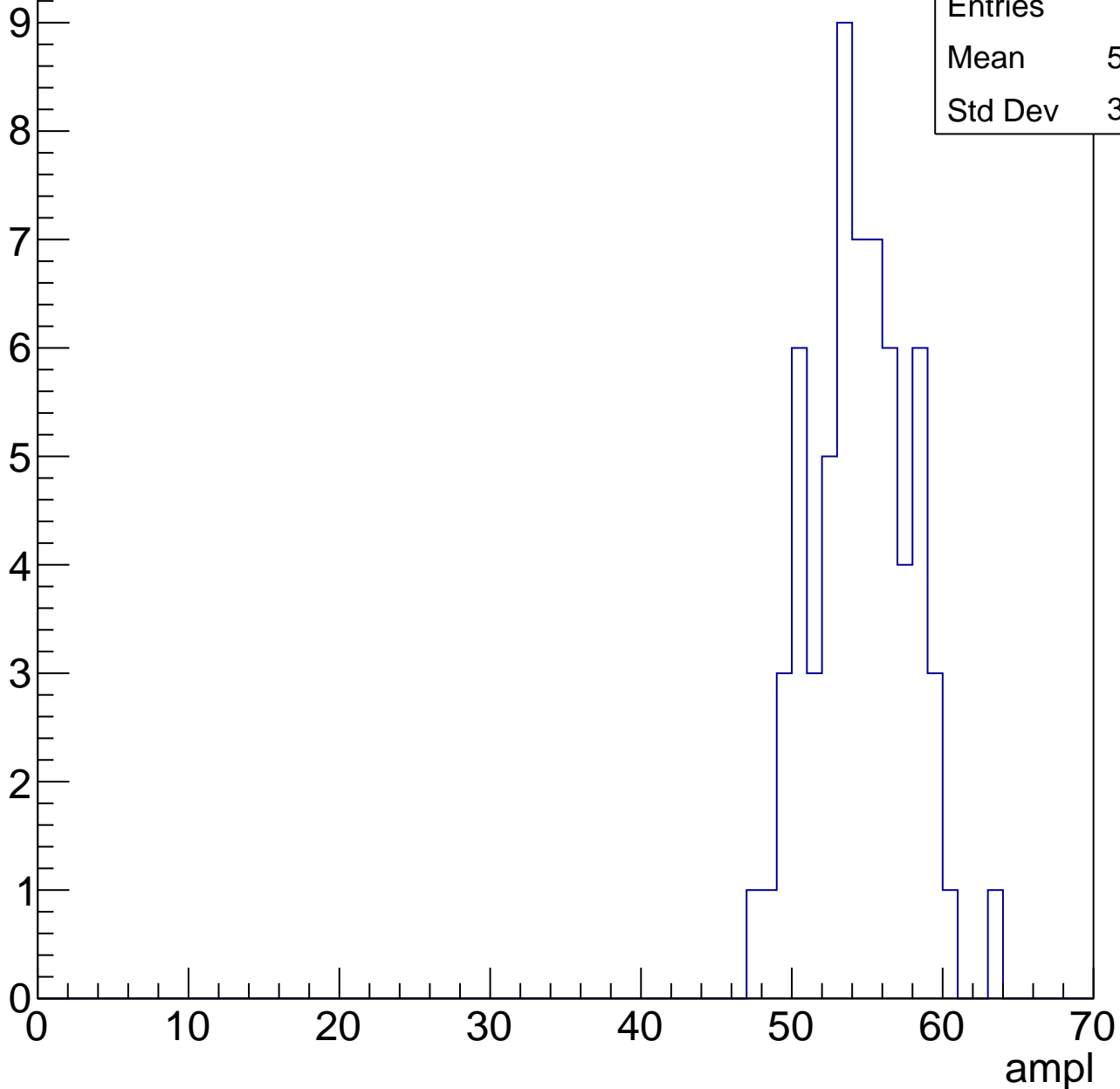


B1L102S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 54.08 |
| Std Dev | 3.253 |

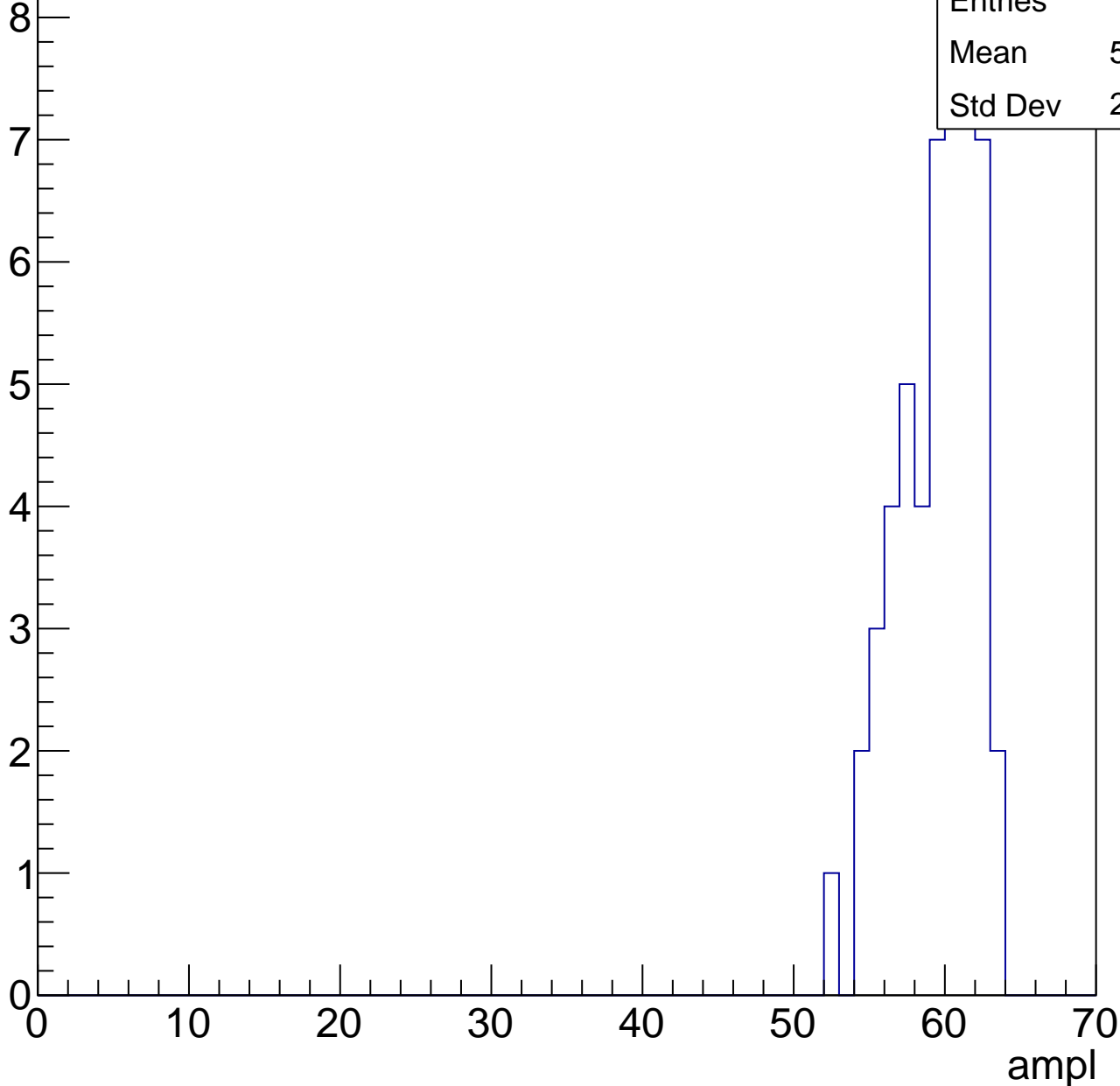


B1L102S, U15-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.96 |
| Std Dev | 2.589 |

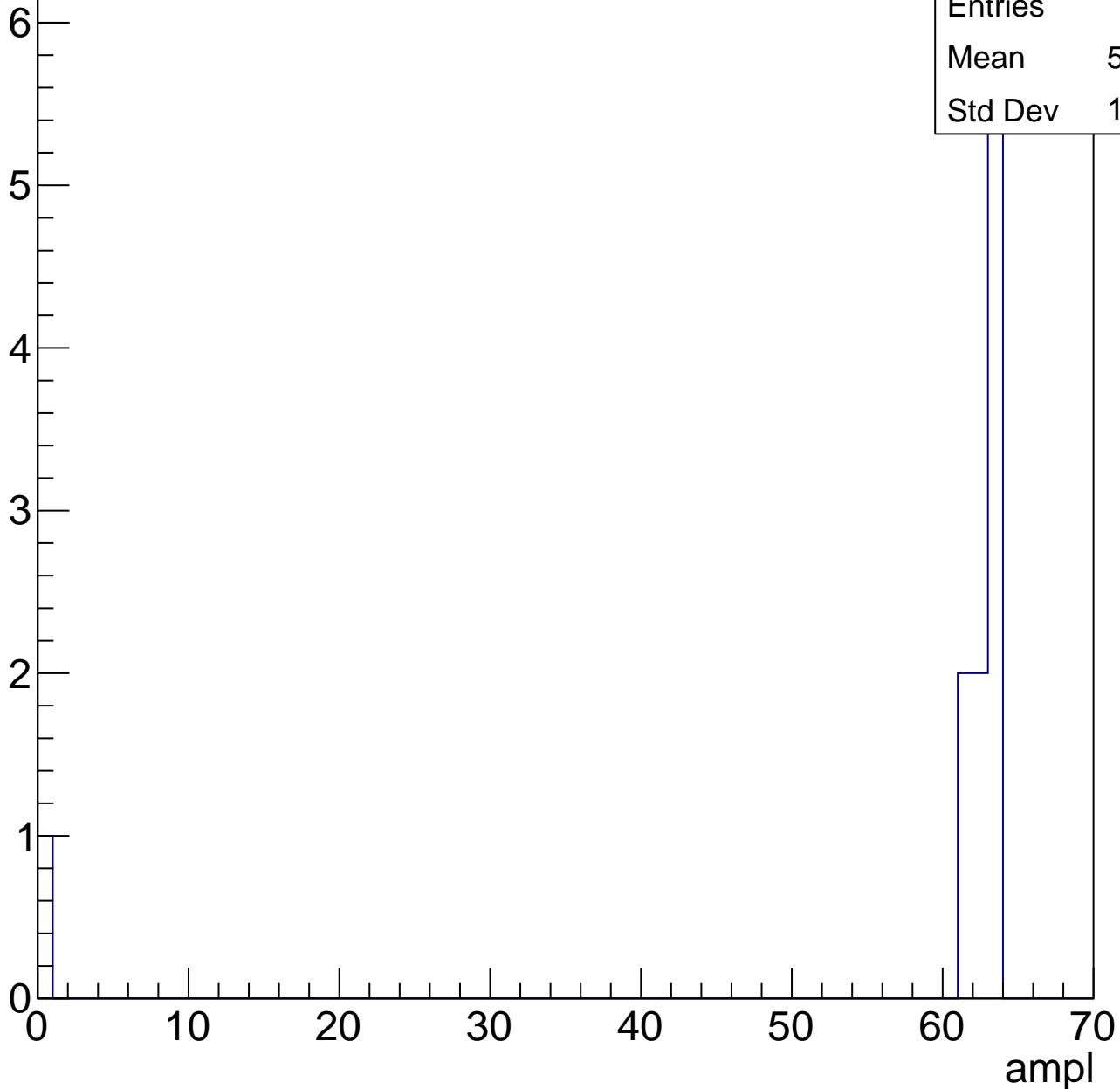


B1L102S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

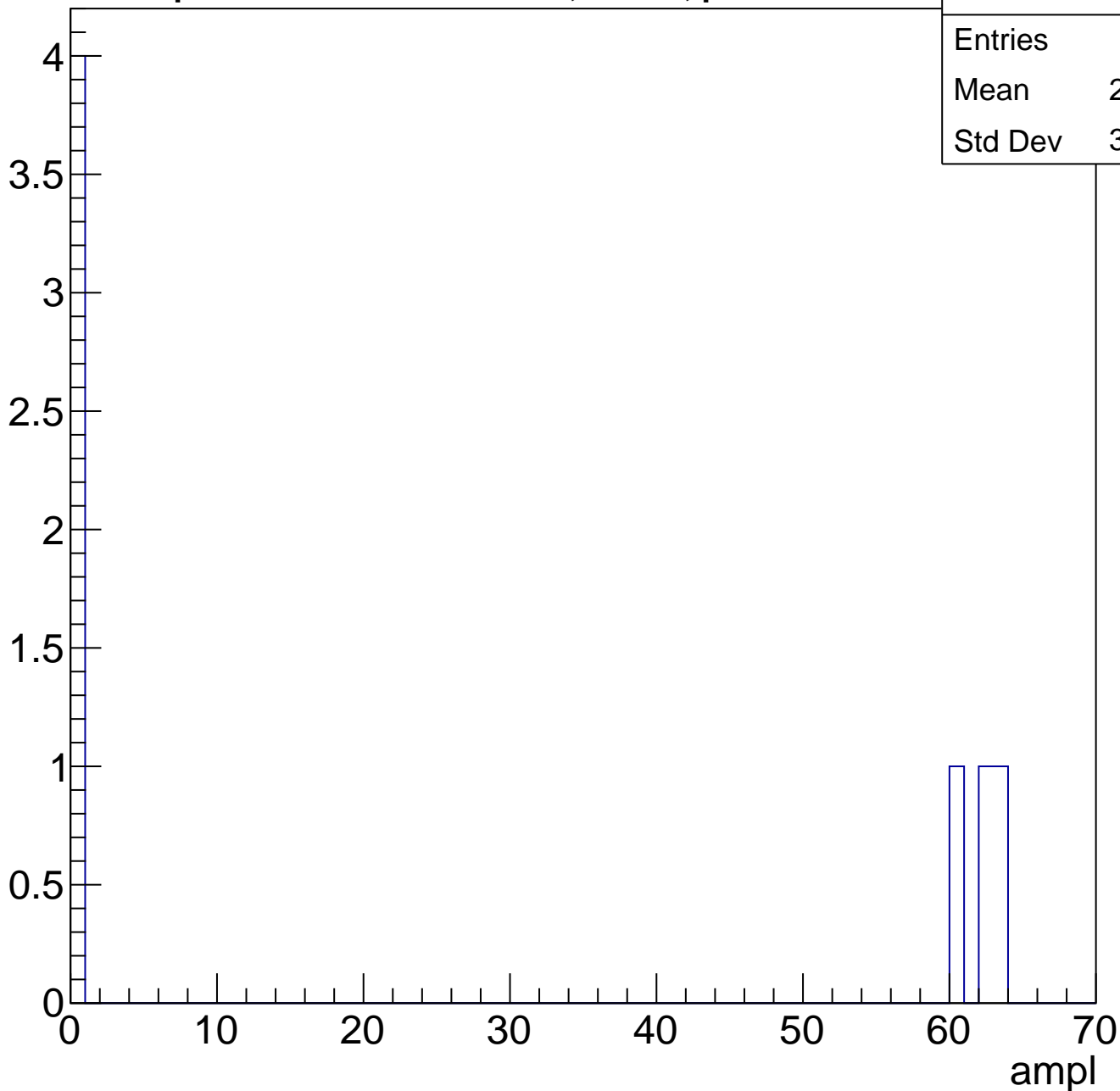
| | |
|---------|-------|
| Entries | 11 |
| Mean | 56.73 |
| Std Dev | 17.95 |



B1L102S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 7 |
| Mean | 26.43 |
| Std Dev | 30.53 |

B1L102S, U15-ch29, adc0

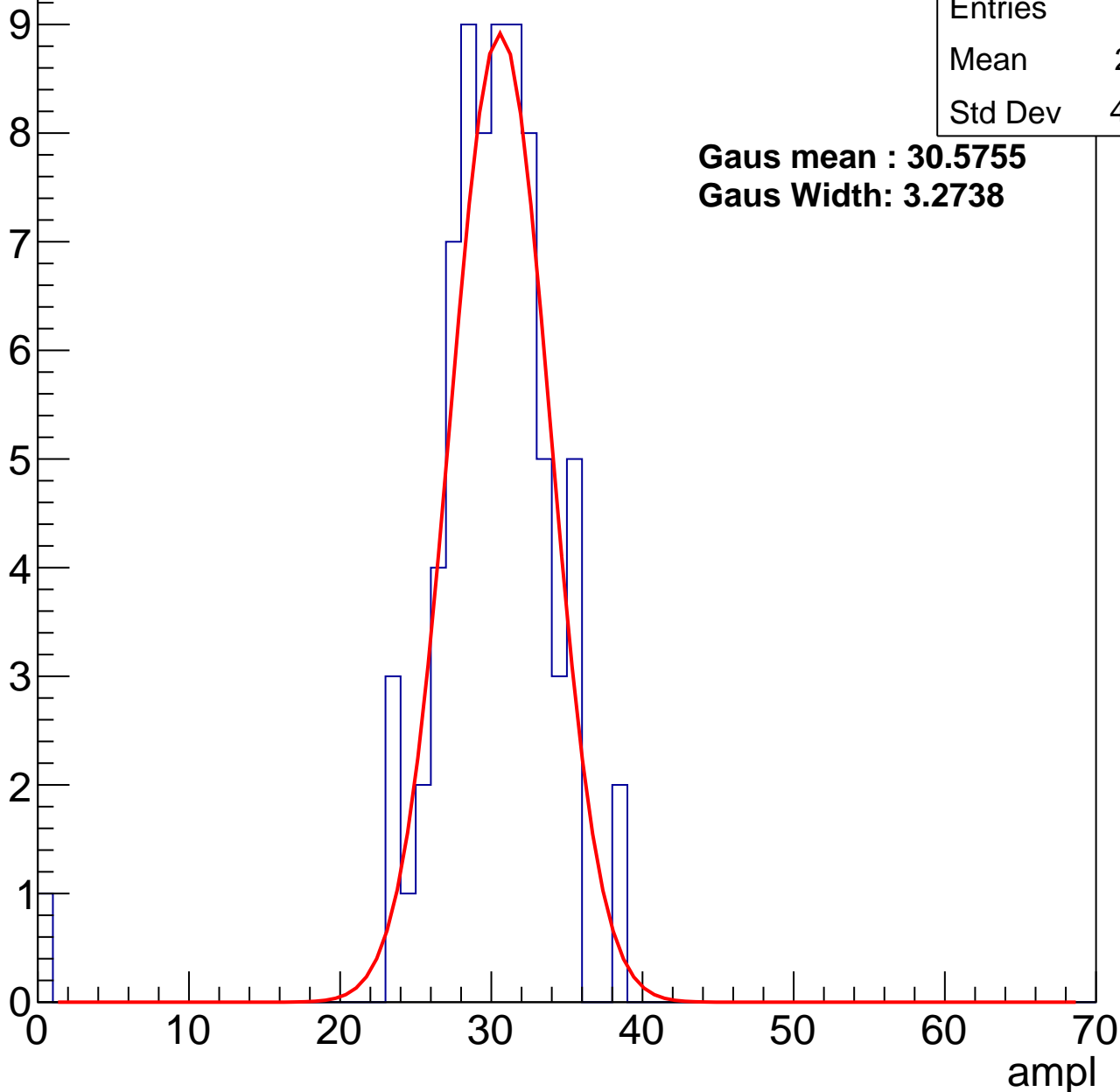
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 29.51 |
| Std Dev | 4.692 |

Gaus mean : 30.5755

Gaus Width: 3.2738



B1L102S, U15-ch29, adc1

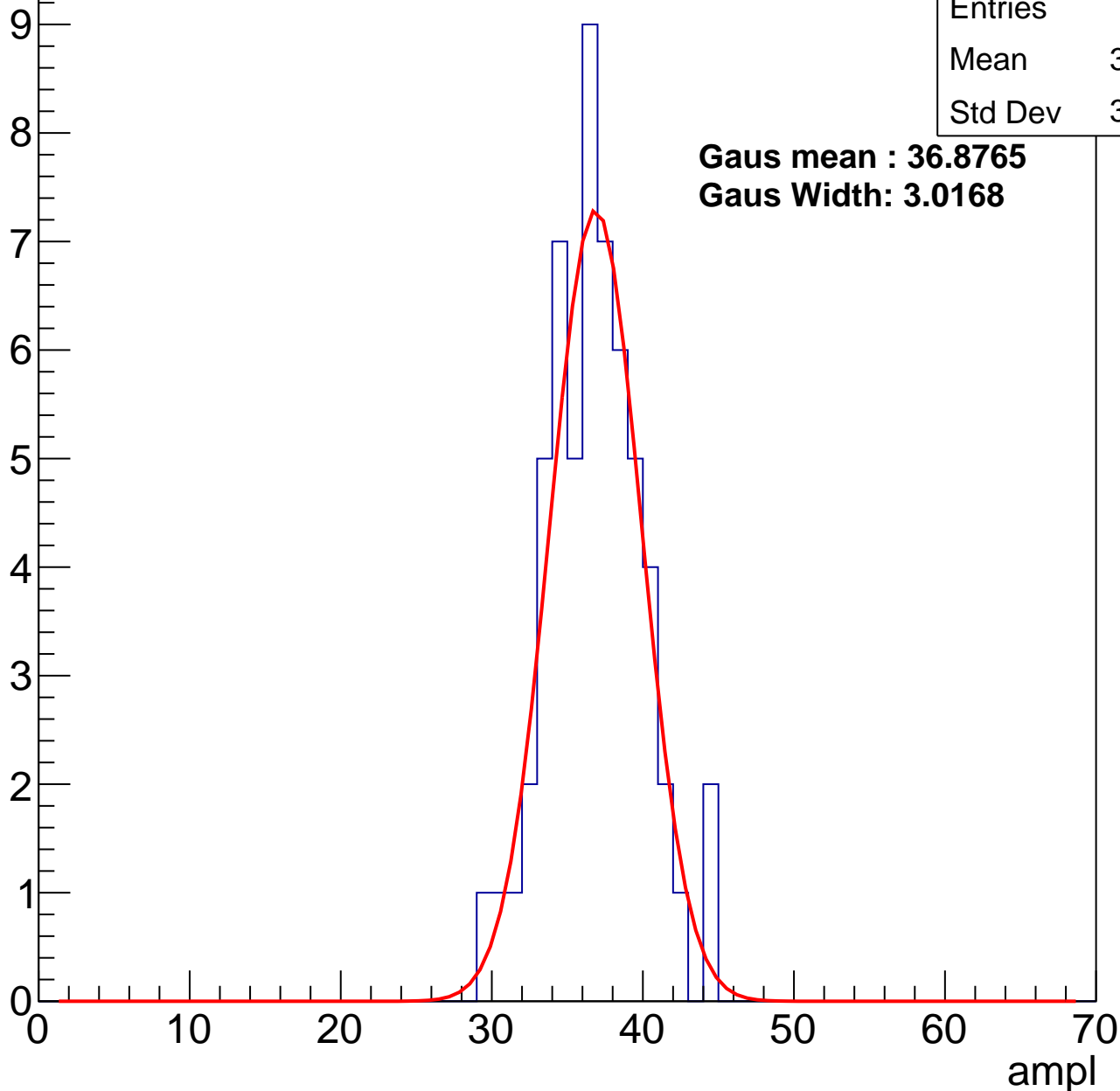
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 36.38 |
| Std Dev | 3.123 |

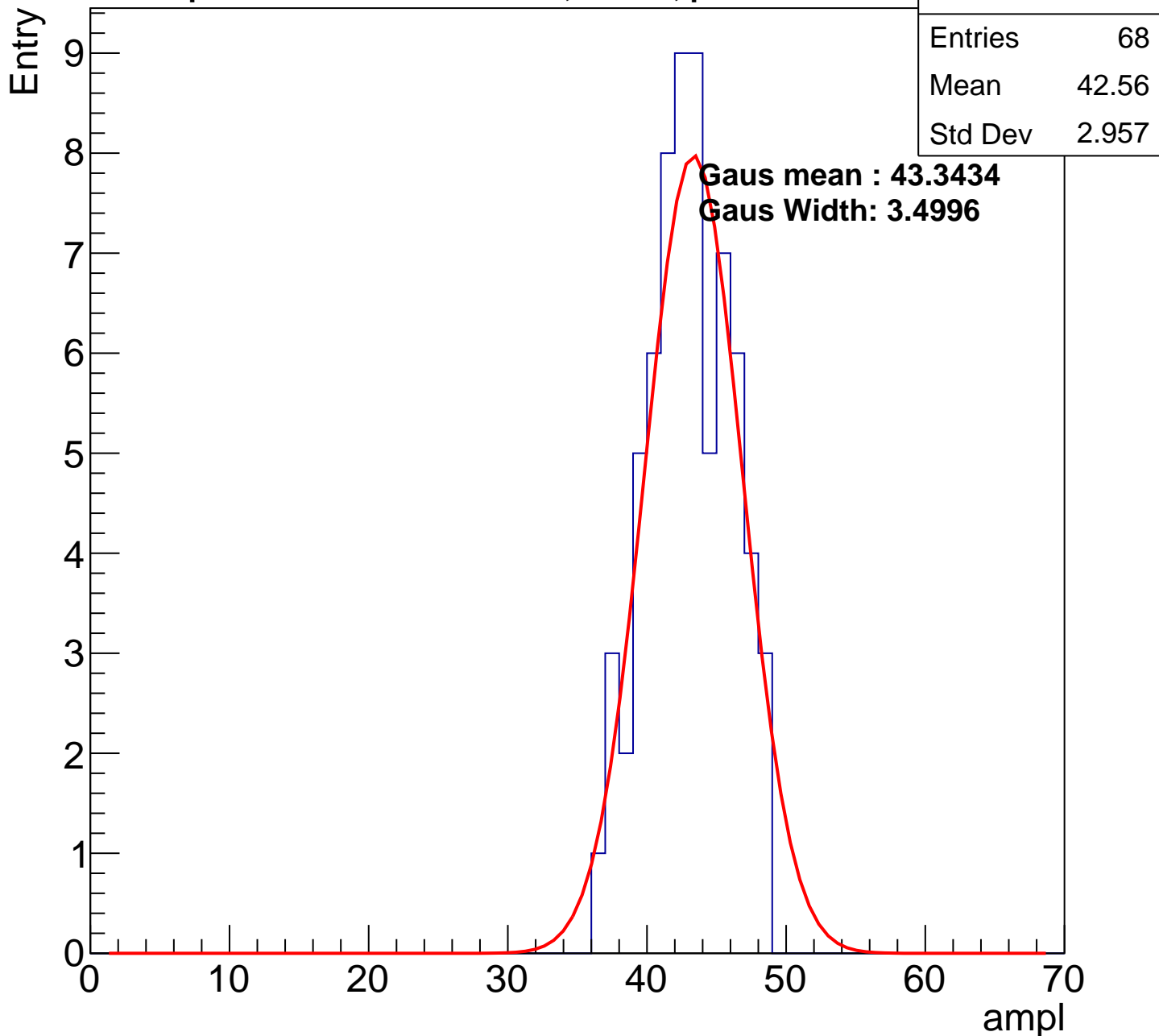
Gaus mean : 36.8765

Gaus Width: 3.0168



B1L102S, U15-ch29, adc2

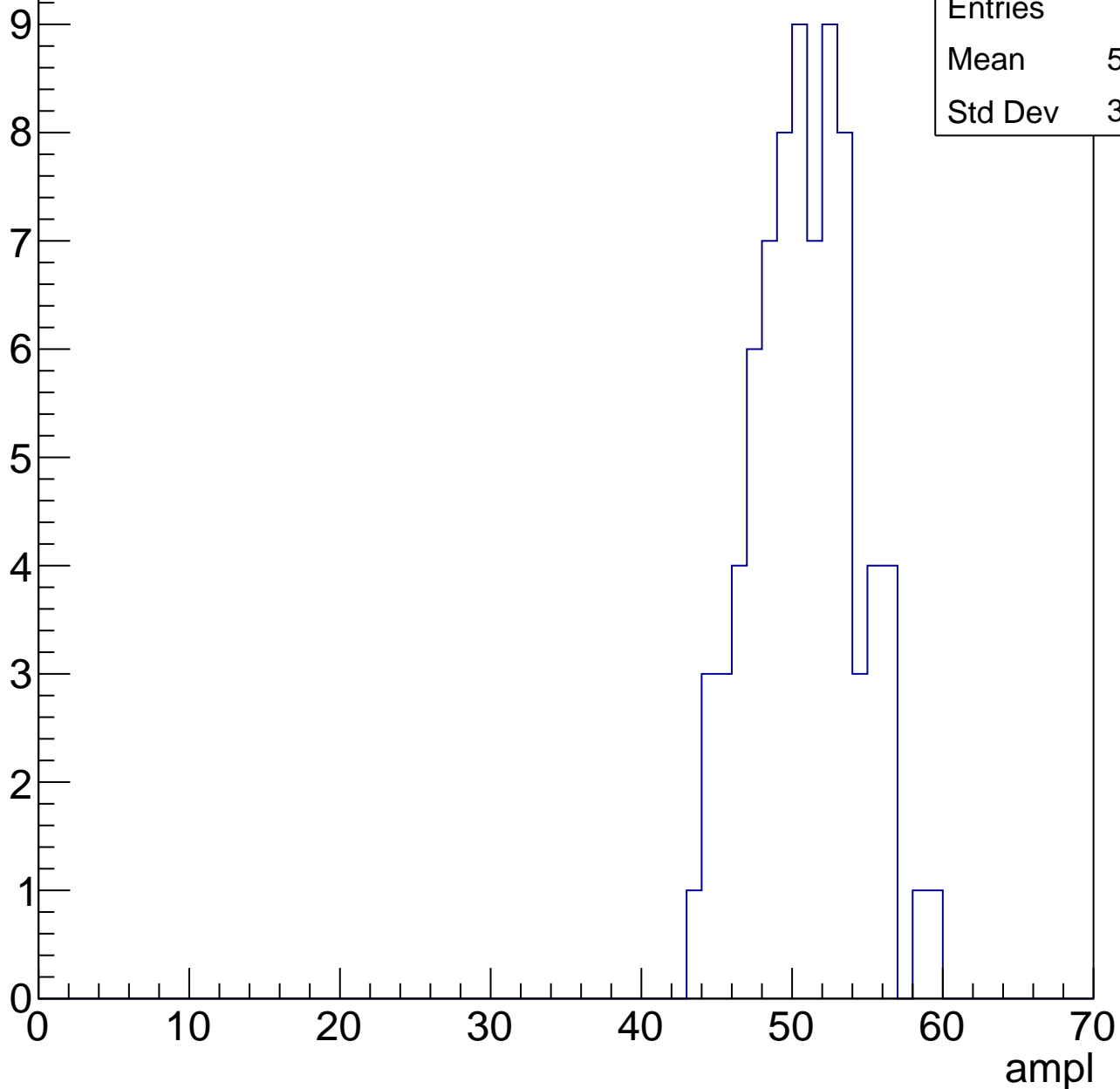
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



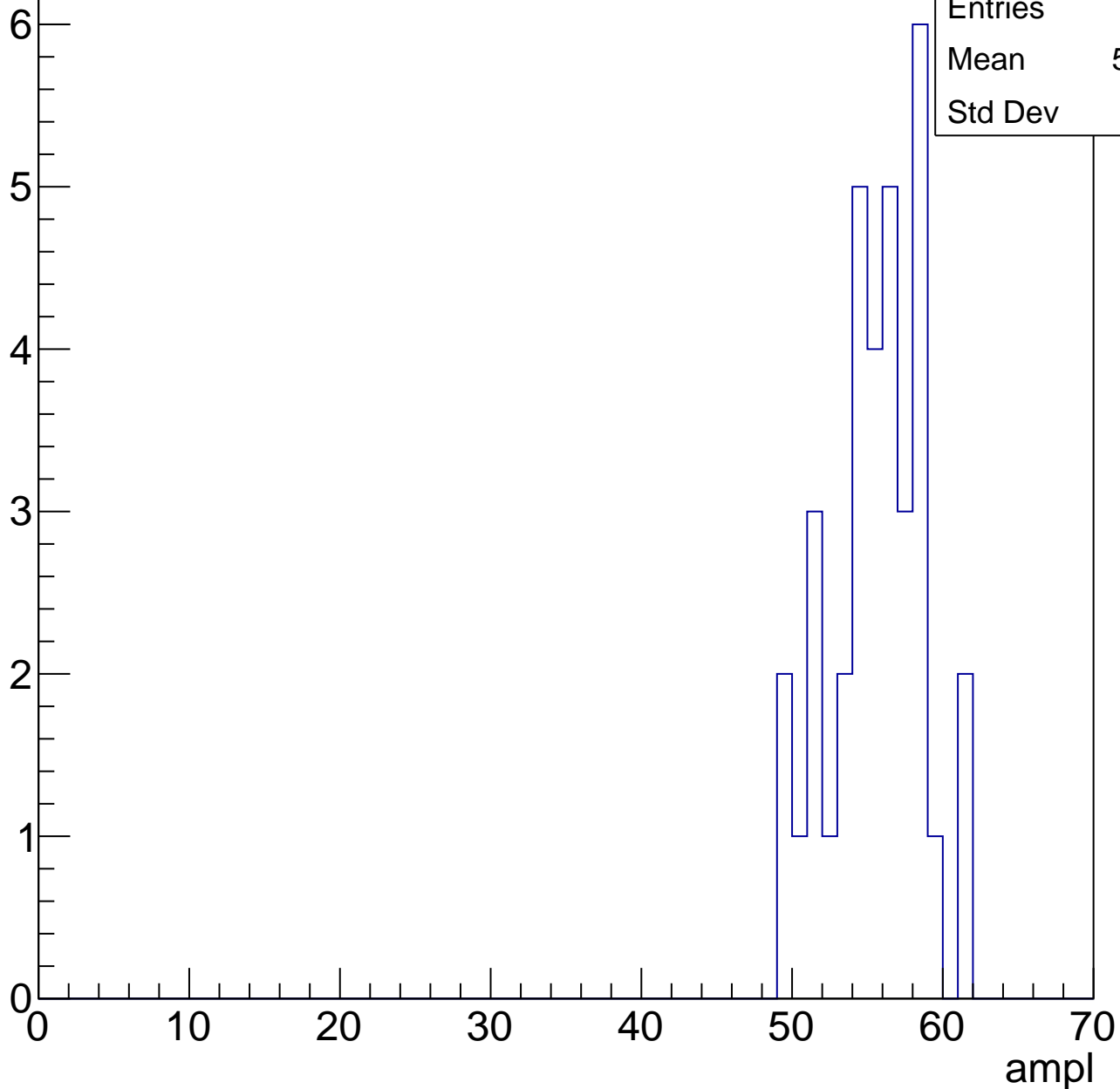
| | |
|---------|-------|
| Entries | 78 |
| Mean | 50.33 |
| Std Dev | 3.452 |

B1L102S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

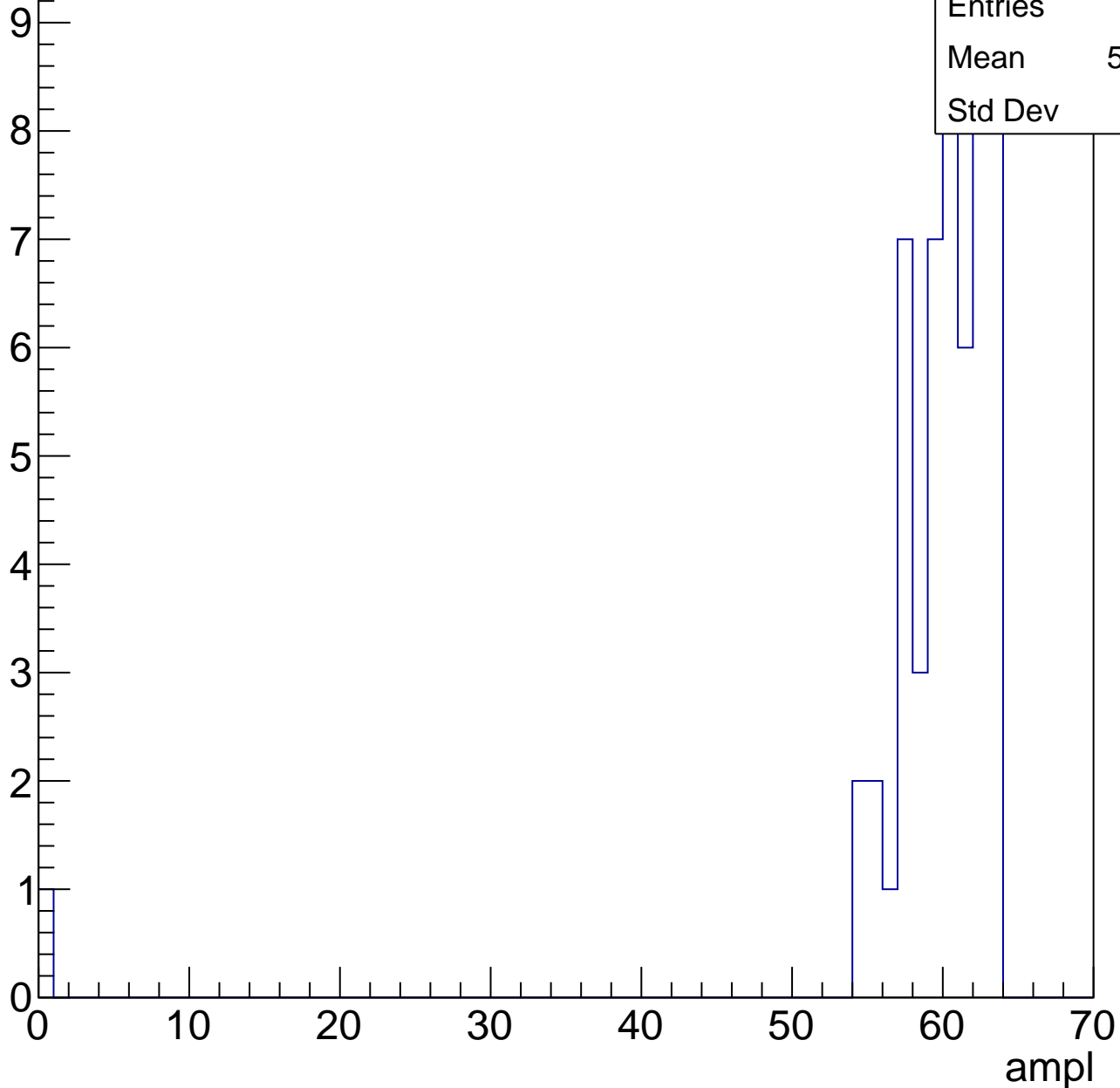
| | |
|---------|-------|
| Entries | 35 |
| Mean | 55.11 |
| Std Dev | 3.05 |



B1L102S, U15-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

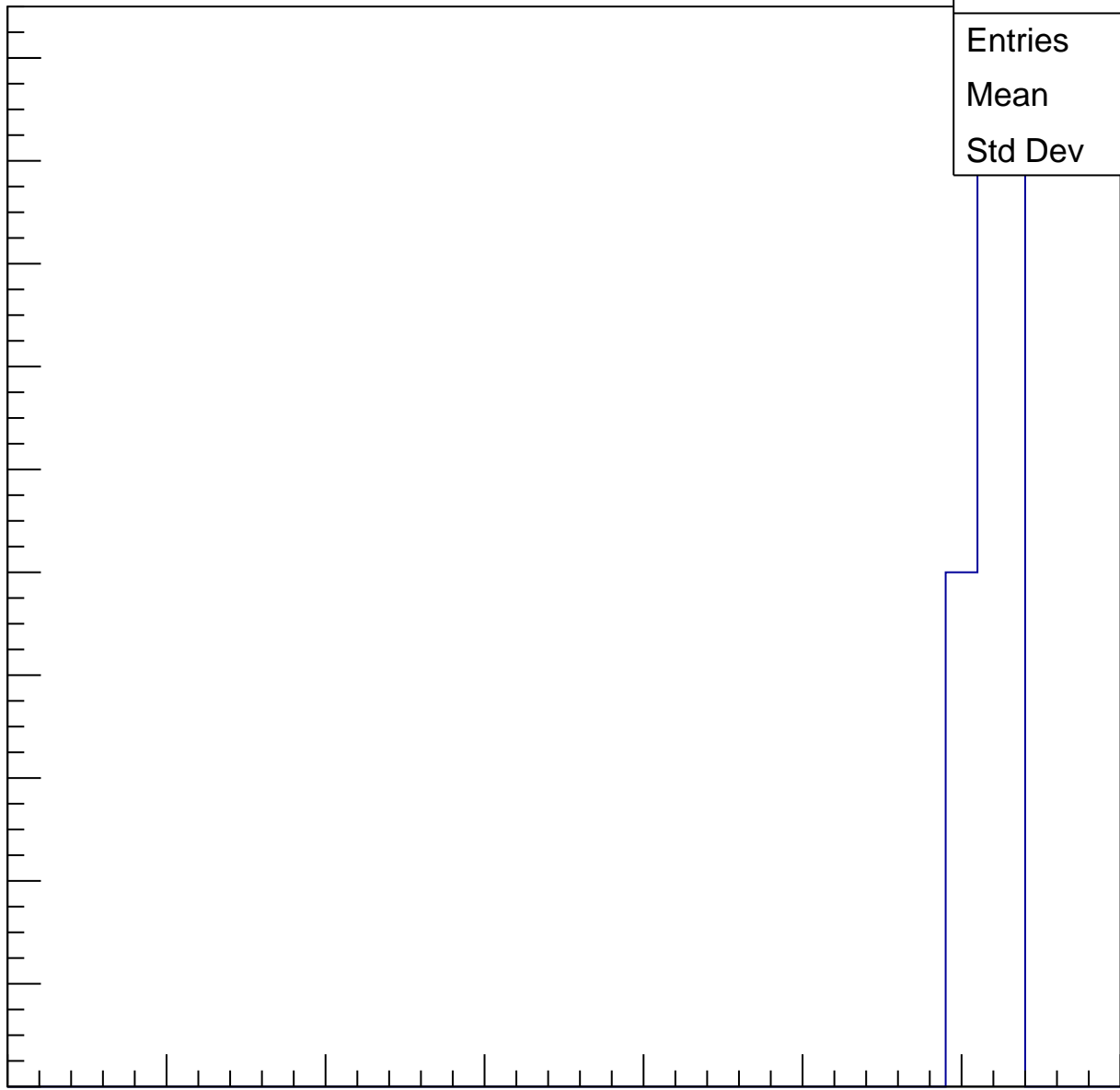
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 61.38 |
| Std Dev | 1.317 |

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch30, adc0

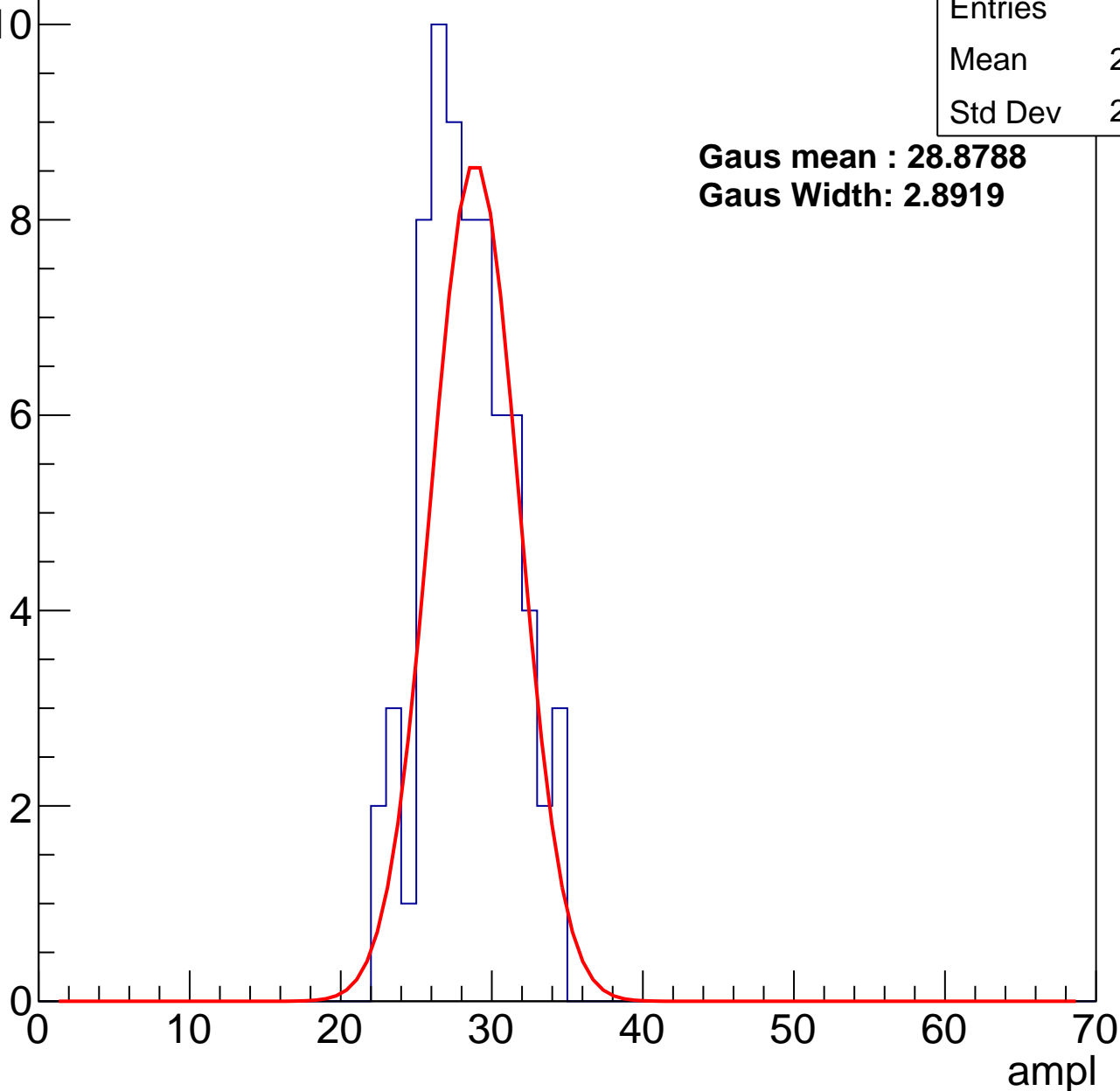
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 27.97 |
| Std Dev | 2.908 |

Gaus mean : 28.8788

Gaus Width: 2.8919



B1L102S, U15-ch30, adc1

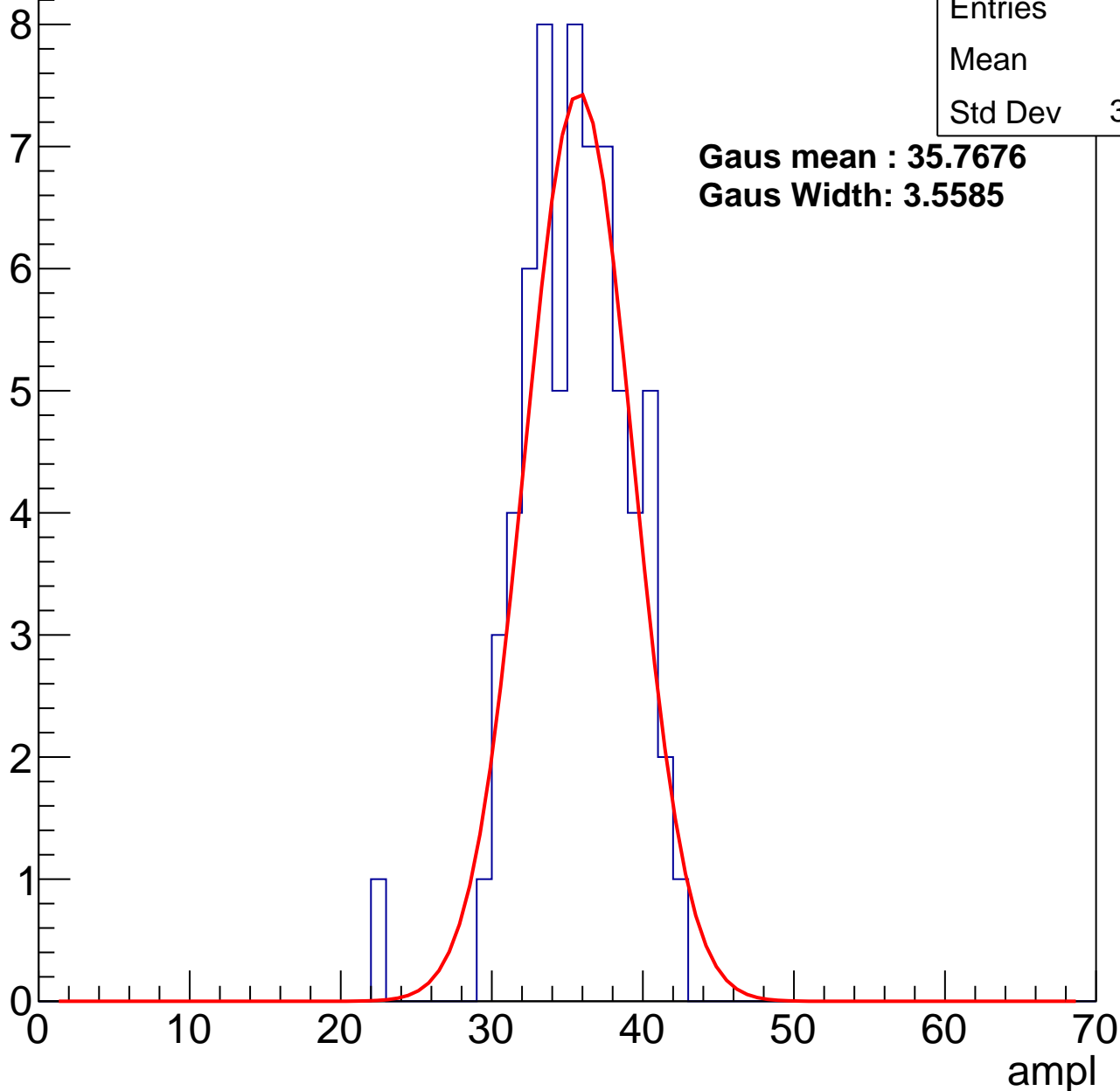
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 35.1 |
| Std Dev | 3.507 |

Gaus mean : 35.7676

Gaus Width: 3.5585



B1L102S, U15-ch30, adc2

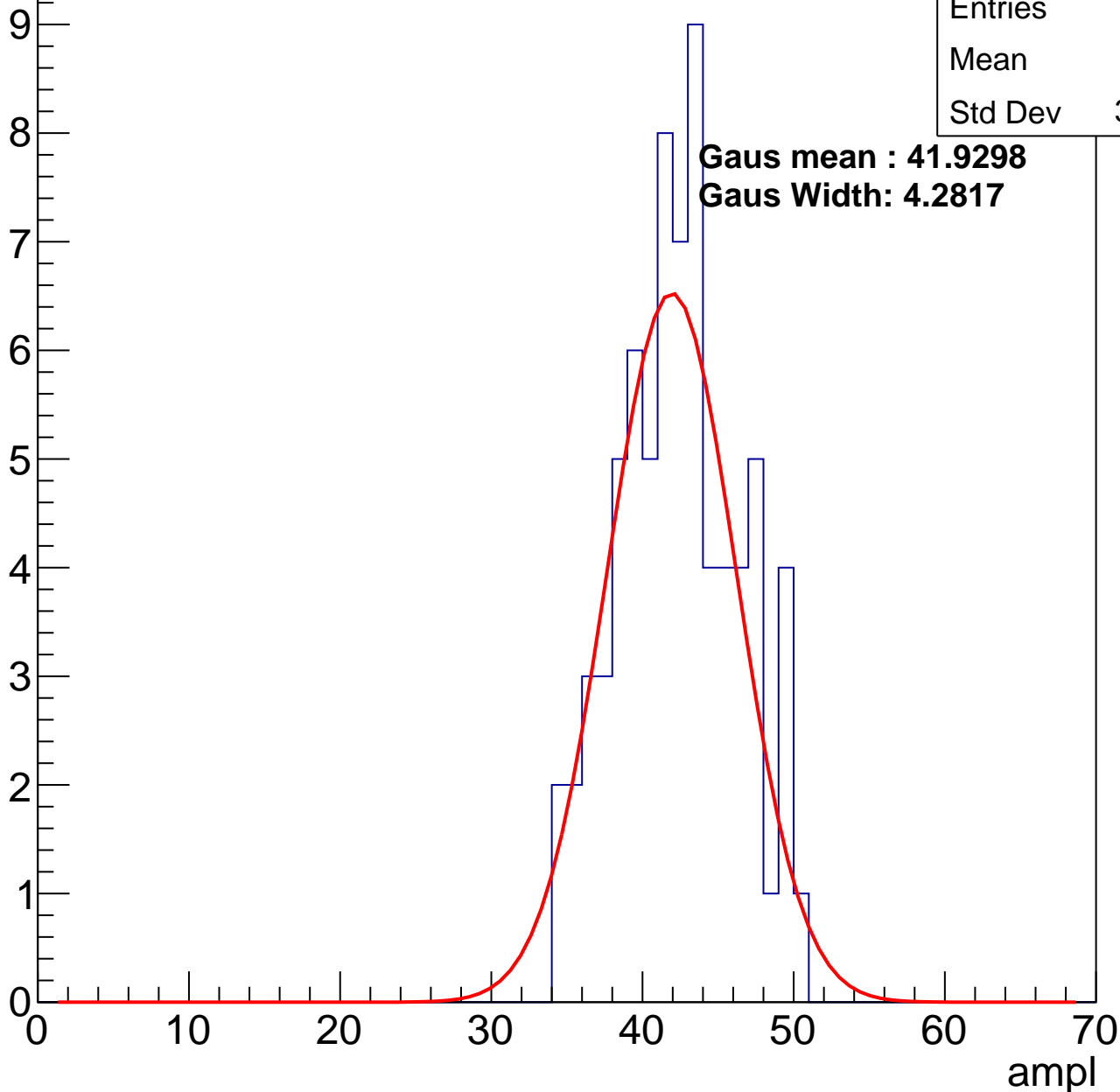
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 41.9 |
| Std Dev | 3.911 |

Gaus mean : 41.9298

Gaus Width: 4.2817

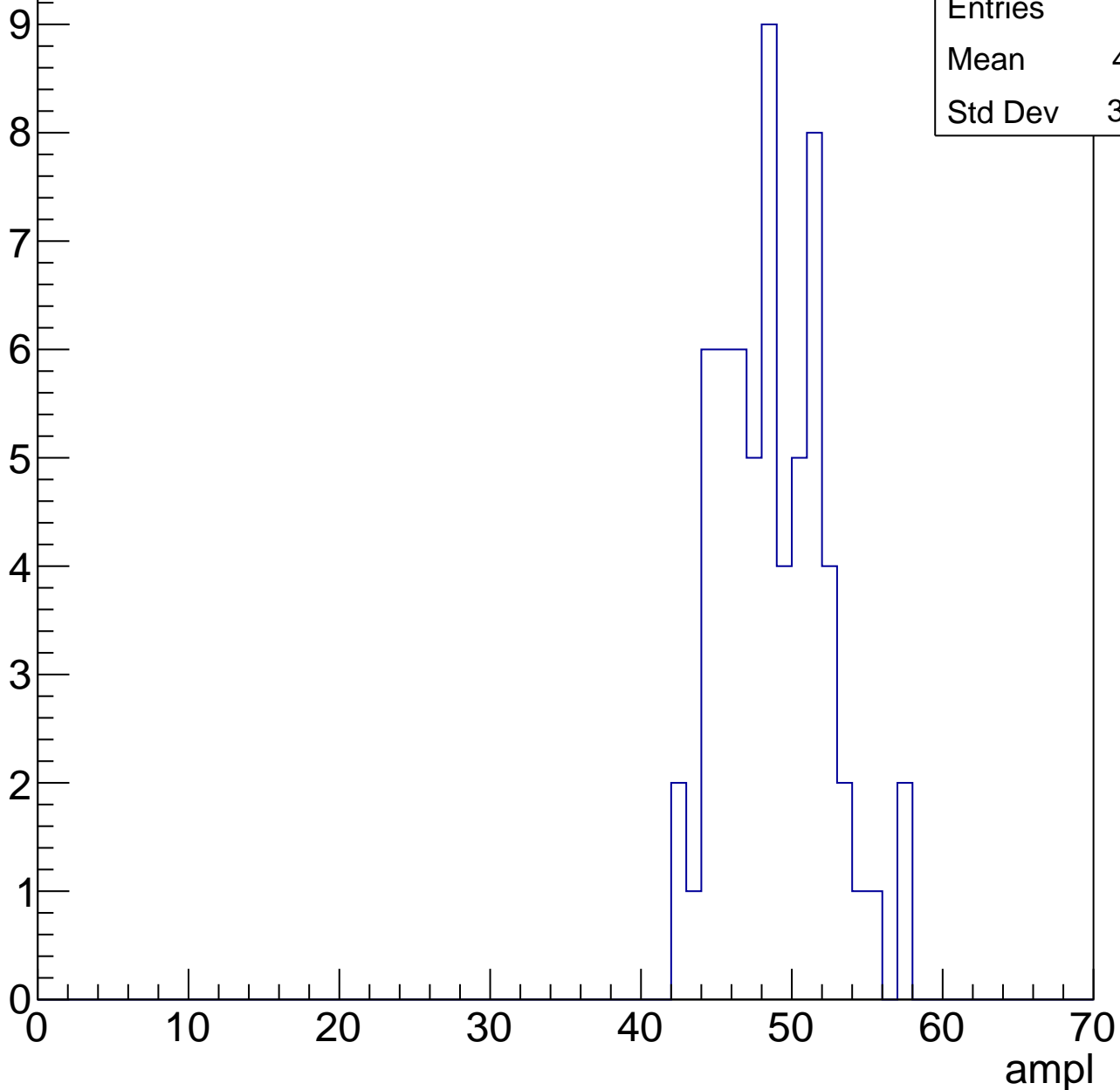


B1L102S, U15-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 48.31 |
| Std Dev | 3.425 |

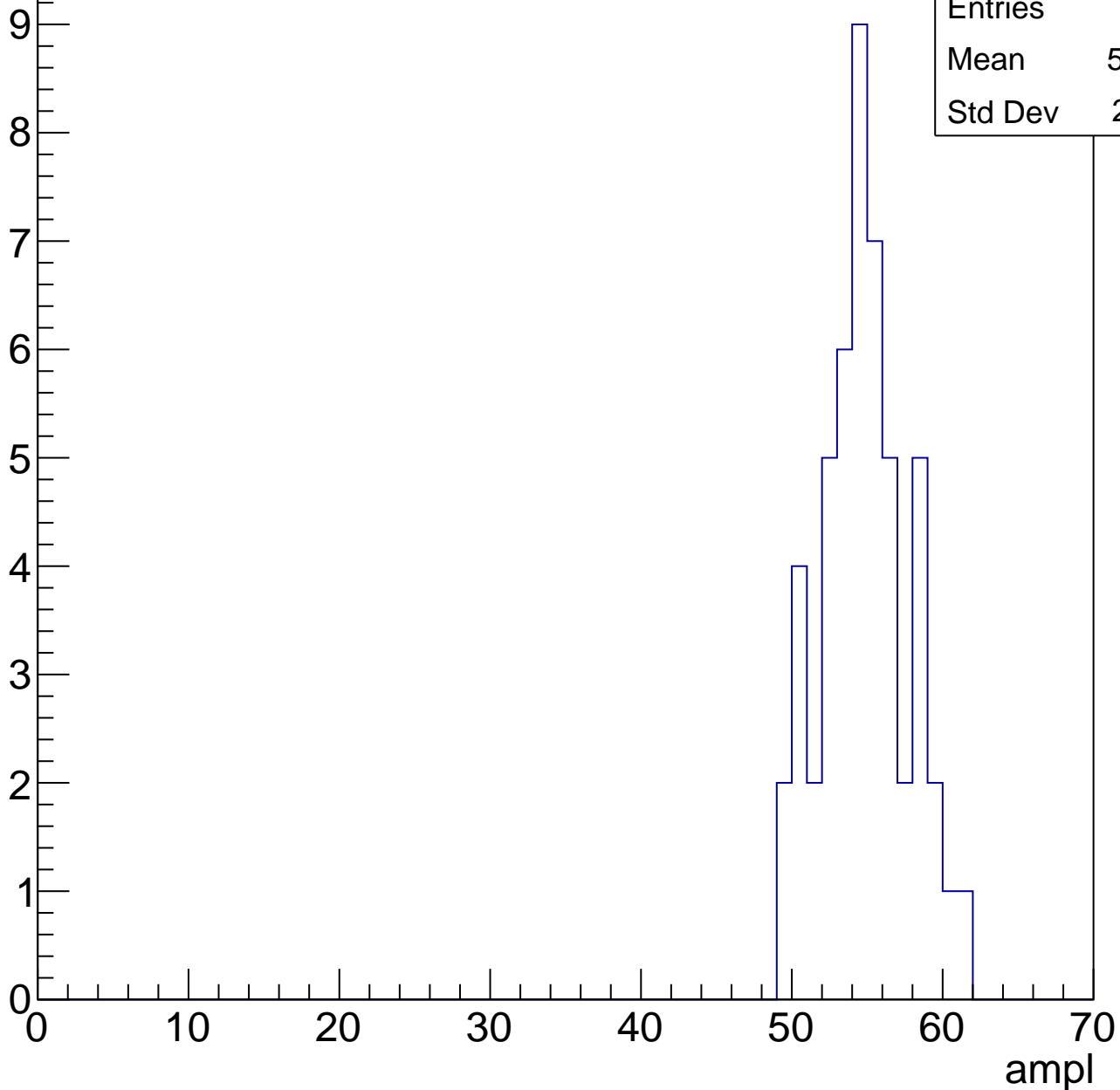


B1L102S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.35 |
| Std Dev | 2.841 |

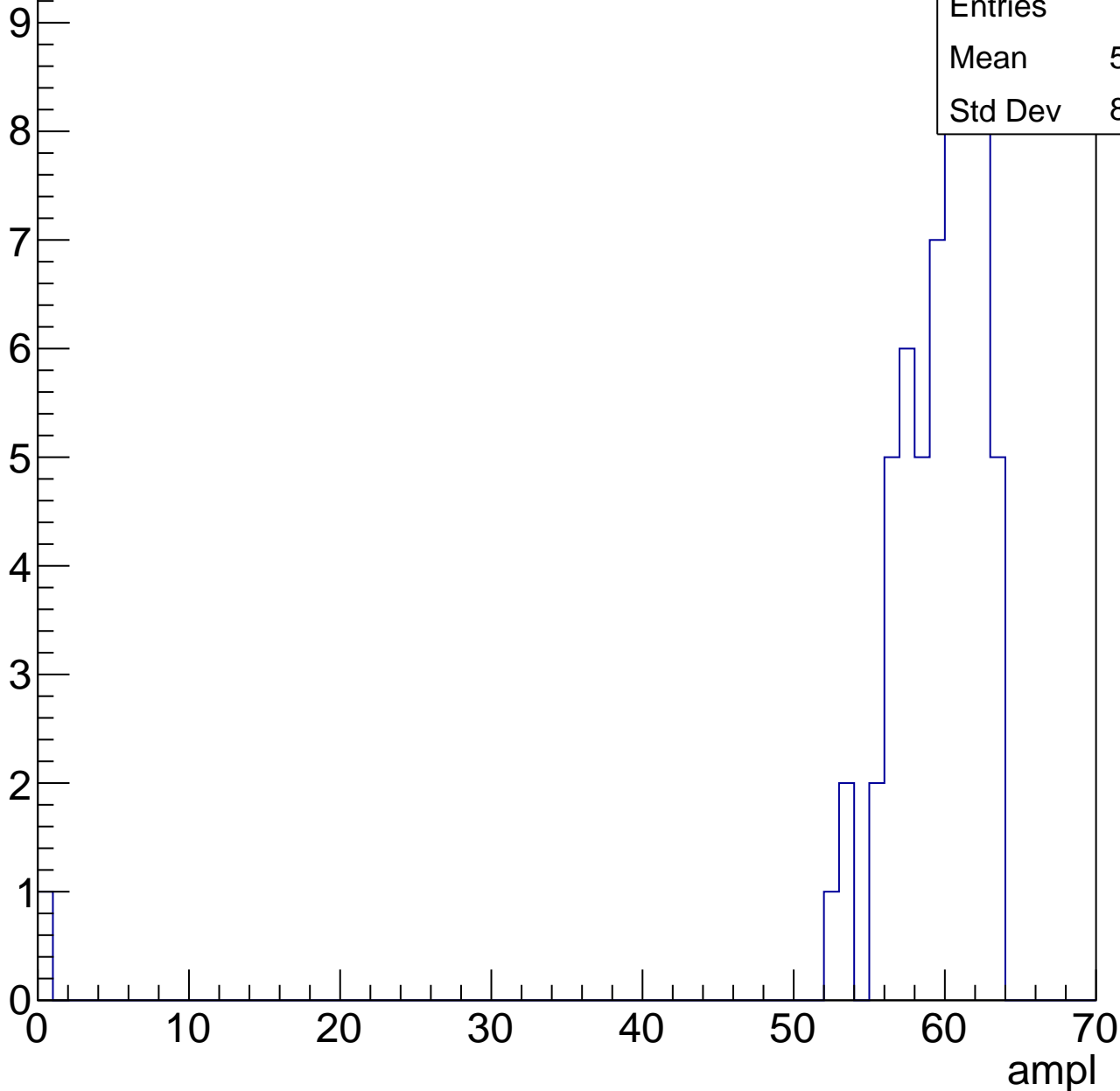


B1L102S, U15-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

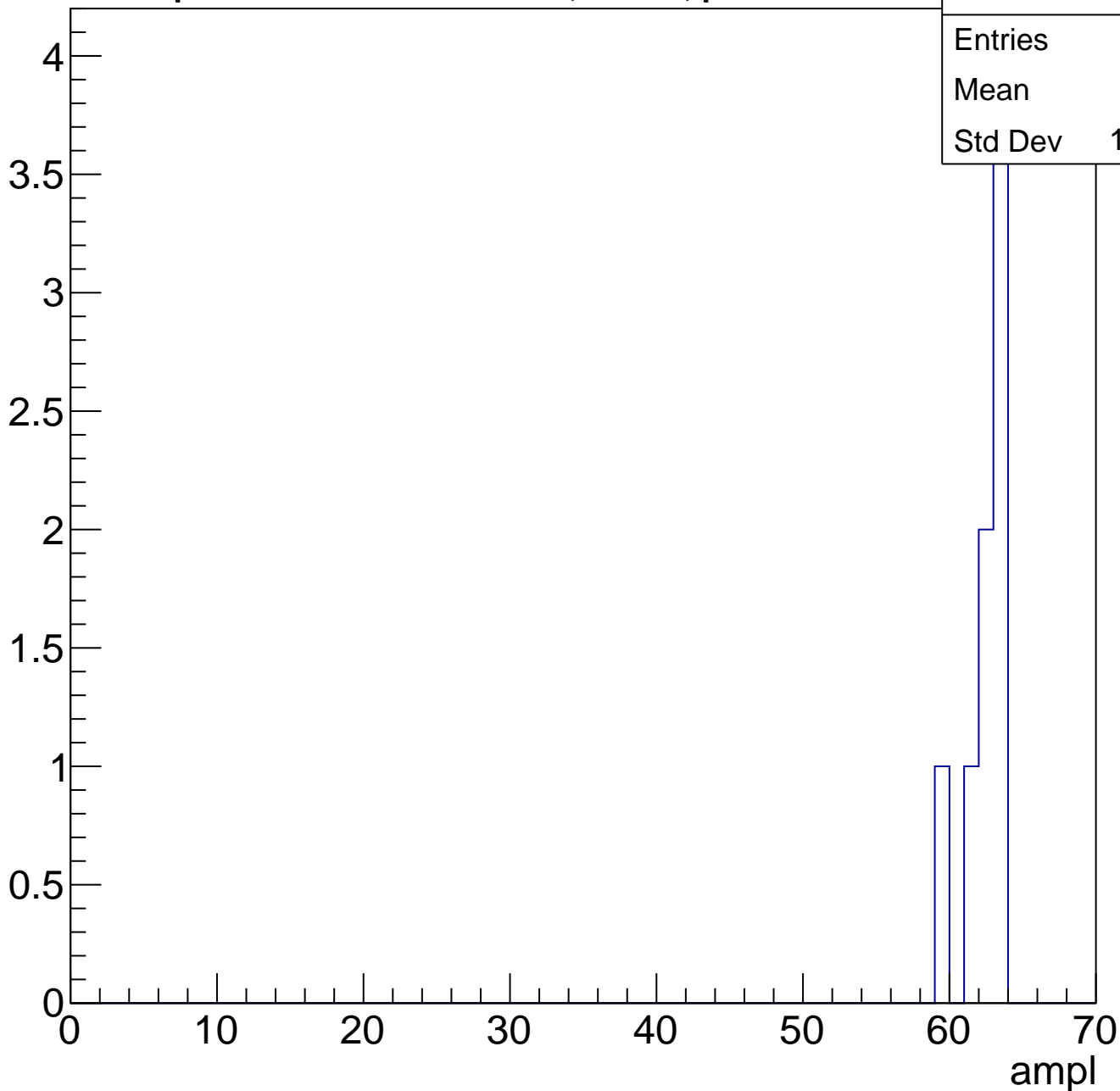
| | |
|---------|-------|
| Entries | 59 |
| Mean | 58.17 |
| Std Dev | 8.089 |



B1L102S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch31, adc0

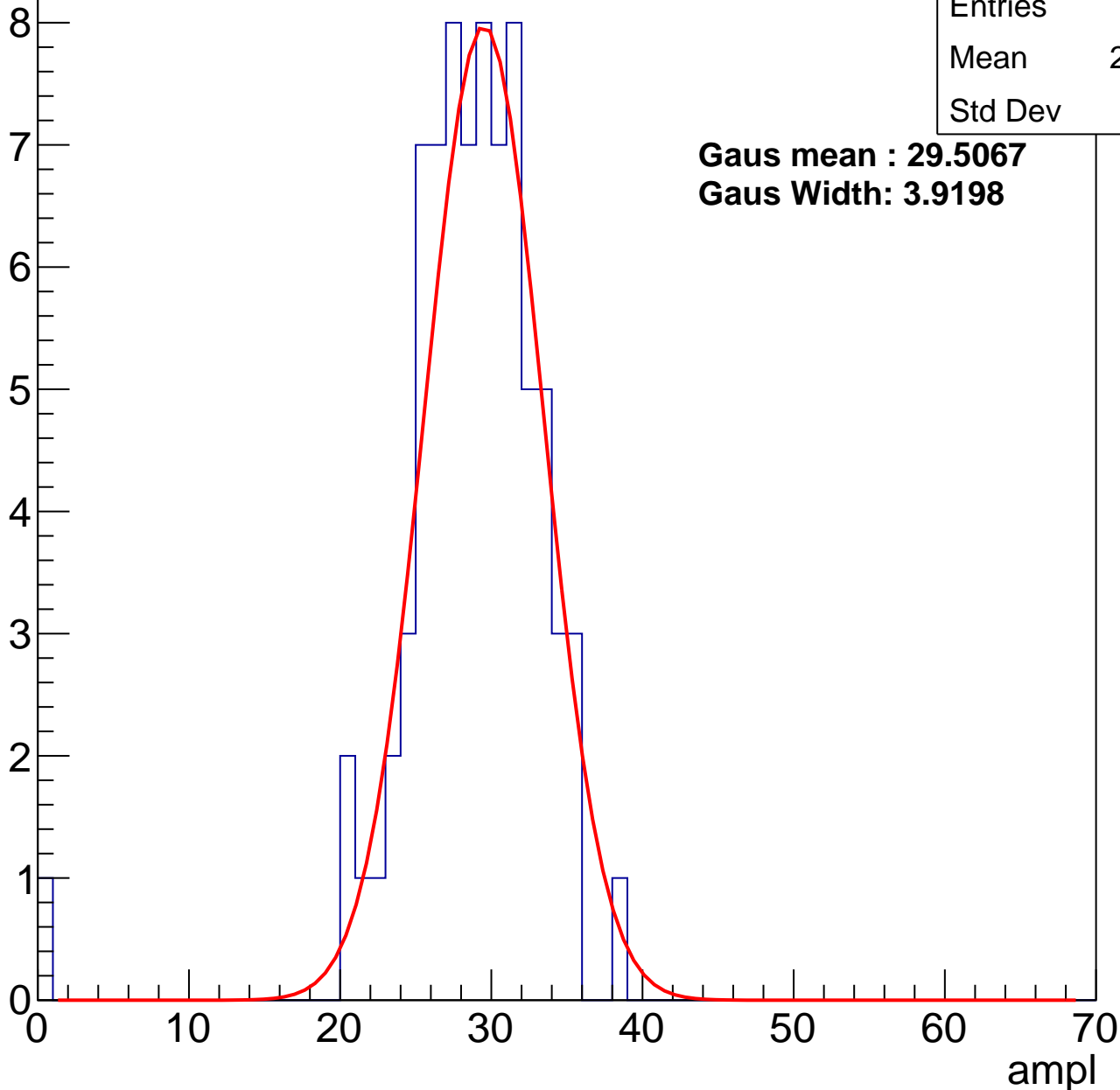
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 28.23 |
| Std Dev | 4.85 |

Gaus mean : 29.5067

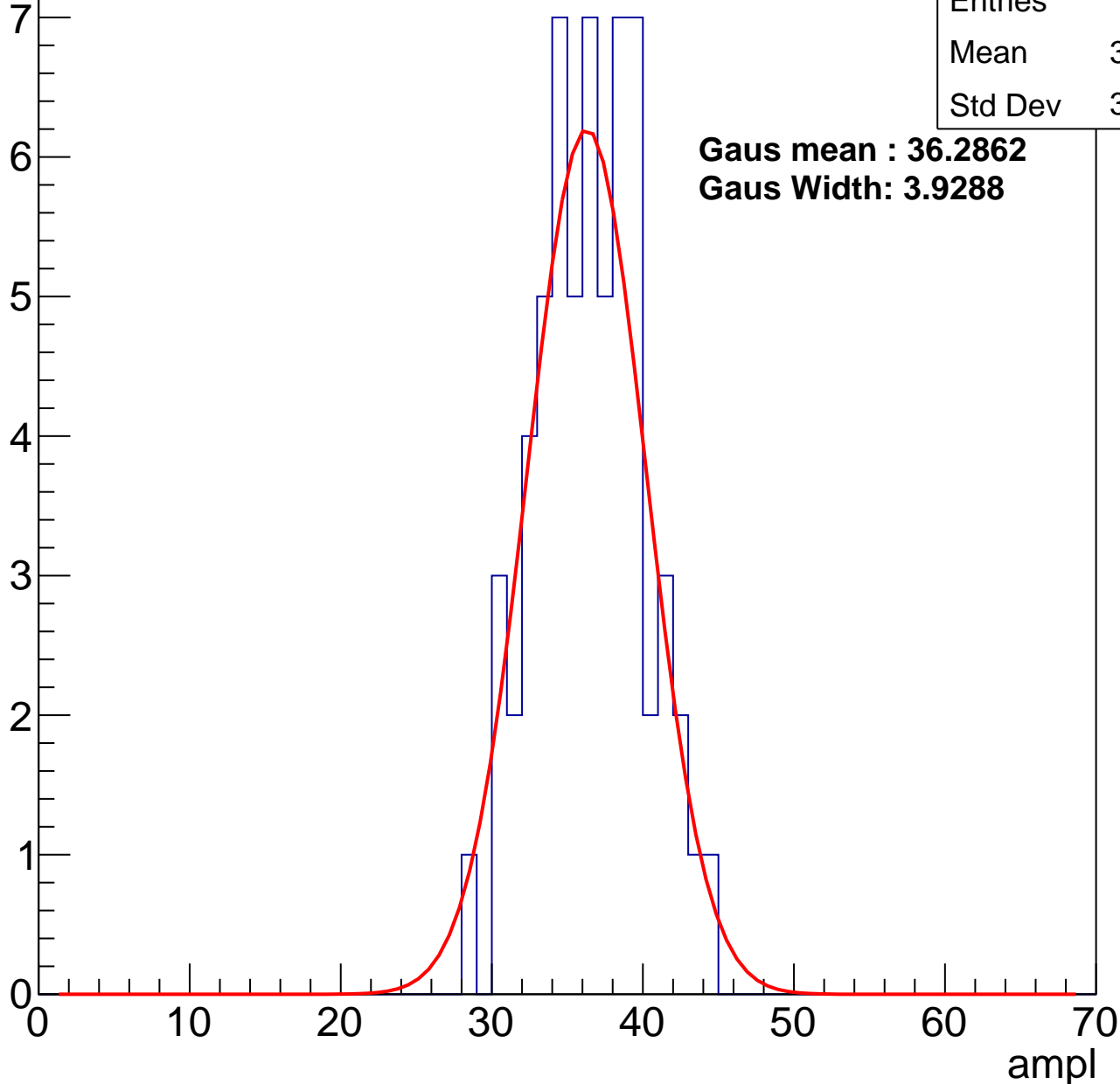
Gaus Width: 3.9198



B1L102S, U15-ch31, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch31, adc2

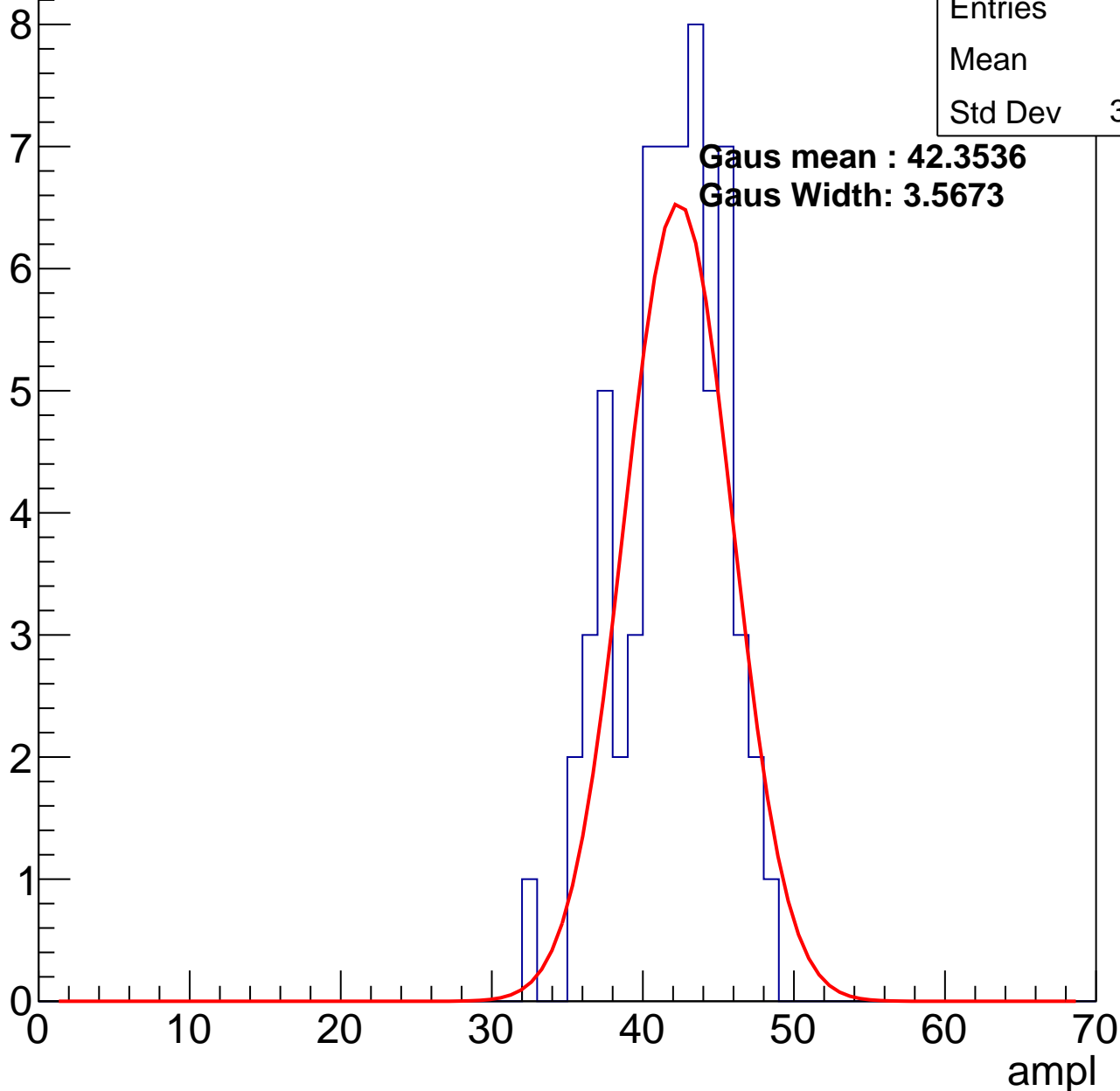
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 41.4 |
| Std Dev | 3.397 |

Gaus mean : 42.3536

Gaus Width: 3.5673

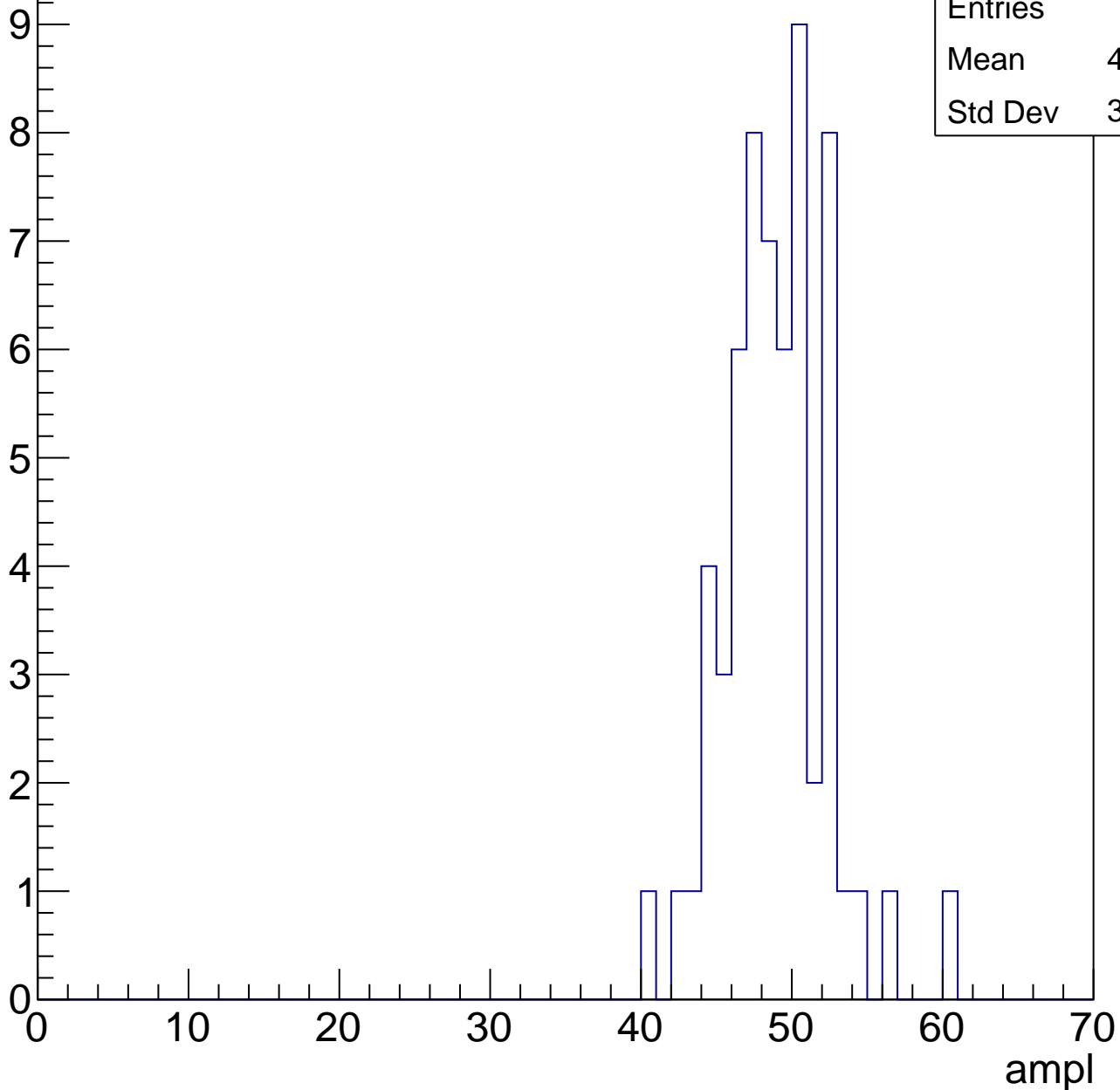


B1L102S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.48 |
| Std Dev | 3.394 |

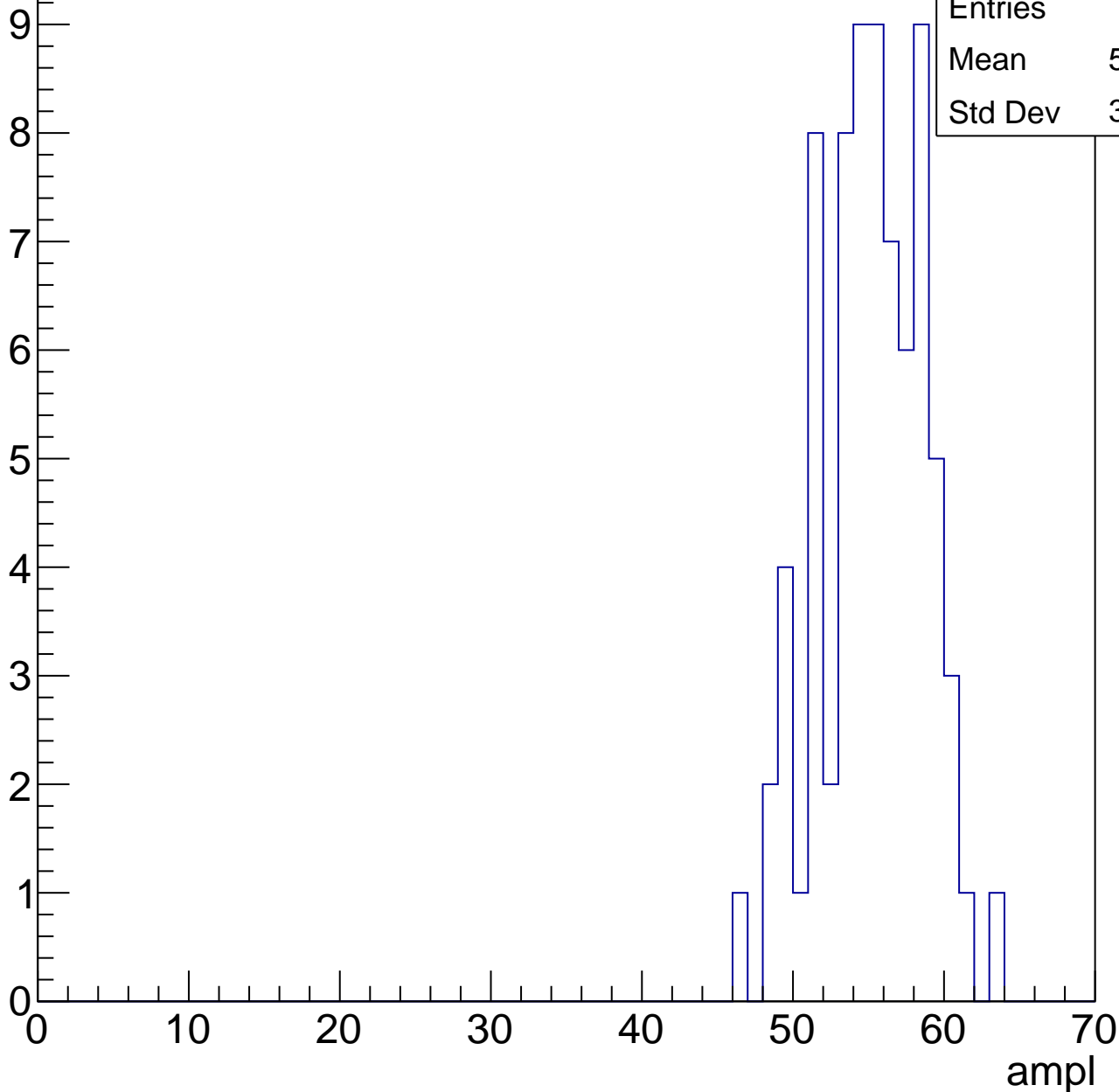


B1L102S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 54.74 |
| Std Dev | 3.435 |

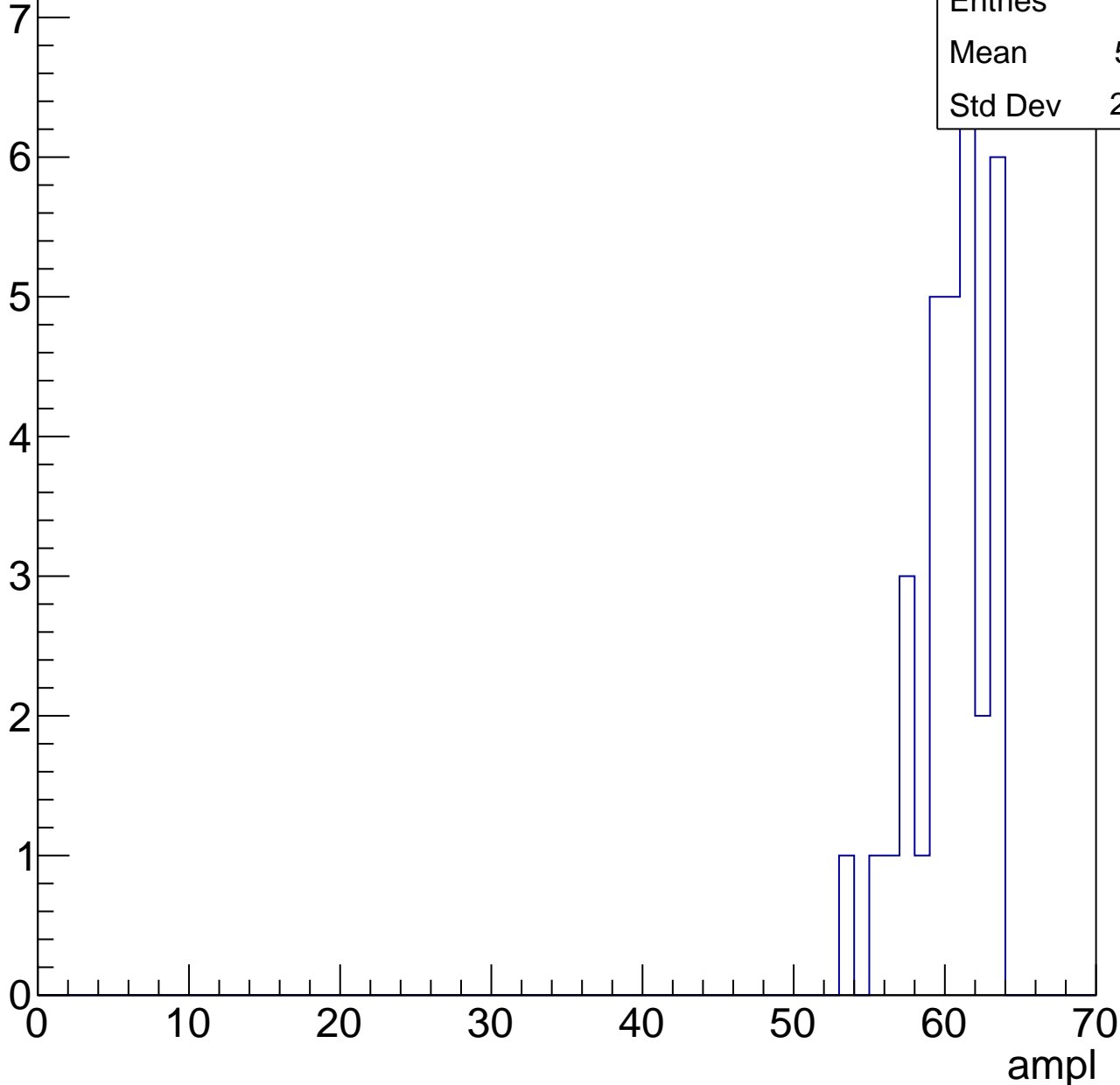


B1L102S, U15-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 32 |
| Mean | 59.91 |
| Std Dev | 2.467 |

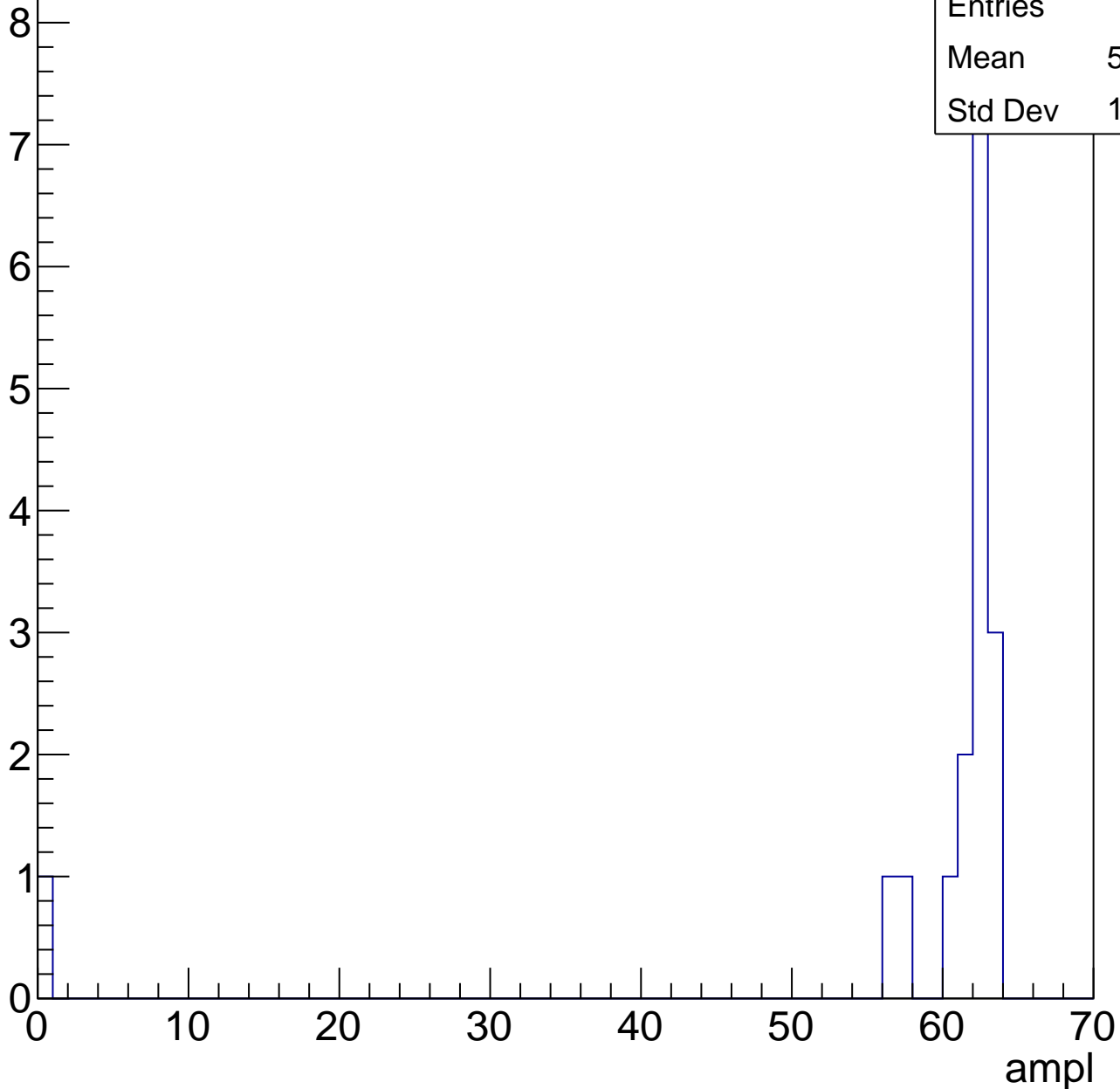


B1L102S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 57.65 |
| Std Dev | 14.54 |



B1L102S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch32, adc0

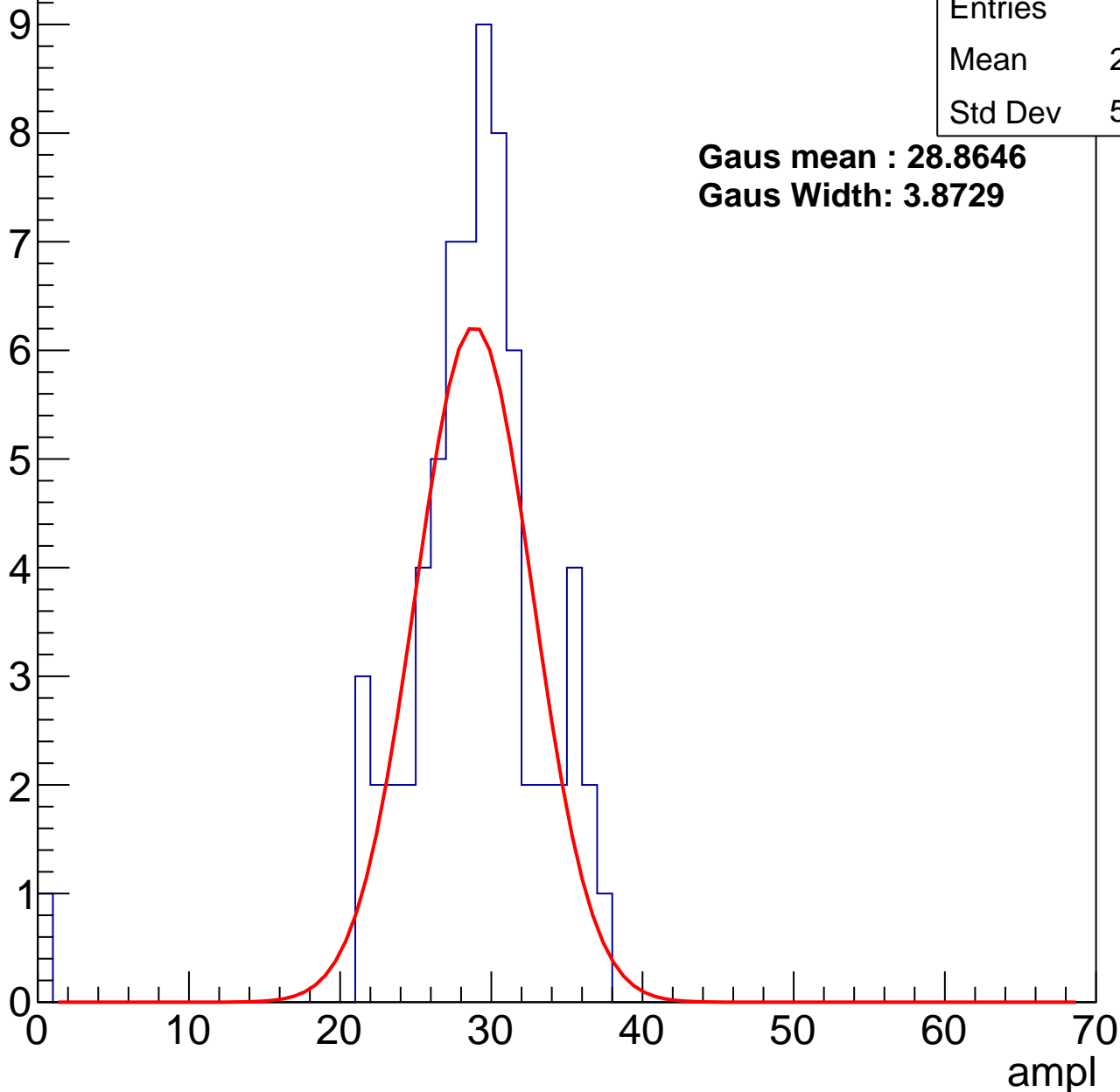
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.26 |
| Std Dev | 5.104 |

Gaus mean : 28.8646

Gaus Width: 3.8729



B1L102S, U15-ch32, adc1

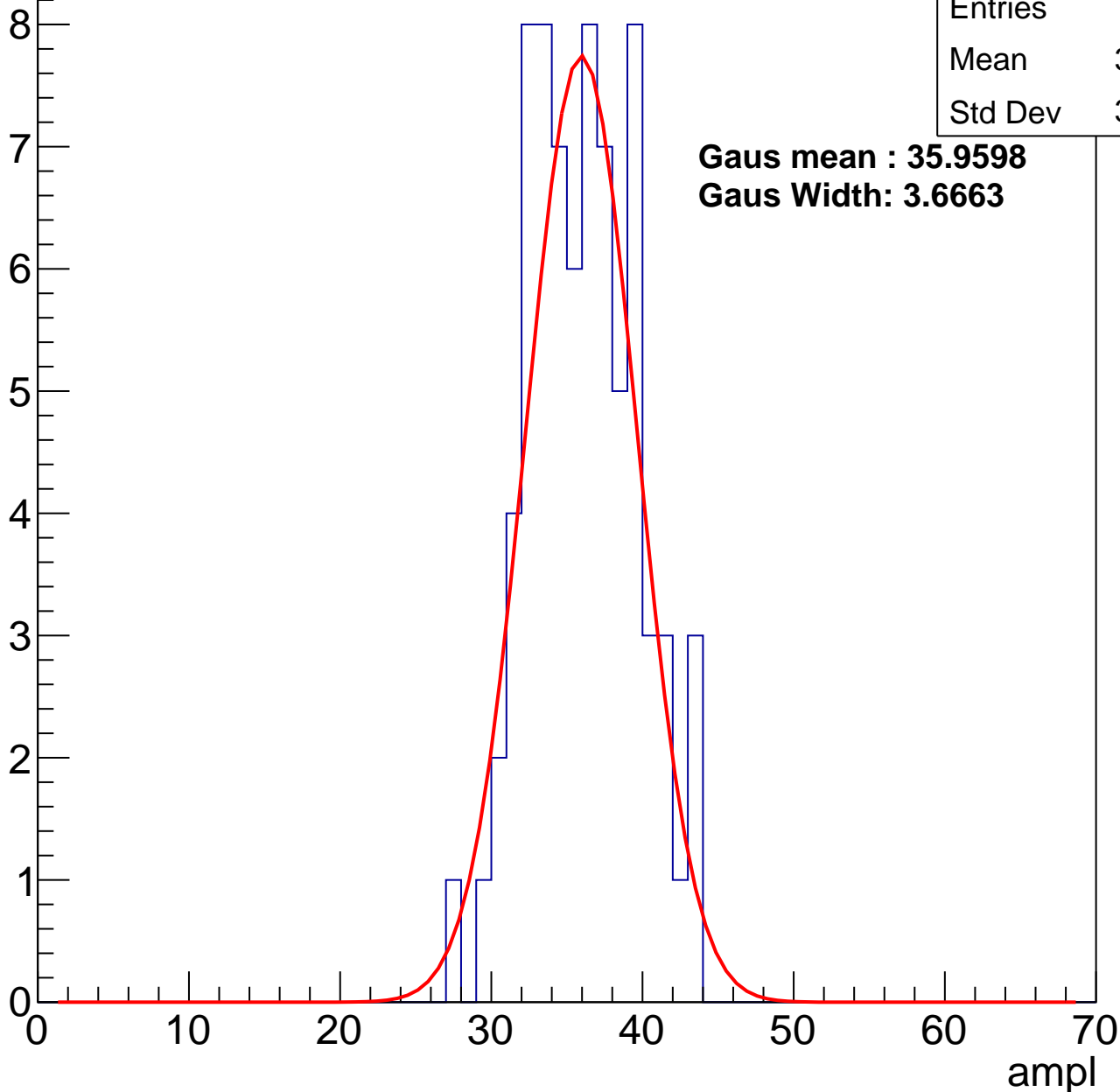
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 35.61 |
| Std Dev | 3.521 |

Gaus mean : 35.9598

Gaus Width: 3.6663



B1L102S, U15-ch32, adc2

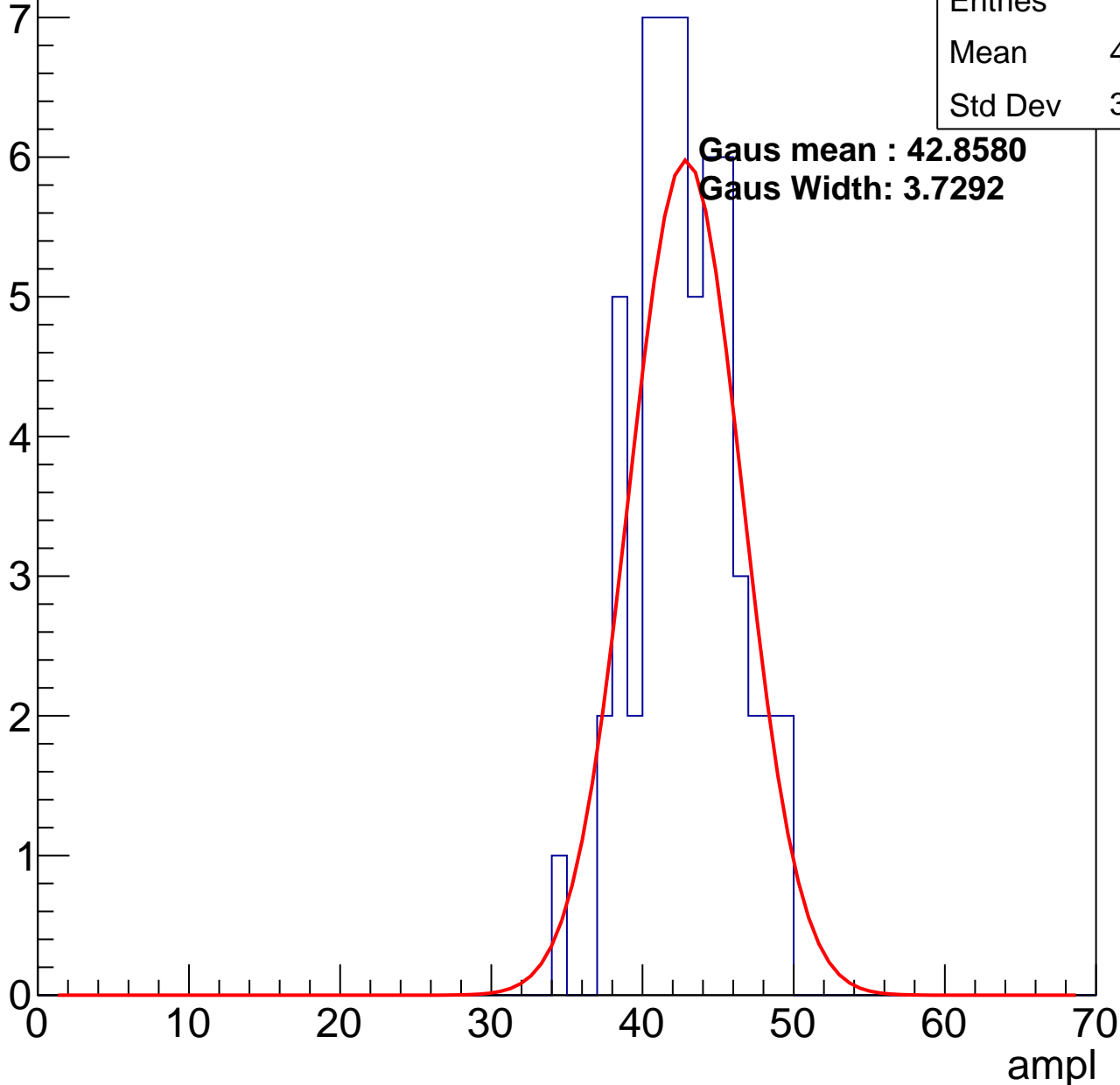
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 42.32 |
| Std Dev | 3.224 |

Gaus mean : 42.8580

Gaus Width: 3.7292

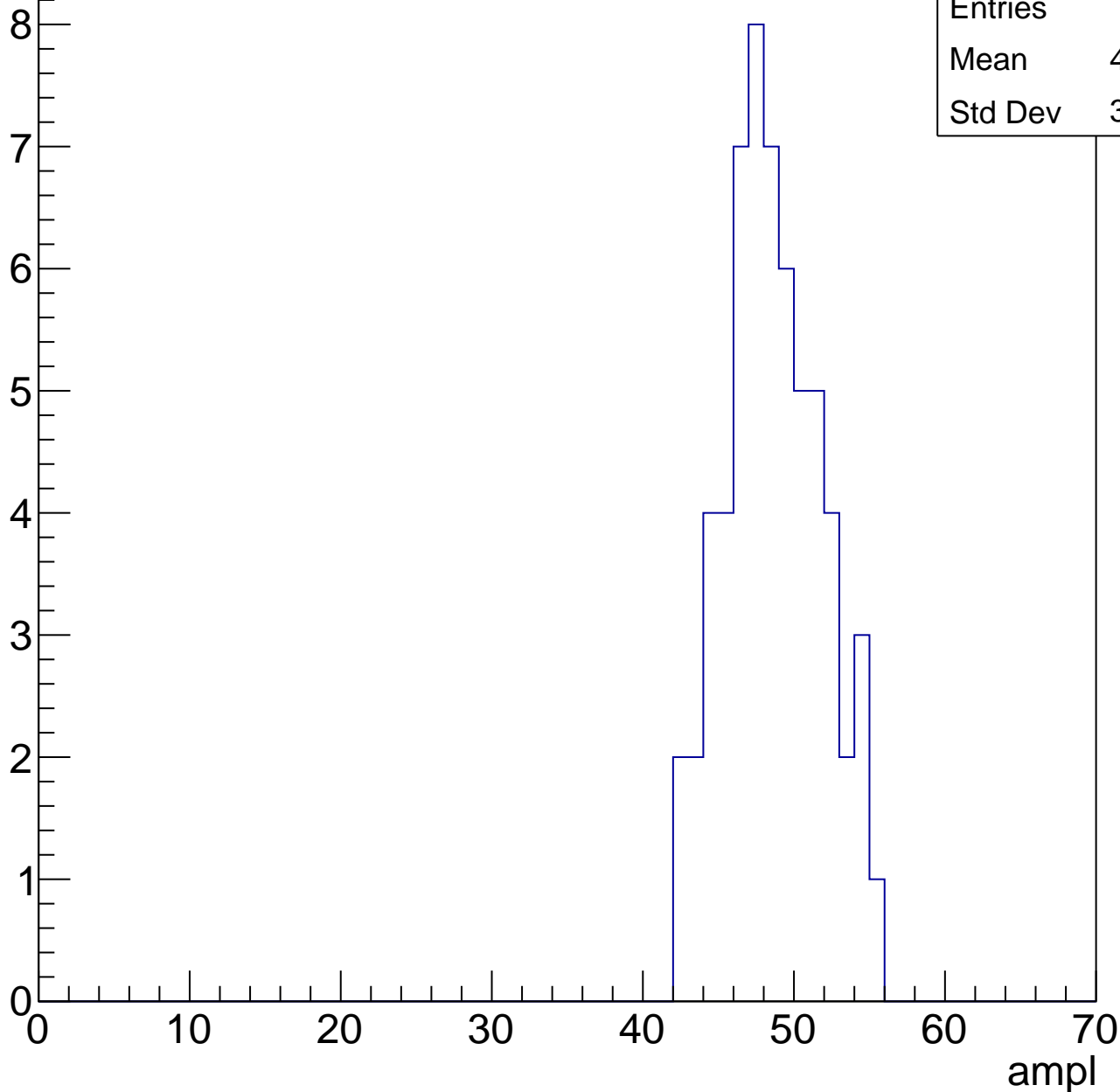


B1L102S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.17 |
| Std Dev | 3.158 |

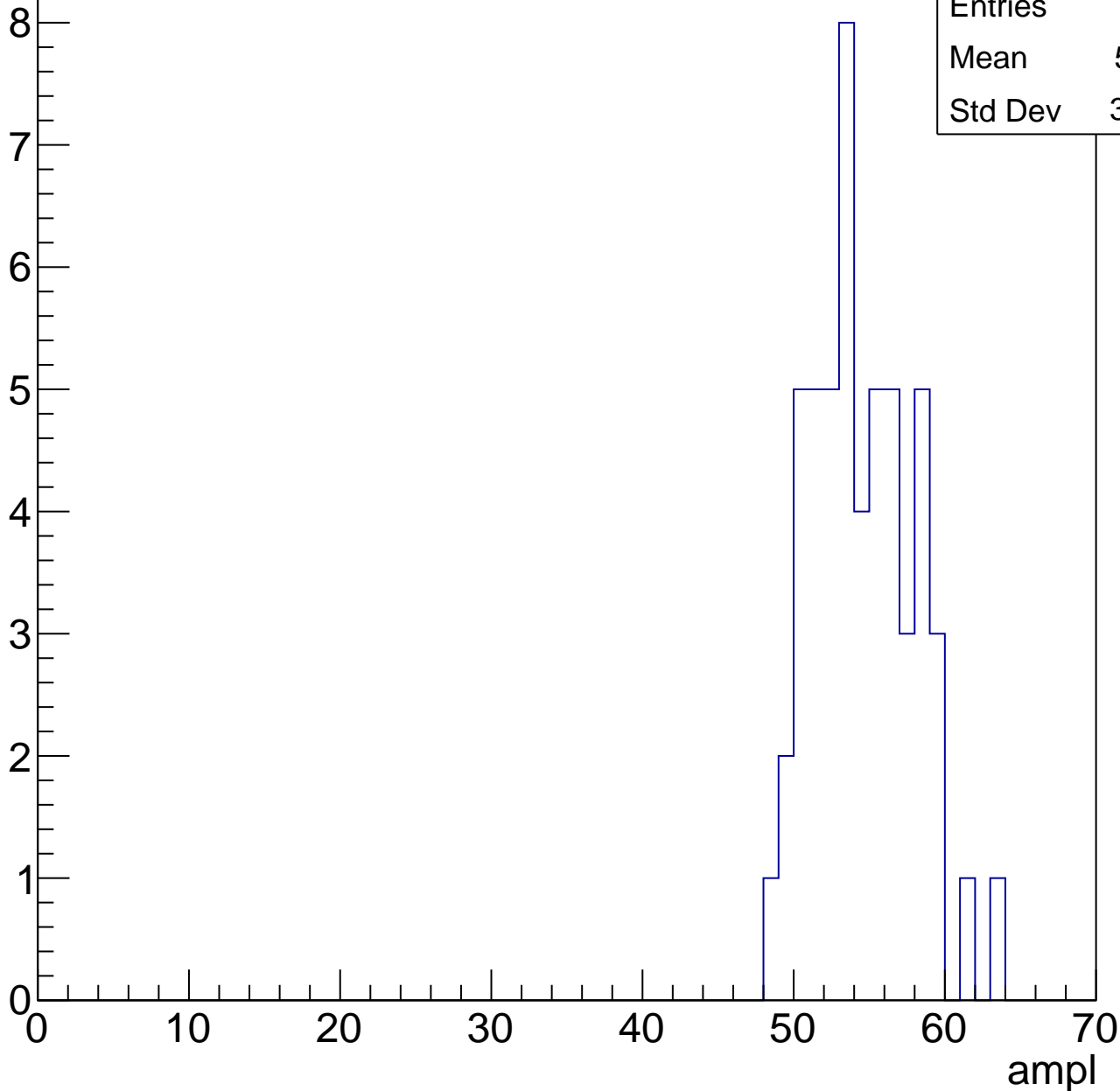


B1L102S, U15-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 54.11 |
| Std Dev | 3.295 |



B1L102S, U15-ch32, adc5

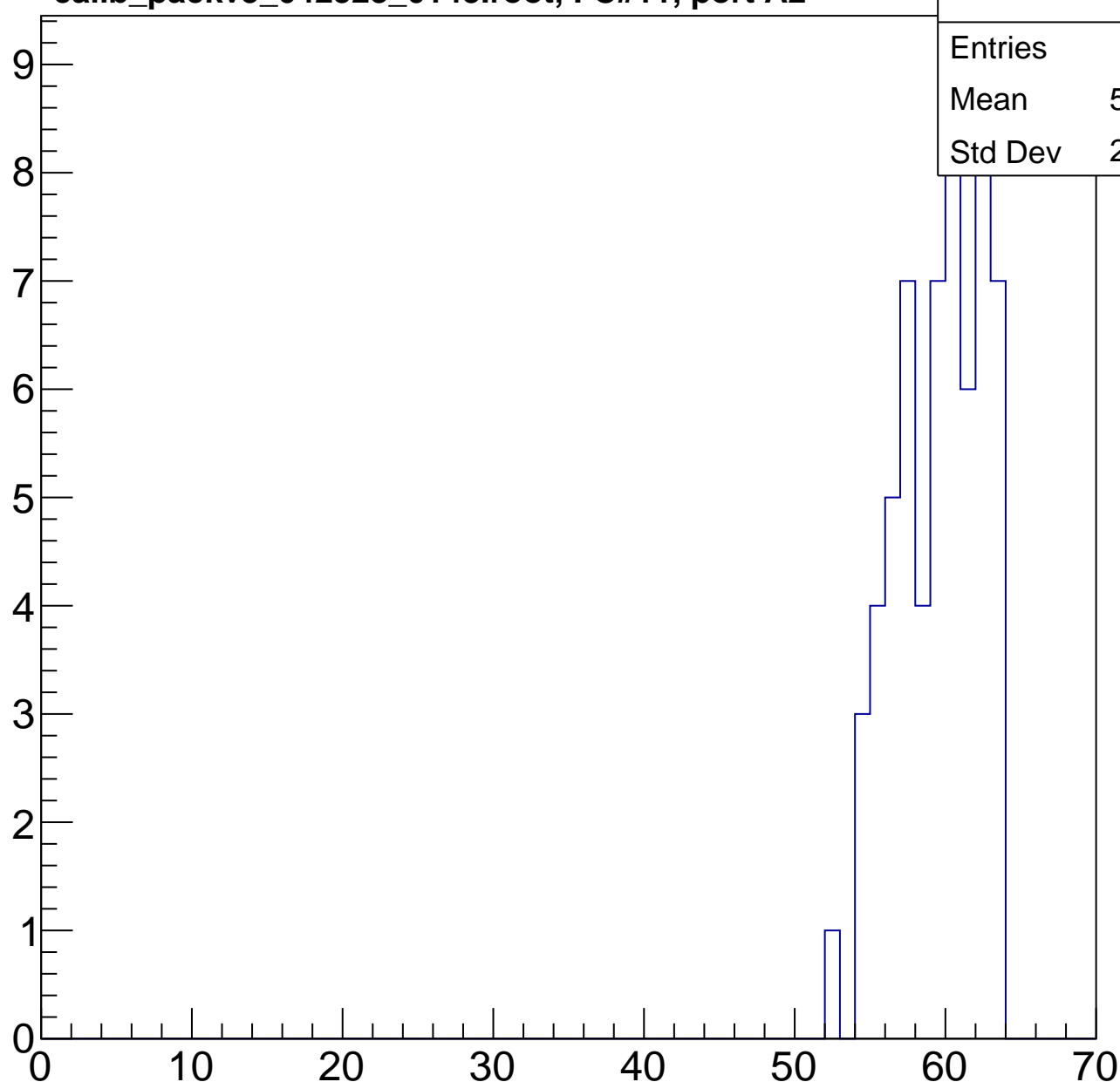
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 61 |
| Mean | 59.07 |
| Std Dev | 2.833 |

ampl



B1L102S, U15-ch32, adc6

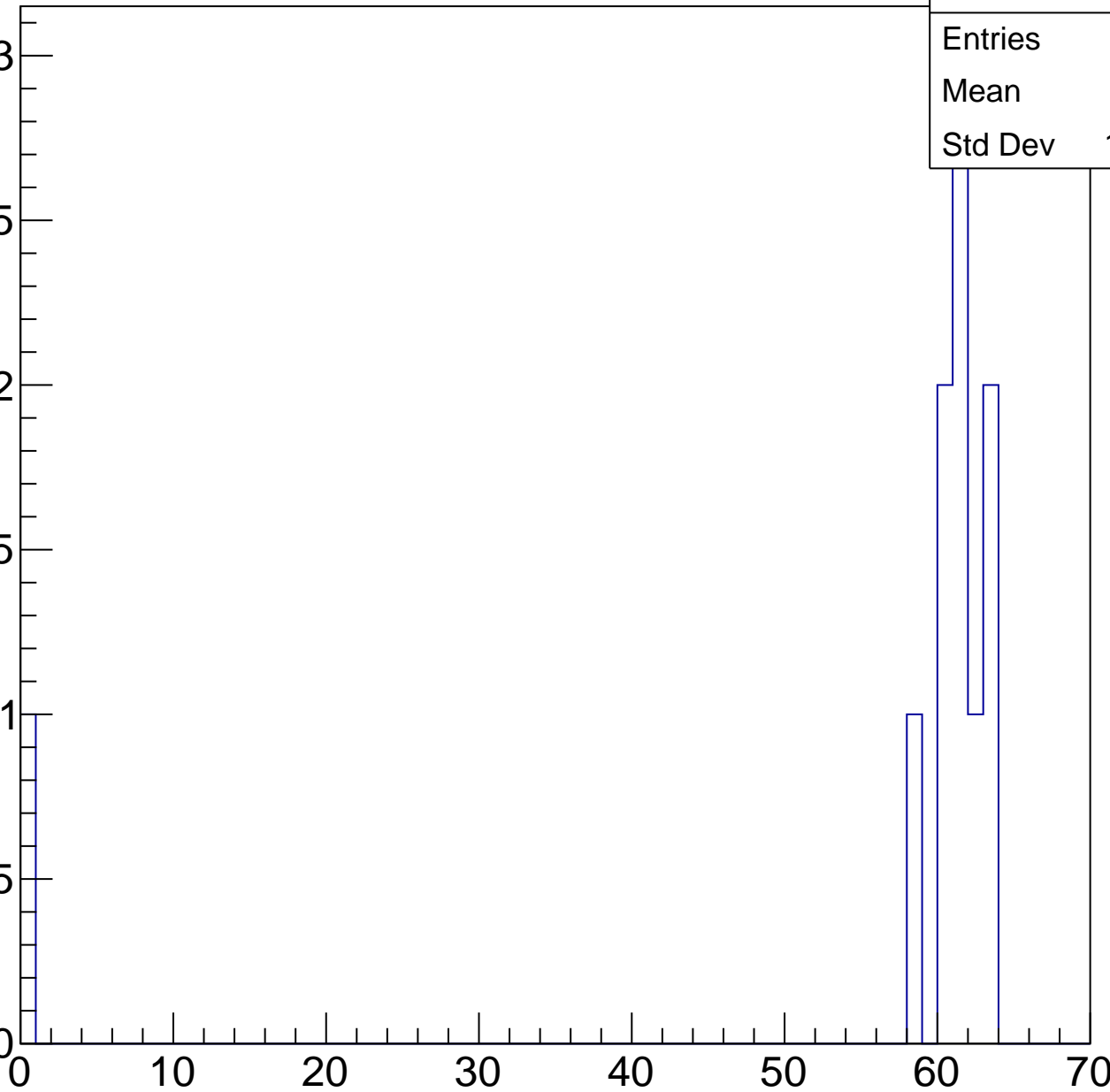
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 10 |
| Mean | 54.9 |
| Std Dev | 18.35 |

ampl



B1L102S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch33, adc0

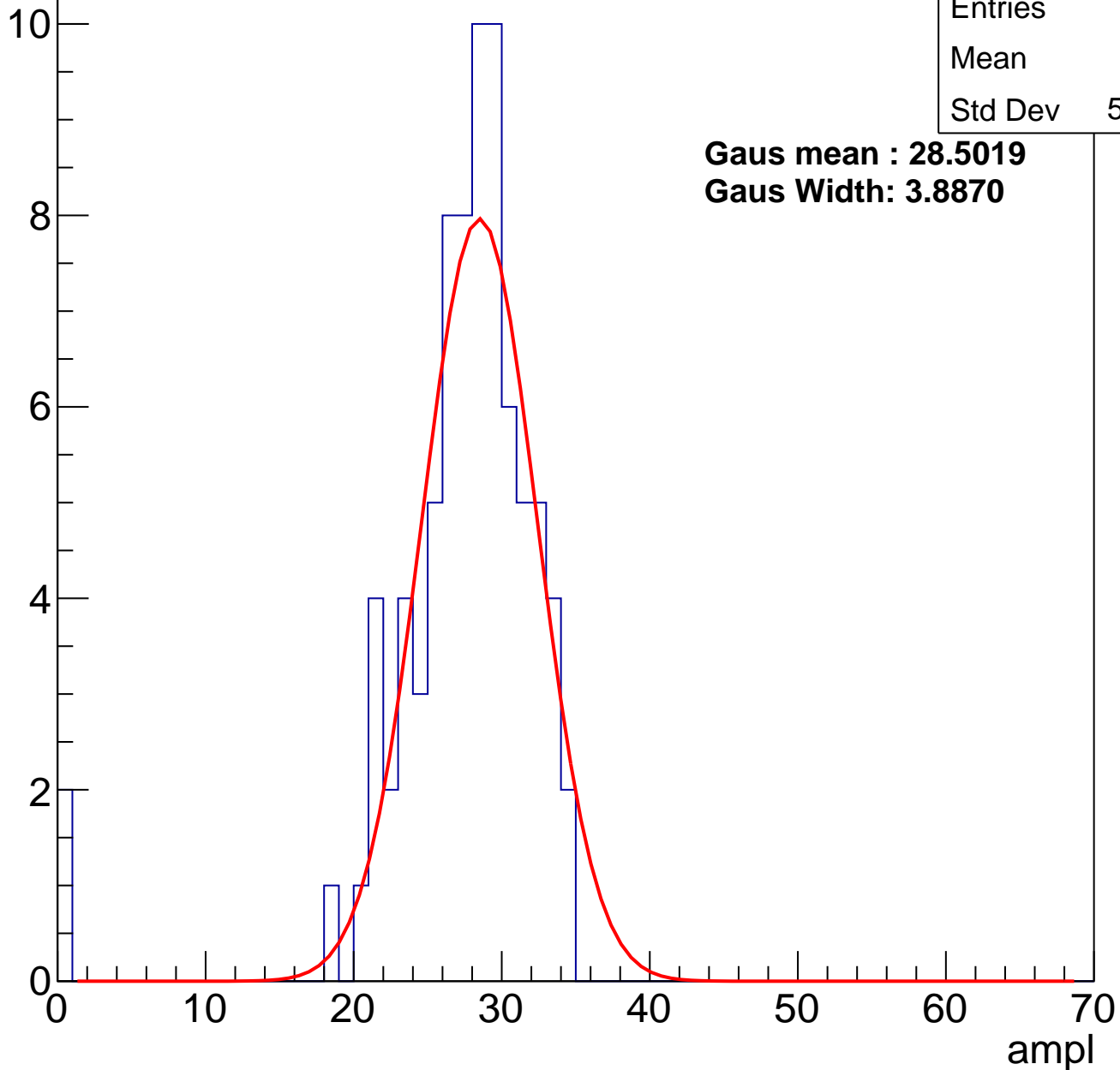
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 26.8 |
| Std Dev | 5.533 |

Gaus mean : 28.5019

Gaus Width: 3.8870

Entry



B1L102S, U15-ch33, adc1

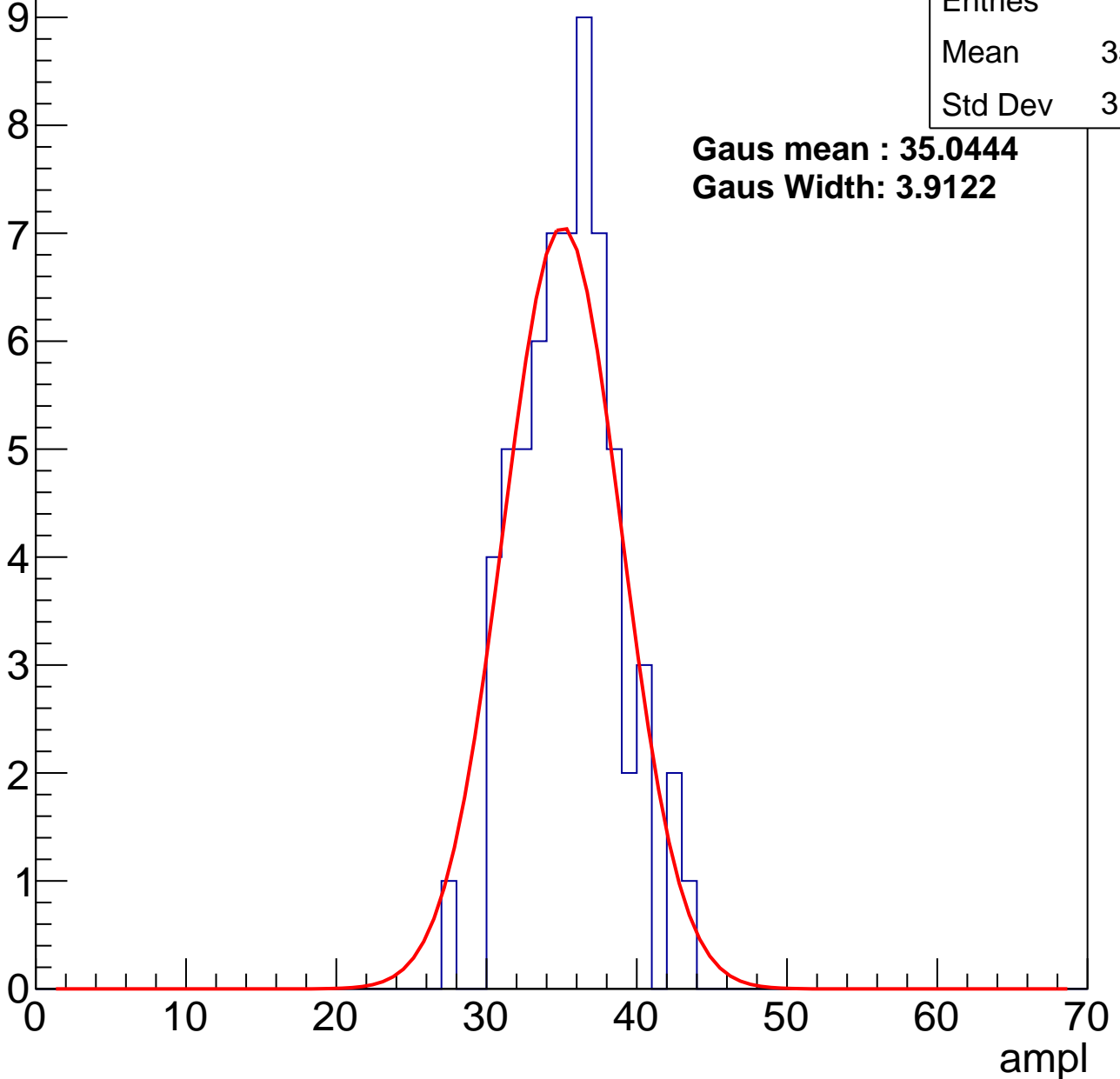
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.02 |
| Std Dev | 3.238 |

Gaus mean : 35.0444

Gaus Width: 3.9122



B1L102S, U15-ch33, adc2

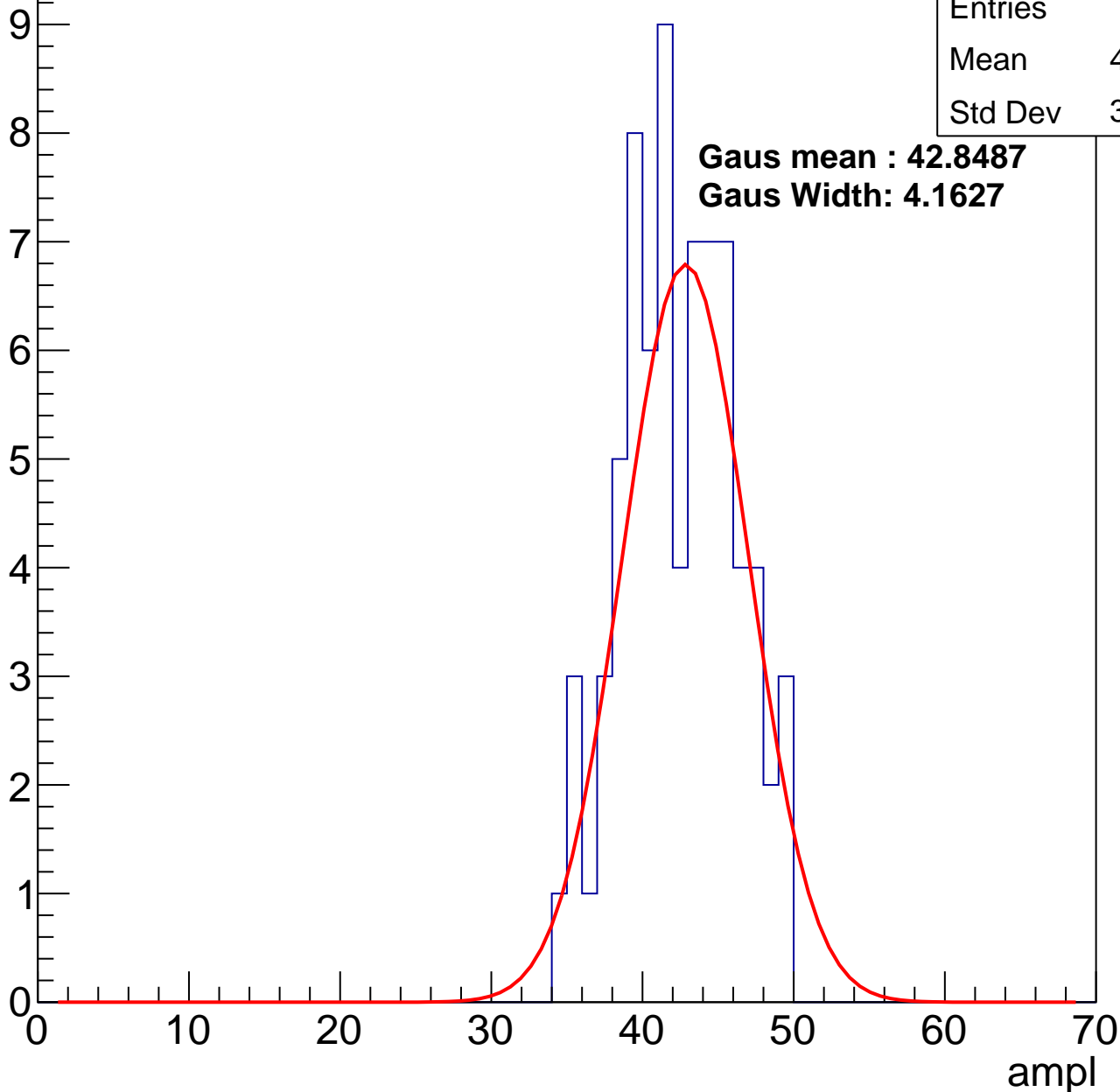
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 41.95 |
| Std Dev | 3.654 |

Gaus mean : 42.8487

Gaus Width: 4.1627

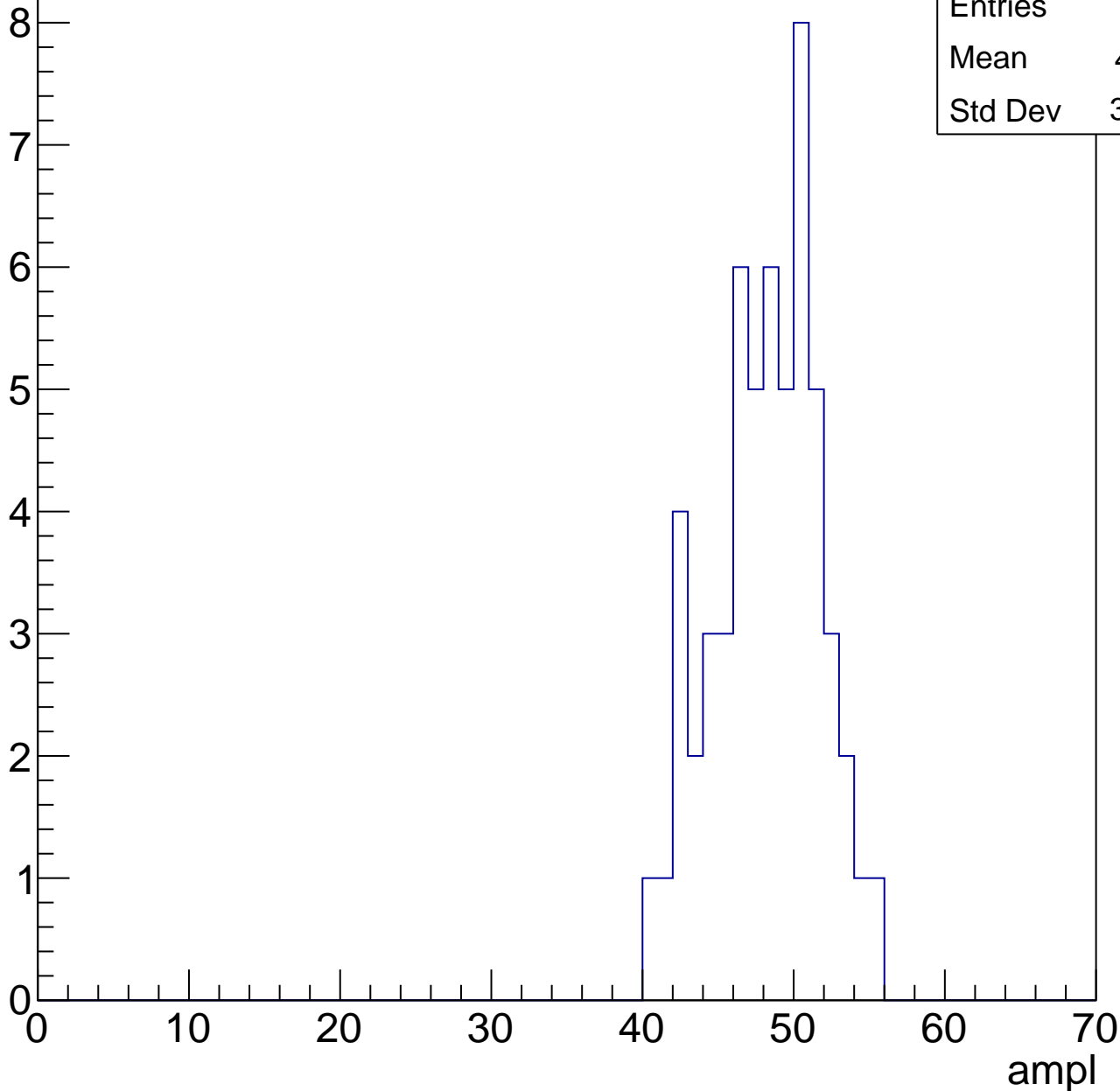


B1L102S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 47.71 |
| Std Dev | 3.463 |

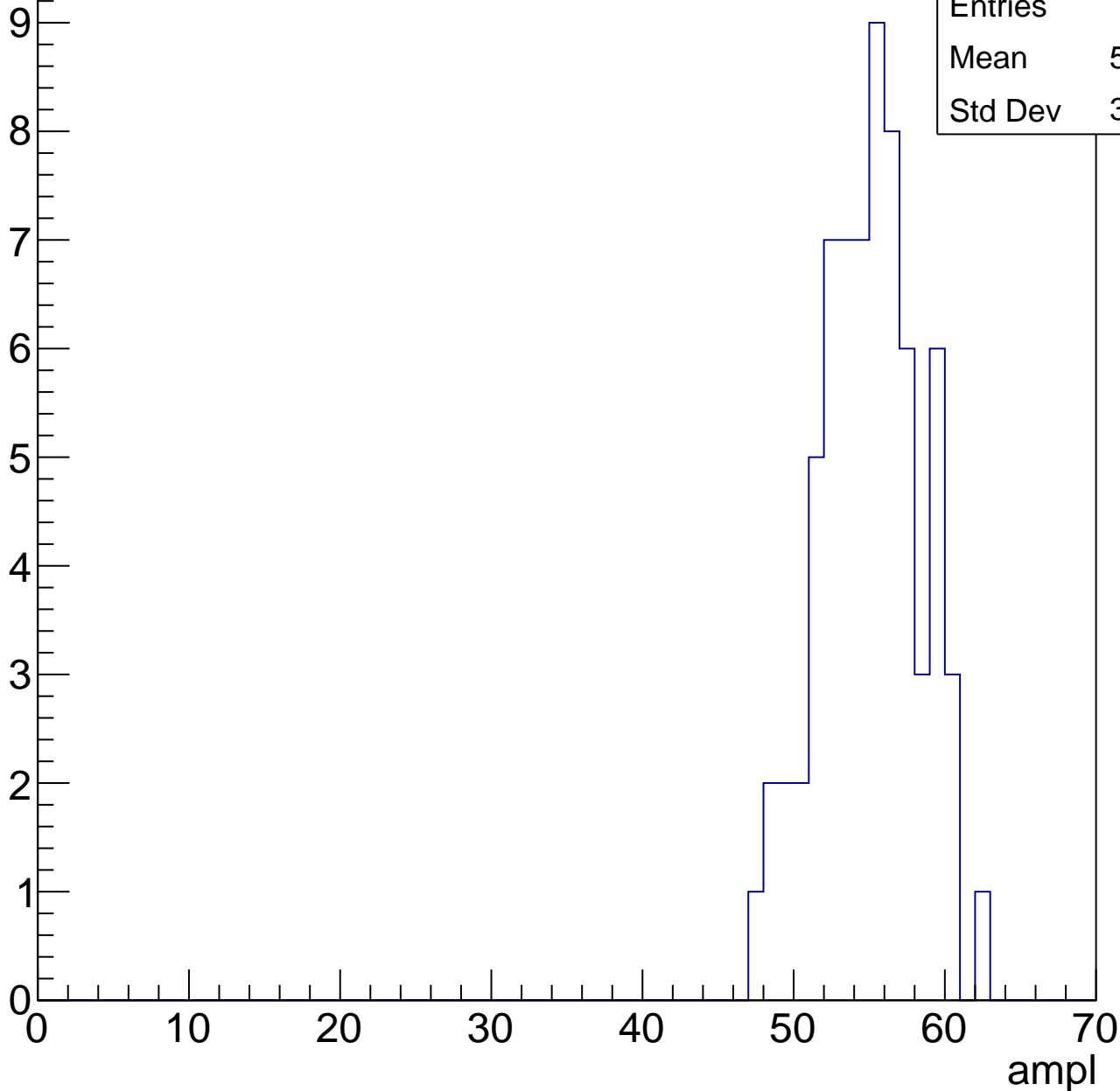


B1L102S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 54.55 |
| Std Dev | 3.246 |

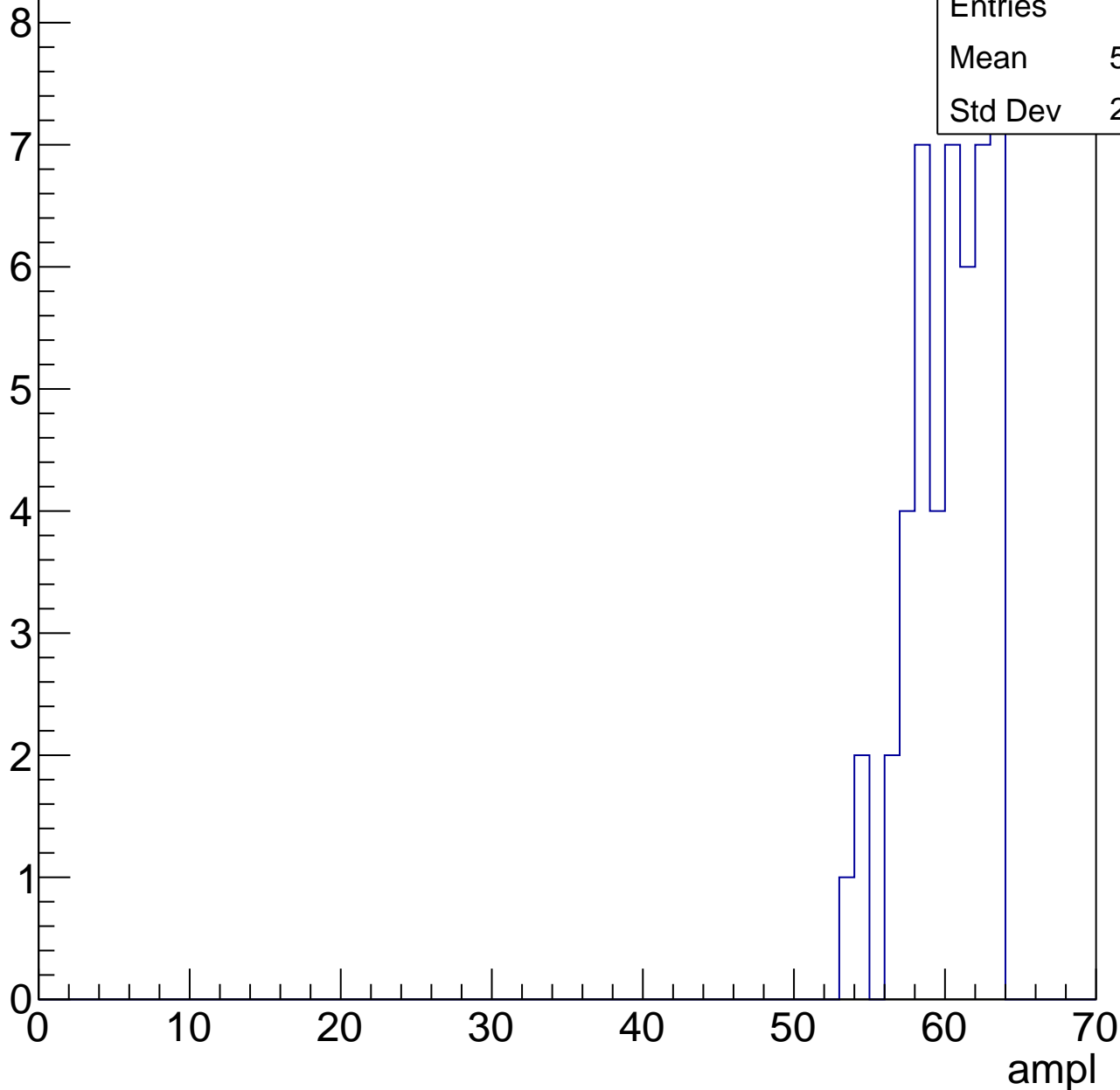


B1L102S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

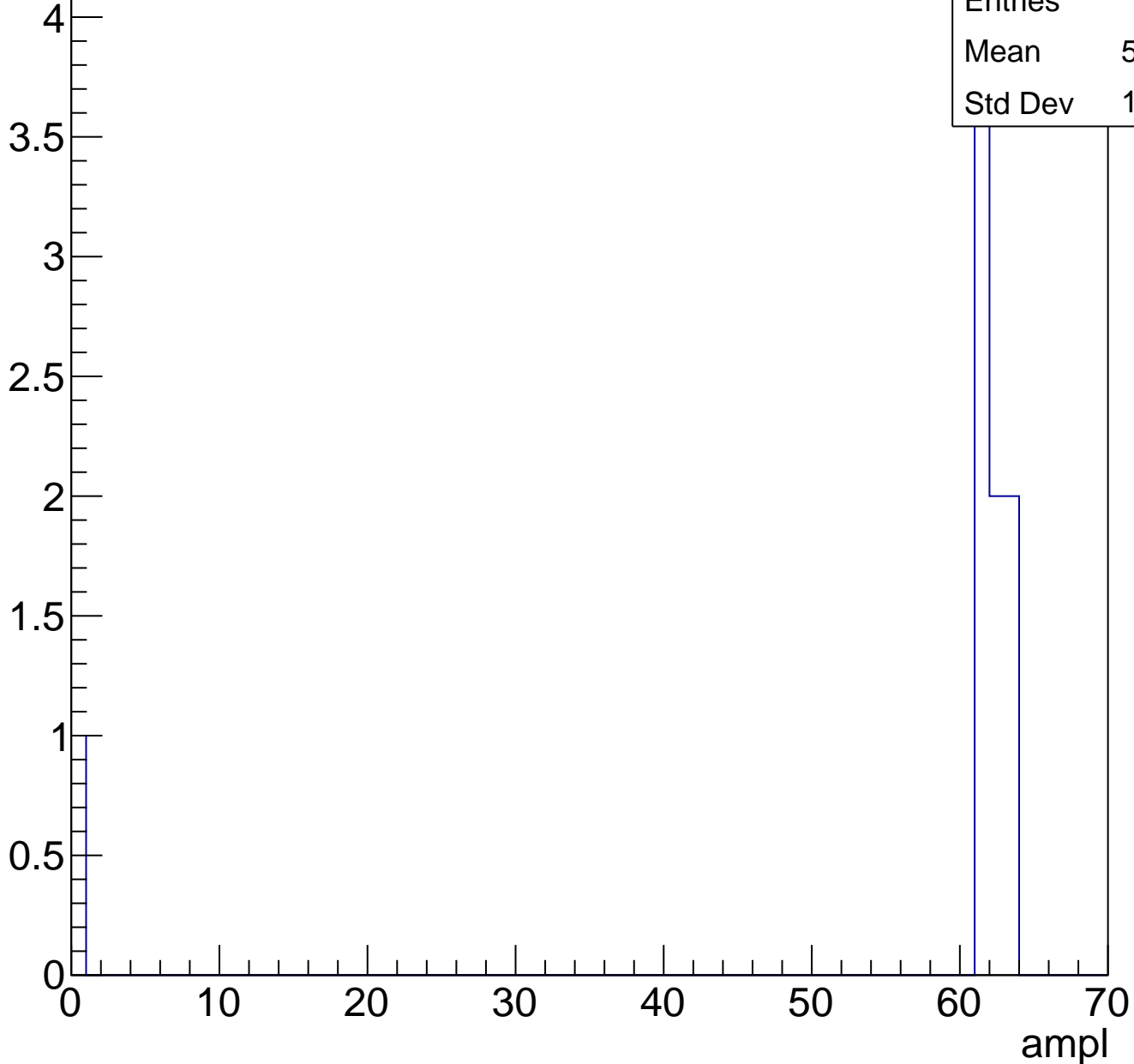
| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.73 |
| Std Dev | 2.596 |



B1L102S, U15-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch34, adc0

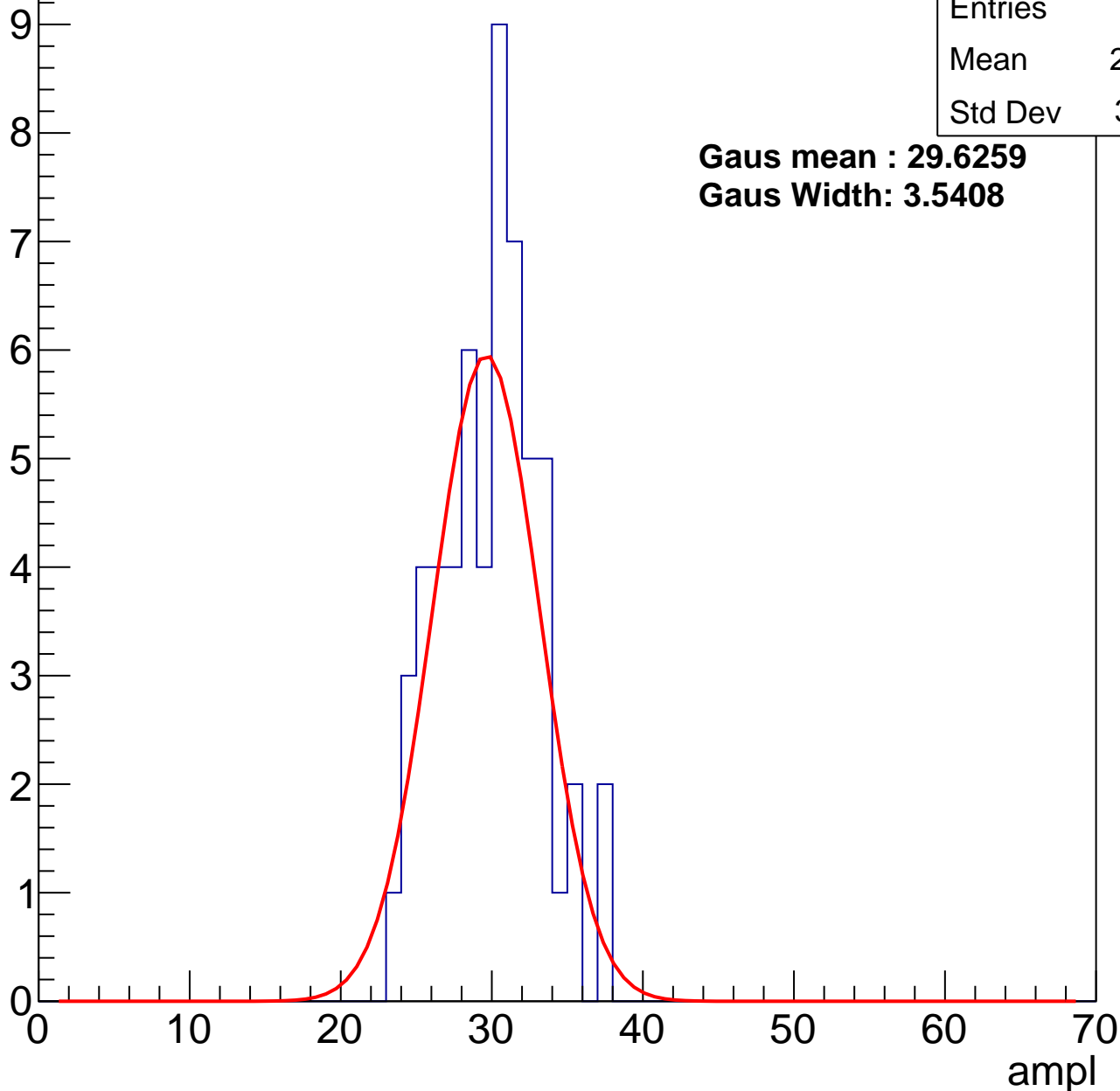
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 29.49 |
| Std Dev | 3.261 |

Gaus mean : 29.6259

Gaus Width: 3.5408



B1L102S, U15-ch34, adc1

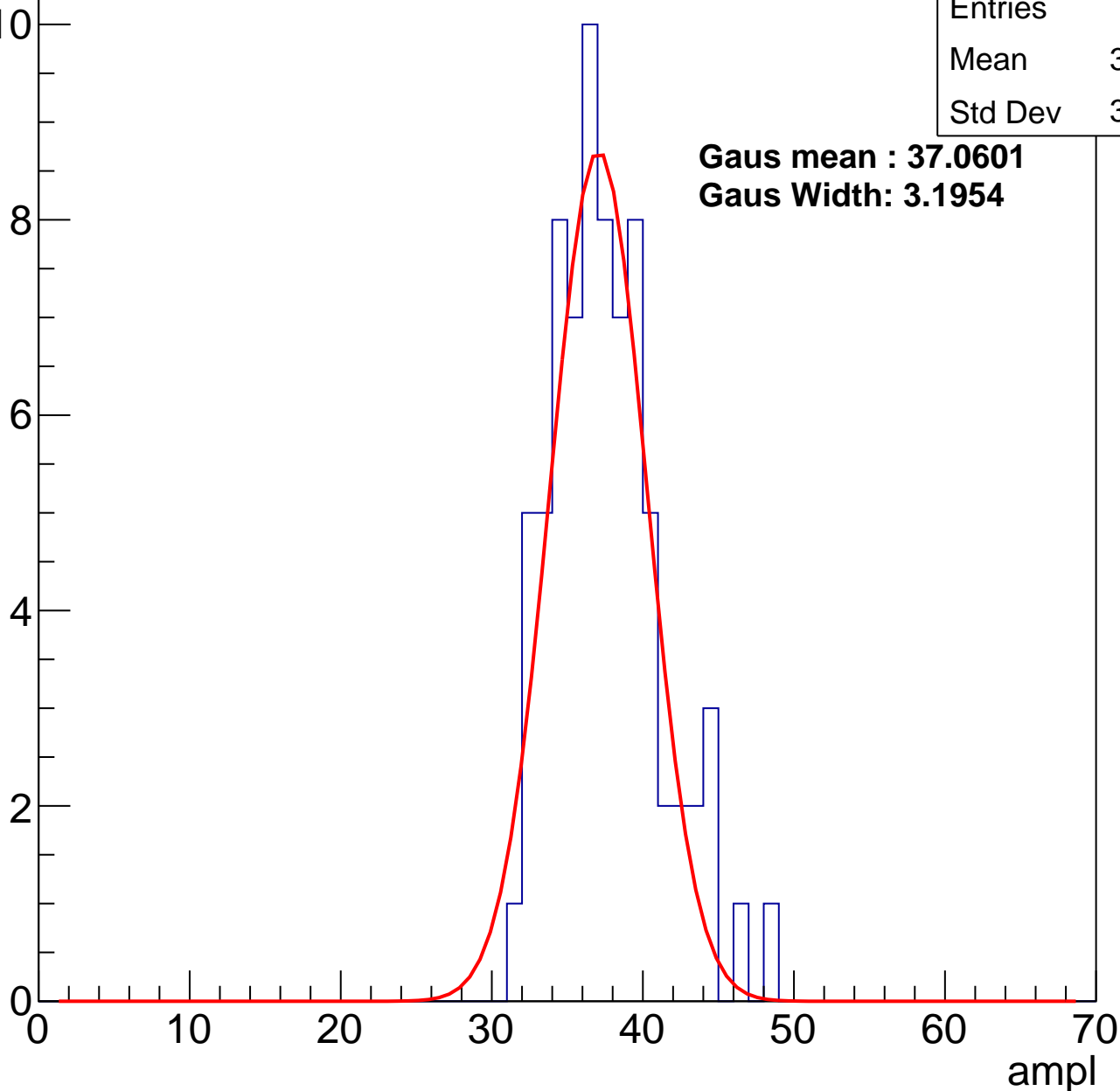
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 37.13 |
| Std Dev | 3.534 |

Gaus mean : 37.0601

Gaus Width: 3.1954



B1L102S, U15-ch34, adc2

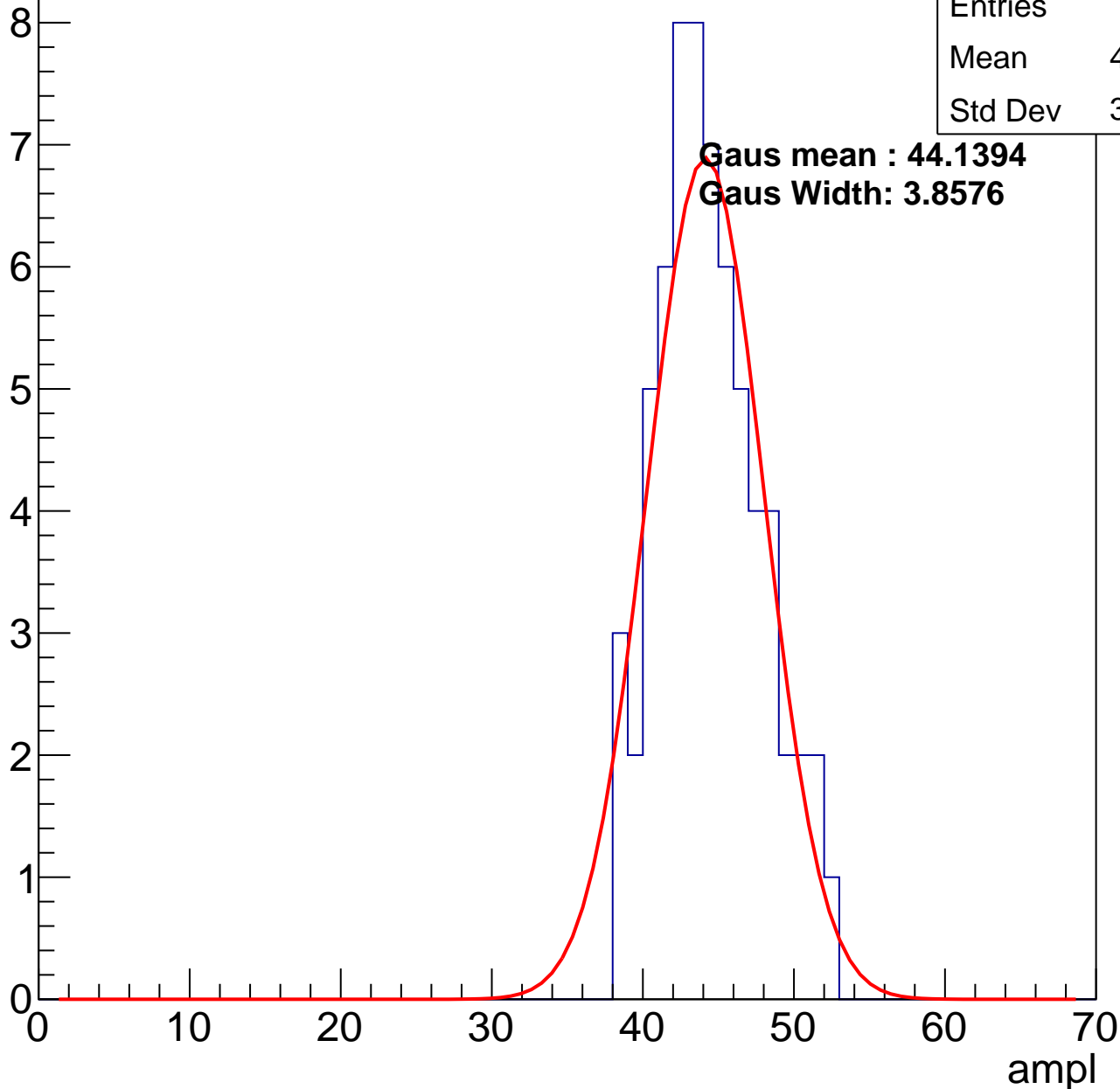
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 43.97 |
| Std Dev | 3.379 |

Gaus mean : 44.1394

Gaus Width: 3.8576

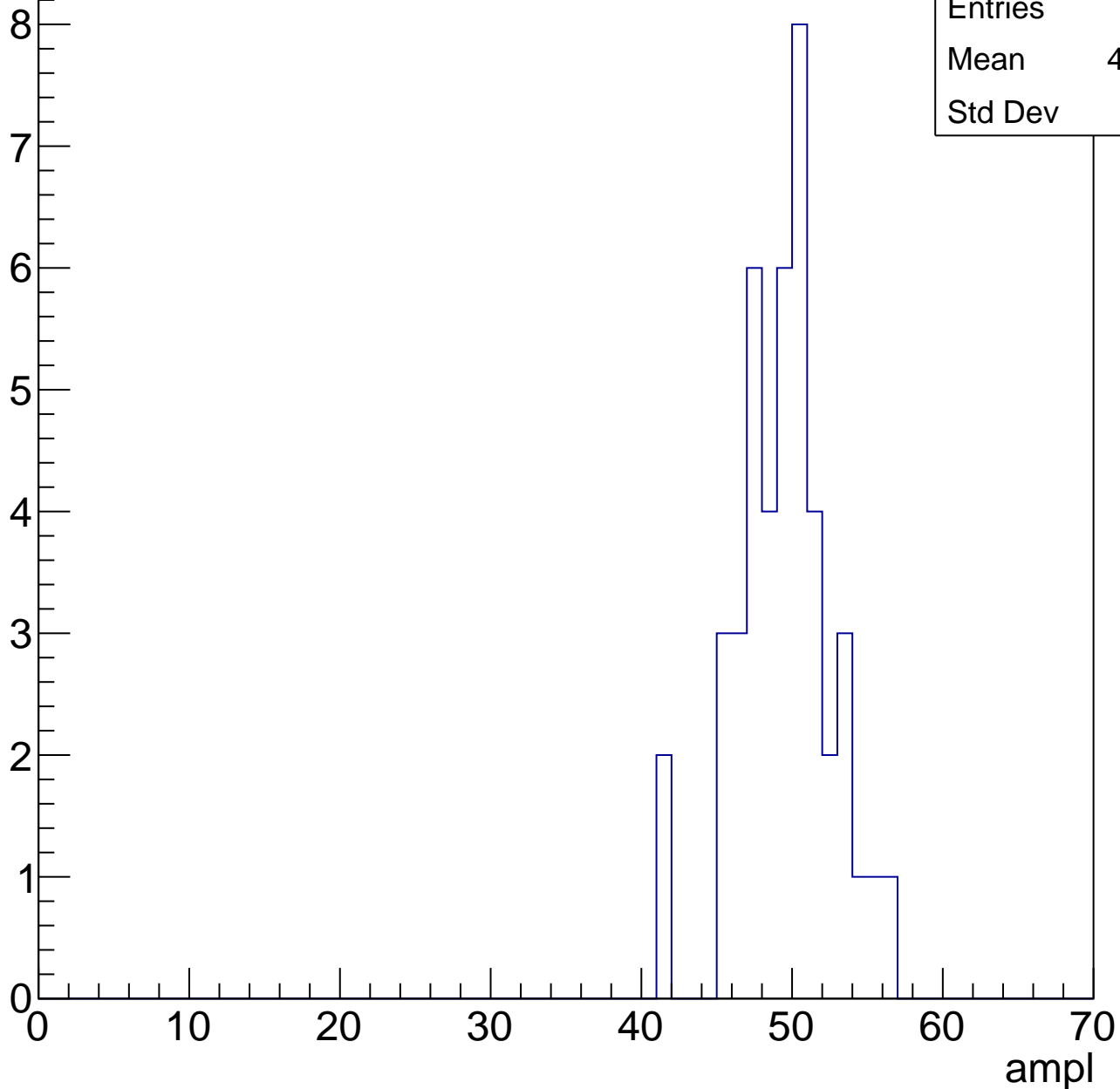


B1L102S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 48.98 |
| Std Dev | 3.13 |

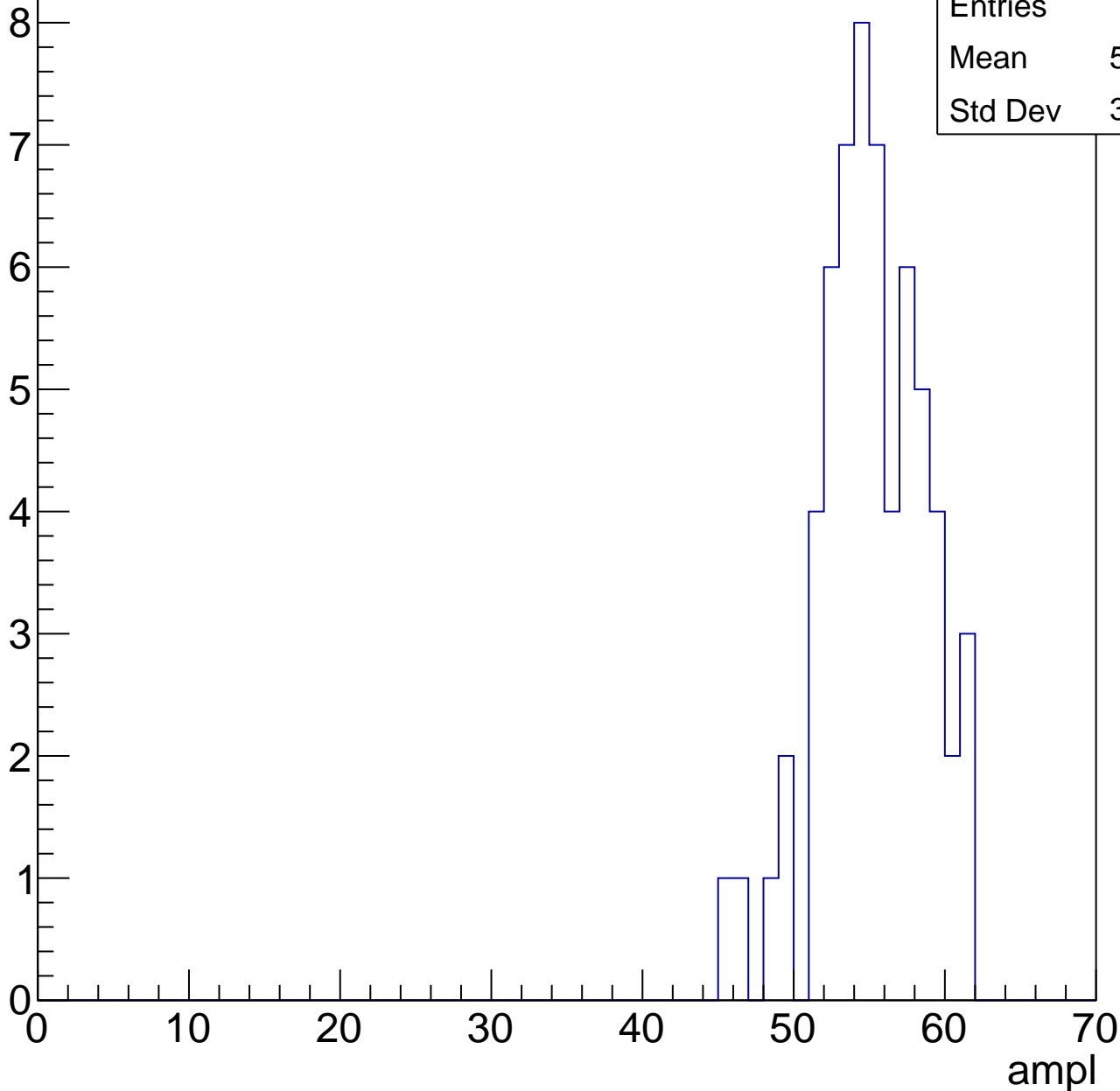


B1L102S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 54.69 |
| Std Dev | 3.495 |

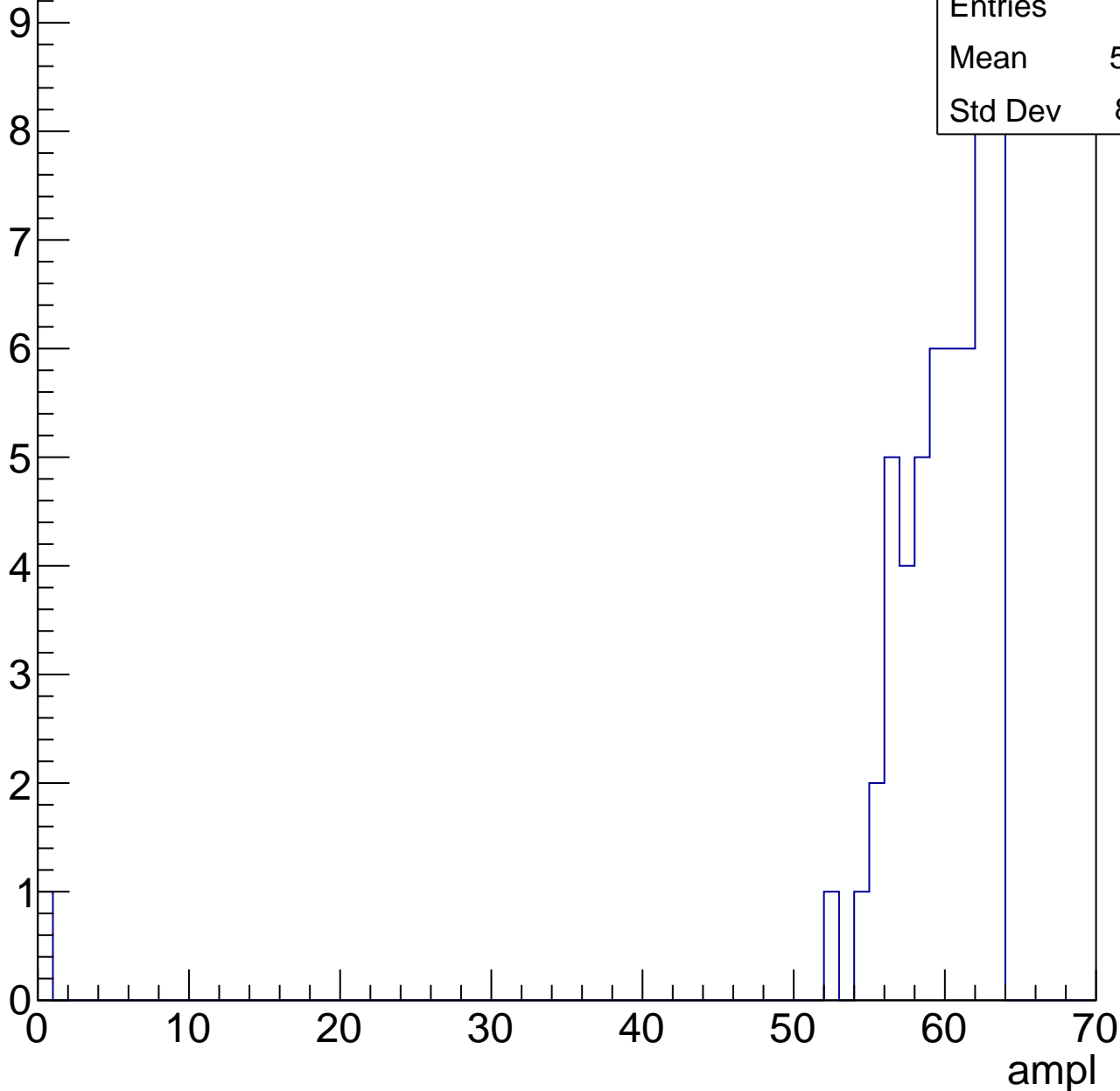


B1L102S, U15-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

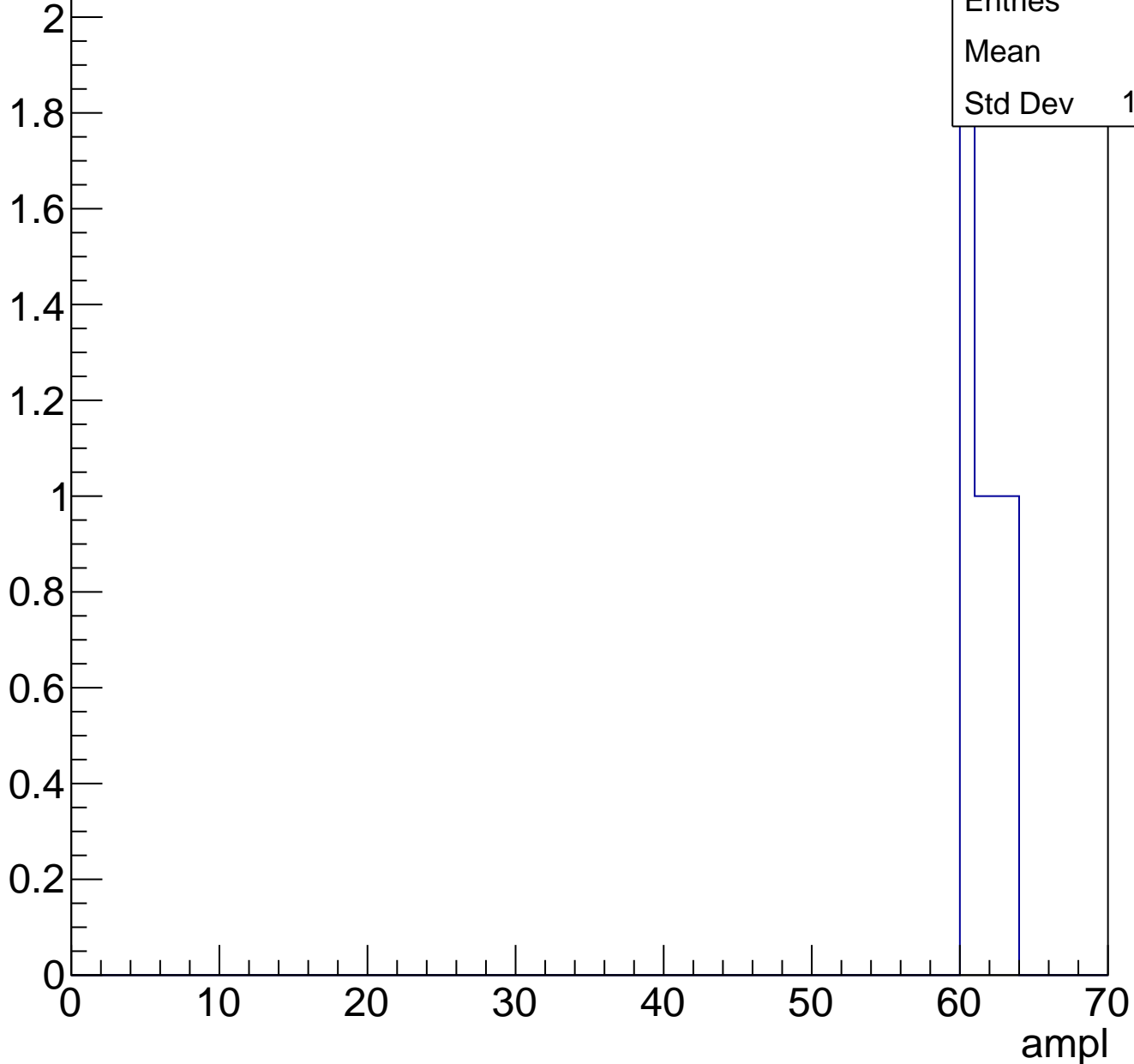
| | |
|---------|-------|
| Entries | 54 |
| Mean | 58.44 |
| Std Dev | 8.471 |



B1L102S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 28.33 |
| Std Dev | 26.11 |

B1L102S, U15-ch35, adc0

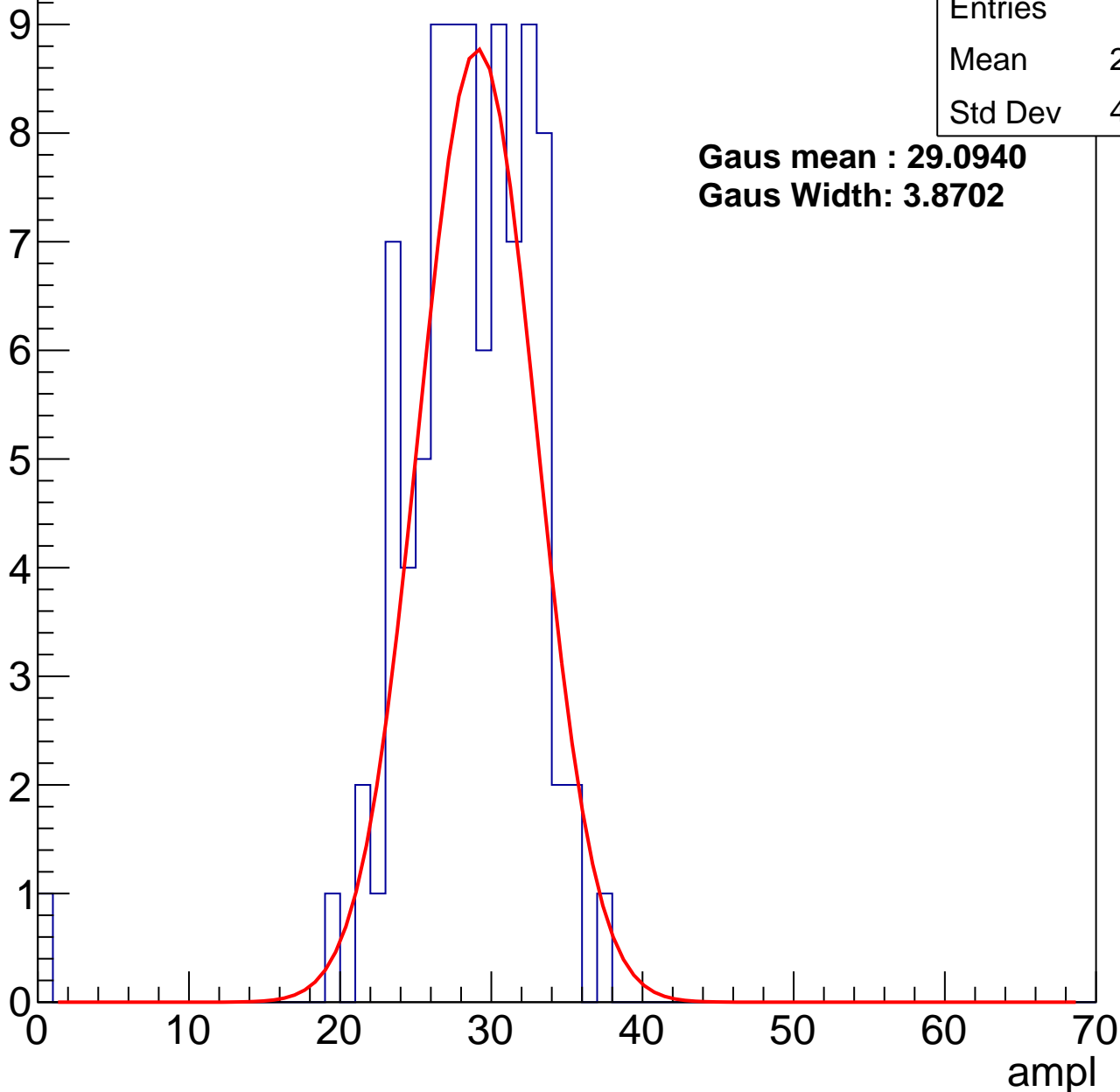
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 92 |
| Mean | 28.07 |
| Std Dev | 4.695 |

Gaus mean : 29.0940

Gaus Width: 3.8702

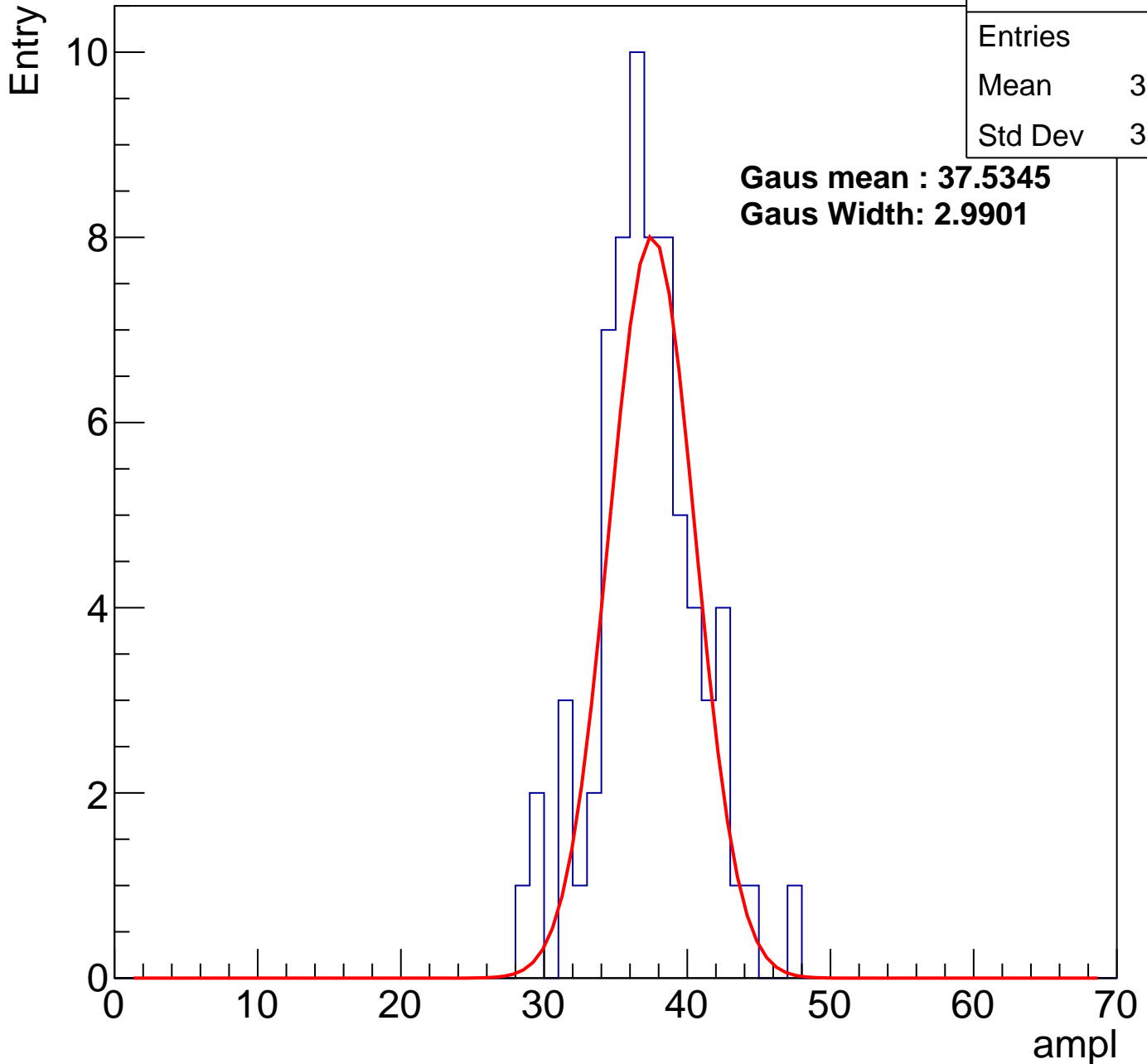


B1L102S, U15-ch35, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 36.74 |
| Std Dev | 3.554 |

Gaus mean : 37.5345
Gaus Width: 2.9901



B1L102S, U15-ch35, adc2

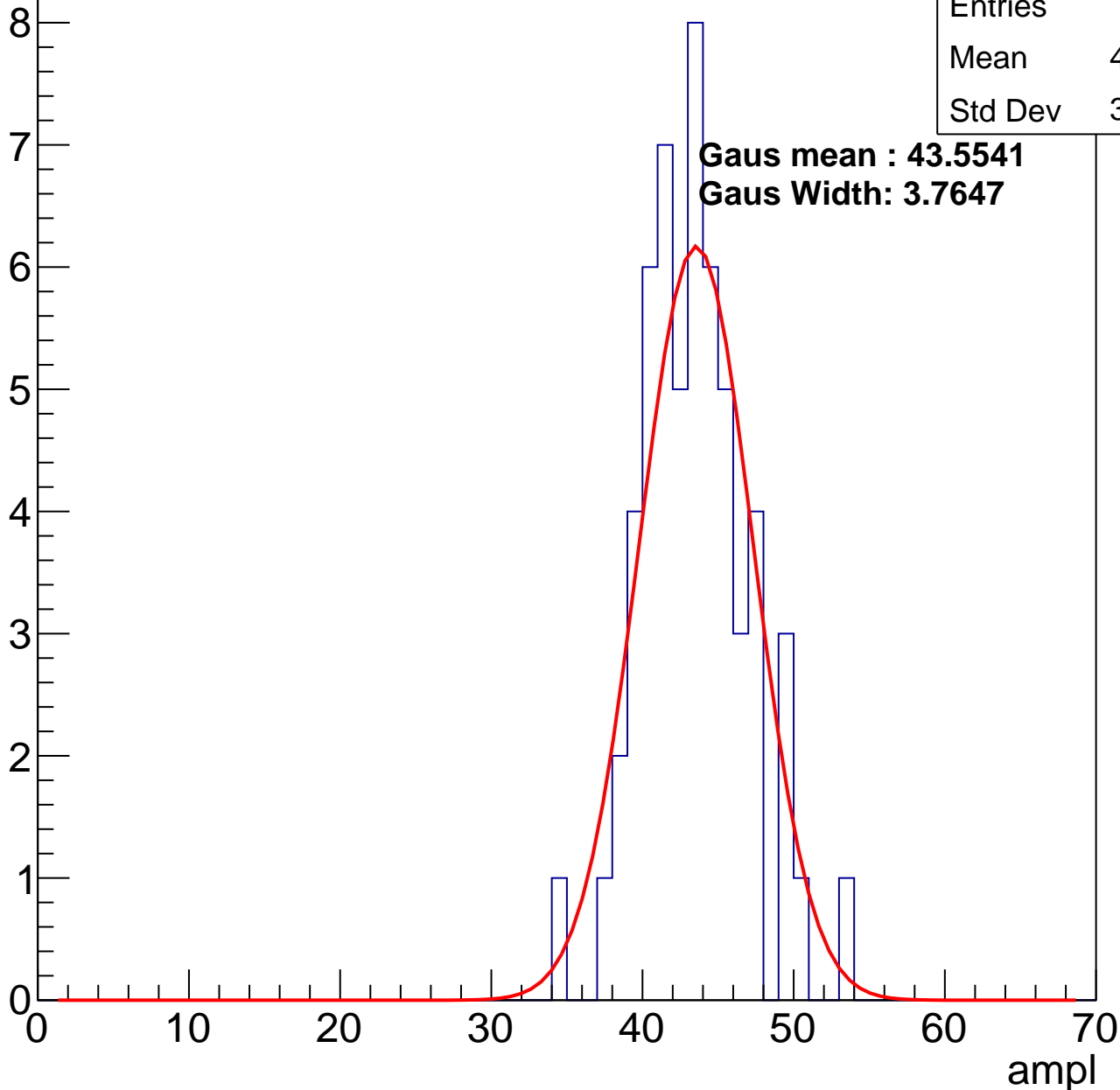
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 42.96 |
| Std Dev | 3.484 |

Gaus mean : 43.5541

Gaus Width: 3.7647

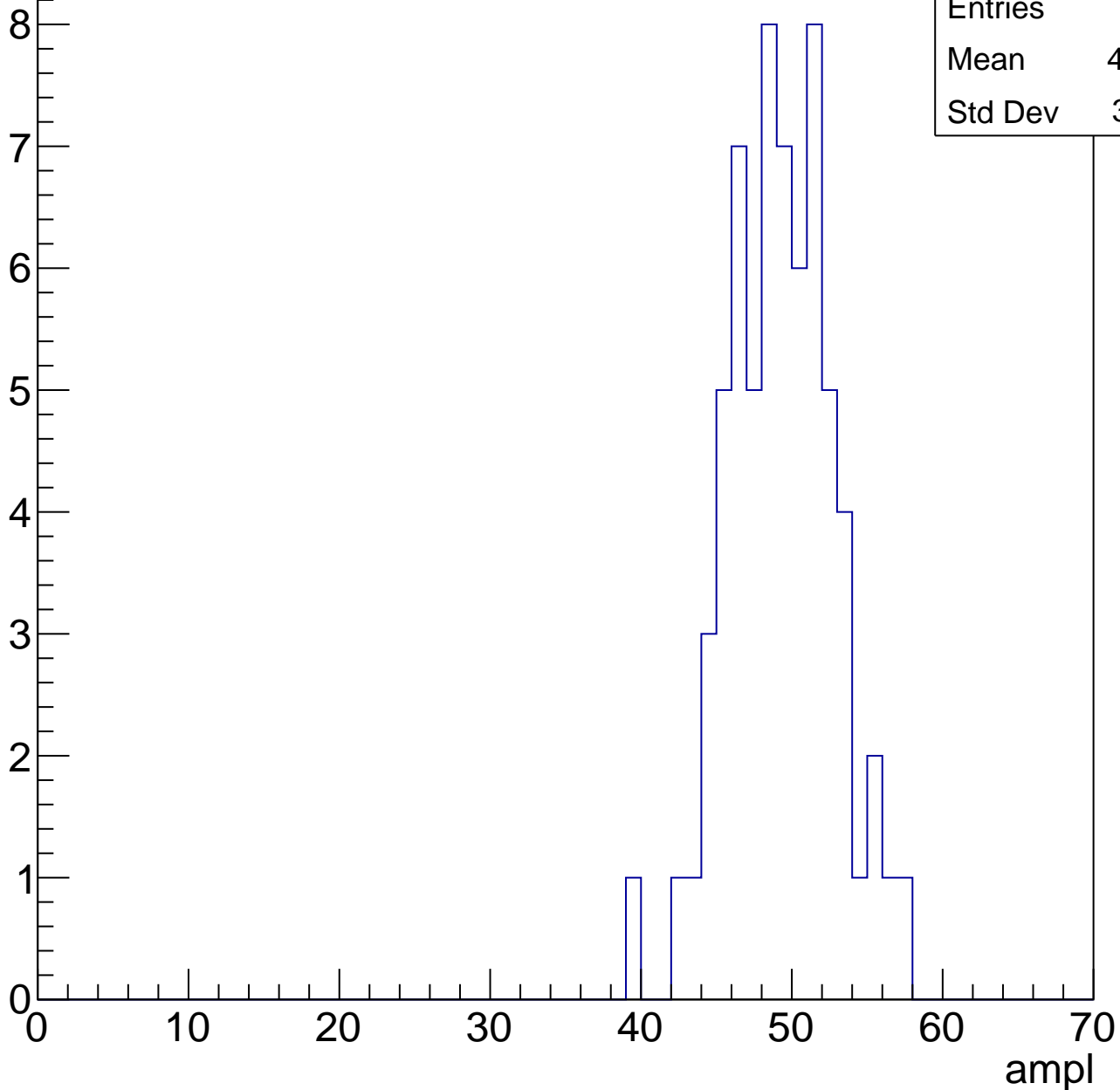


B1L102S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.82 |
| Std Dev | 3.451 |

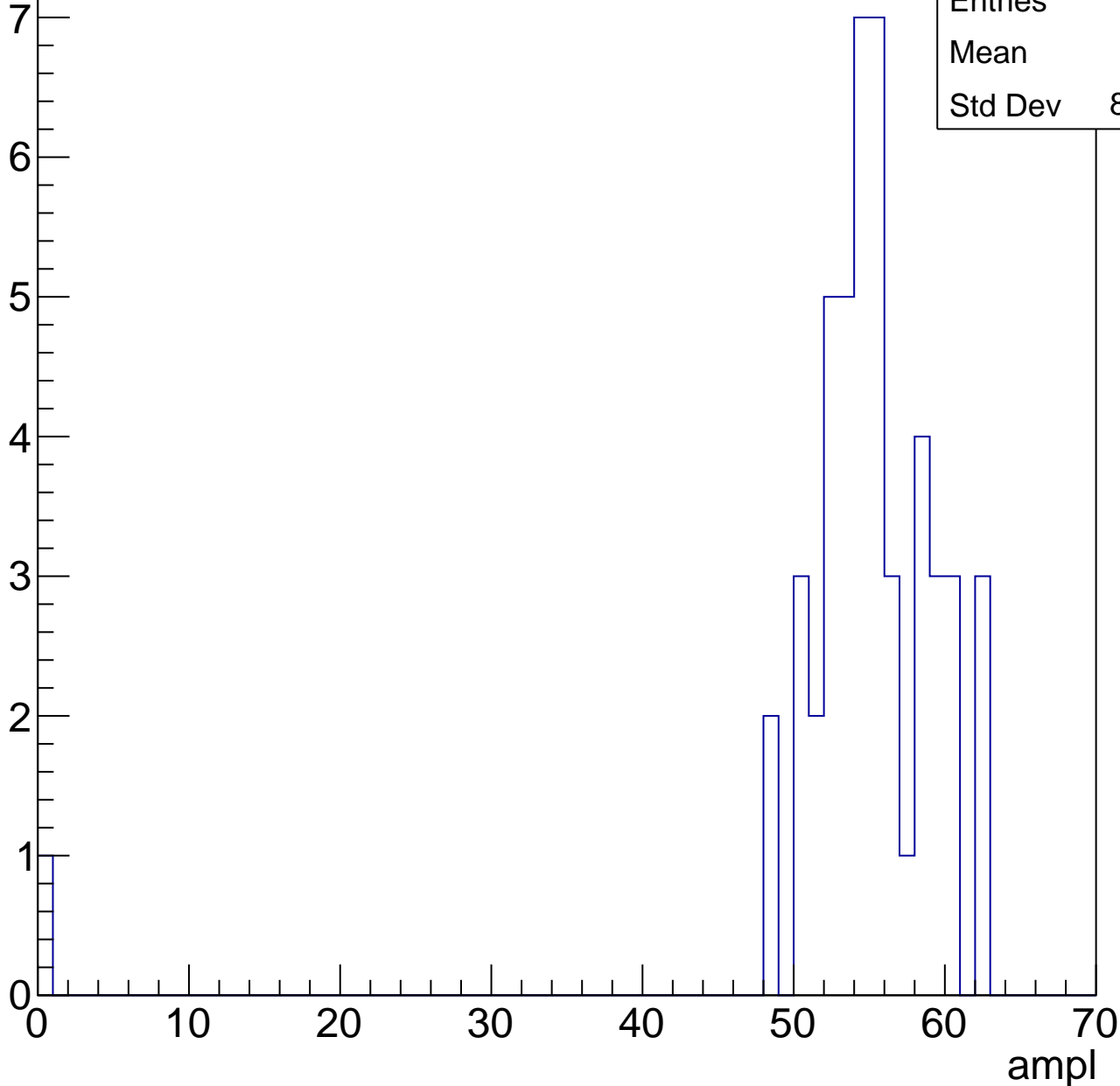


B1L102S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

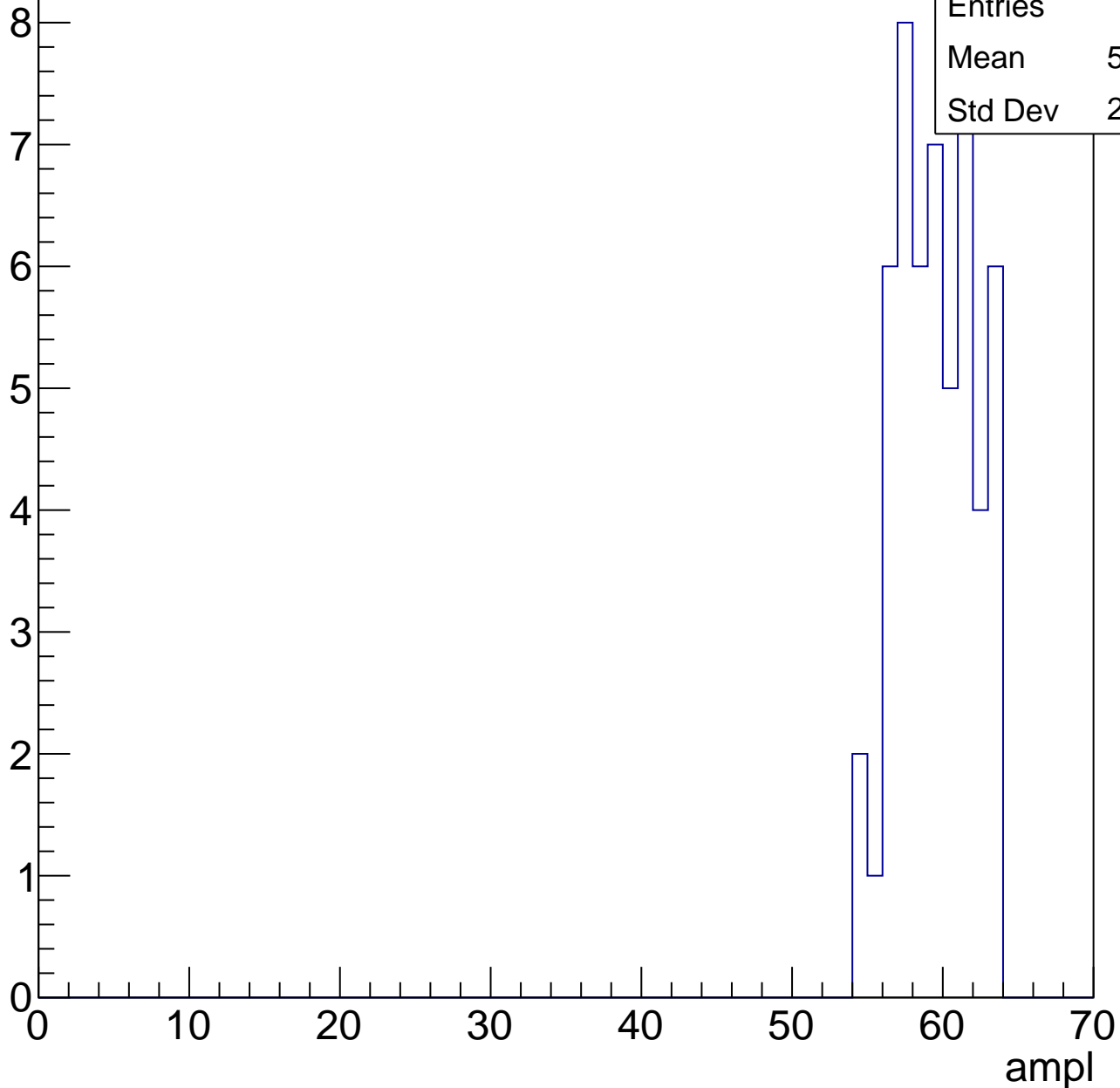
| | |
|---------|-------|
| Entries | 49 |
| Mean | 53.8 |
| Std Dev | 8.504 |



B1L102S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

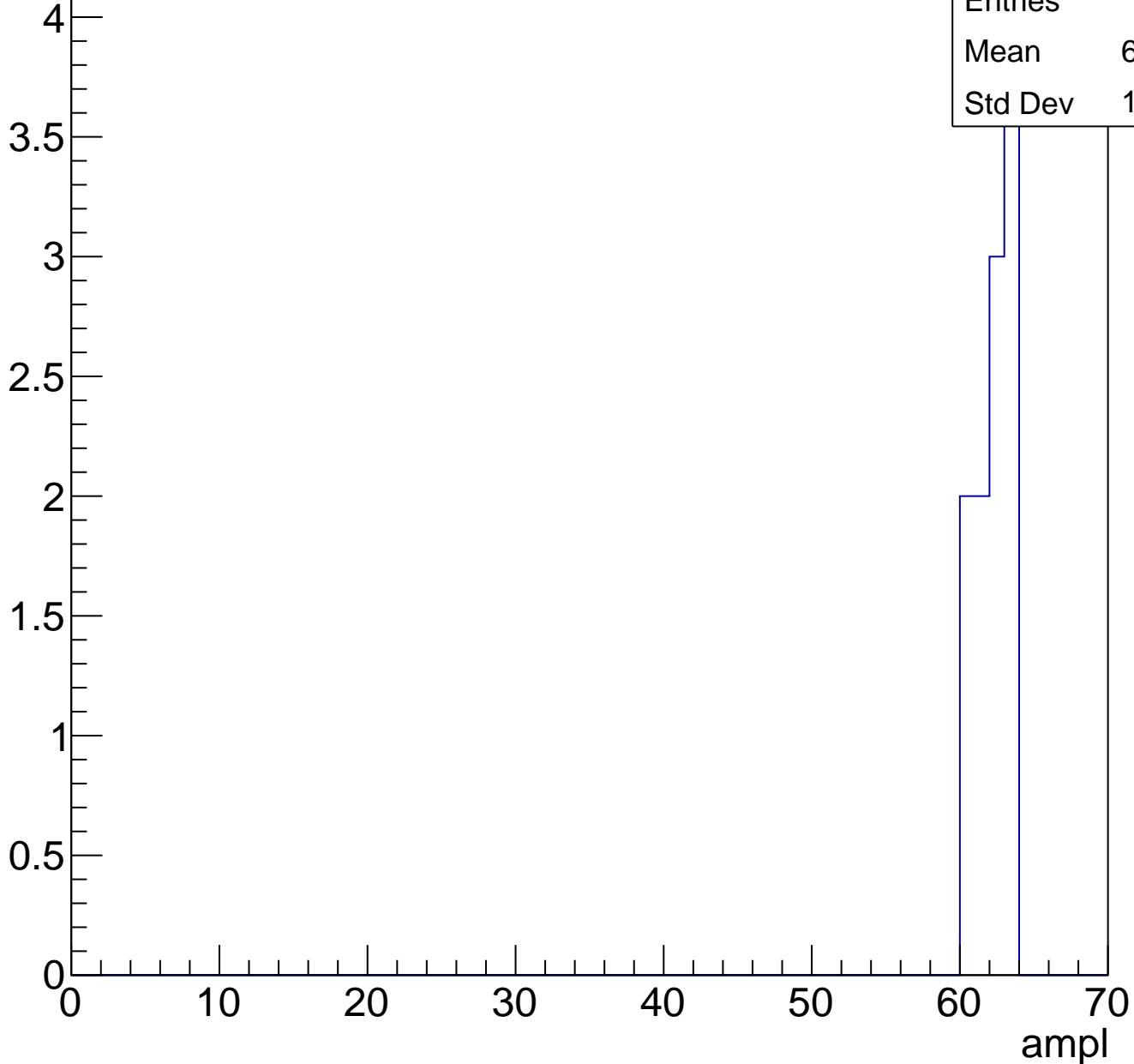


| | |
|---------|-------|
| Entries | 53 |
| Mean | 59.06 |
| Std Dev | 2.483 |

B1L102S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch36, adc0

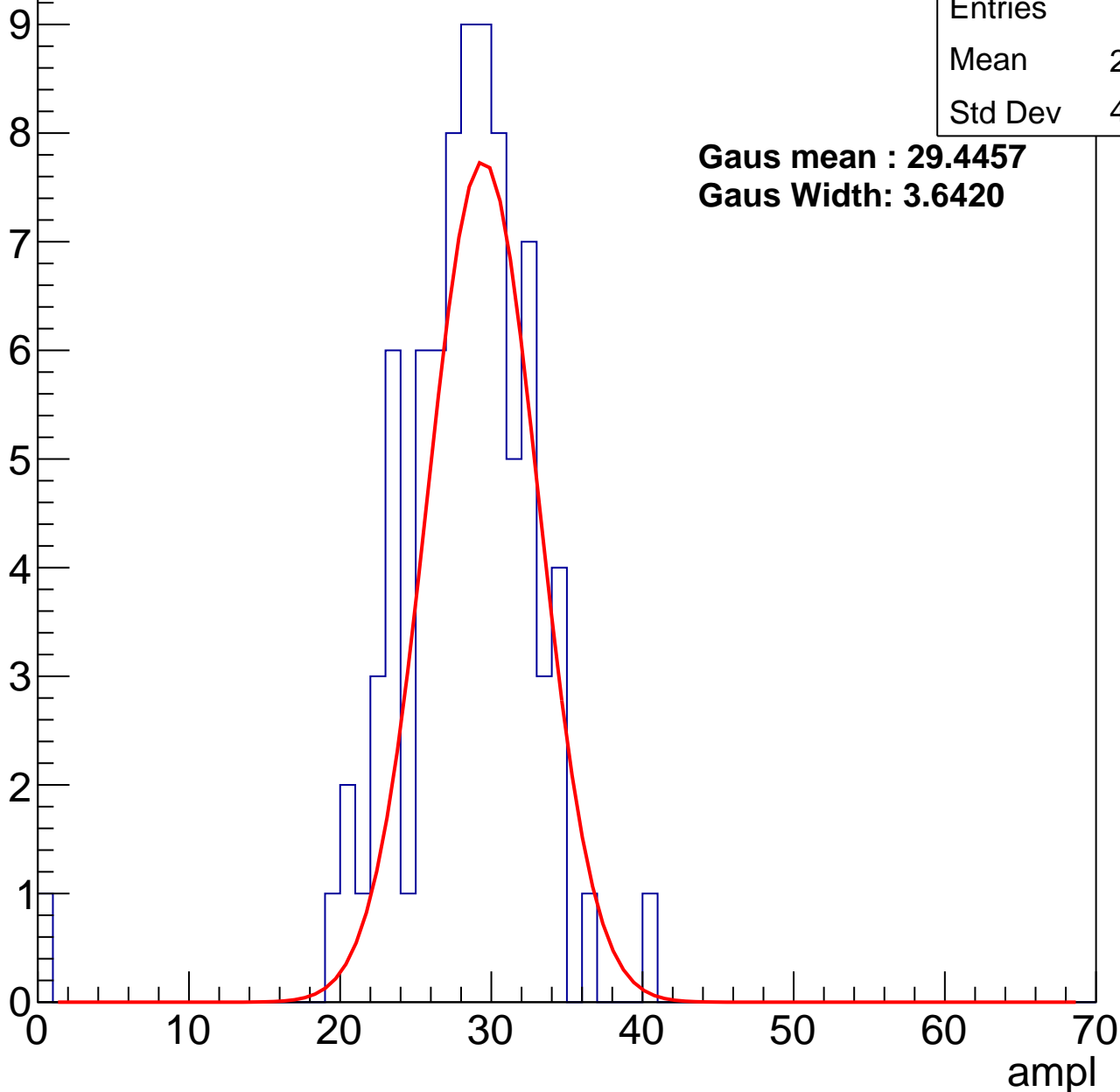
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 27.72 |
| Std Dev | 4.964 |

Gaus mean : 29.4457

Gaus Width: 3.6420



B1L102S, U15-ch36, adc1

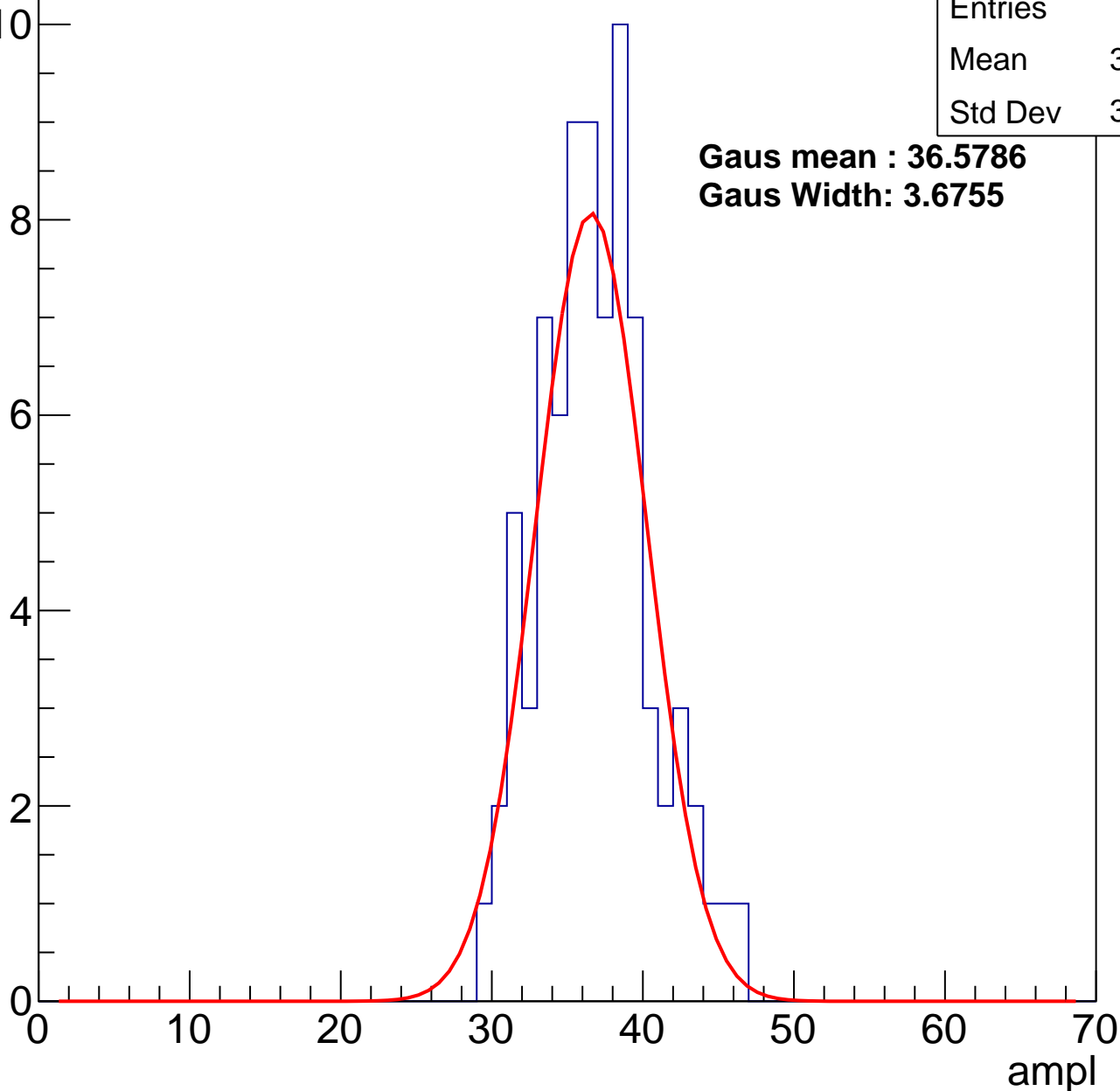
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 36.39 |
| Std Dev | 3.623 |

Gaus mean : 36.5786

Gaus Width: 3.6755



B1L102S, U15-ch36, adc2

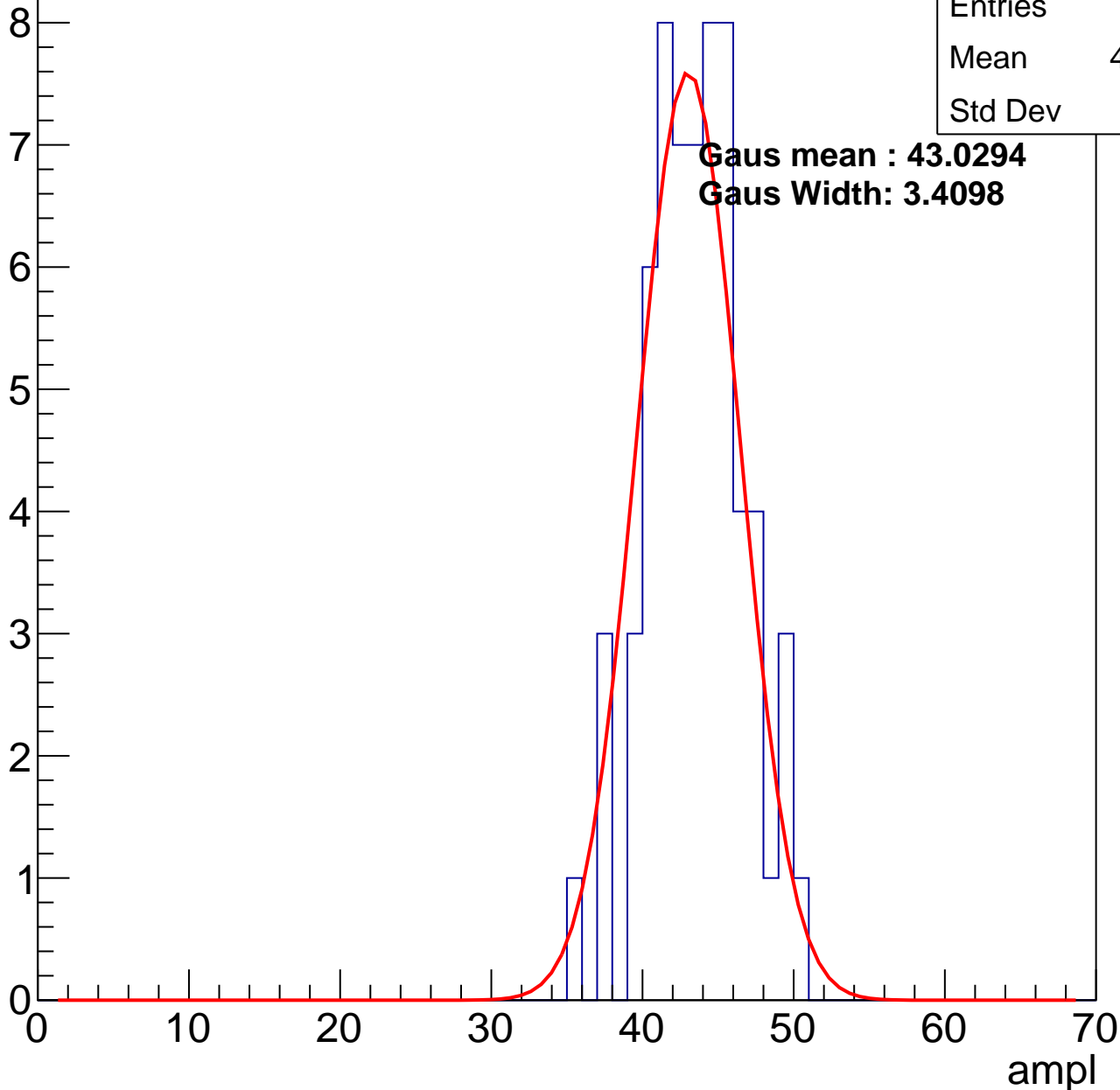
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.05 |
| Std Dev | 3.15 |

Gaus mean : 43.0294

Gaus Width: 3.4098

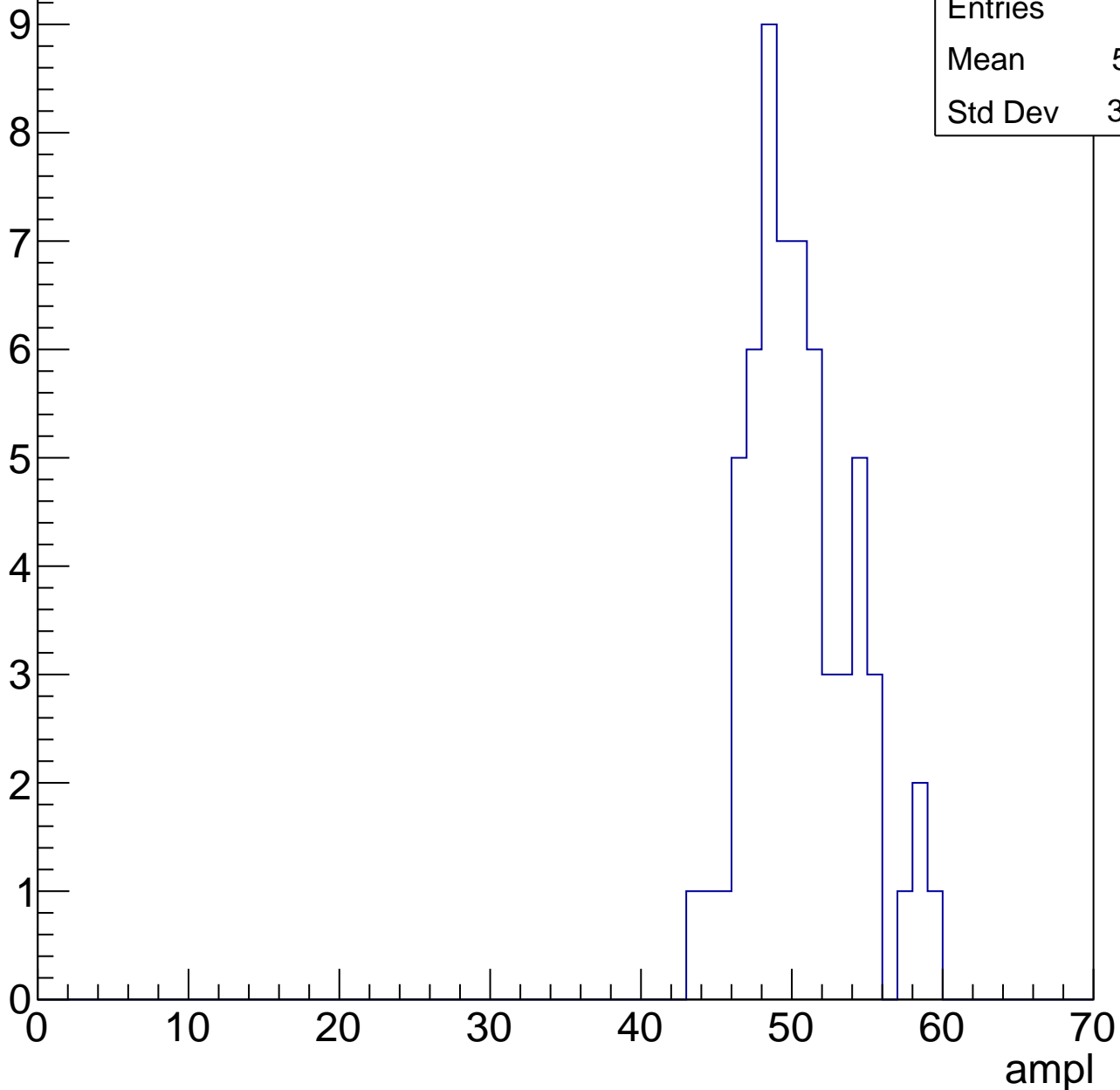


B1L102S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 50.11 |
| Std Dev | 3.493 |

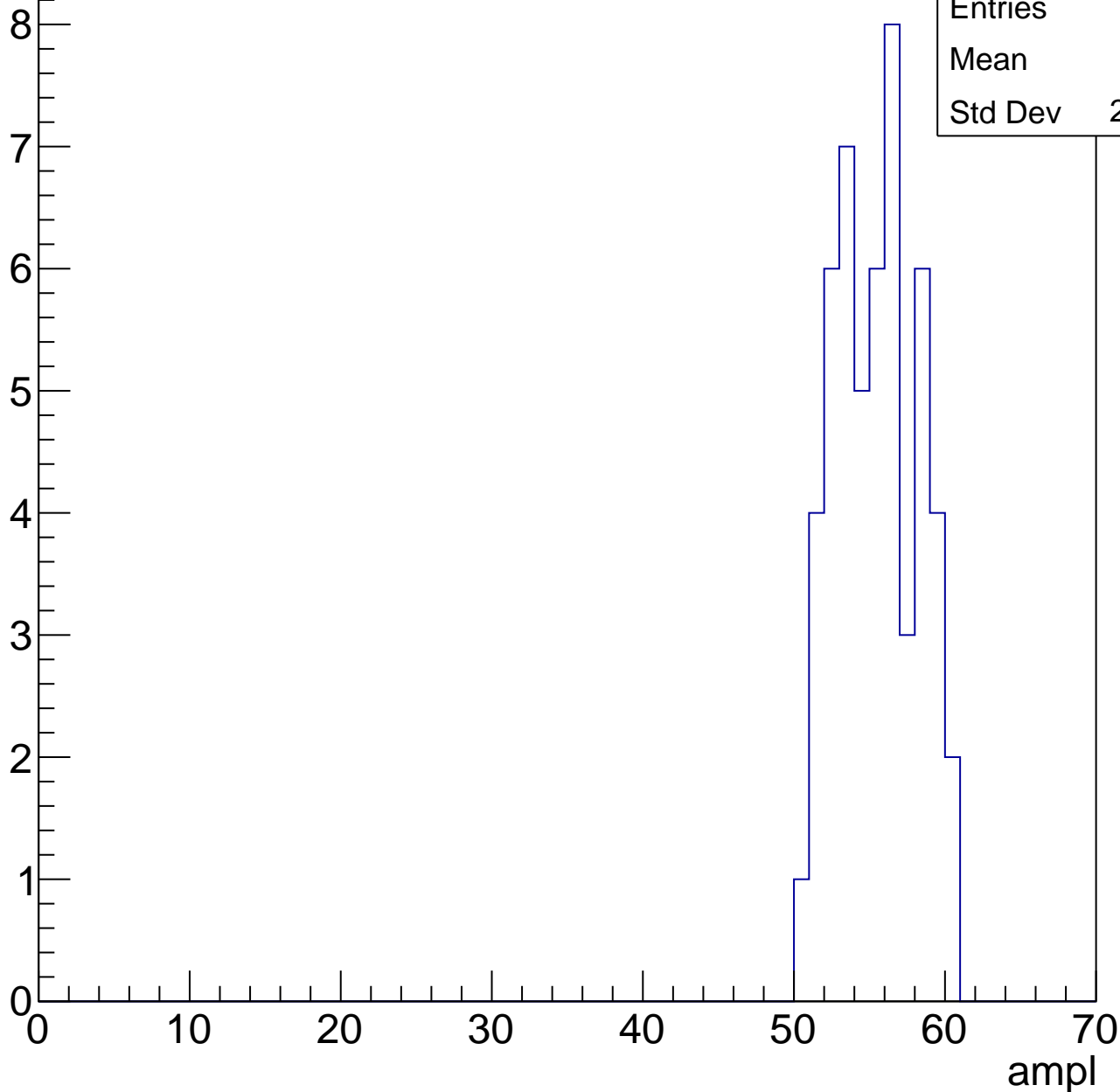


B1L102S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

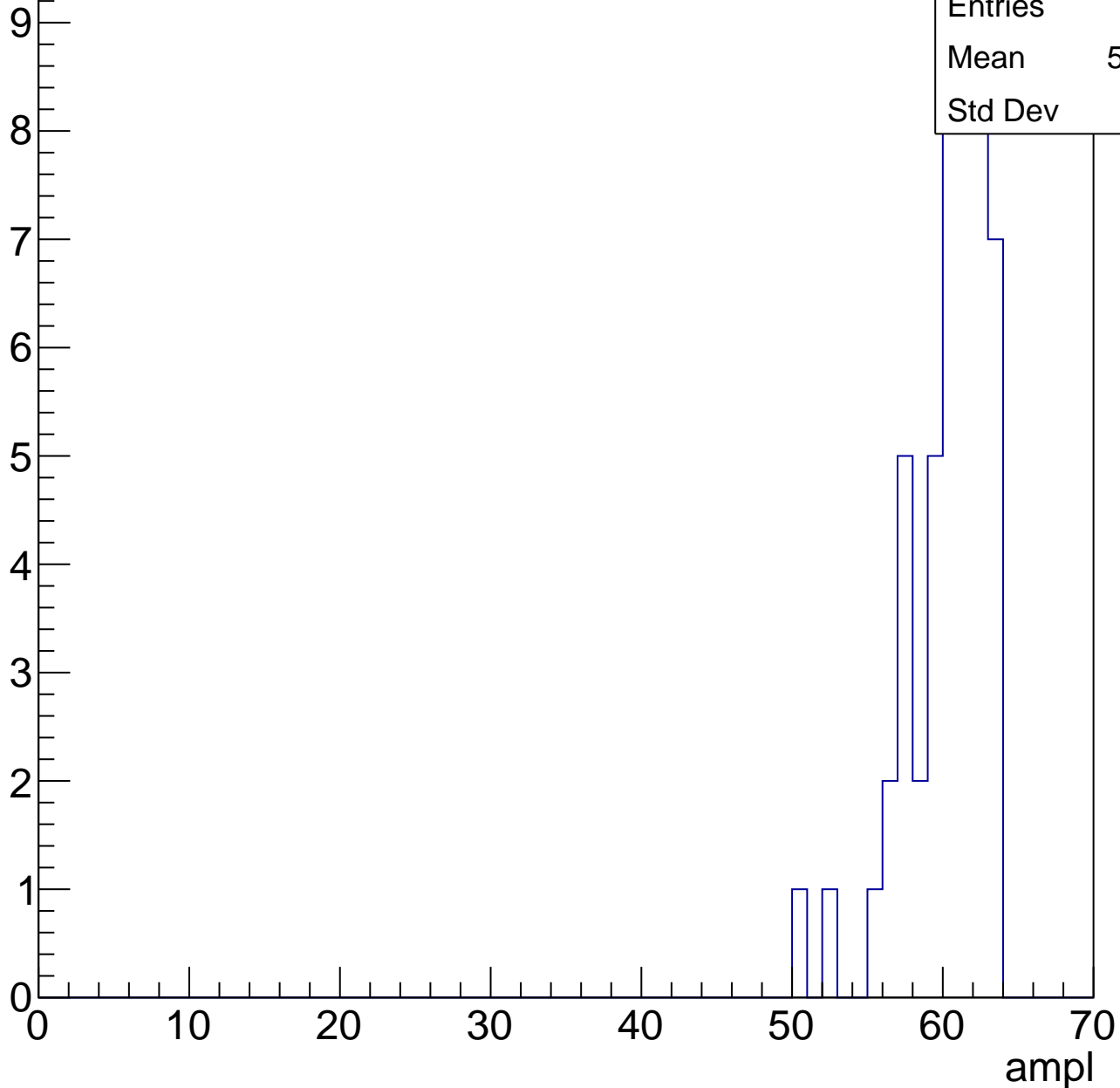
| | |
|---------|-------|
| Entries | 52 |
| Mean | 55 |
| Std Dev | 2.646 |



B1L102S, U15-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

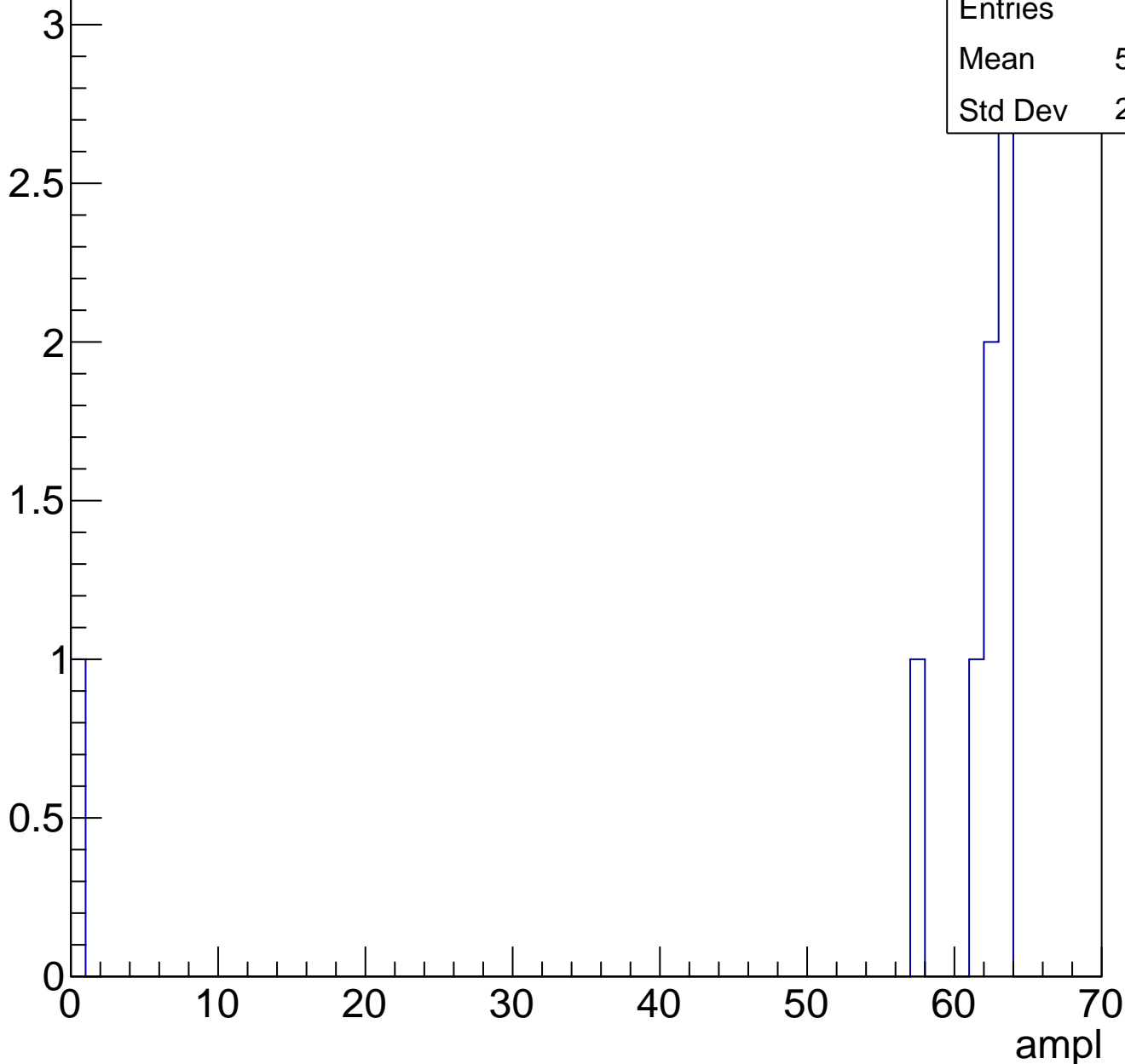
Entry



B1L102S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch37, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 84 |
| Mean | 29.33 |
| Std Dev | 4.97 |

Gaus mean : 30.3316

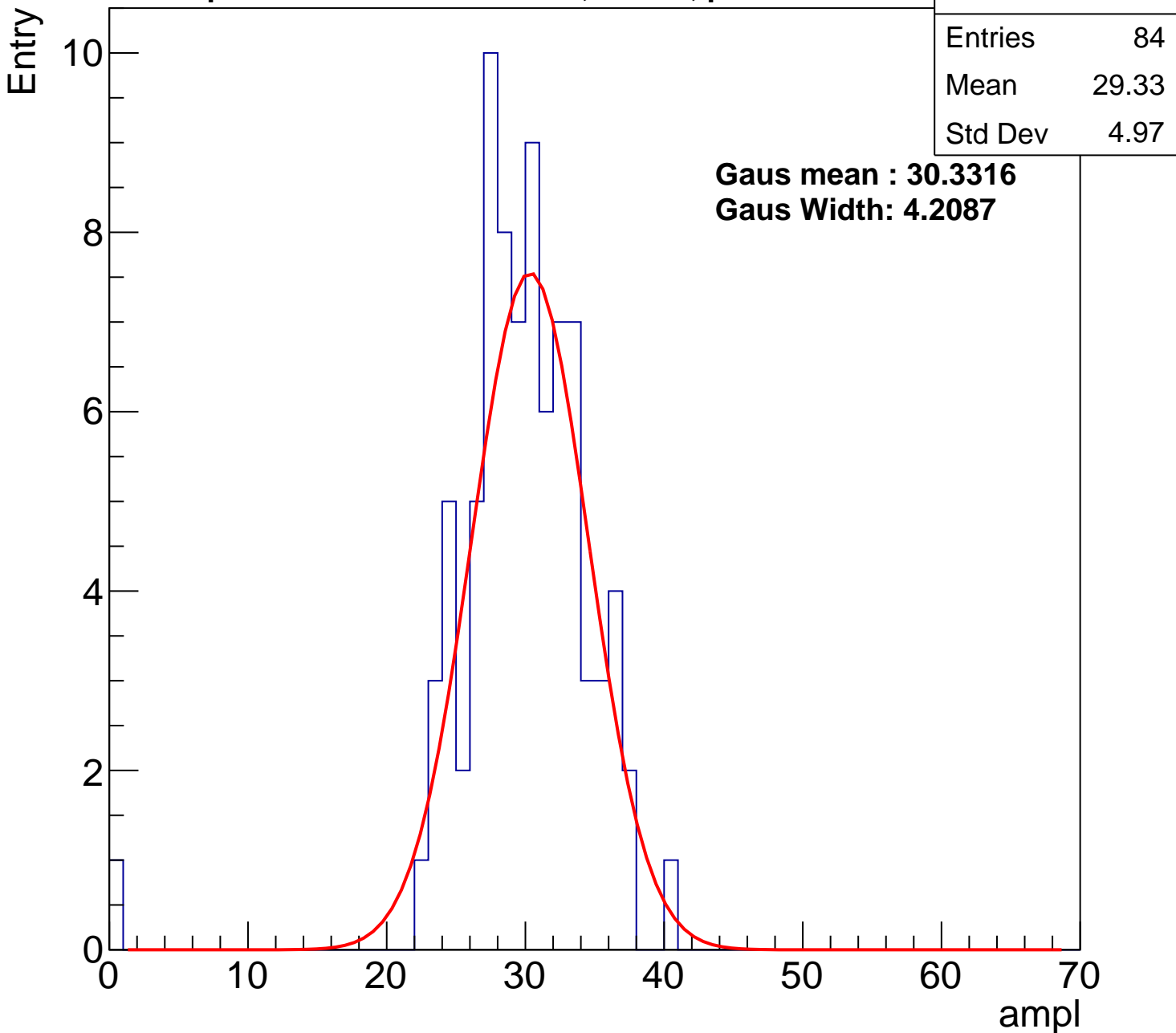
Gaus Width: 4.2087

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch37, adc1

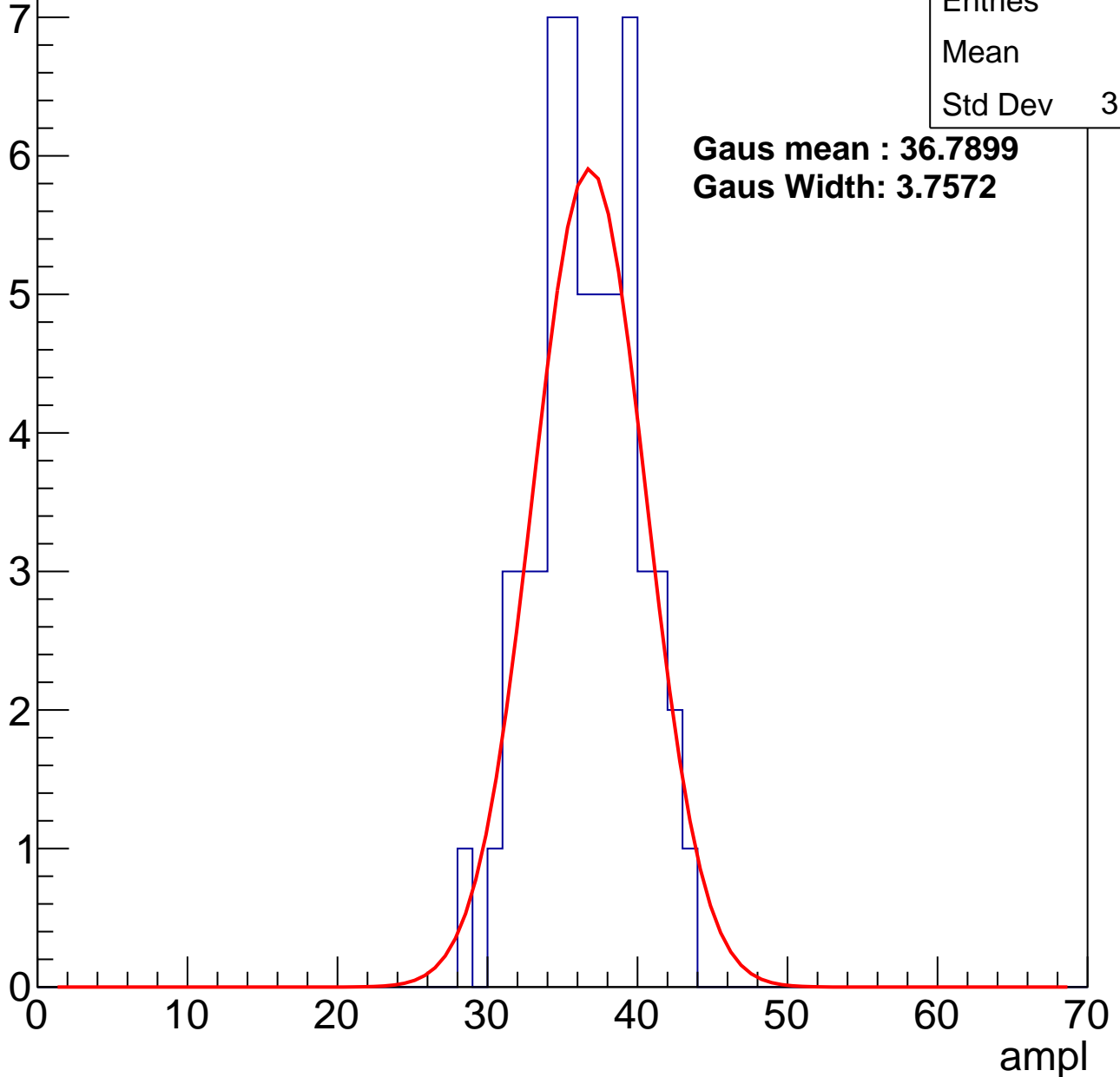
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 36.2 |
| Std Dev | 3.313 |

Gaus mean : 36.7899

Gaus Width: 3.7572



B1L102S, U15-ch37, adc2

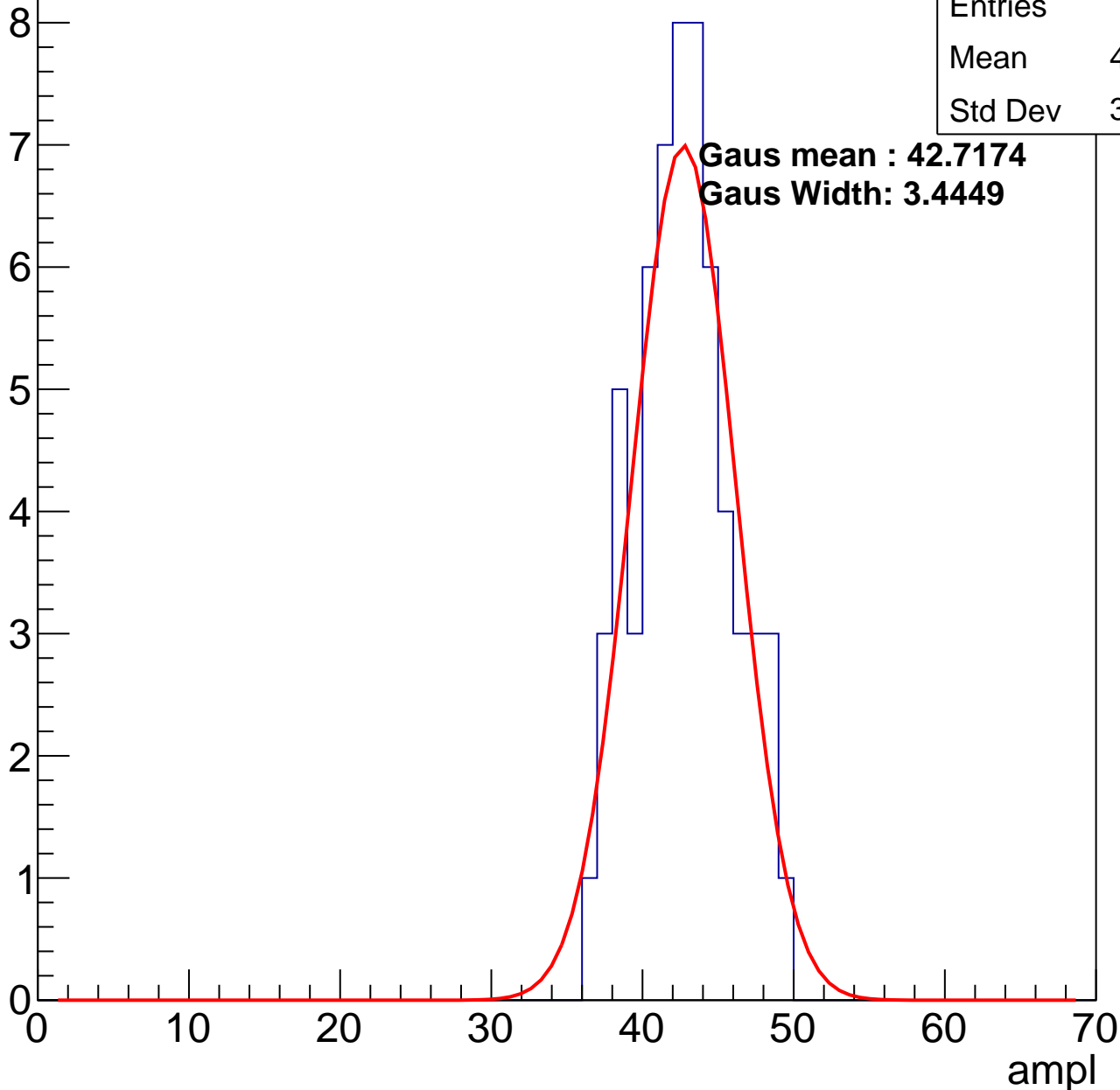
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.25 |
| Std Dev | 3.119 |

Gaus mean : 42.7174

Gaus Width: 3.4449

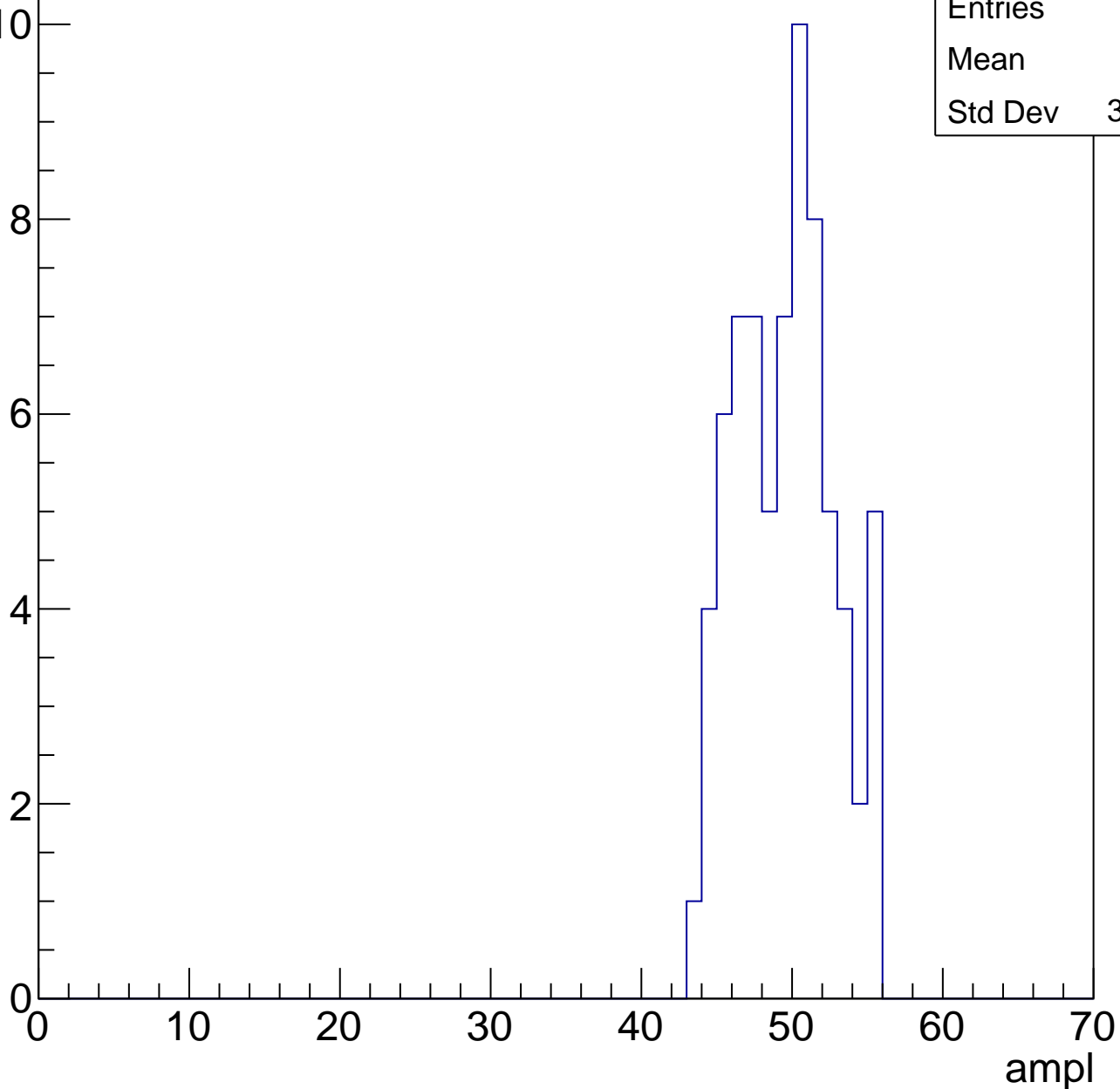


B1L102S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

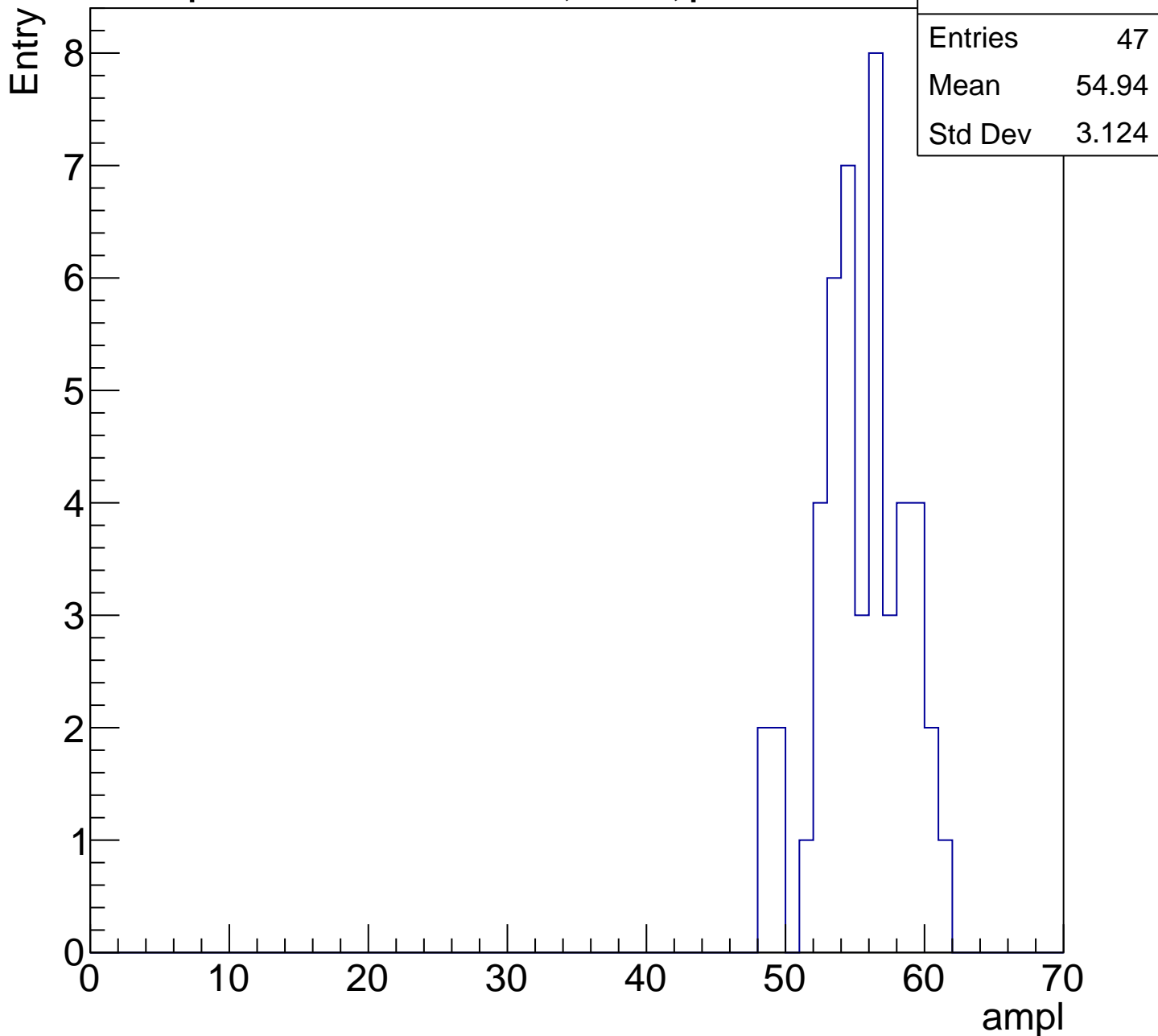
Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 49.1 |
| Std Dev | 3.159 |



B1L102S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch37, adc5

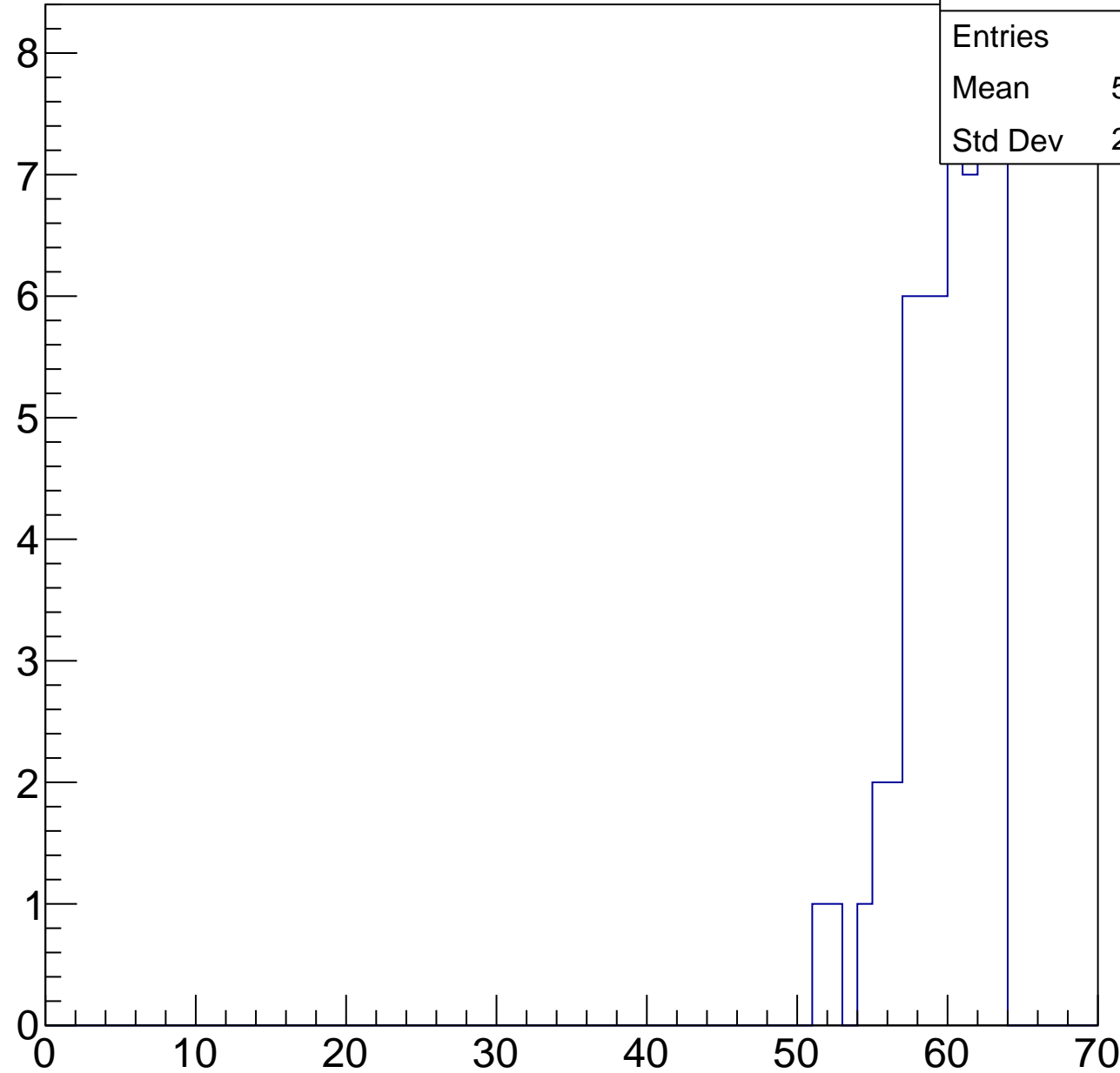
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 56 |
| Mean | 59.46 |
| Std Dev | 2.809 |

ampl



B1L102S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

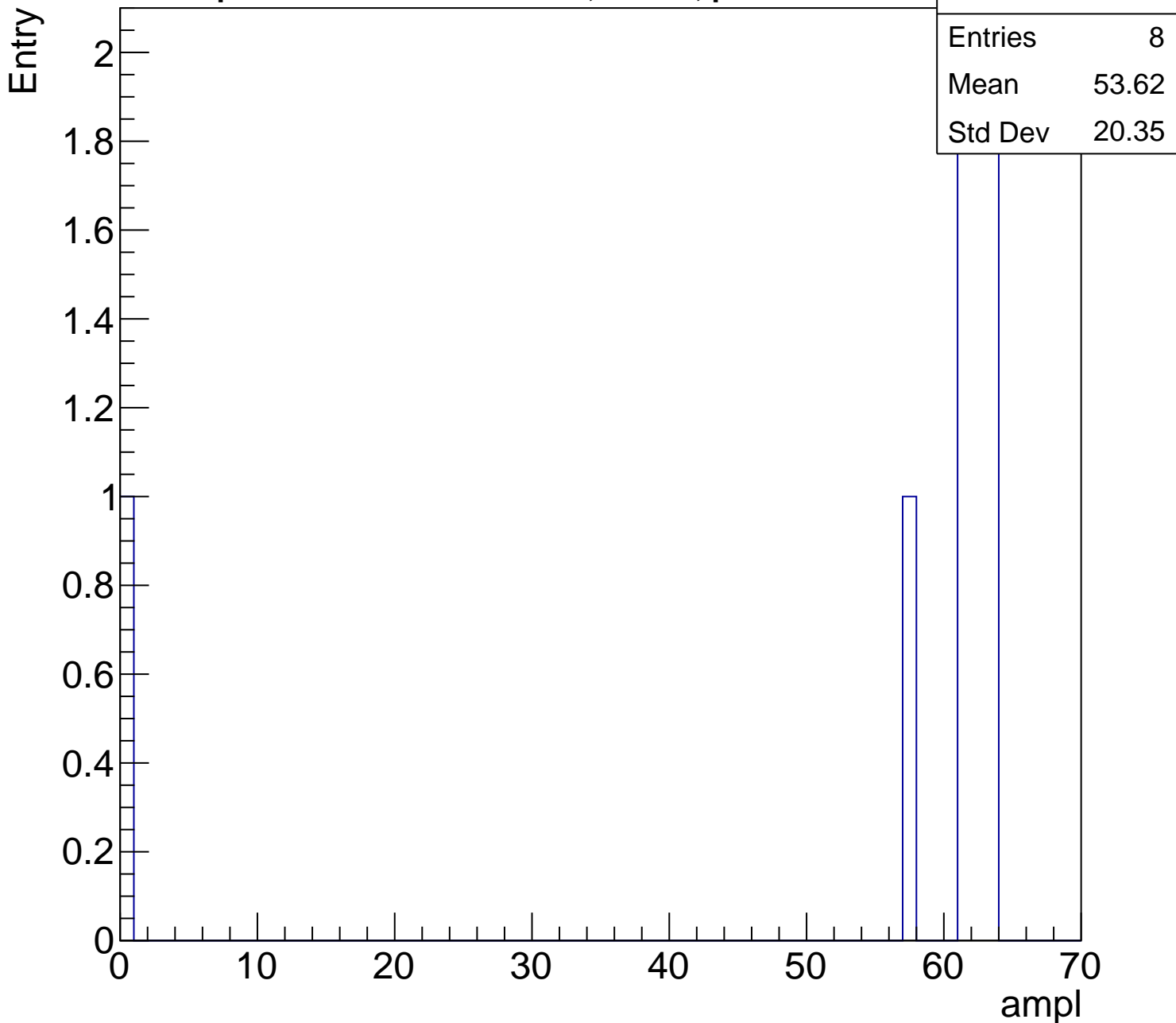
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 53.62 |
| Std Dev | 20.35 |

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch38, adc0

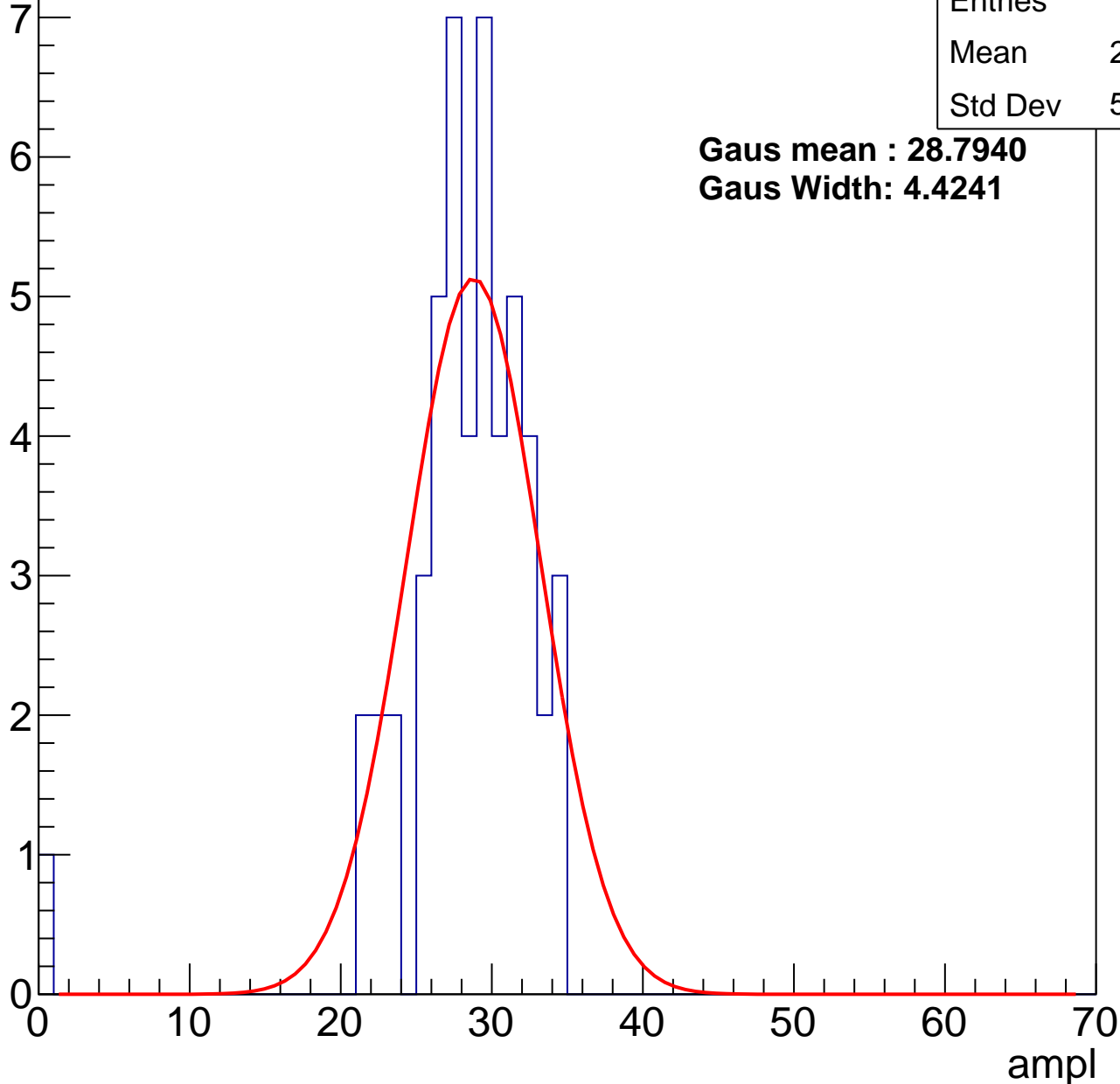
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 27.69 |
| Std Dev | 5.132 |

Gaus mean : 28.7940

Gaus Width: 4.4241



B1L102S, U15-ch38, adc1

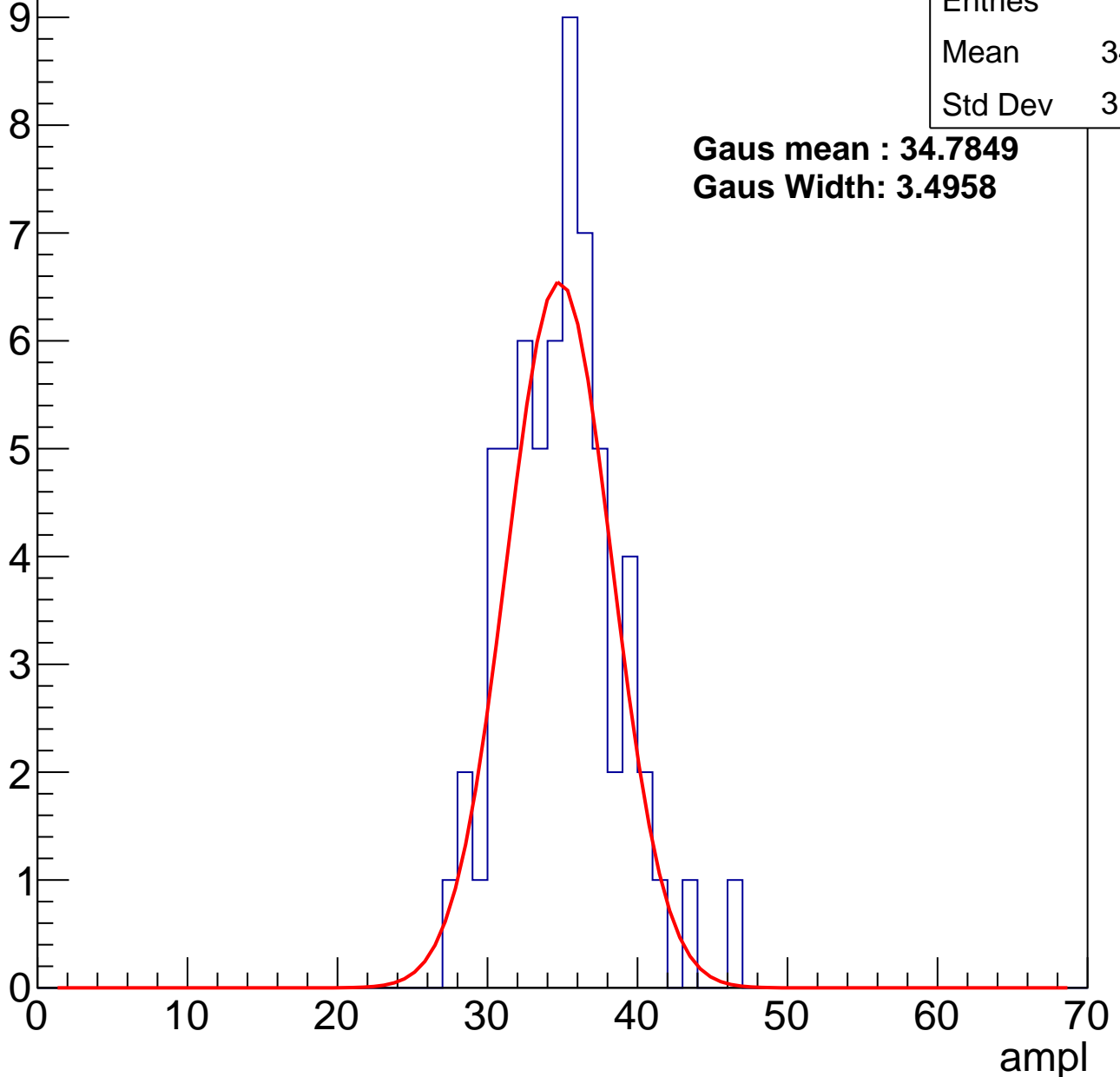
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 34.48 |
| Std Dev | 3.664 |

Gaus mean : 34.7849

Gaus Width: 3.4958



B1L102S, U15-ch38, adc2

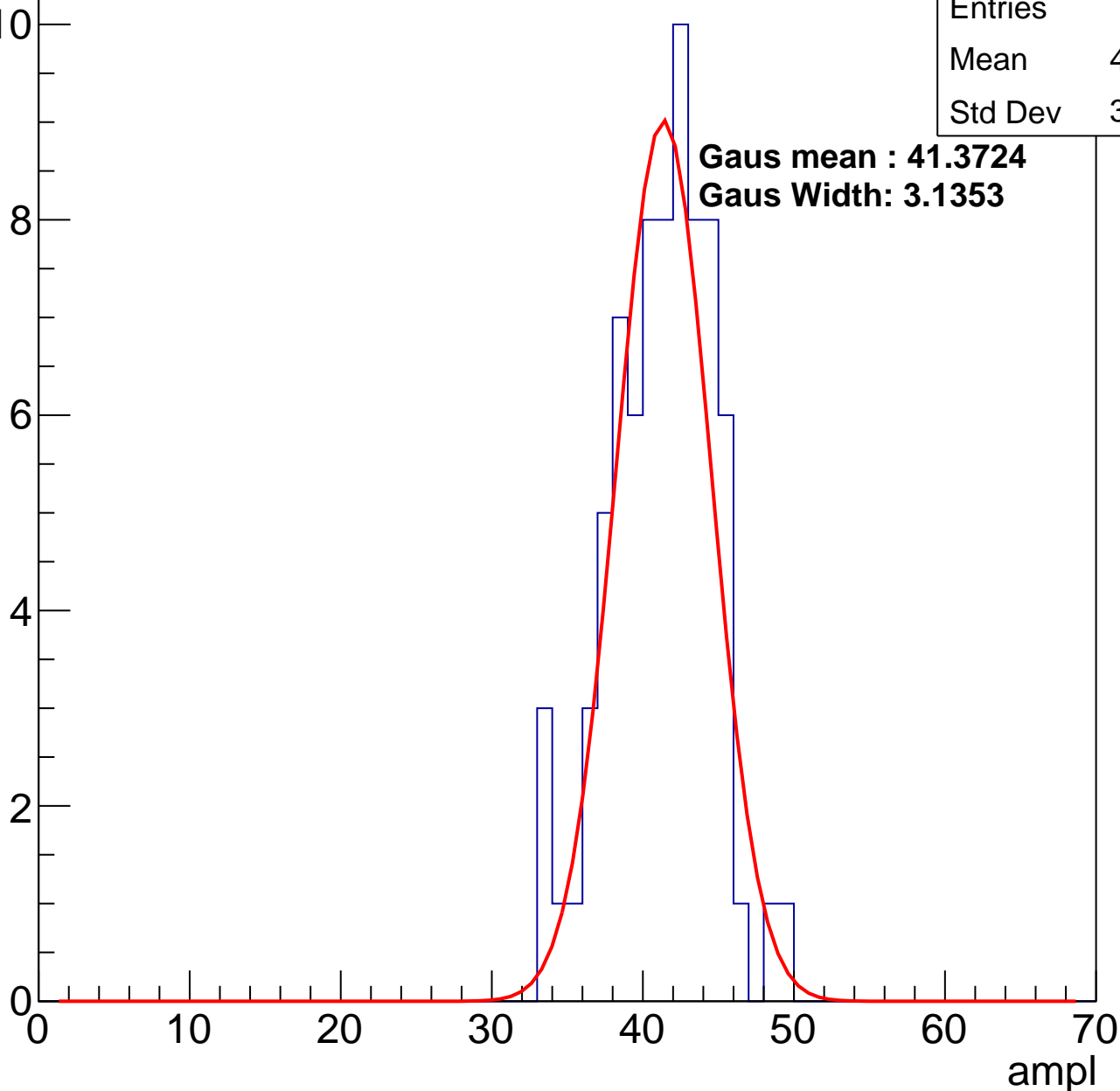
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 40.75 |
| Std Dev | 3.362 |

Gaus mean : 41.3724

Gaus Width: 3.1353

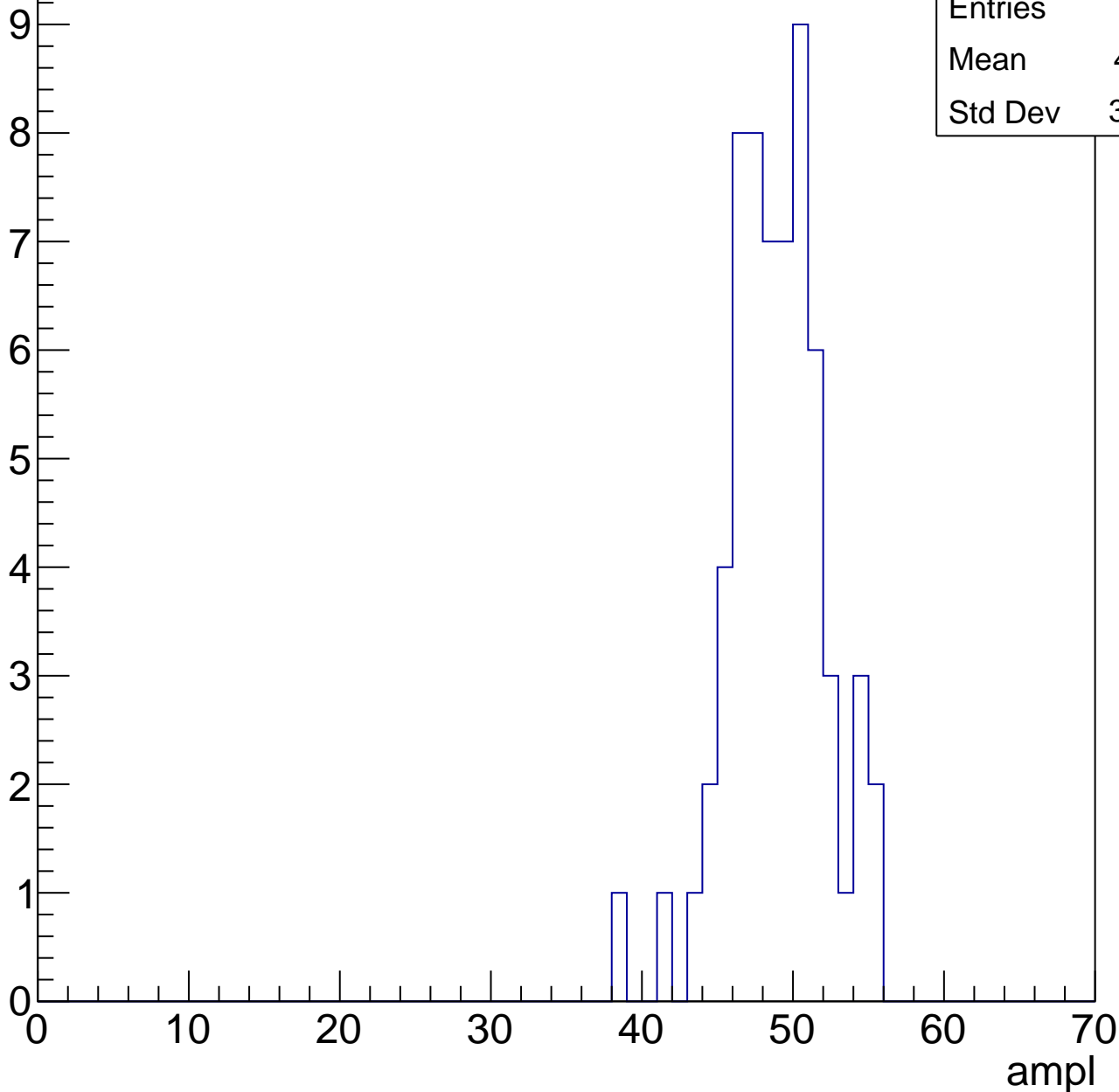


B1L102S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

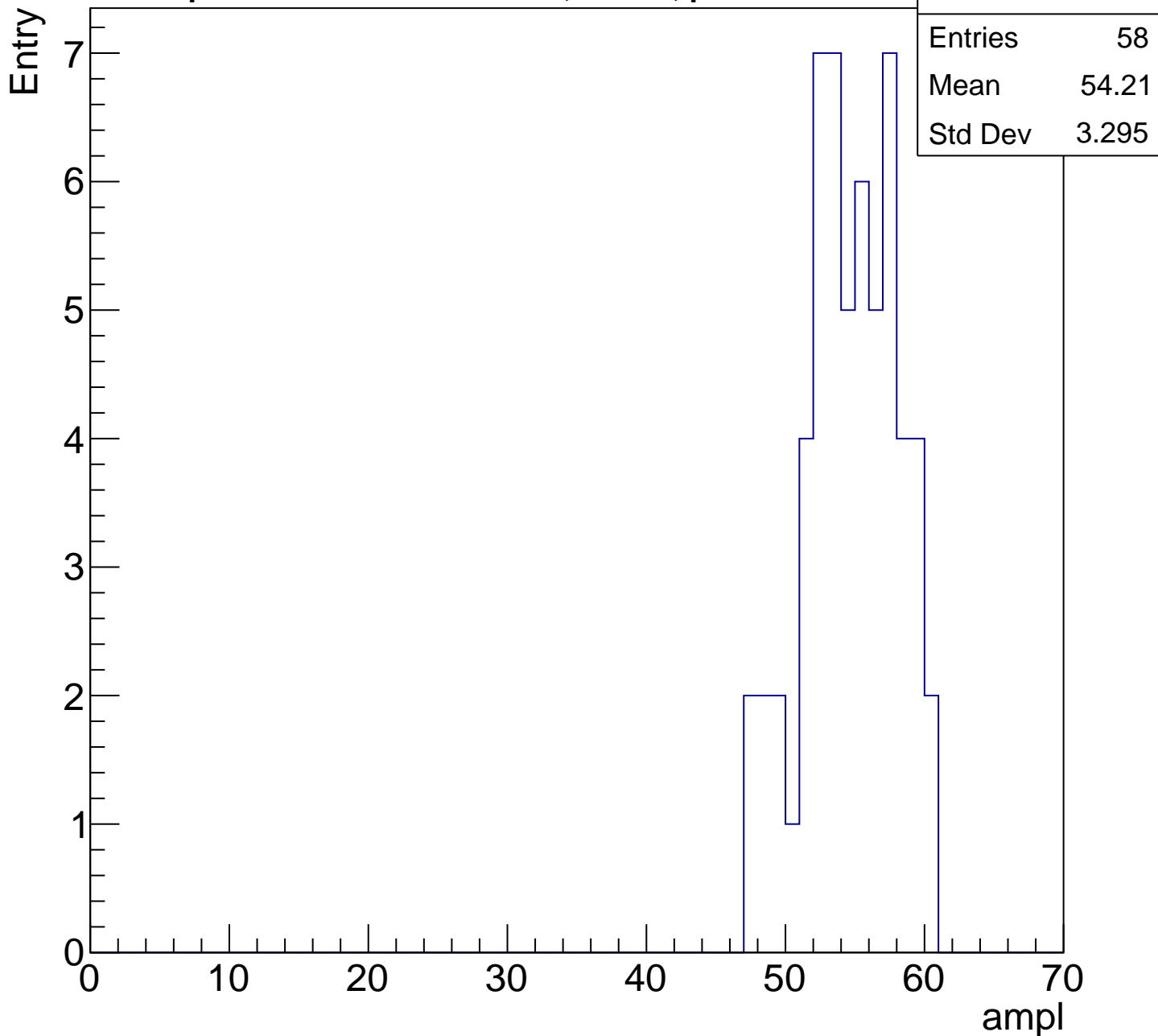
Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.41 |
| Std Dev | 3.205 |



B1L102S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch38, adc5

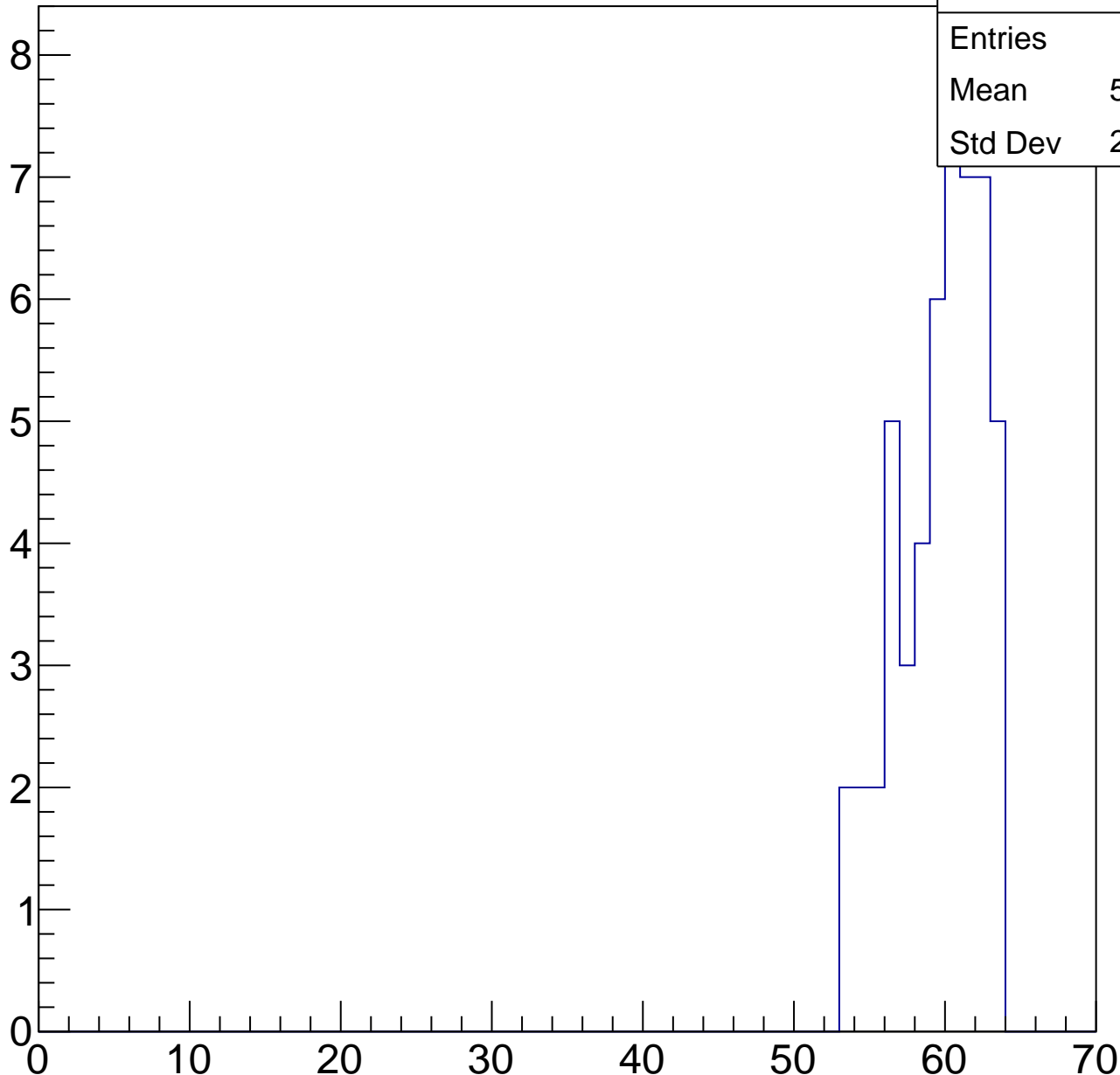
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 51 |
| Mean | 59.16 |
| Std Dev | 2.775 |

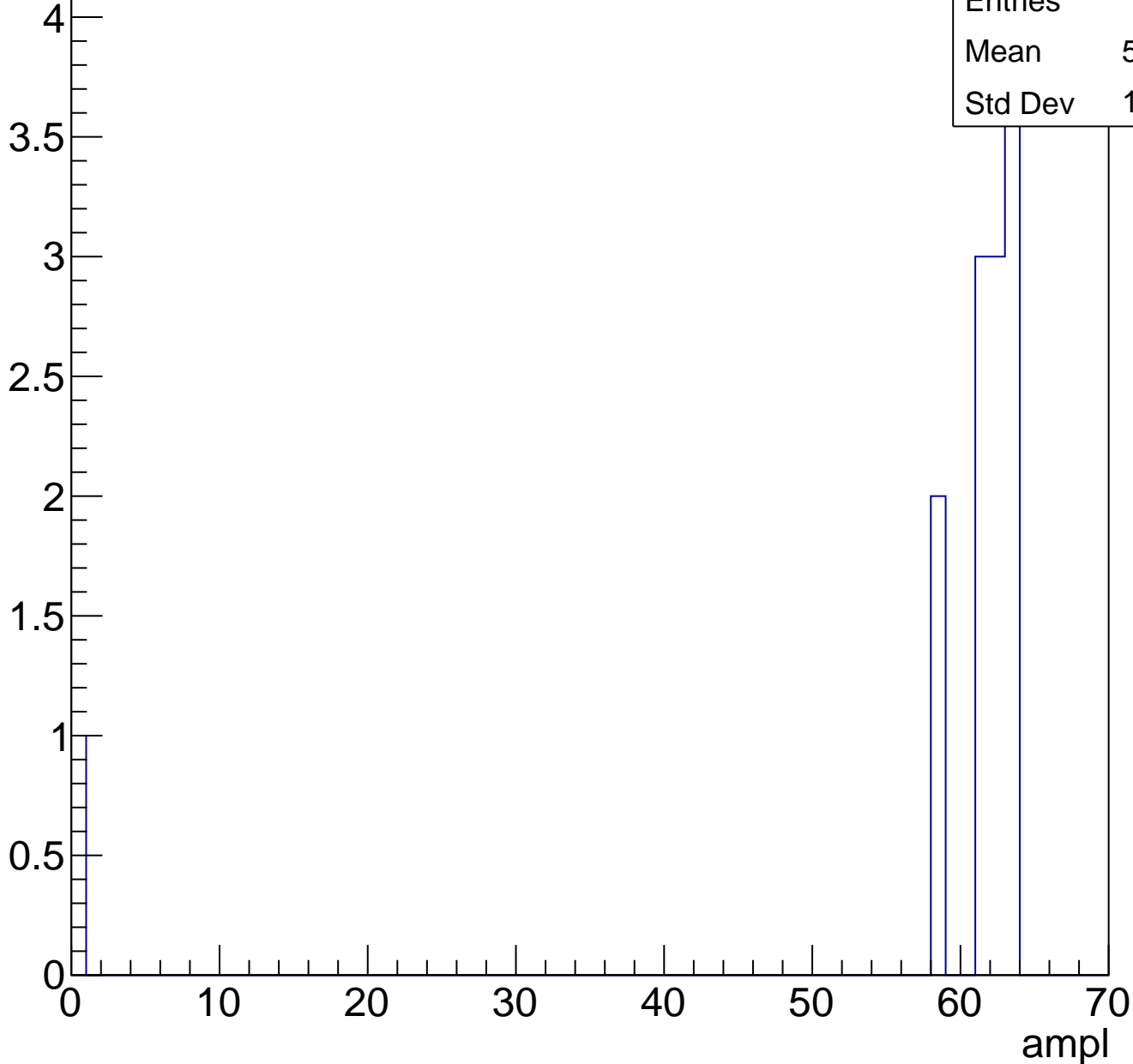
ampl



B1L102S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch39, adc0

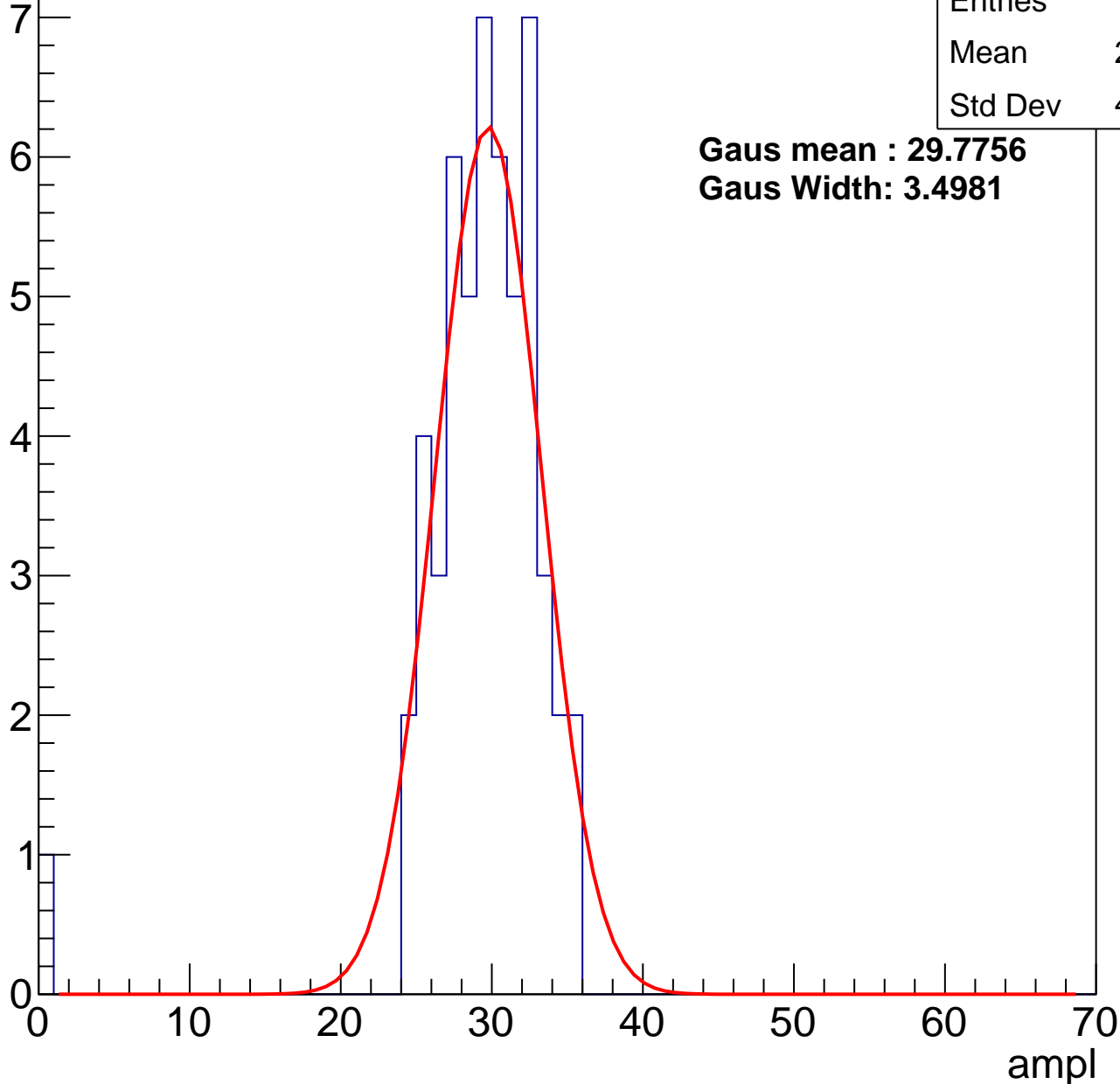
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 28.81 |
| Std Dev | 4.891 |

Gaus mean : 29.7756

Gaus Width: 3.4981



B1L102S, U15-ch39, adc1

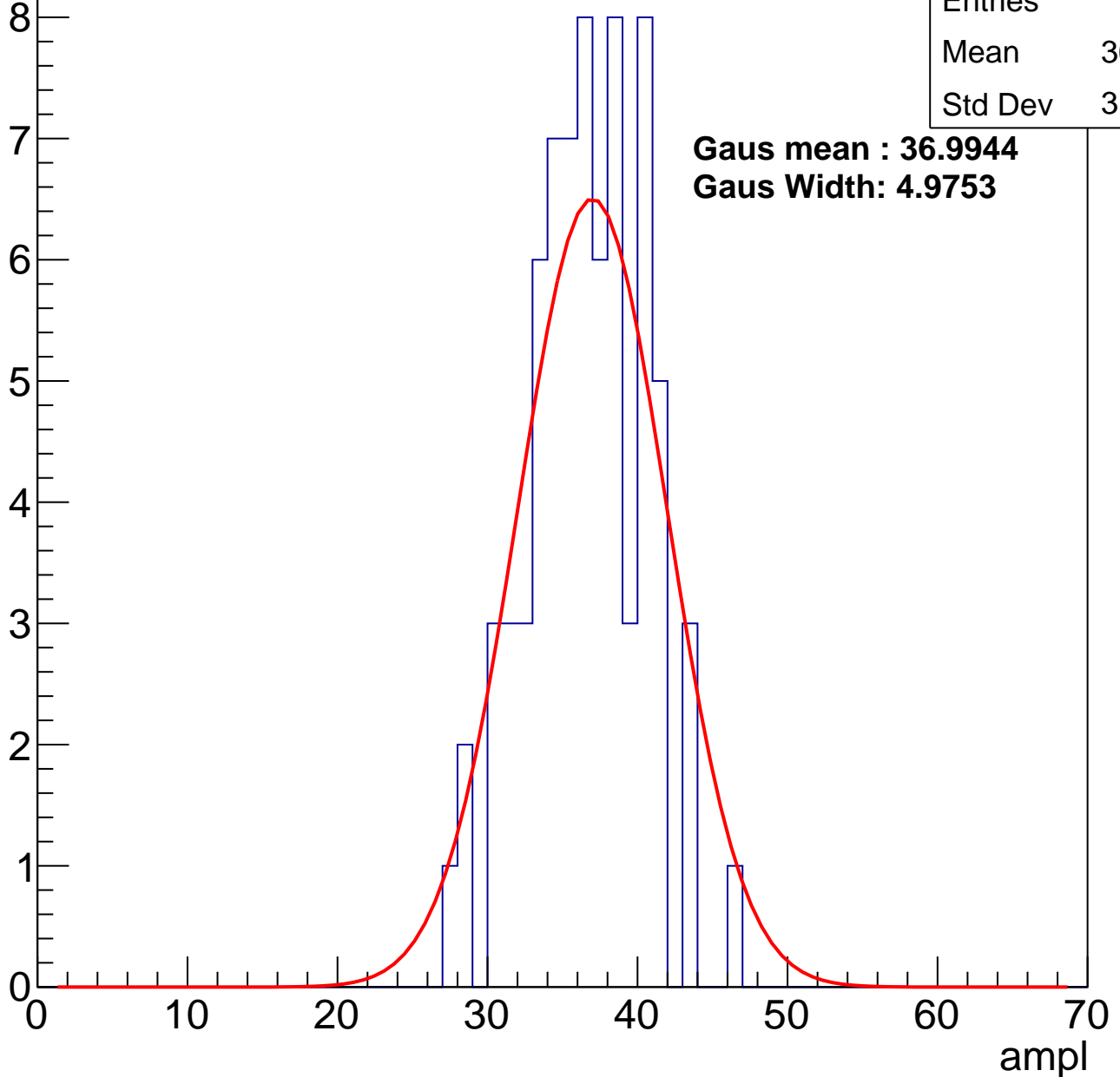
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 36.14 |
| Std Dev | 3.843 |

Gaus mean : 36.9944

Gaus Width: 4.9753



B1L102S, U15-ch39, adc2

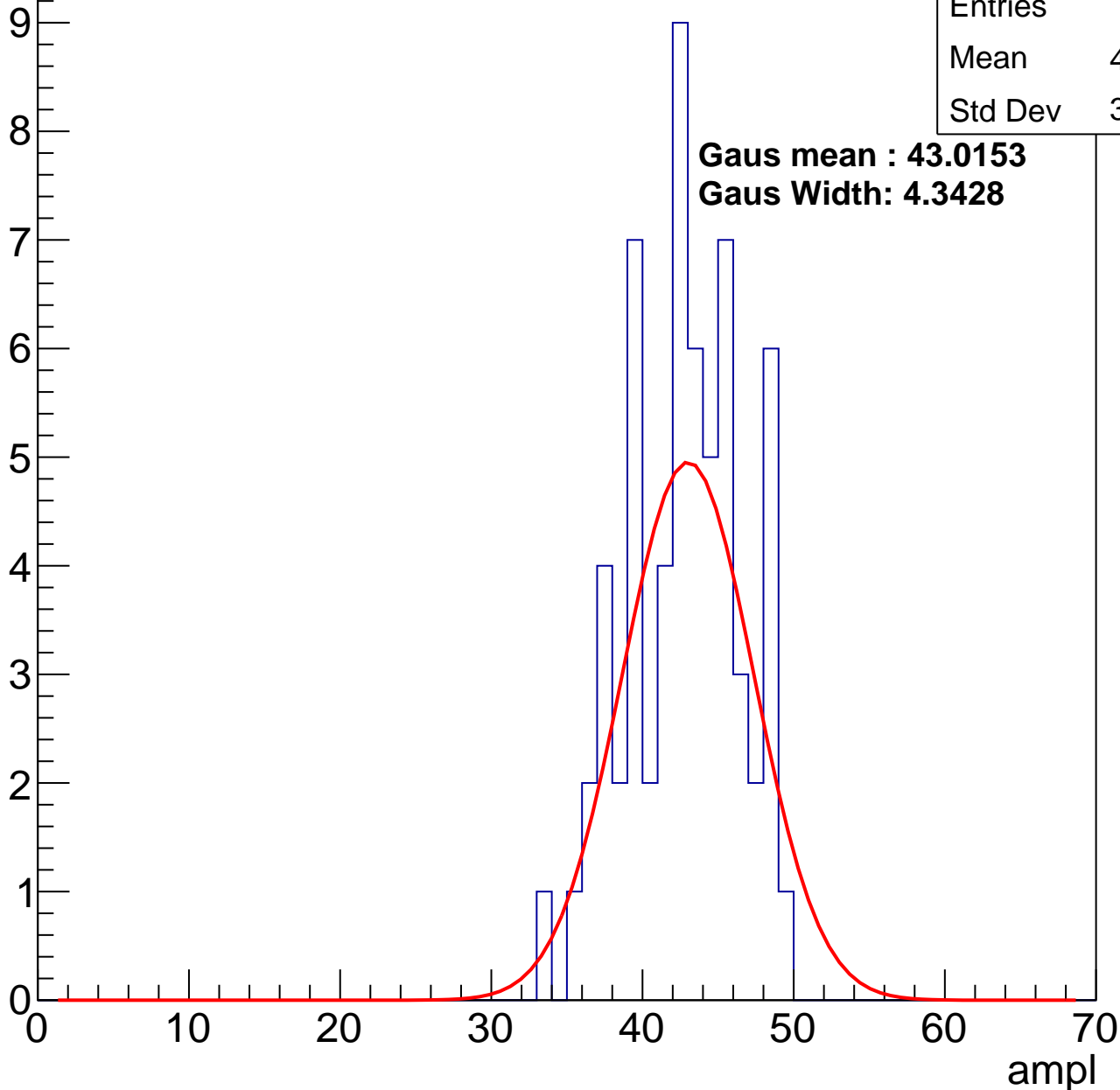
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.27 |
| Std Dev | 3.716 |

Gaus mean : 43.0153

Gaus Width: 4.3428

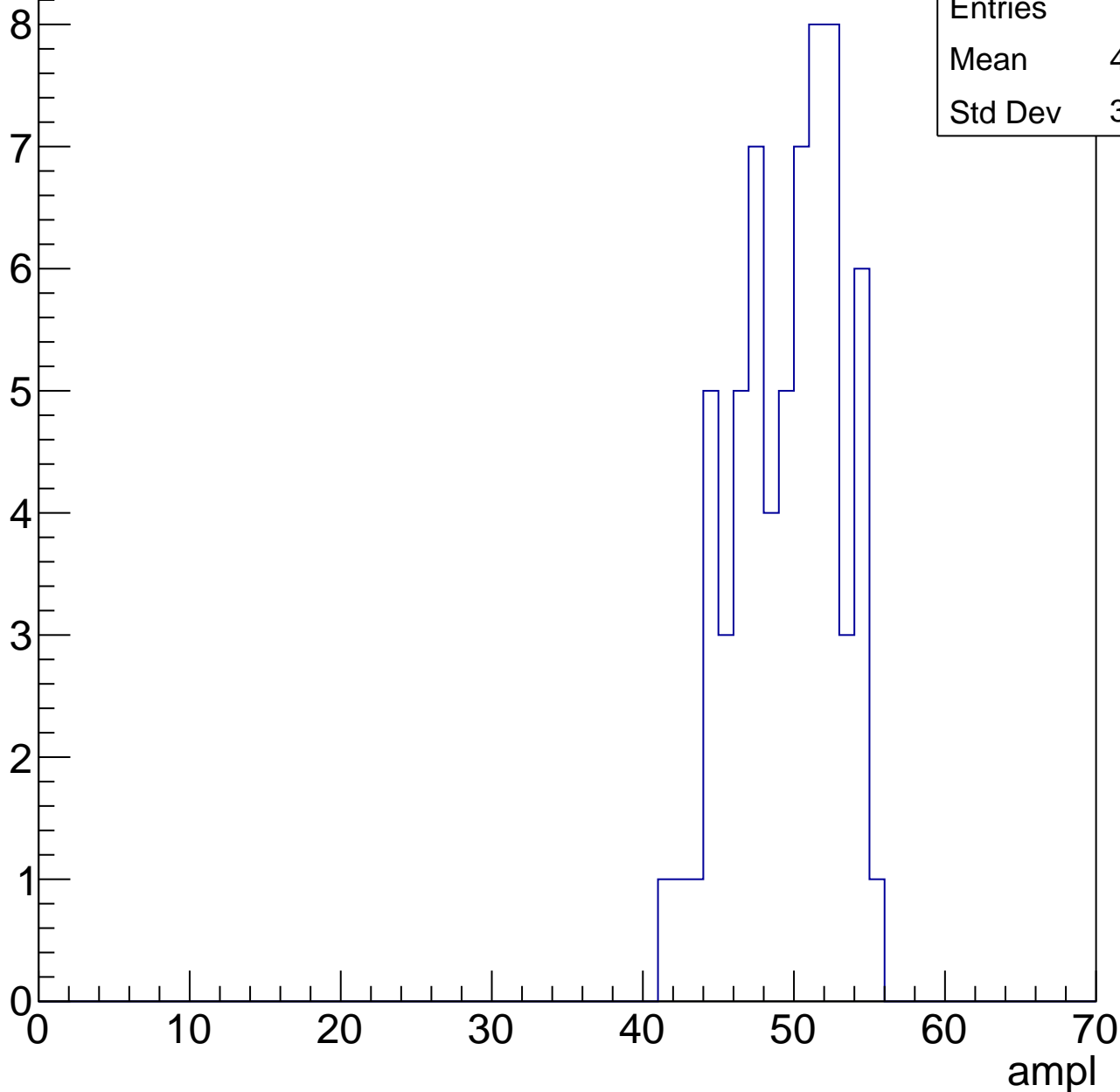


B1L102S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 49.06 |
| Std Dev | 3.383 |

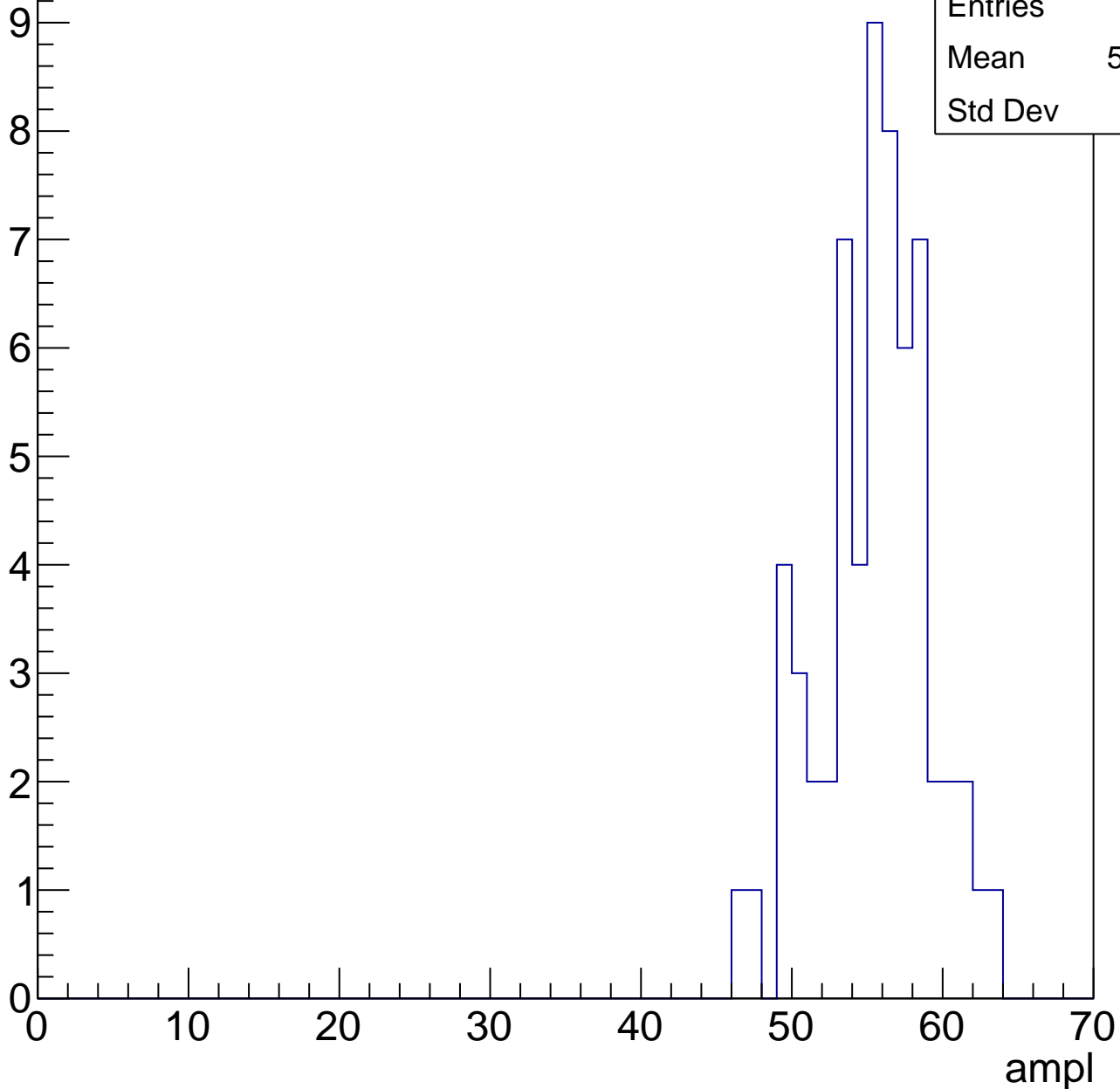


B1L102S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 54.97 |
| Std Dev | 3.61 |

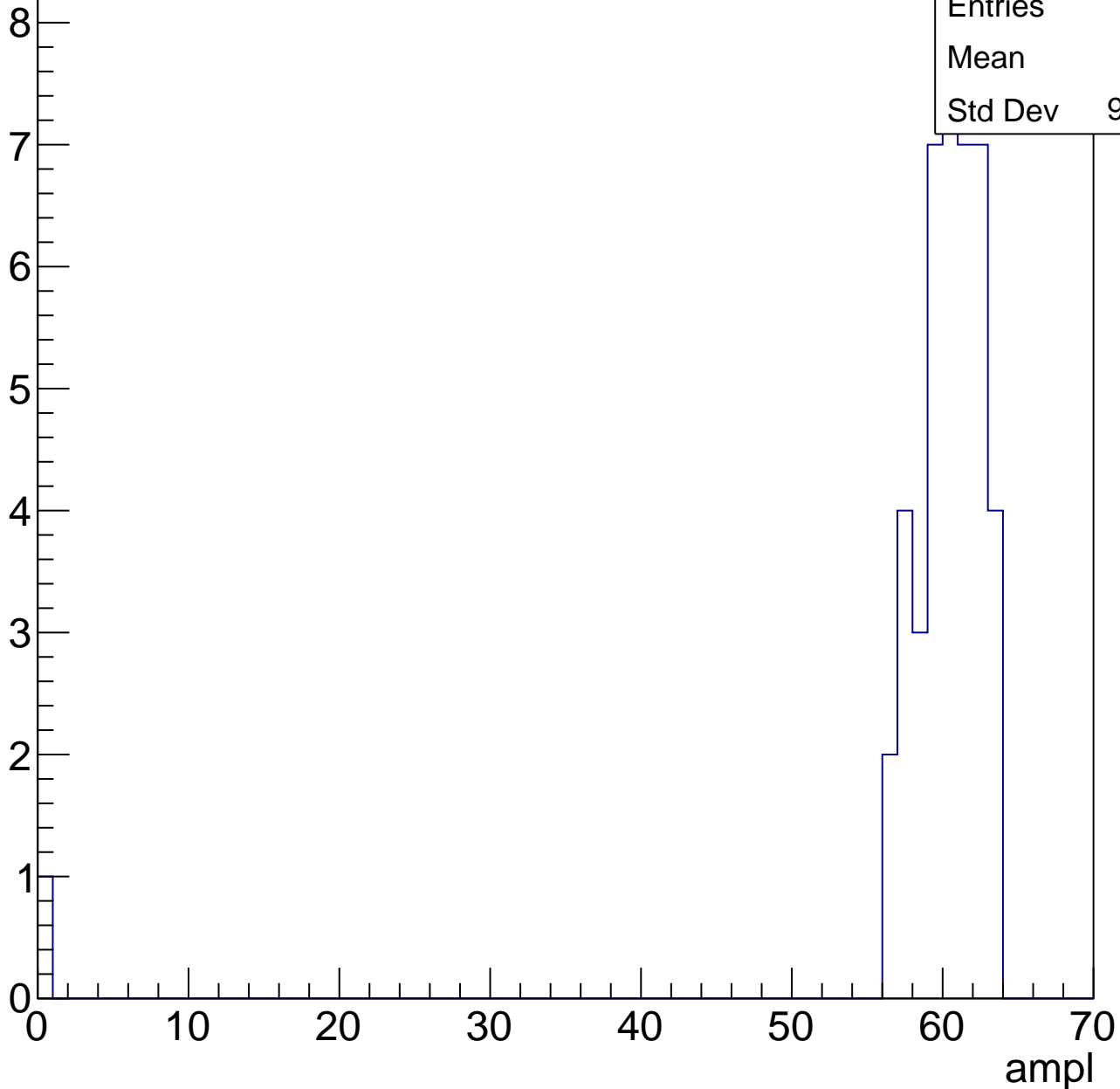


B1L102S, U15-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.6 |
| Std Dev | 9.244 |

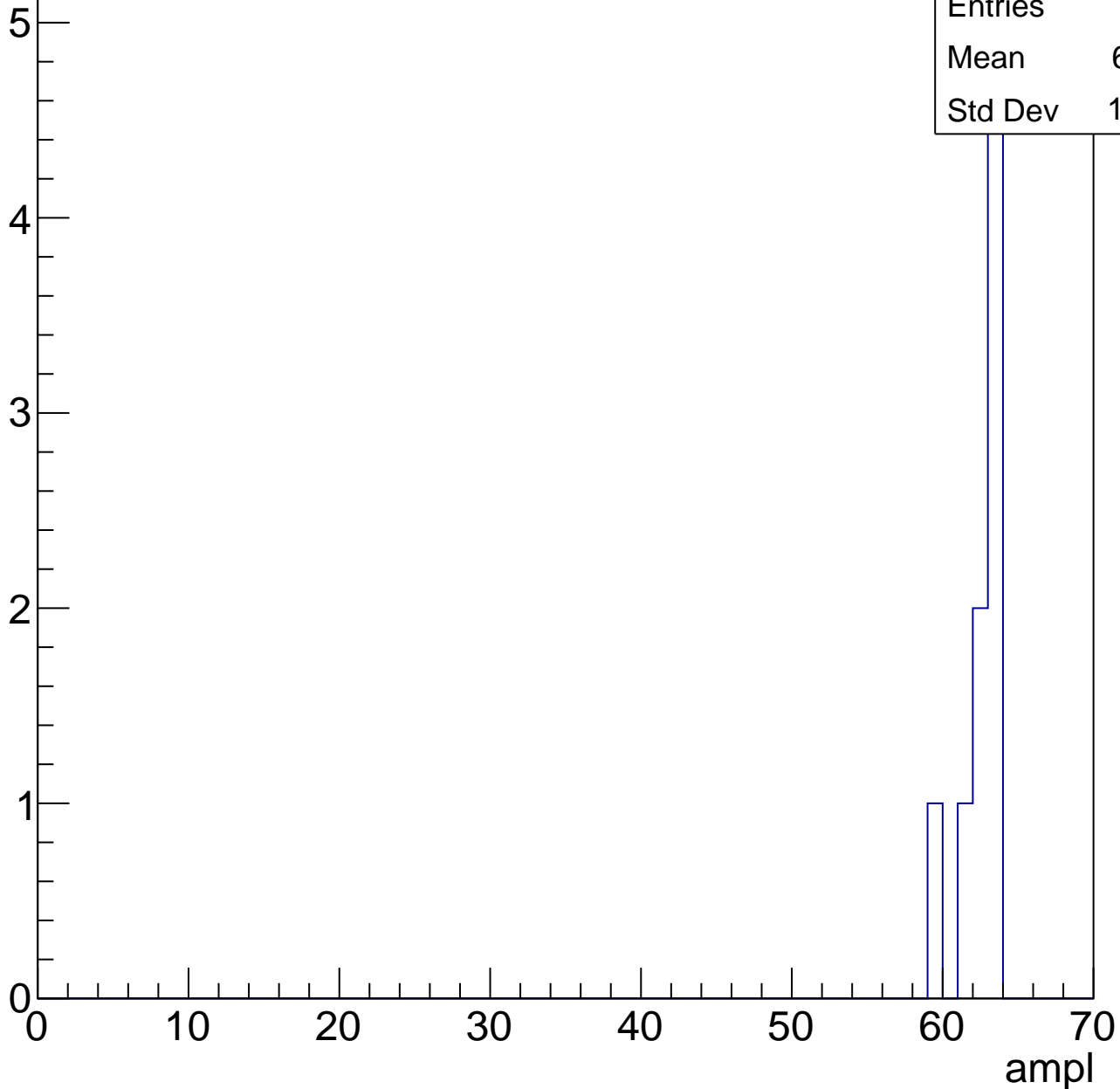


B1L102S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 62.11 |
| Std Dev | 1.286 |



B1L102S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch40, adc0

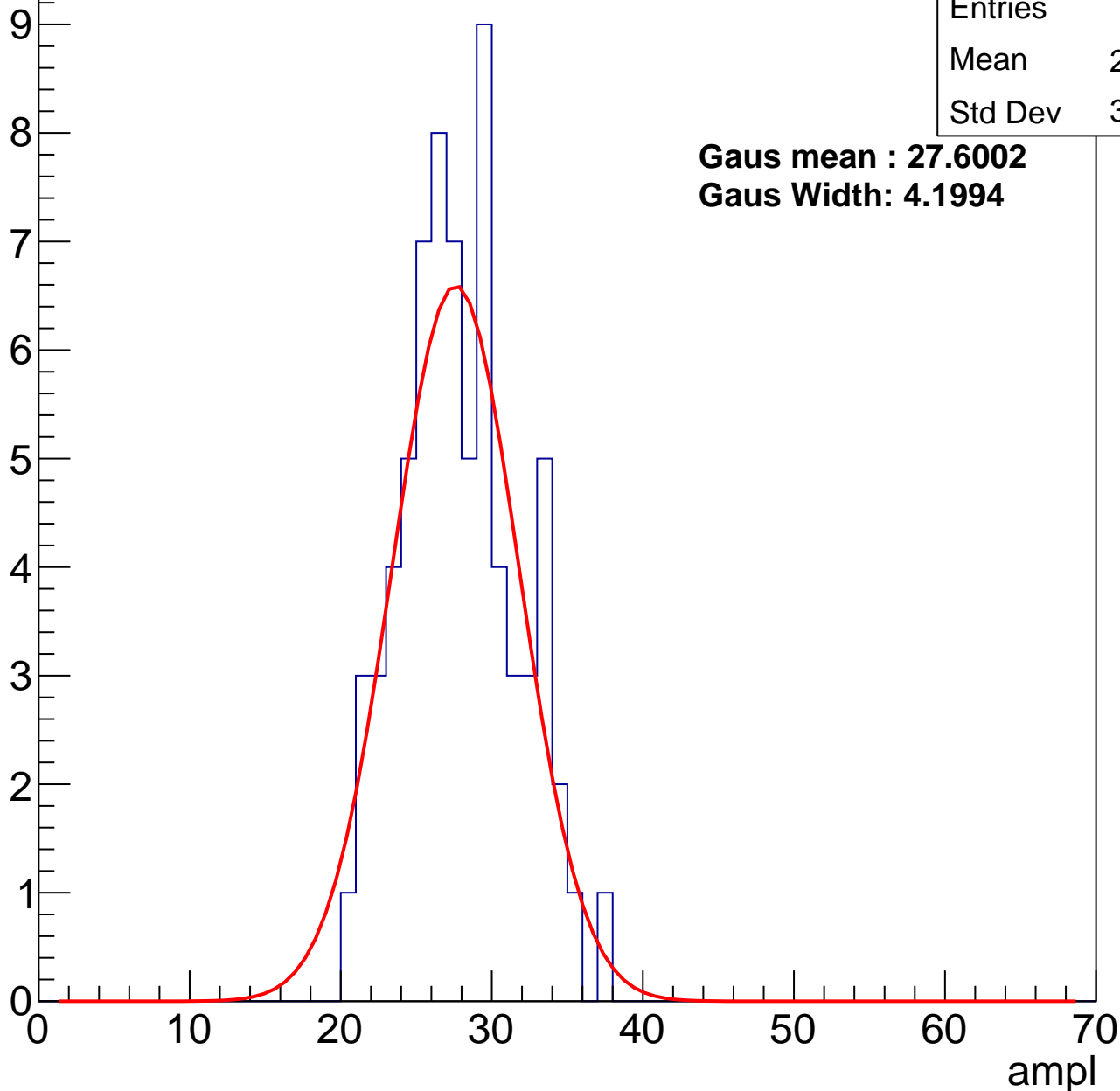
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 27.44 |
| Std Dev | 3.763 |

Gaus mean : 27.6002

Gaus Width: 4.1994



B1L102S, U15-ch40, adc1

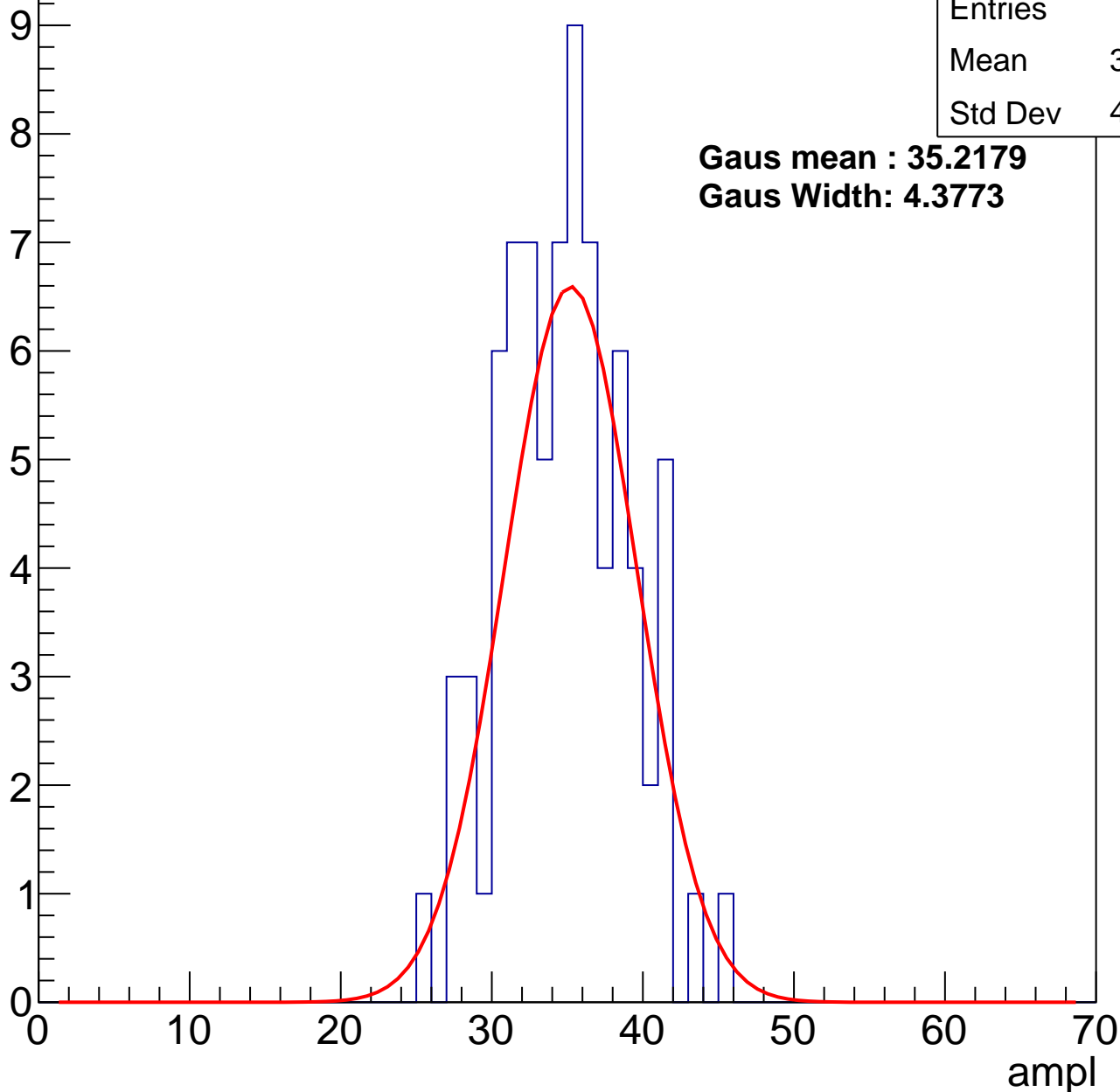
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 34.37 |
| Std Dev | 4.104 |

Gaus mean : 35.2179

Gaus Width: 4.3773



B1L102S, U15-ch40, adc2

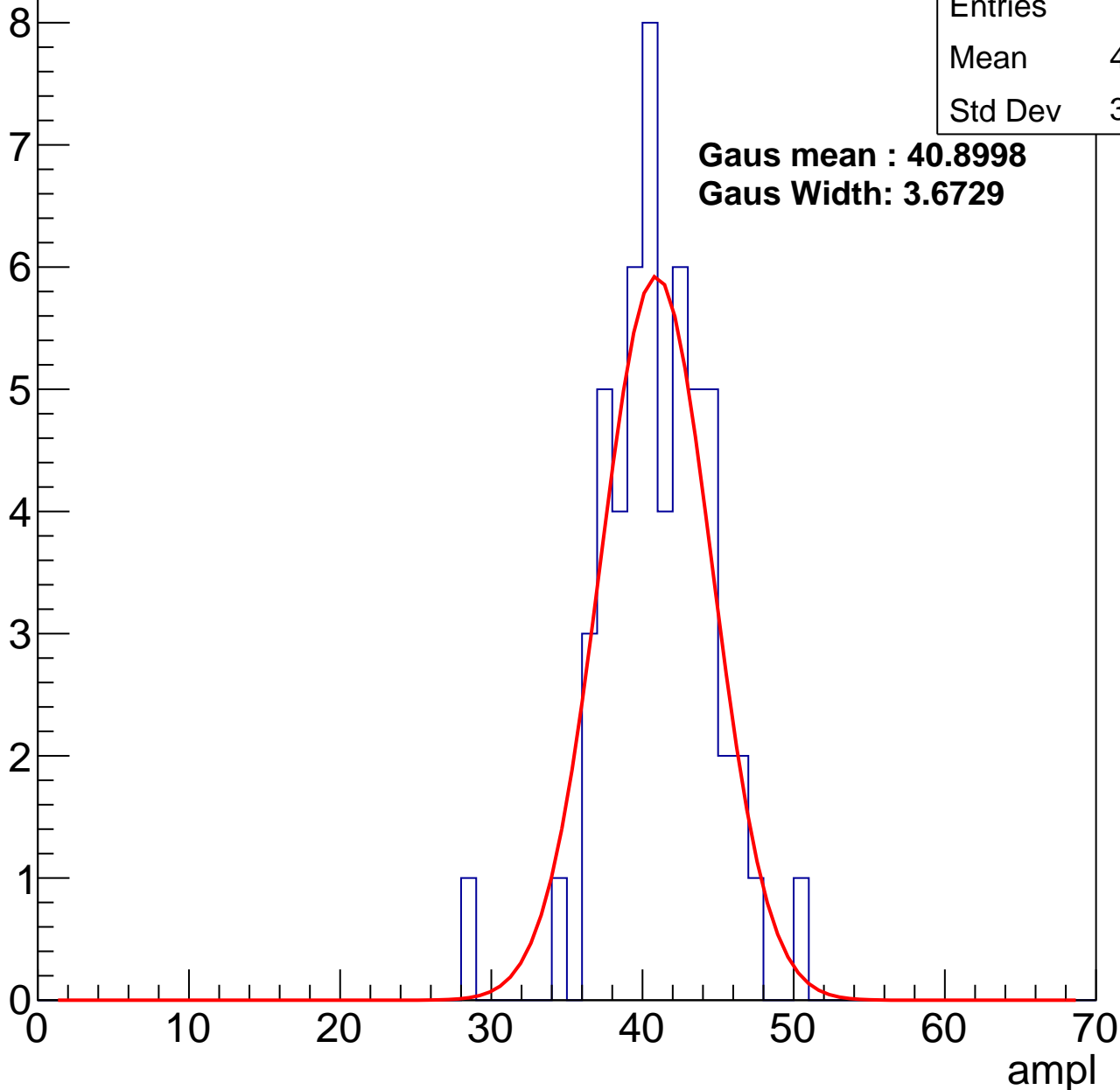
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 40.57 |
| Std Dev | 3.604 |

Gaus mean : 40.8998

Gaus Width: 3.6729

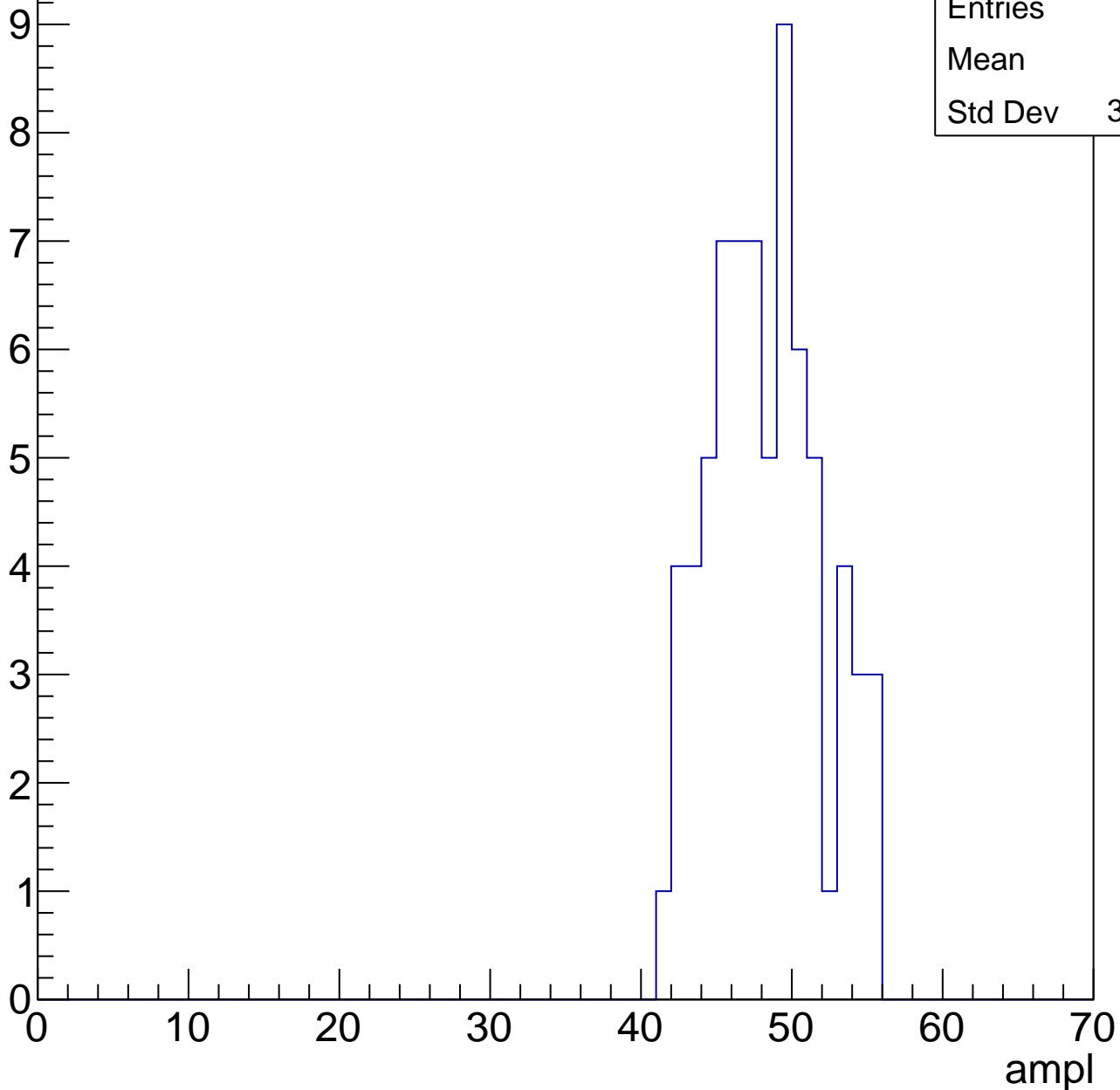


B1L102S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 47.8 |
| Std Dev | 3.594 |

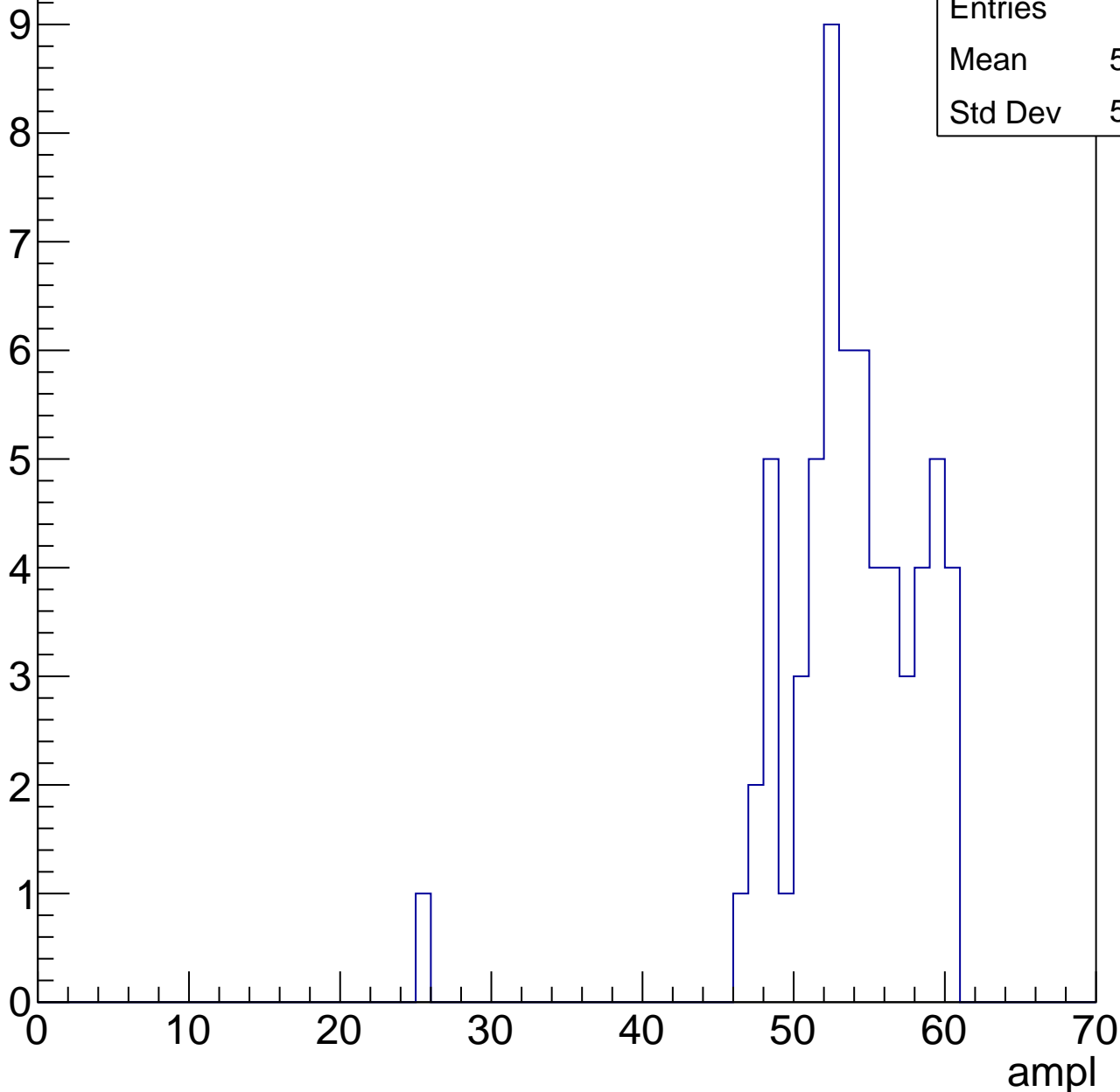


B1L102S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

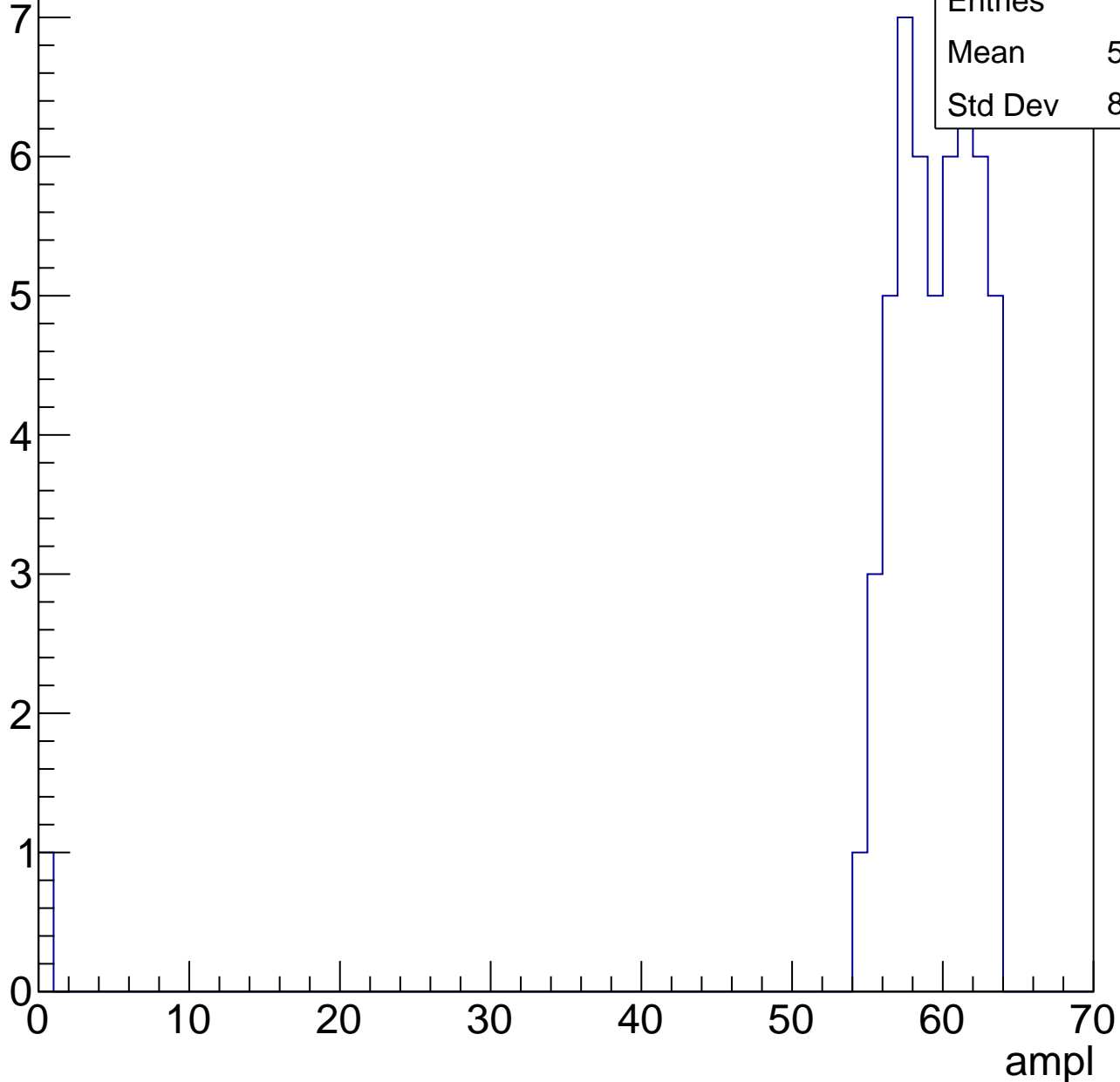
| | |
|---------|-------|
| Entries | 63 |
| Mean | 53.19 |
| Std Dev | 5.167 |



B1L102S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

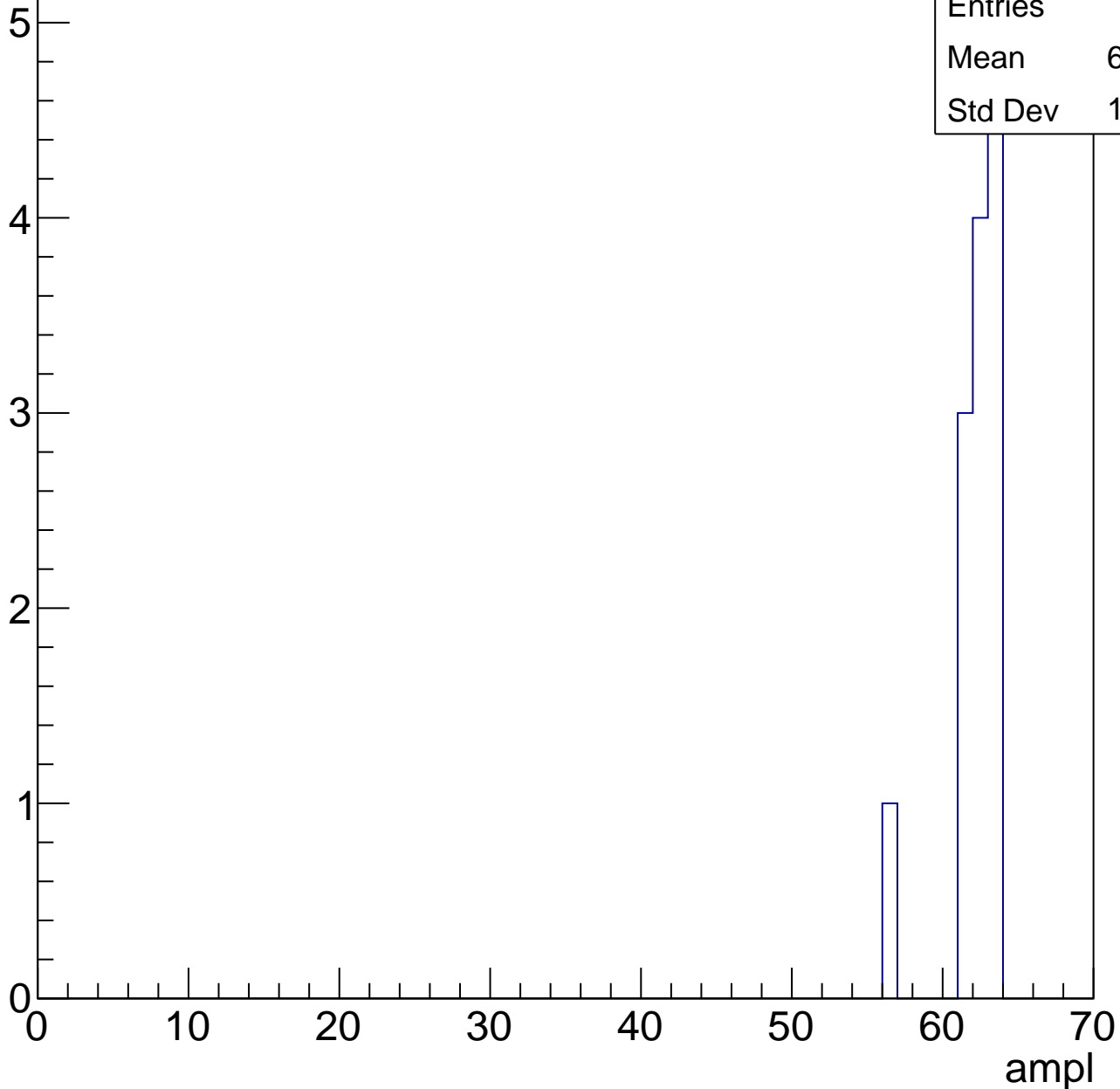


B1L102S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.69 |
| Std Dev | 1.814 |



B1L102S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch41, adc0

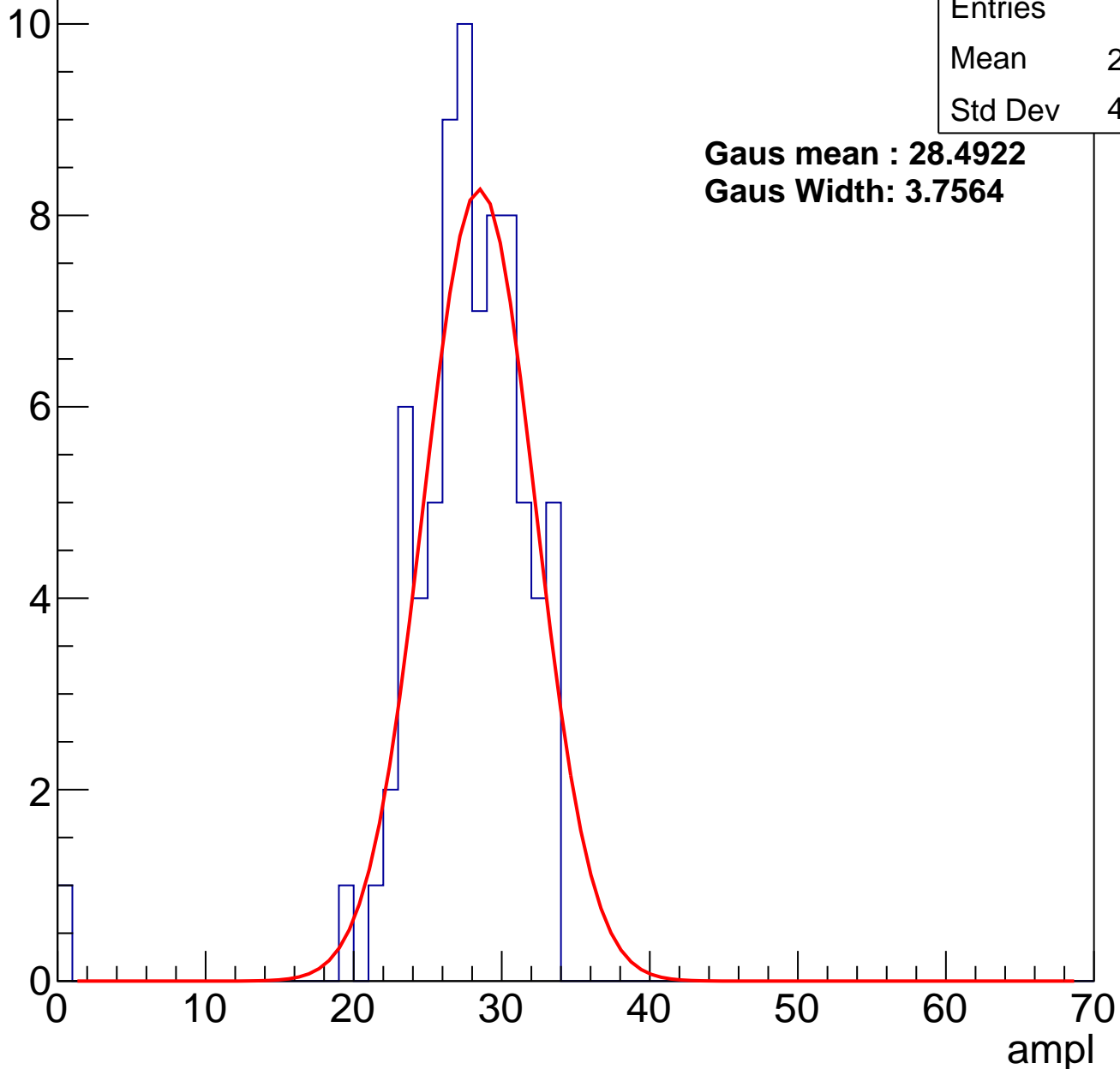
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 76 |
| Mean | 27.14 |
| Std Dev | 4.457 |

Gaus mean : 28.4922

Gaus Width: 3.7564

Entry



B1L102S, U15-ch41, adc1

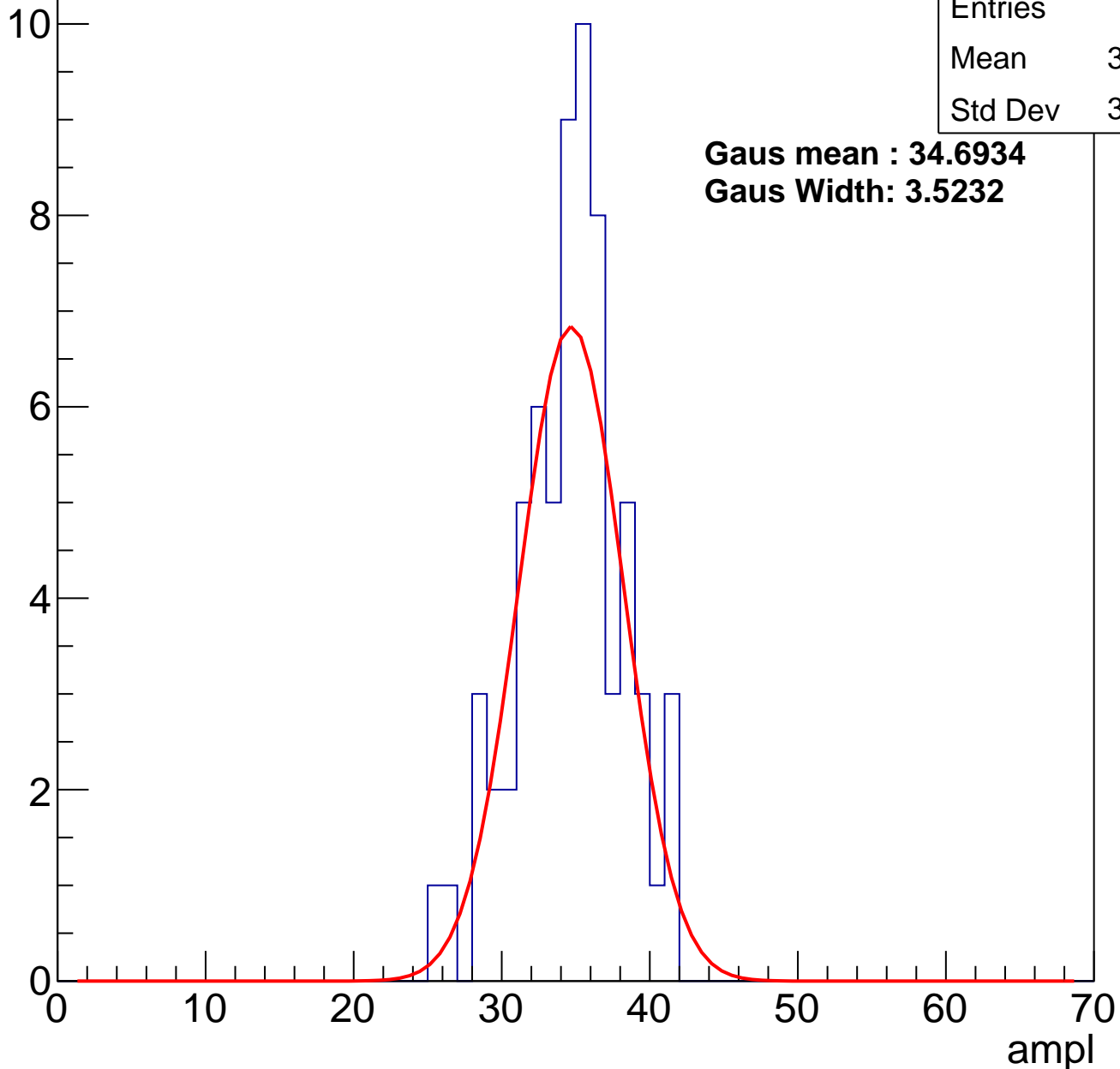
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 34.18 |
| Std Dev | 3.485 |

Gaus mean : 34.6934

Gaus Width: 3.5232

Entry



B1L102S, U15-ch41, adc2

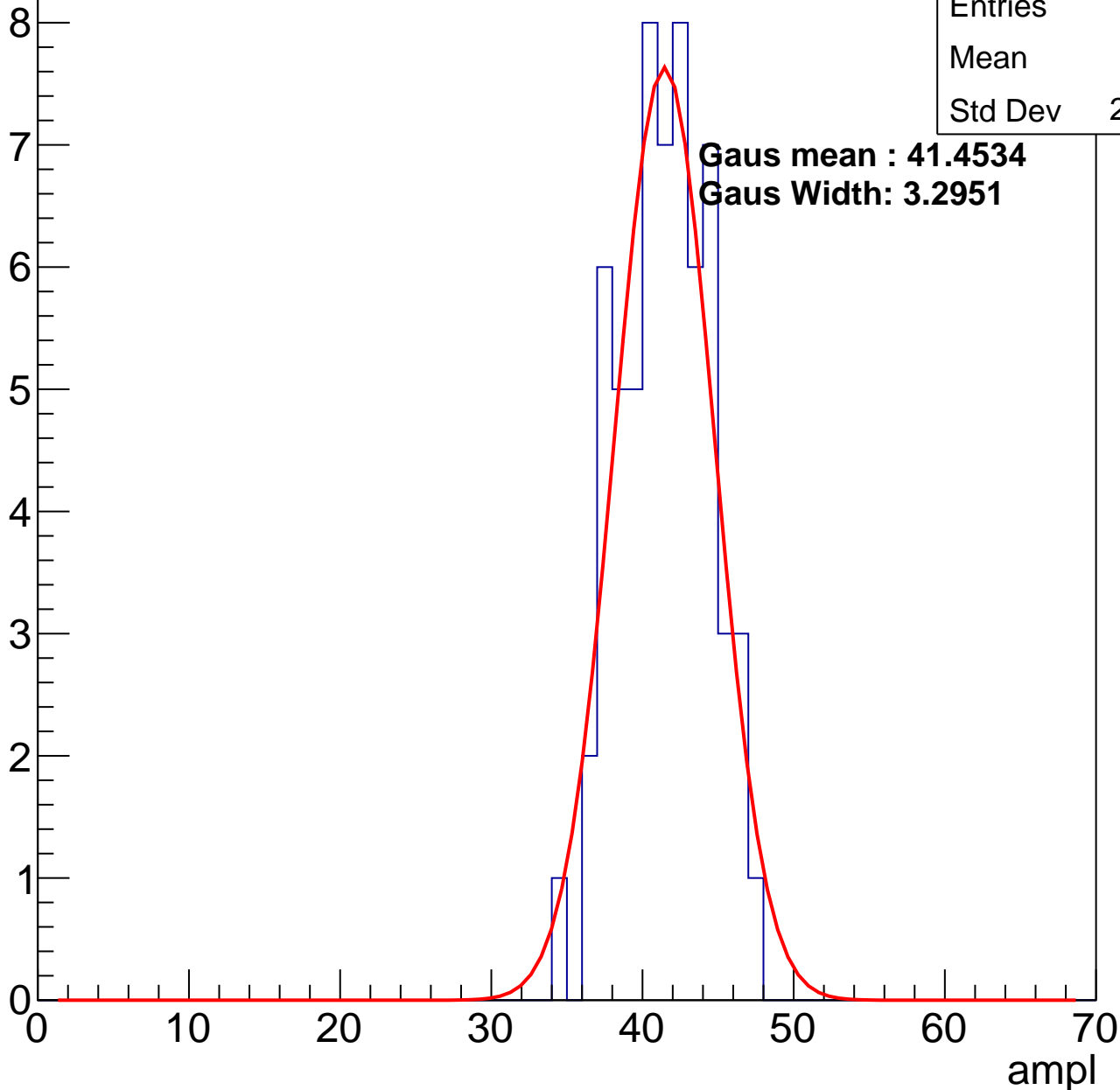
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41 |
| Std Dev | 2.902 |

Gaus mean : 41.4534

Gaus Width: 3.2951

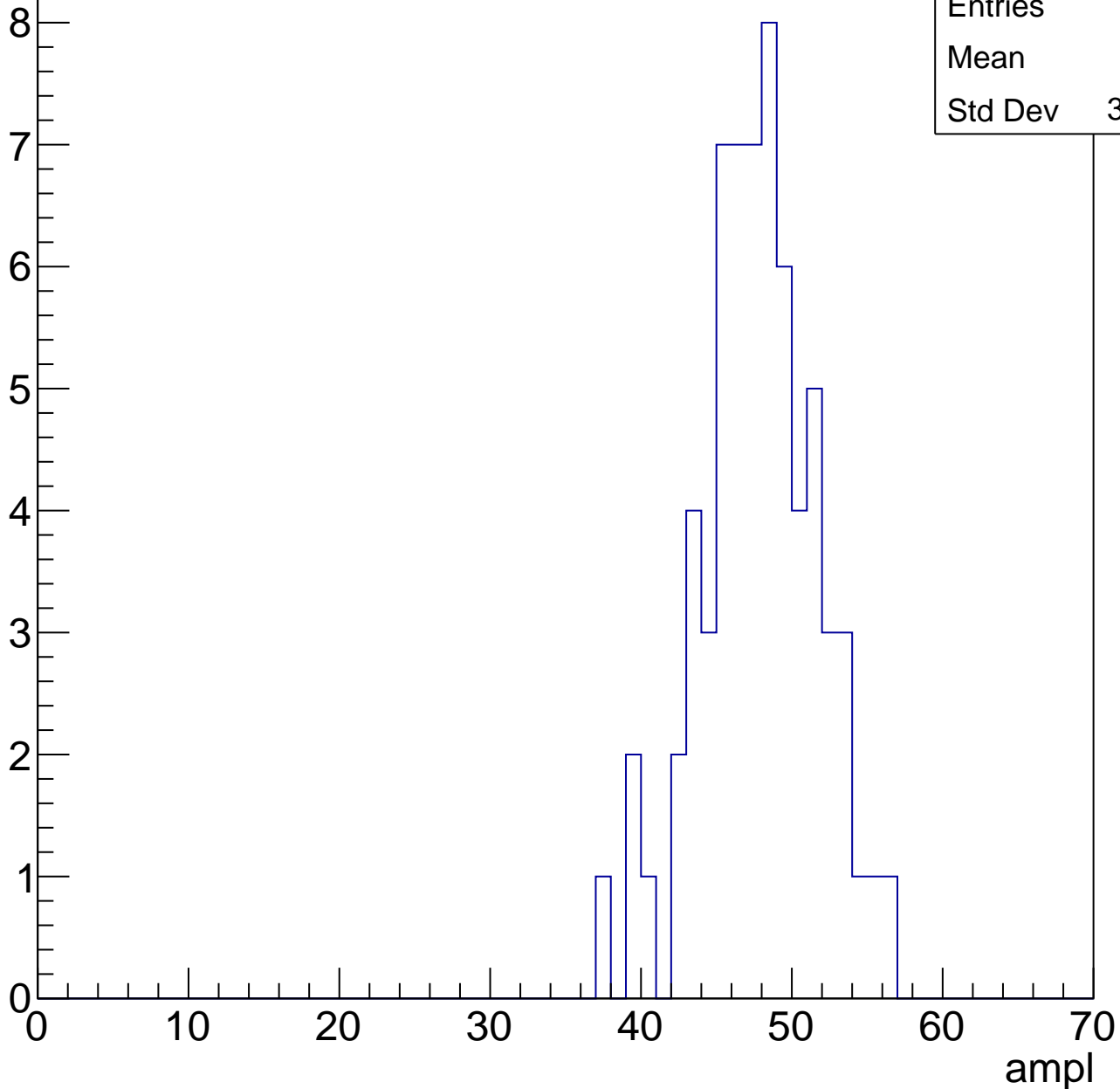


B1L102S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 47.3 |
| Std Dev | 3.837 |

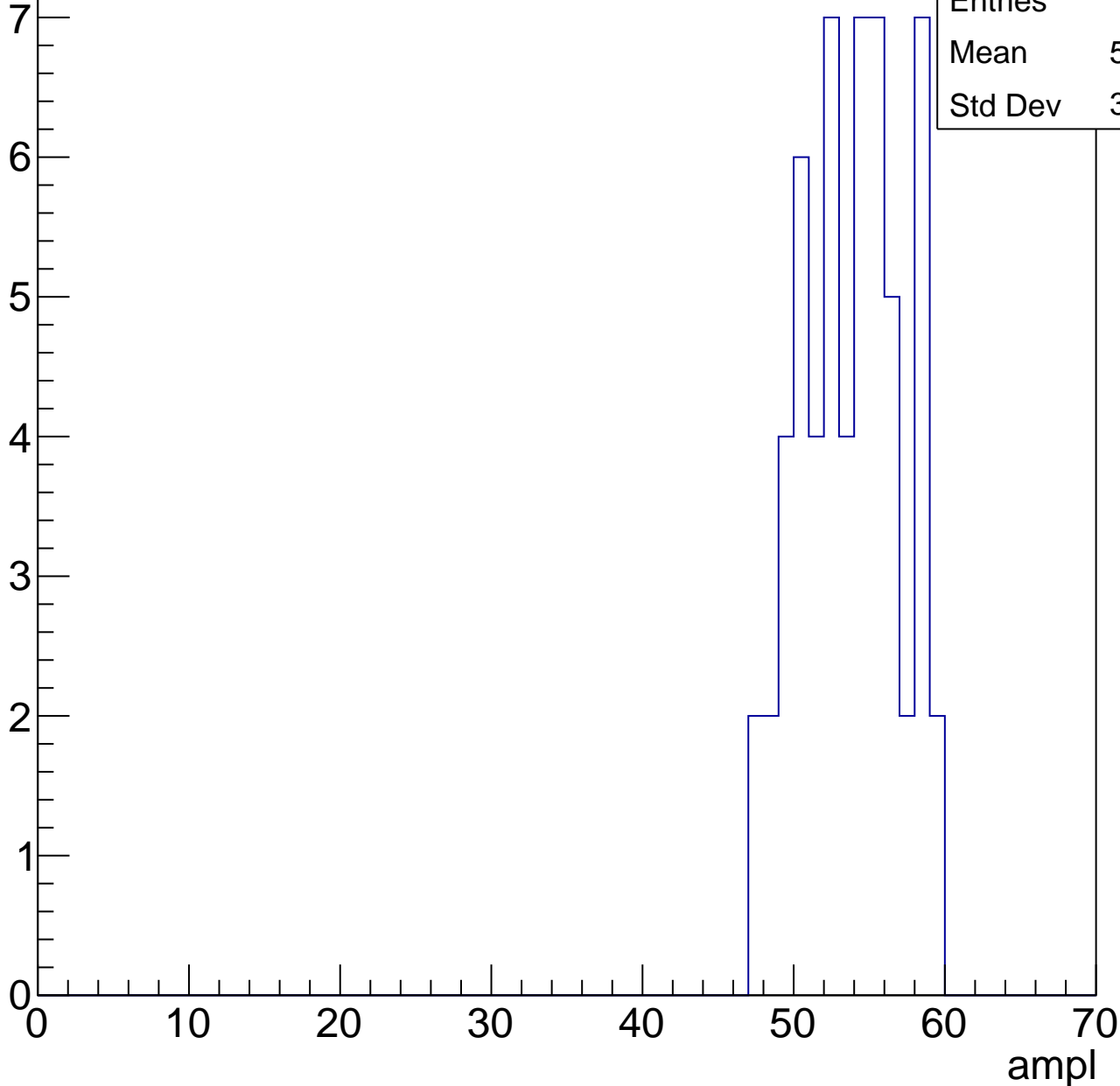


B1L102S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

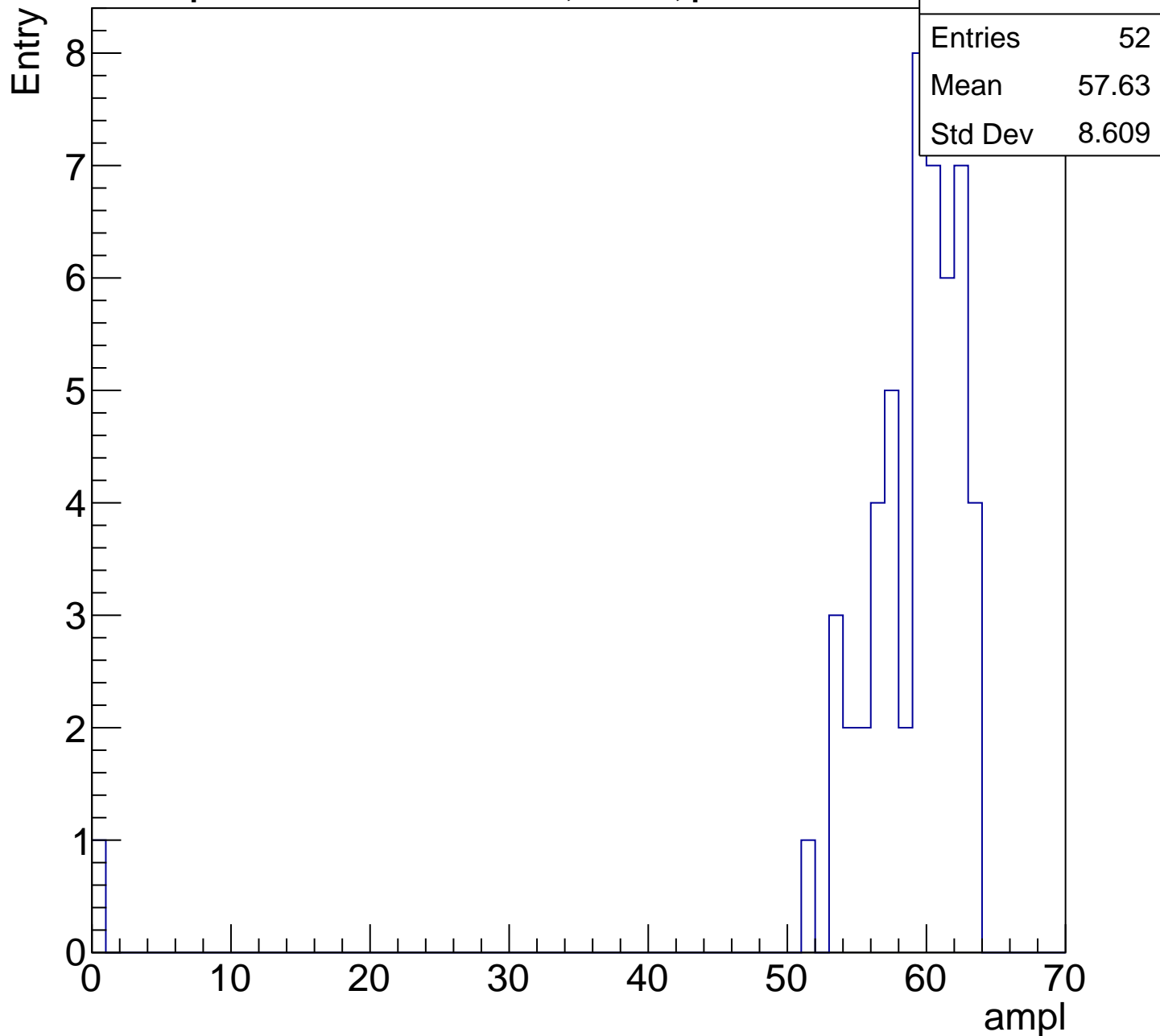
Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 53.34 |
| Std Dev | 3.229 |



B1L102S, U15-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

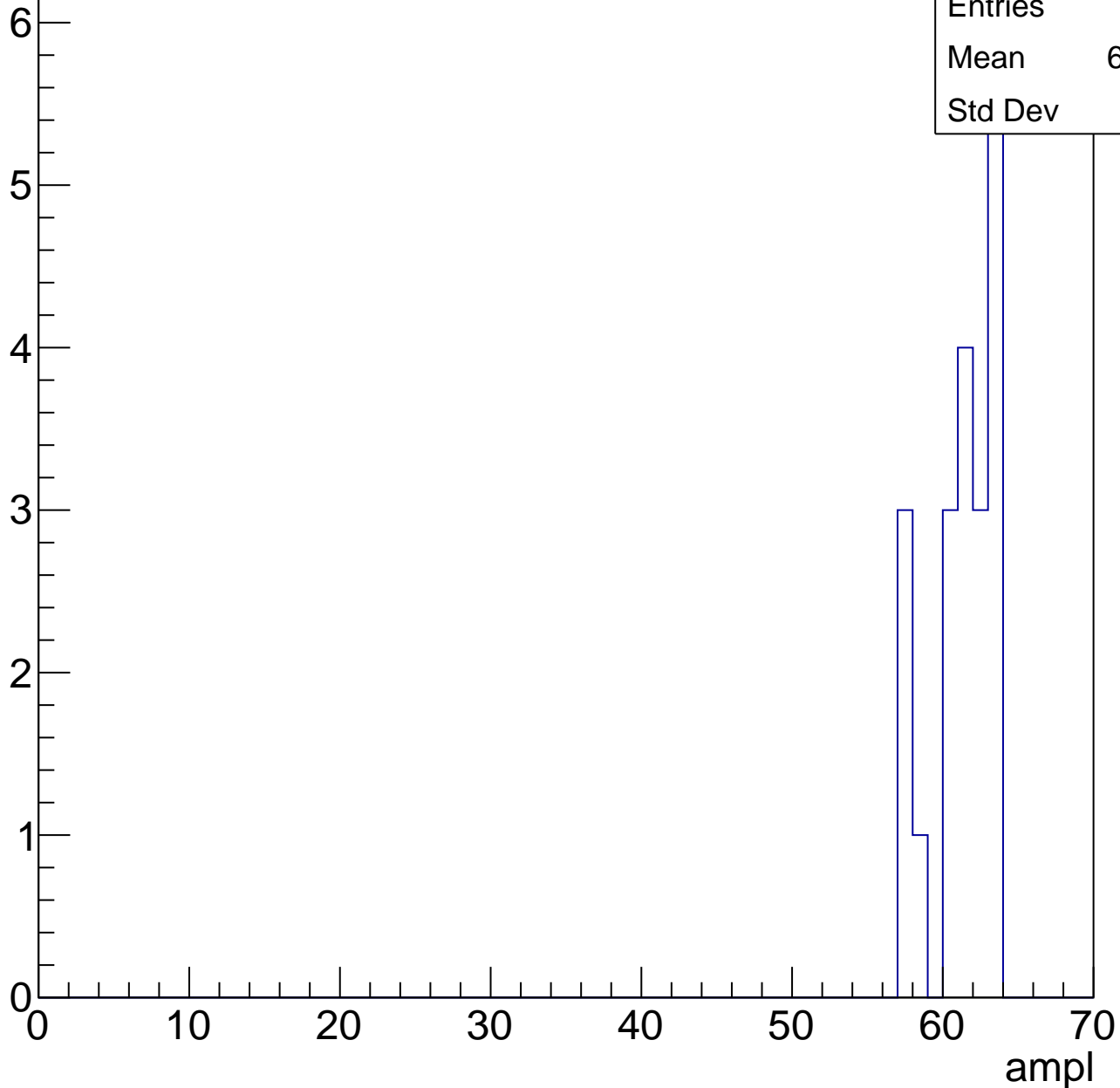


B1L102S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 20 |
| Mean | 60.85 |
| Std Dev | 2.08 |



B1L102S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 9 |
| Std Dev | 9 |

B1L102S, U15-ch42, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 85 |
| Mean | 27.36 |
| Std Dev | 5.177 |

Gaus mean : 28.0397

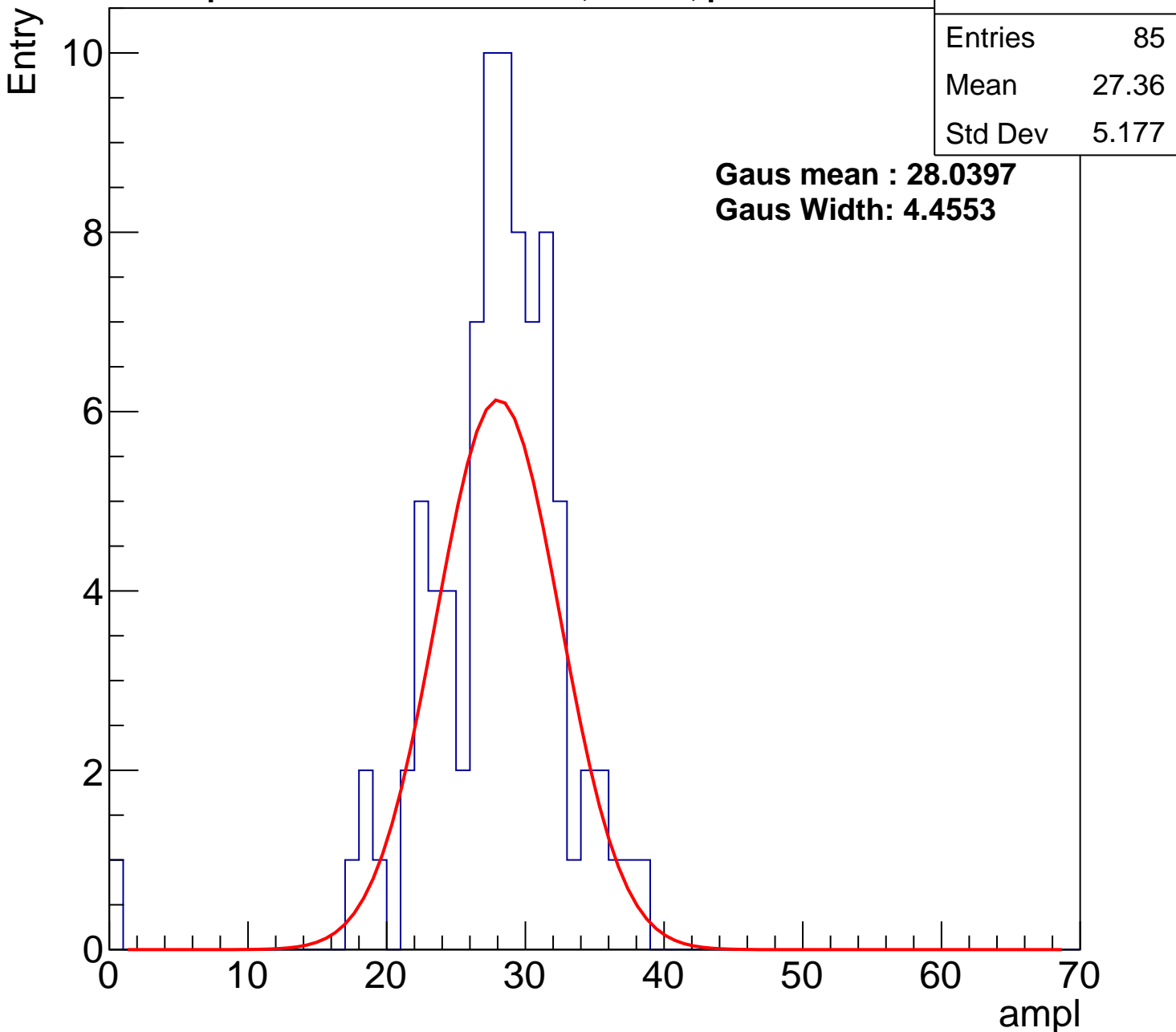
Gaus Width: 4.4553

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch42, adc1

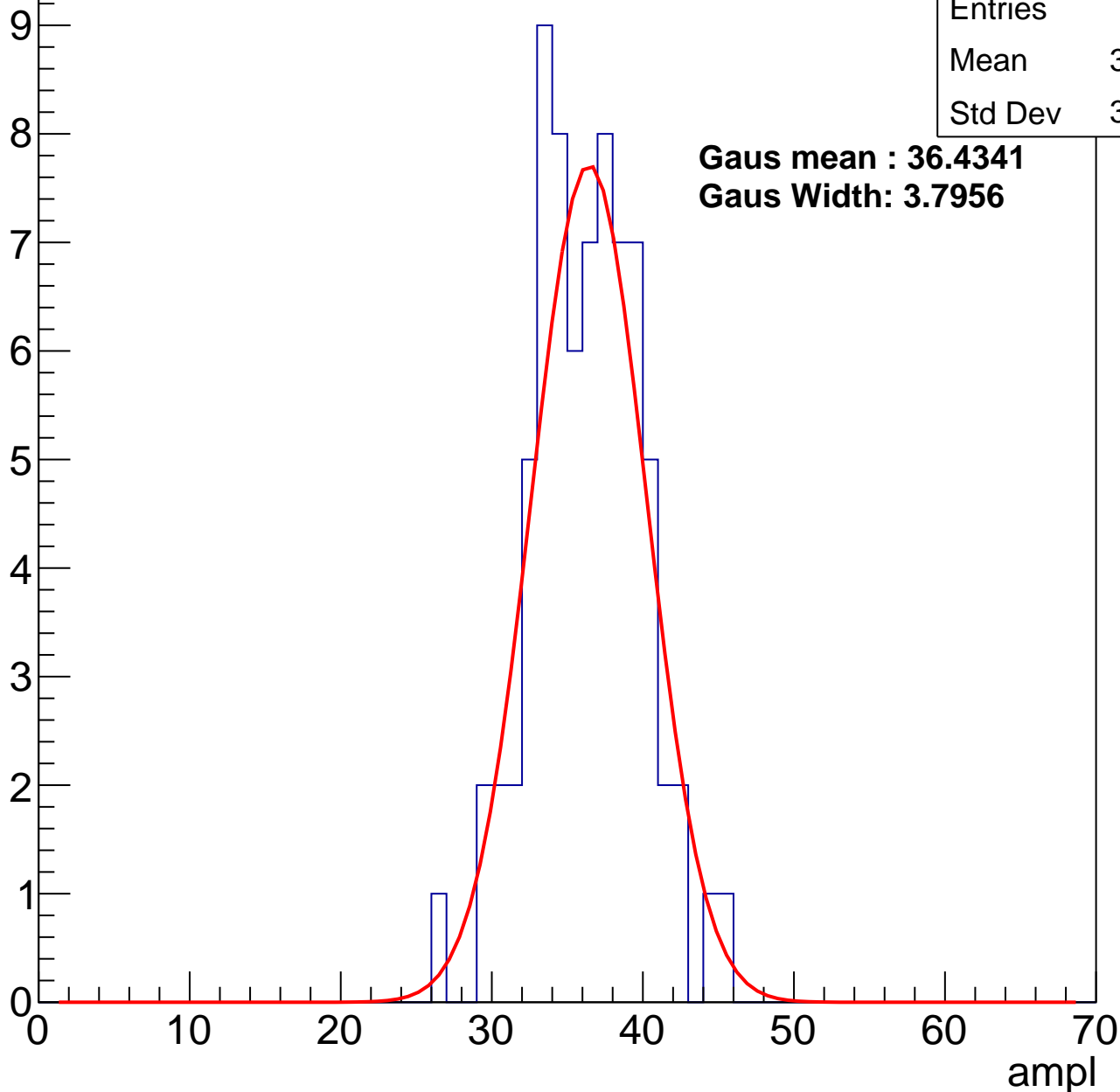
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 35.83 |
| Std Dev | 3.583 |

Gaus mean : 36.4341

Gaus Width: 3.7956



B1L102S, U15-ch42, adc2

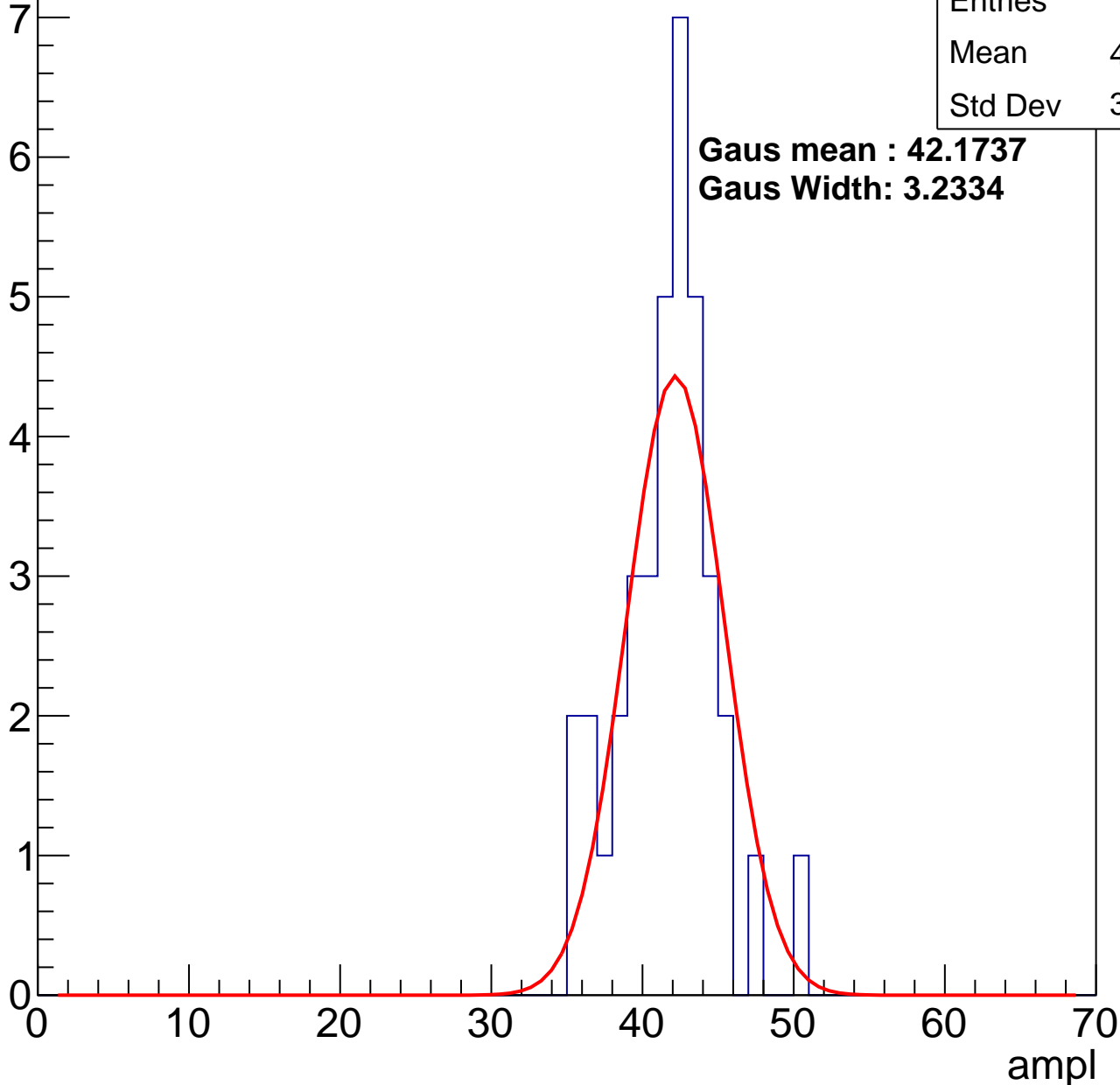
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 41.22 |
| Std Dev | 3.172 |

Gaus mean : 42.1737

Gaus Width: 3.2334

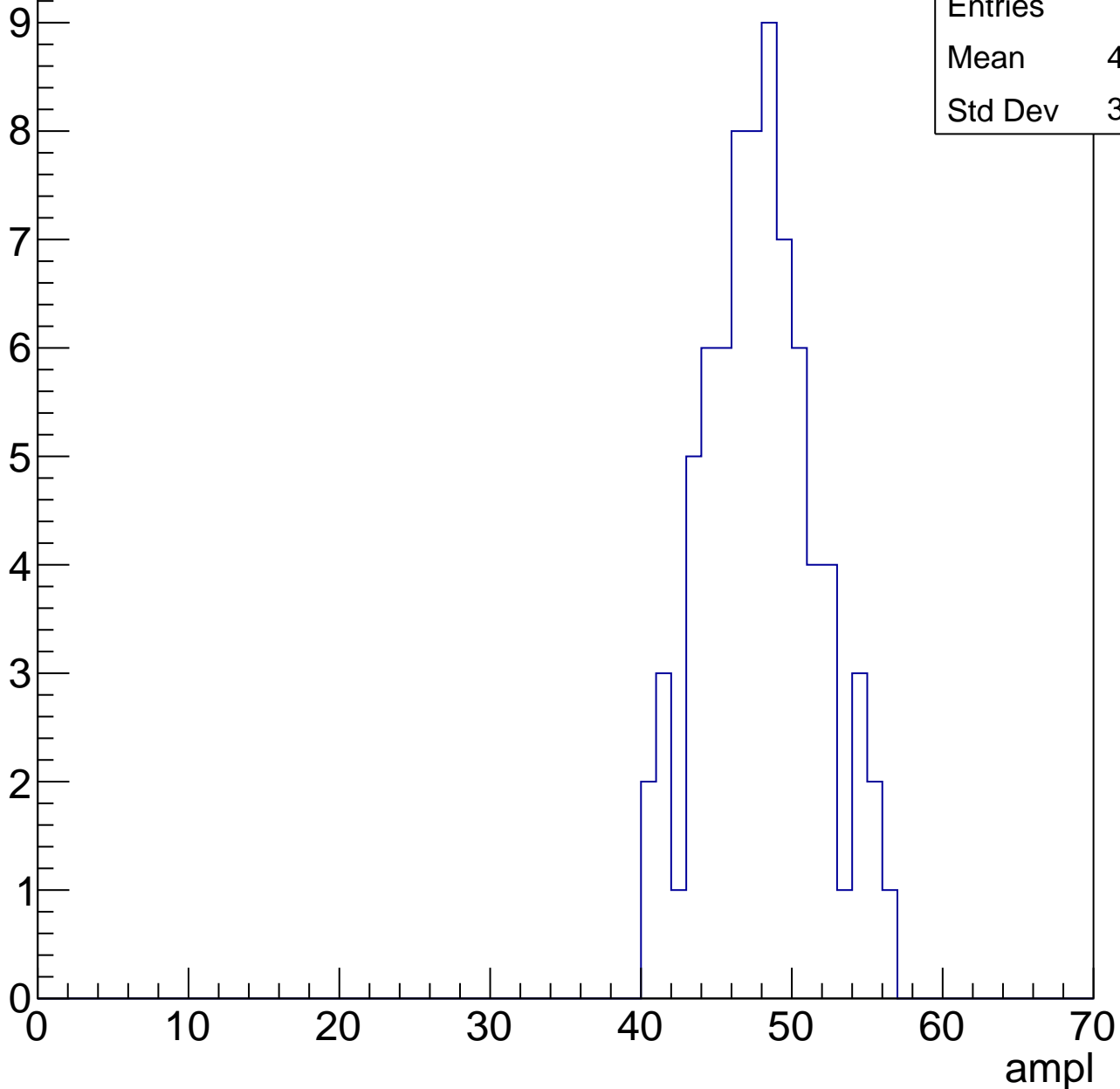


B1L102S, U15-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 47.45 |
| Std Dev | 3.679 |

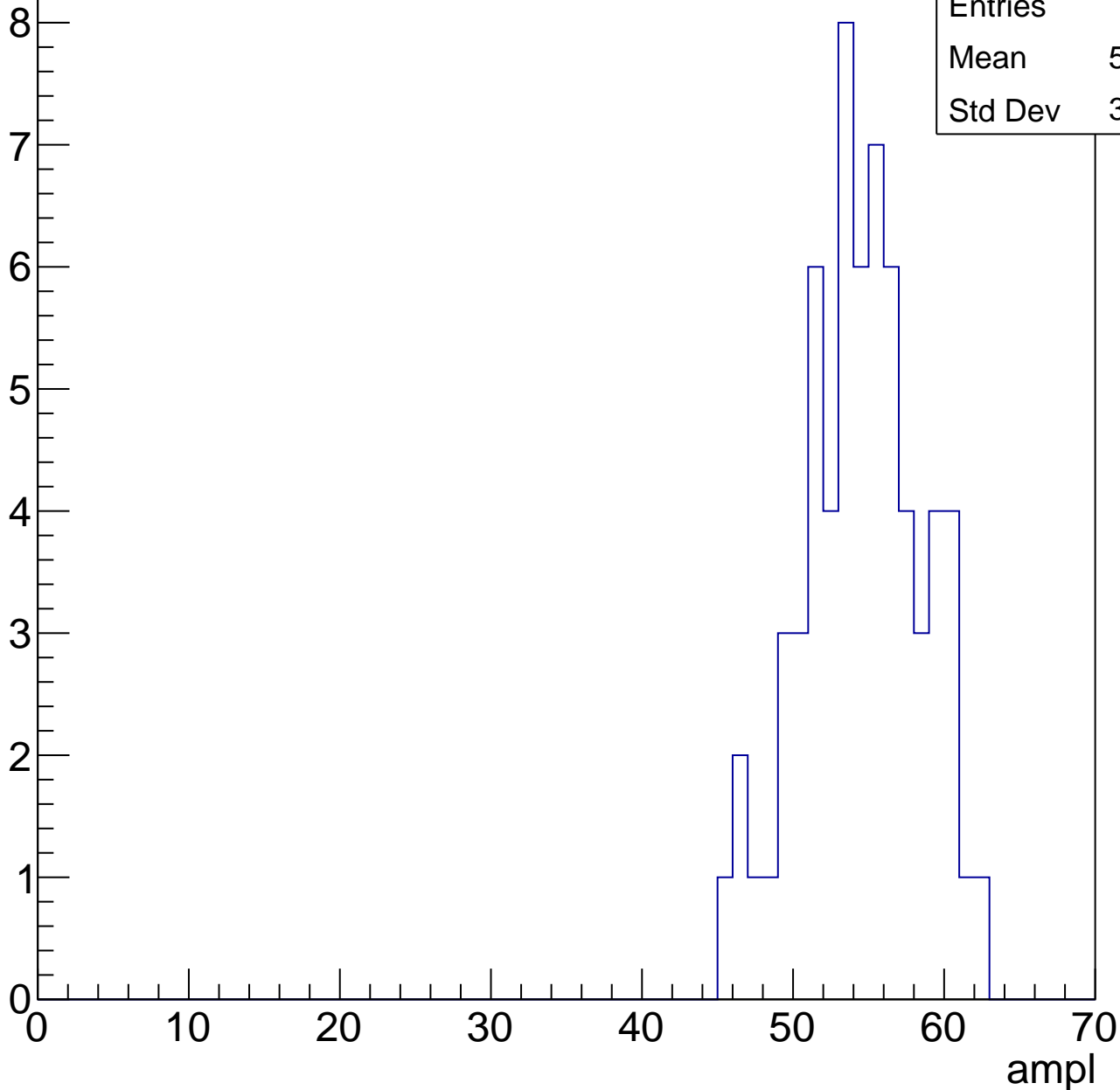


B1L102S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.05 |
| Std Dev | 3.857 |

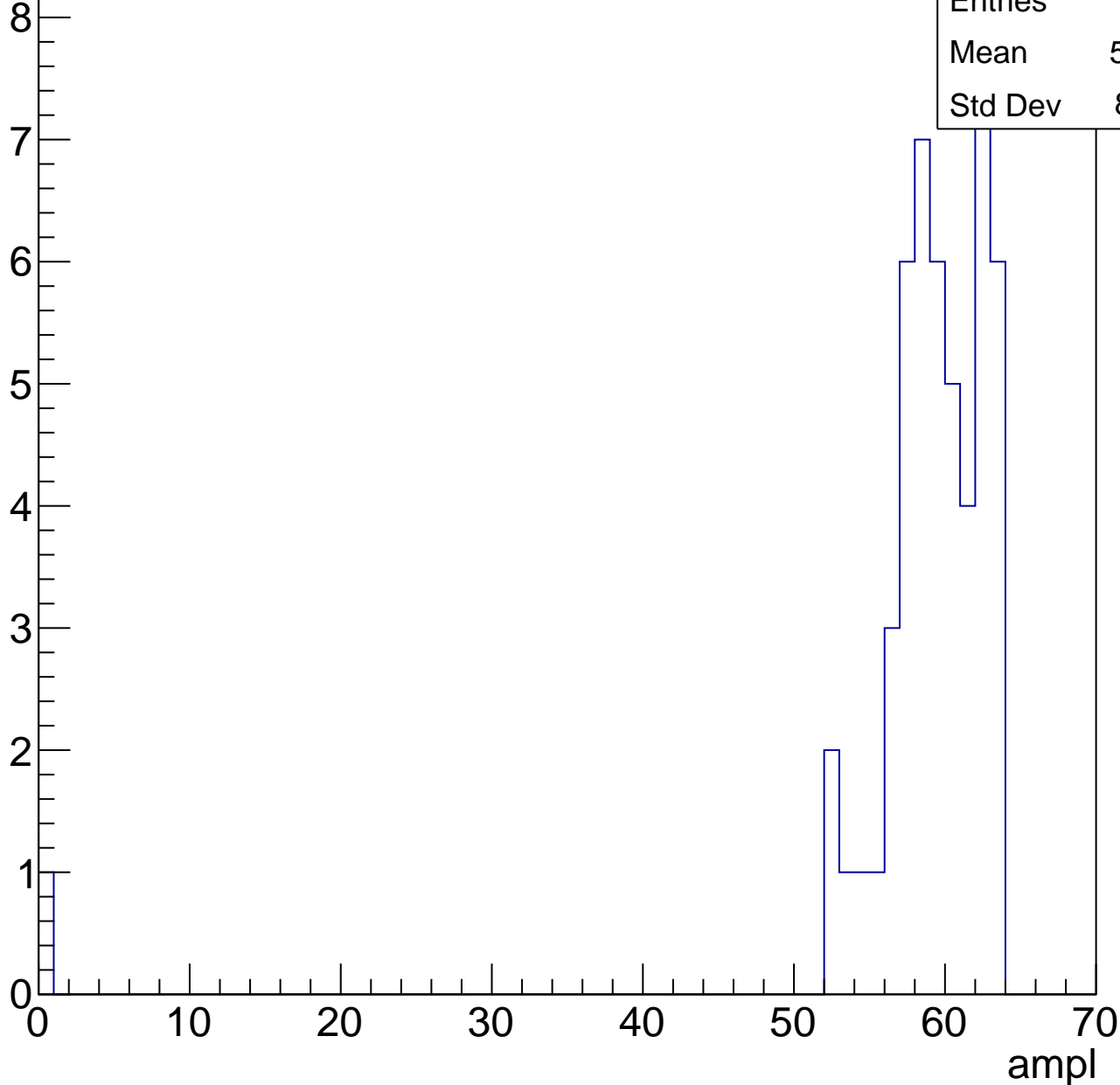


B1L102S, U15-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

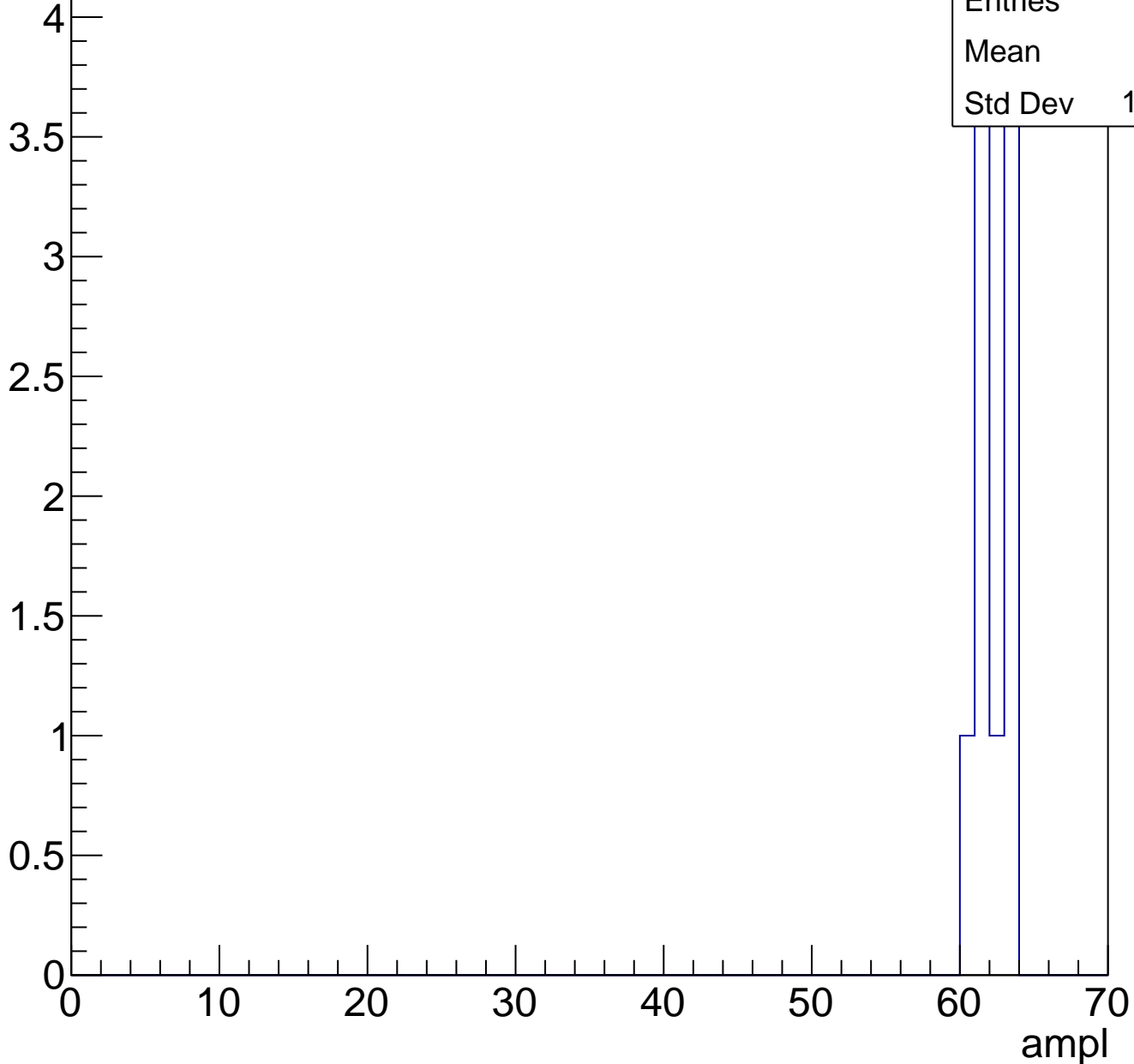
| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.92 |
| Std Dev | 8.681 |



B1L102S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch43, adc0

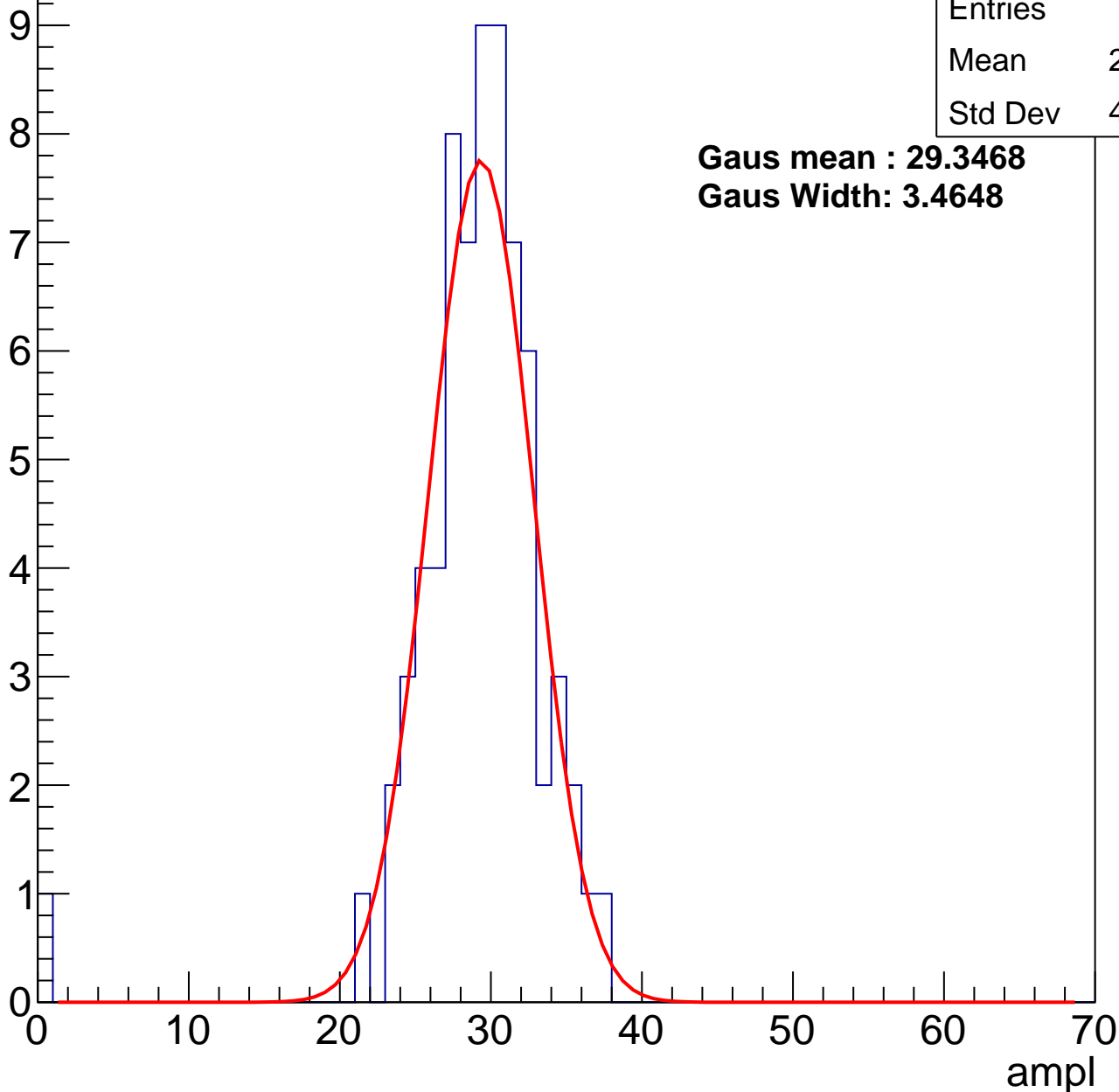
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 28.66 |
| Std Dev | 4.729 |

Gaus mean : 29.3468

Gaus Width: 3.4648



B1L102S, U15-ch43, adc1

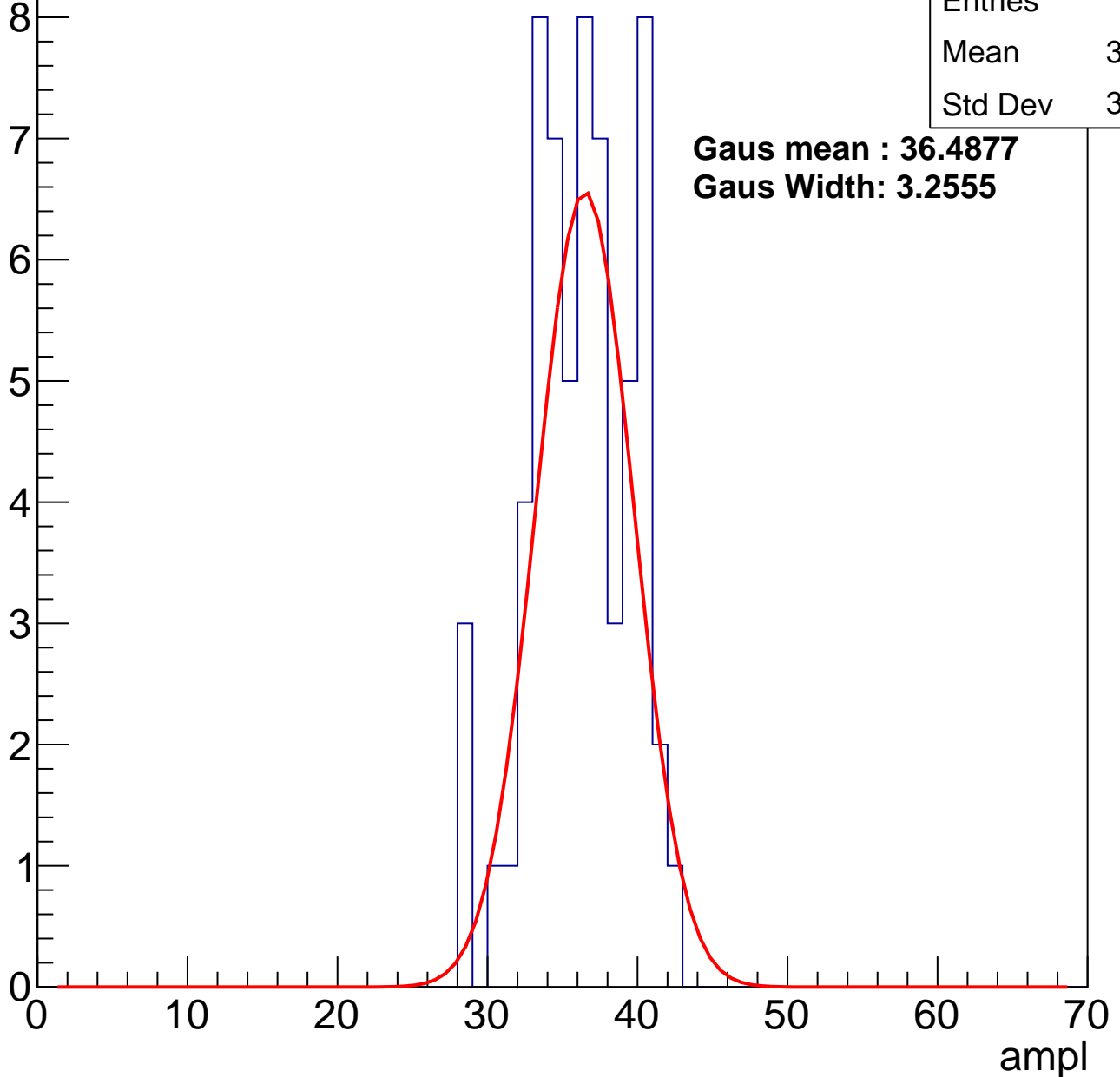
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.71 |
| Std Dev | 3.321 |

Gaus mean : 36.4877

Gaus Width: 3.2555



B1L102S, U15-ch43, adc2

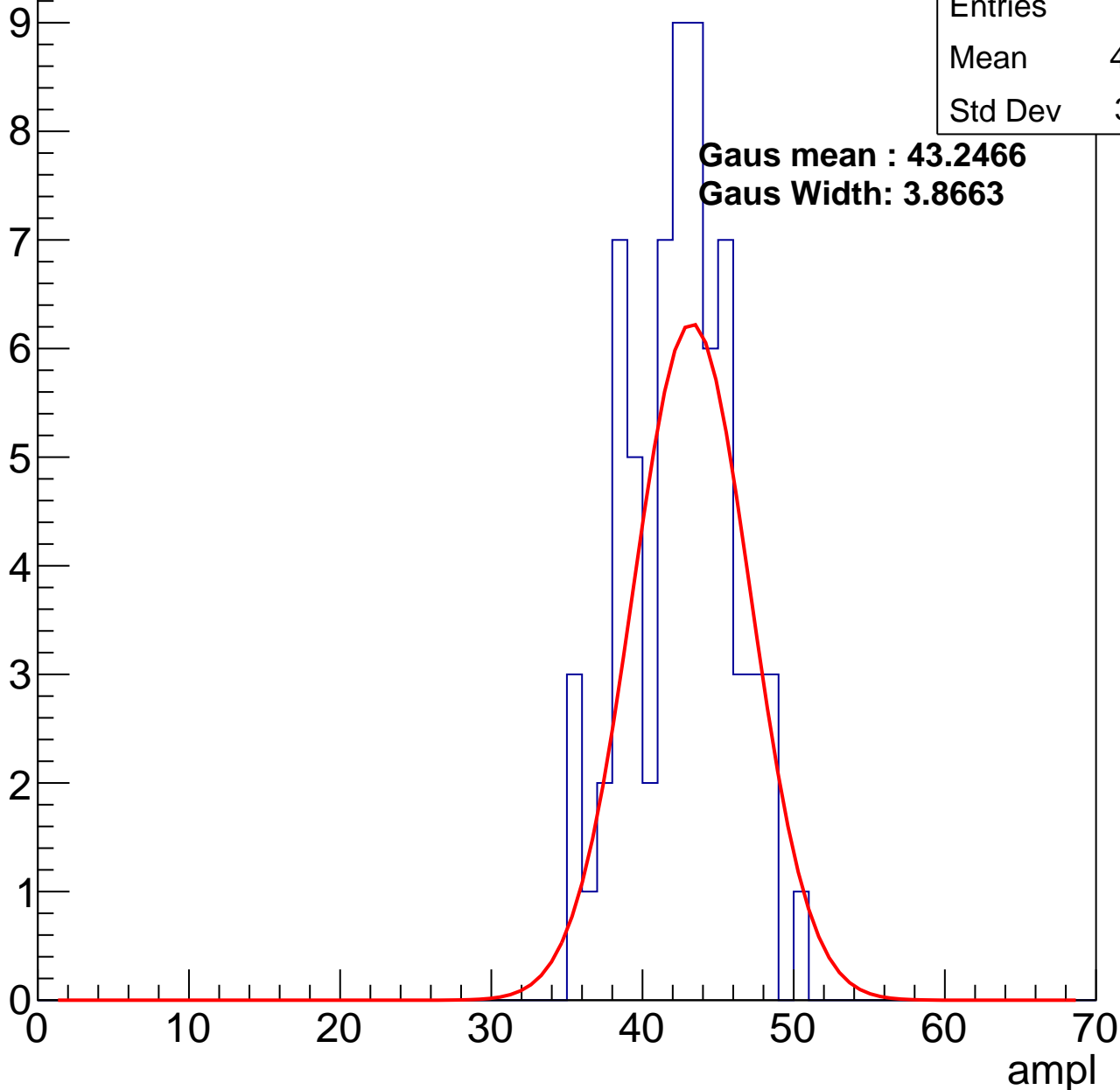
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.06 |
| Std Dev | 3.421 |

Gaus mean : 43.2466

Gaus Width: 3.8663



B1L102S, U15-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 49.25 |
| Std Dev | 3.449 |

Entry

10

8

6

4

2

0

0

10

20

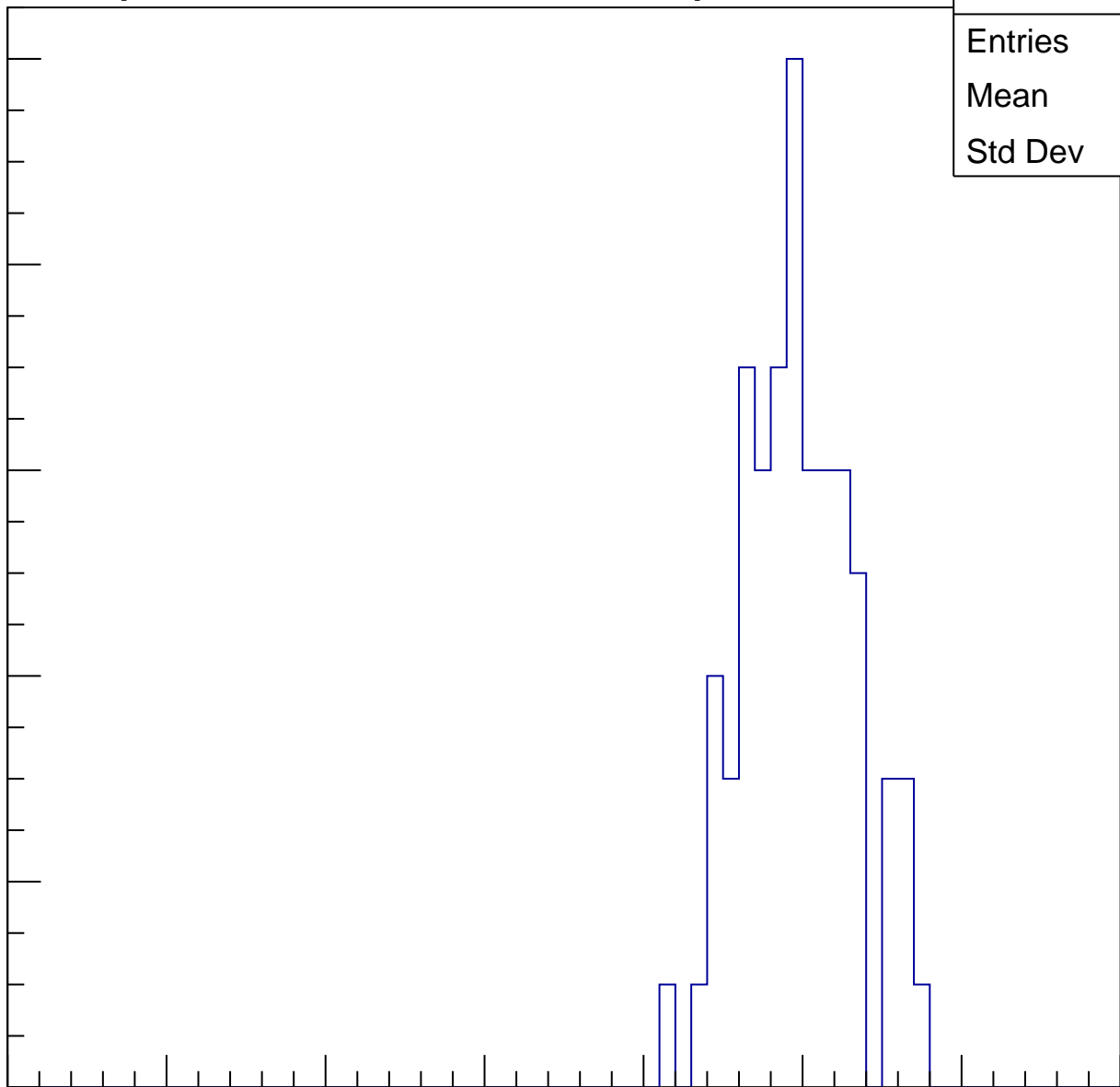
30

40

50

60

ampl

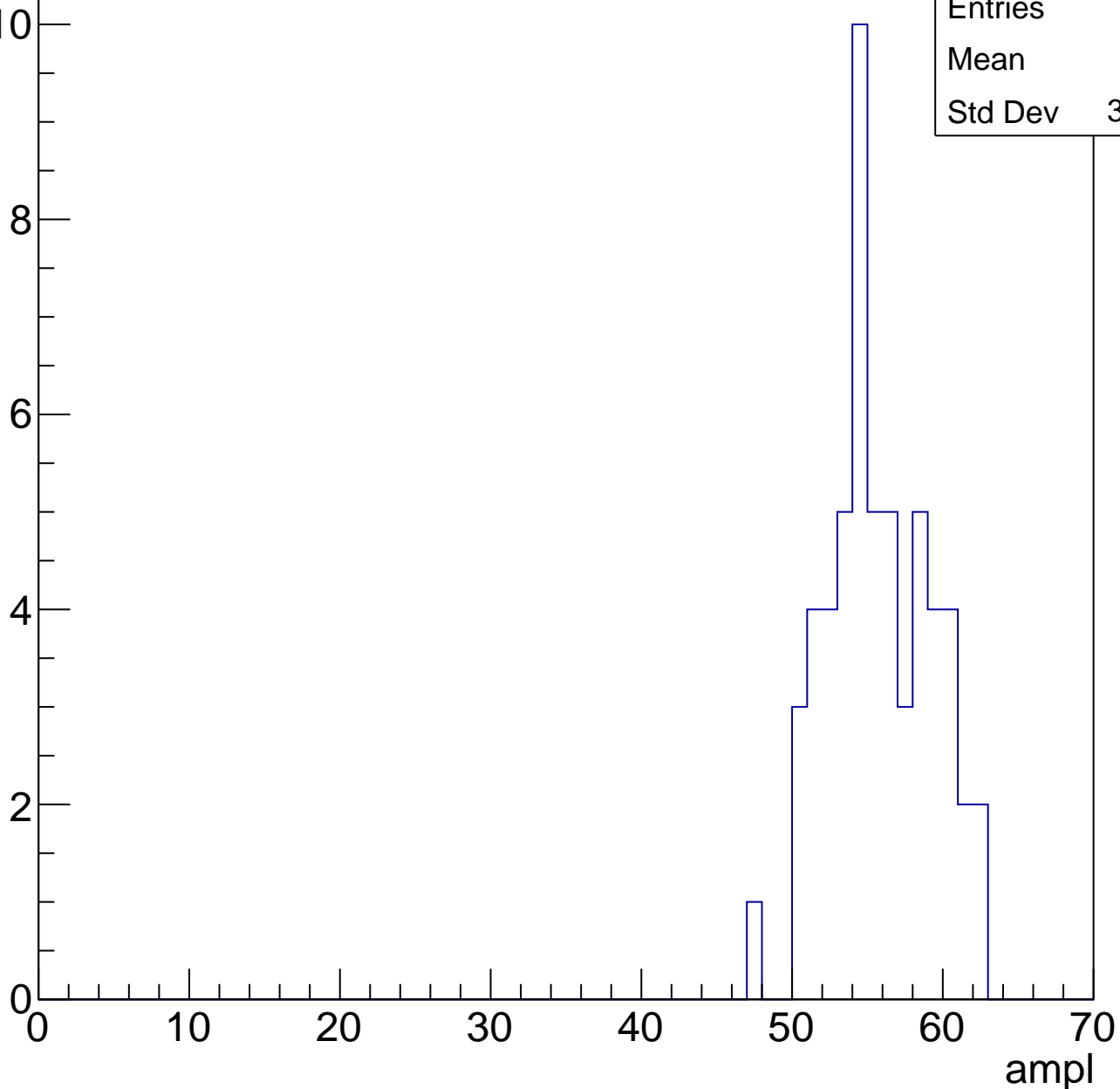


B1L102S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

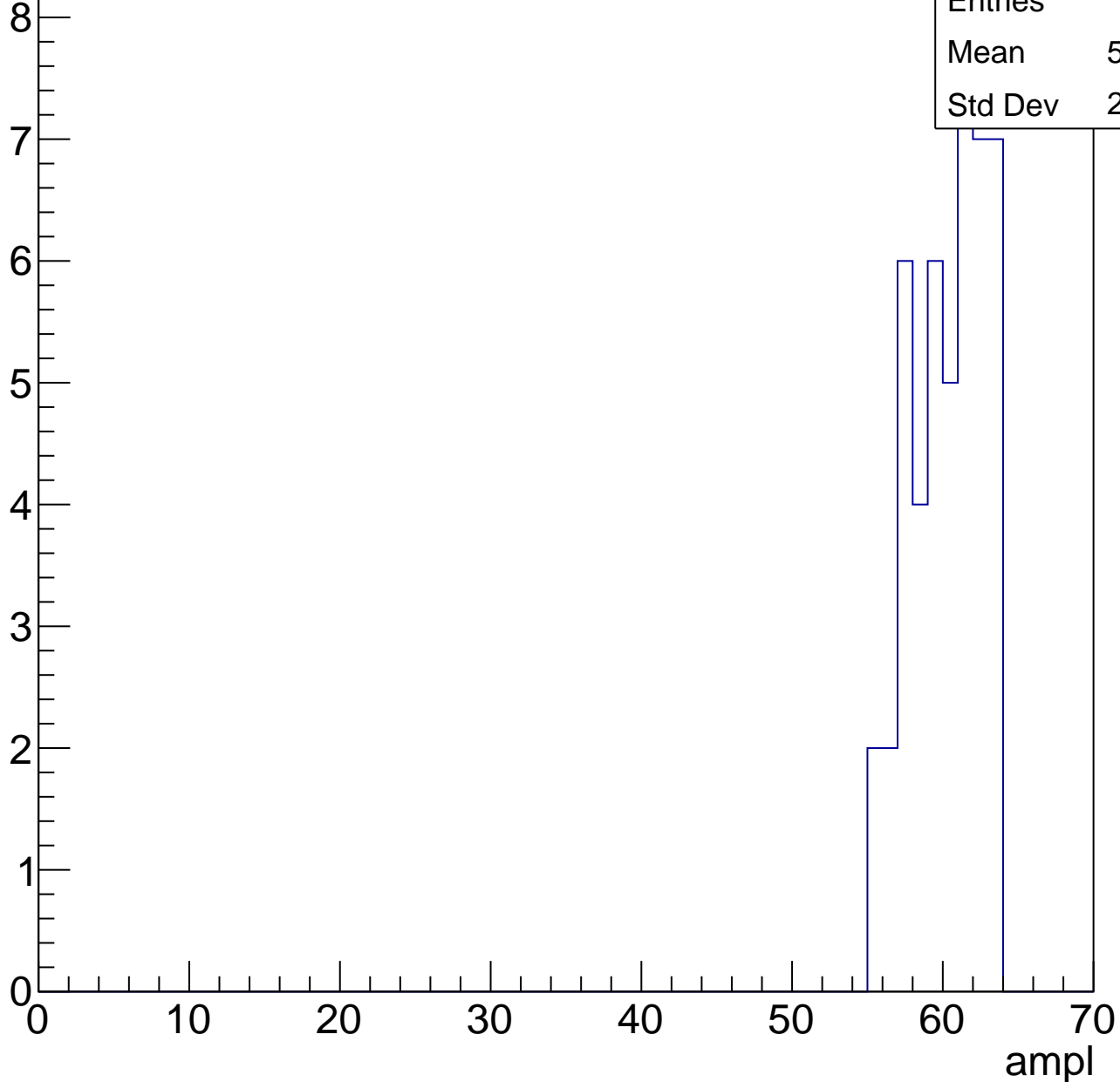
| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.3 |
| Std Dev | 3.403 |



B1L102S, U15-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

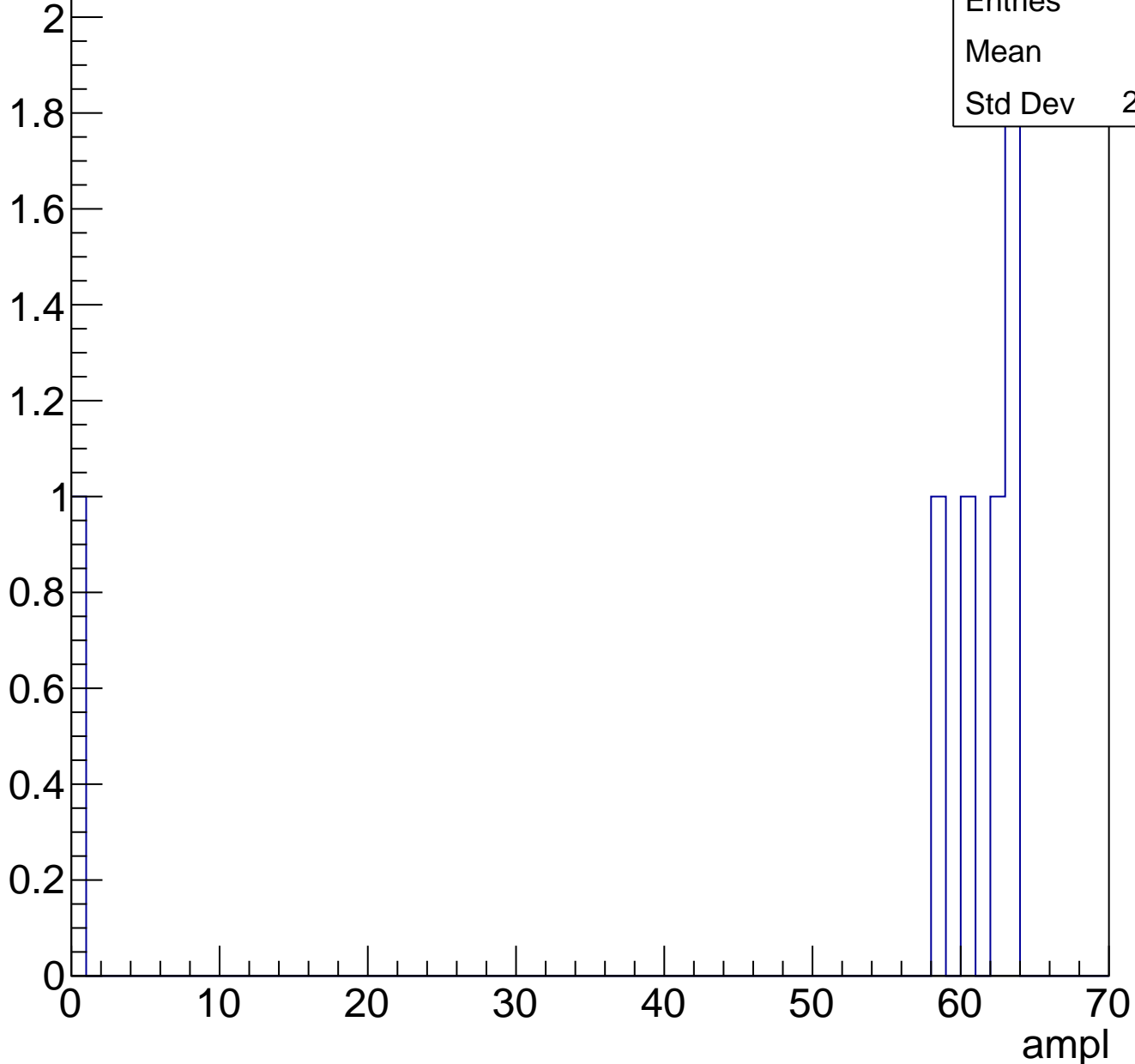
Entry



B1L102S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch44, adc0

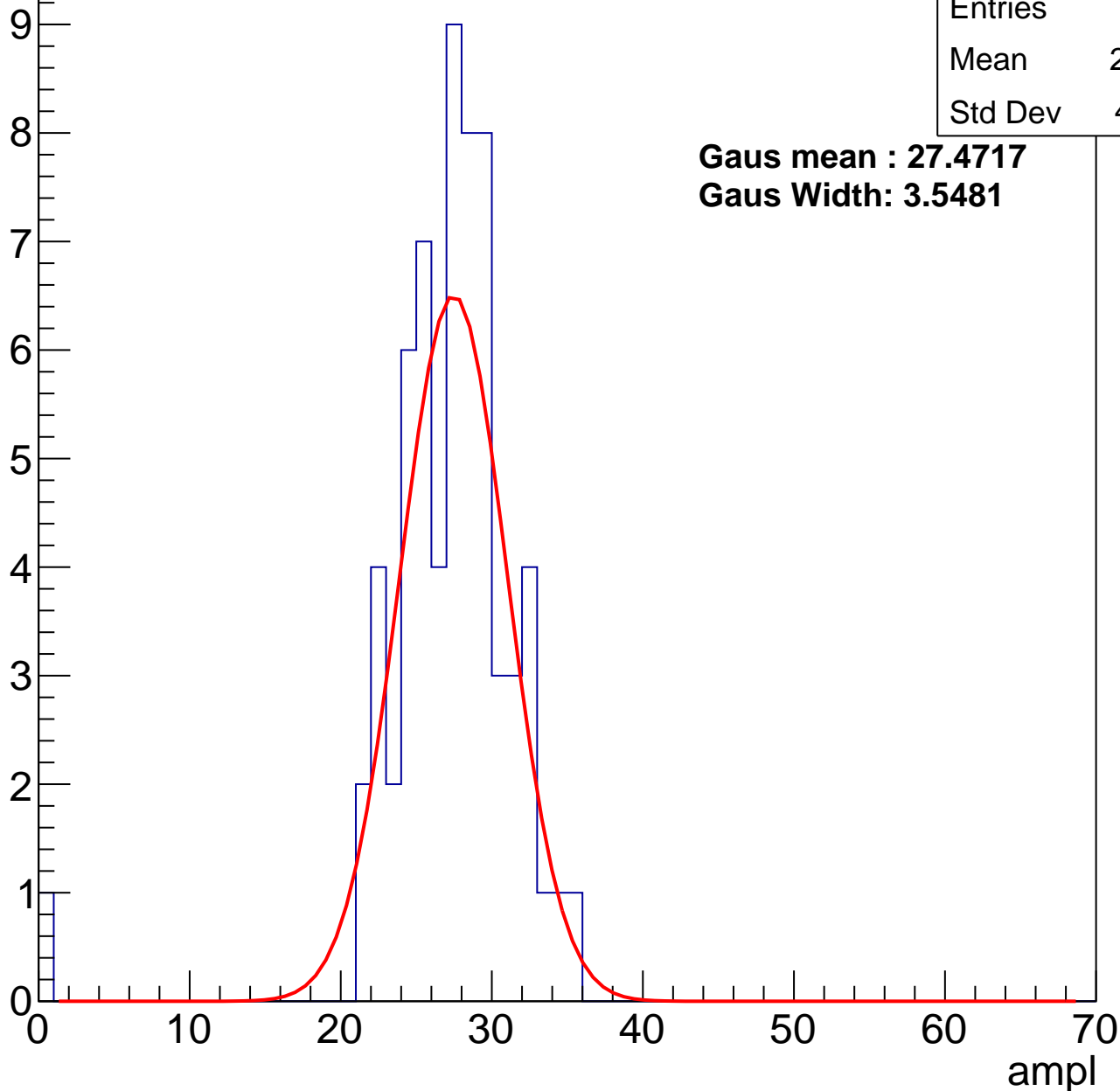
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 26.73 |
| Std Dev | 4.641 |

Gaus mean : 27.4717

Gaus Width: 3.5481



B1L102S, U15-ch44, adc1

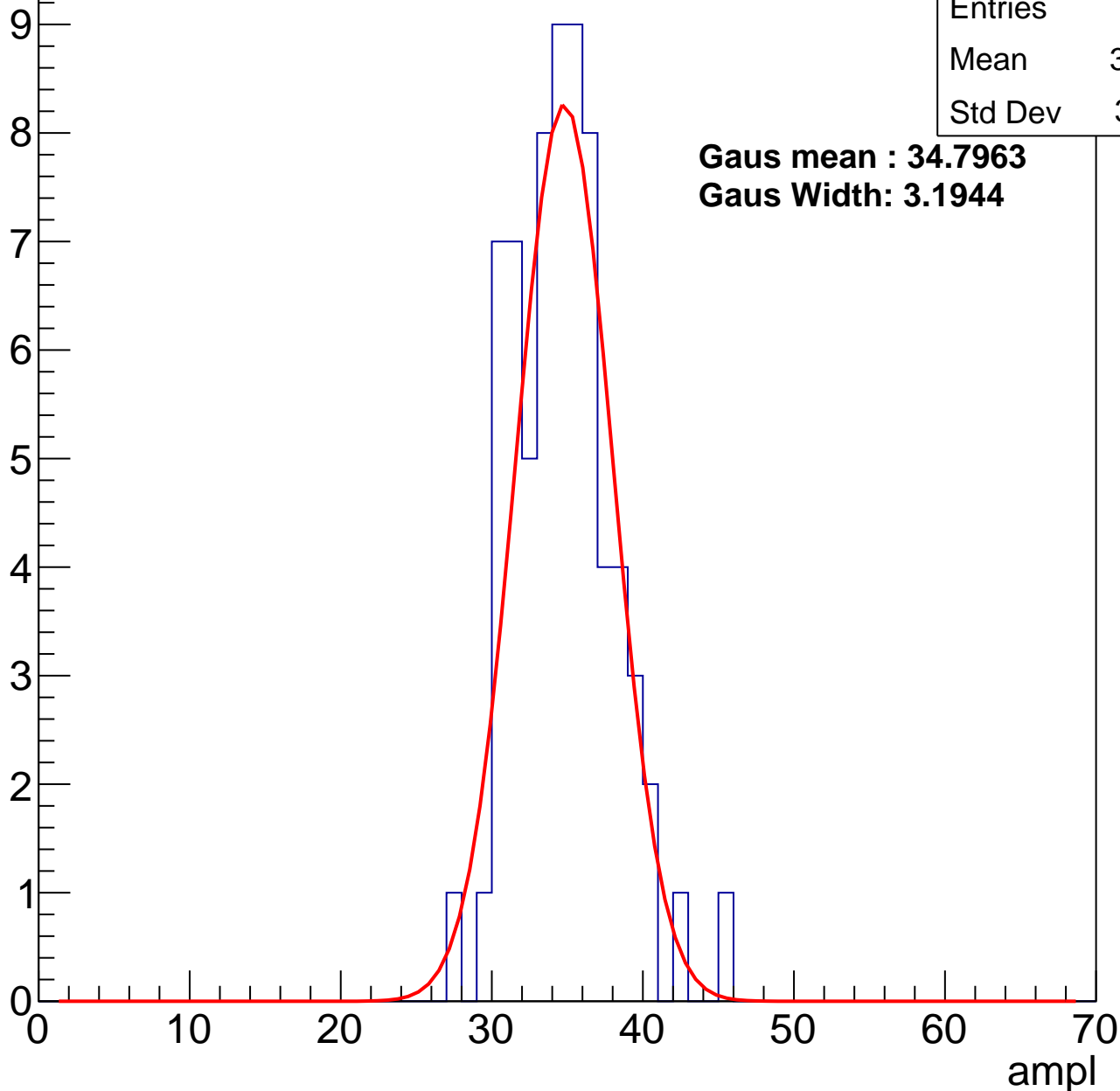
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 34.29 |
| Std Dev | 3.261 |

Gaus mean : 34.7963

Gaus Width: 3.1944



B1L102S, U15-ch44, adc2

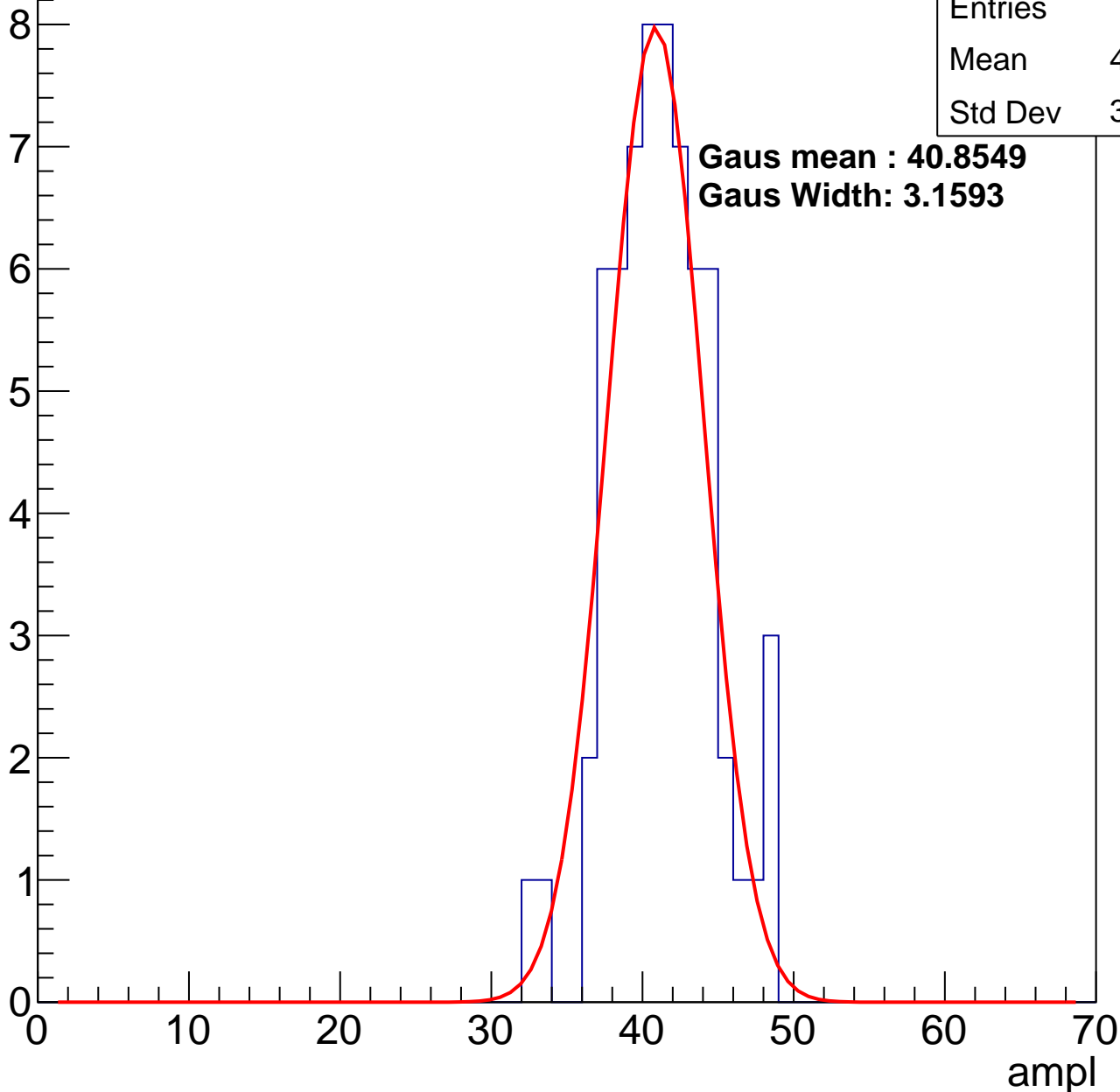
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 40.78 |
| Std Dev | 3.289 |

Gaus mean : 40.8549

Gaus Width: 3.1593

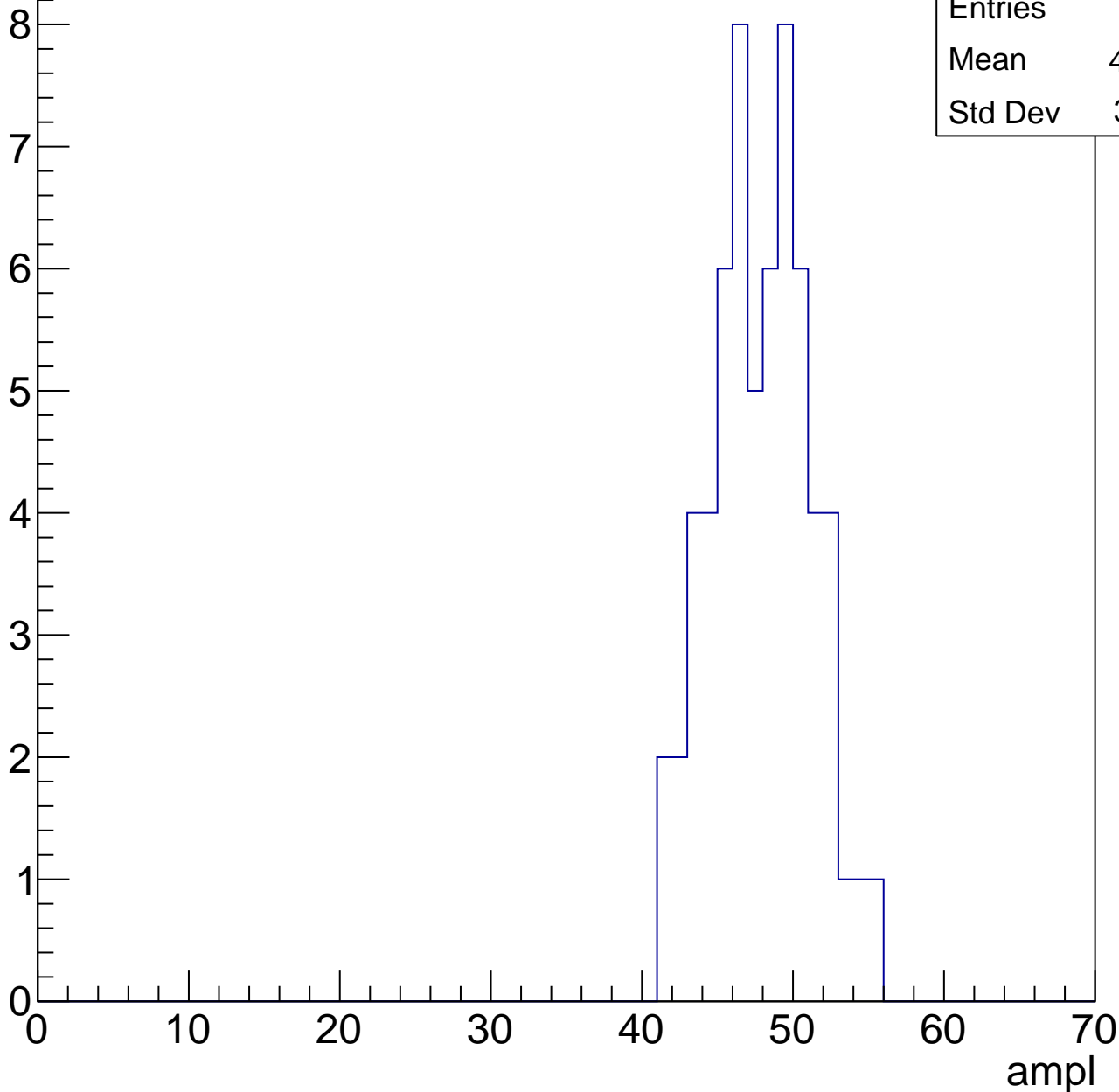


B1L102S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 47.44 |
| Std Dev | 3.231 |

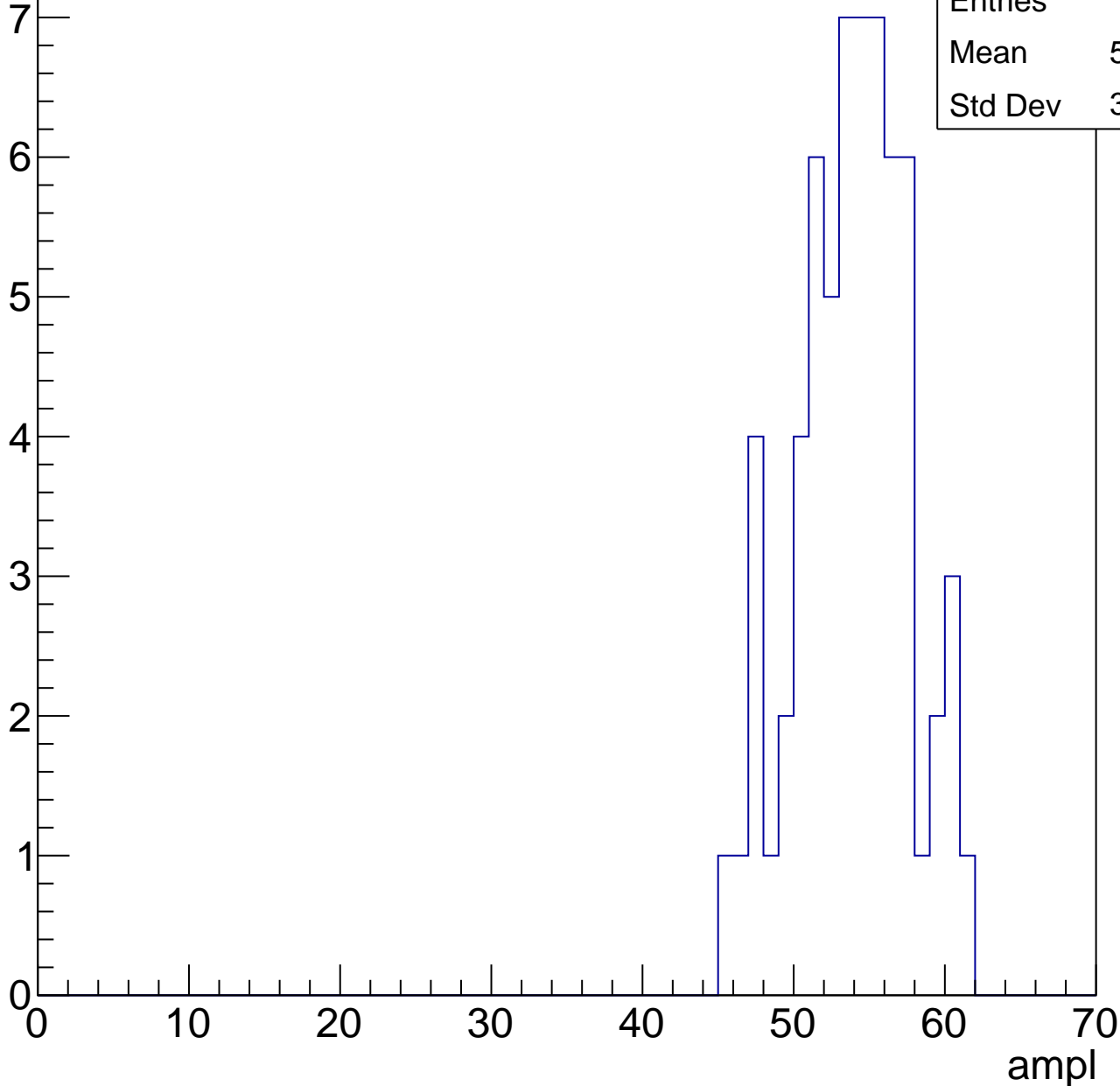


B1L102S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

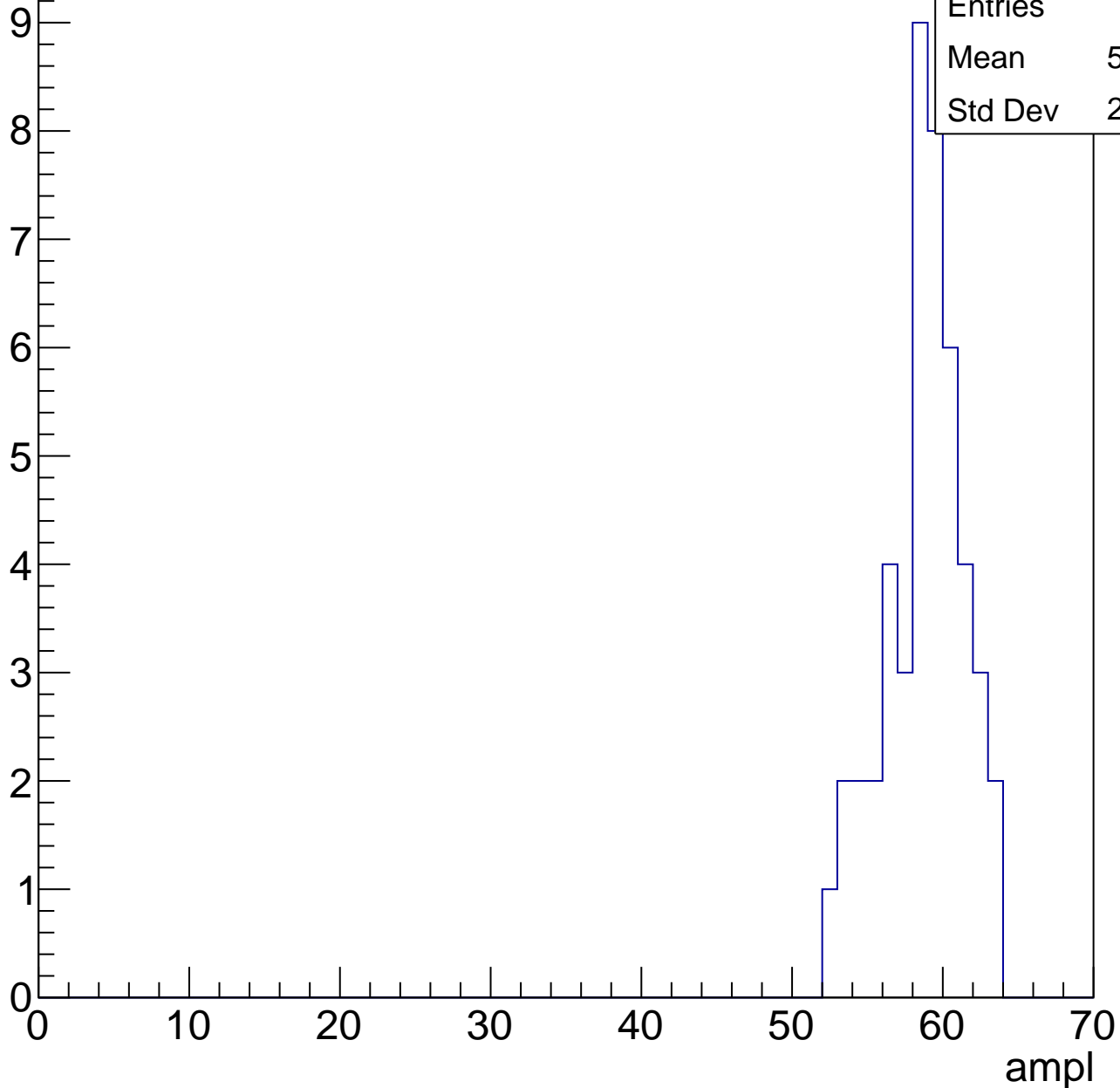
| | |
|---------|-------|
| Entries | 64 |
| Mean | 53.44 |
| Std Dev | 3.665 |



B1L102S, U15-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



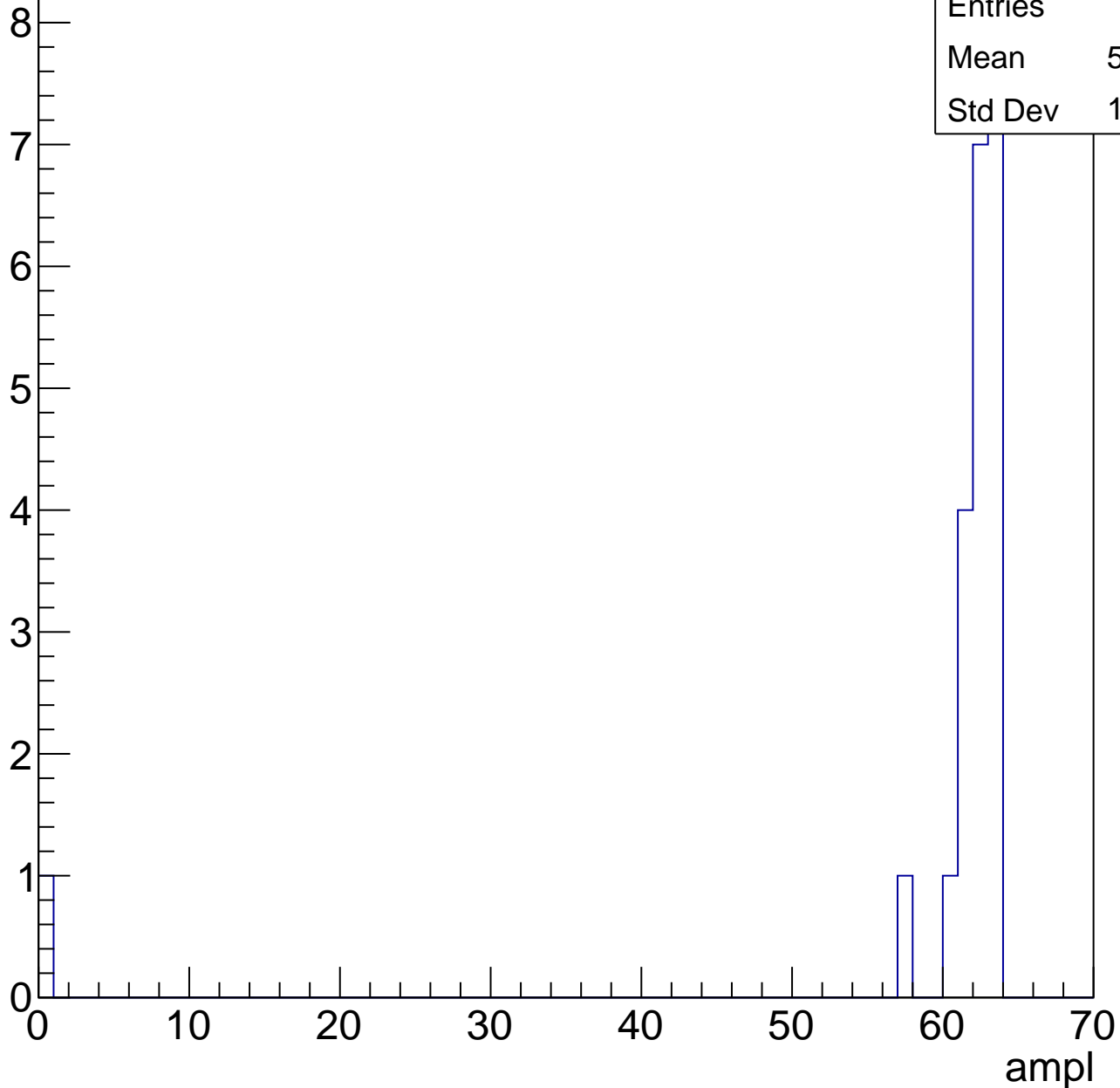
| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.28 |
| Std Dev | 2.626 |

B1L102S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

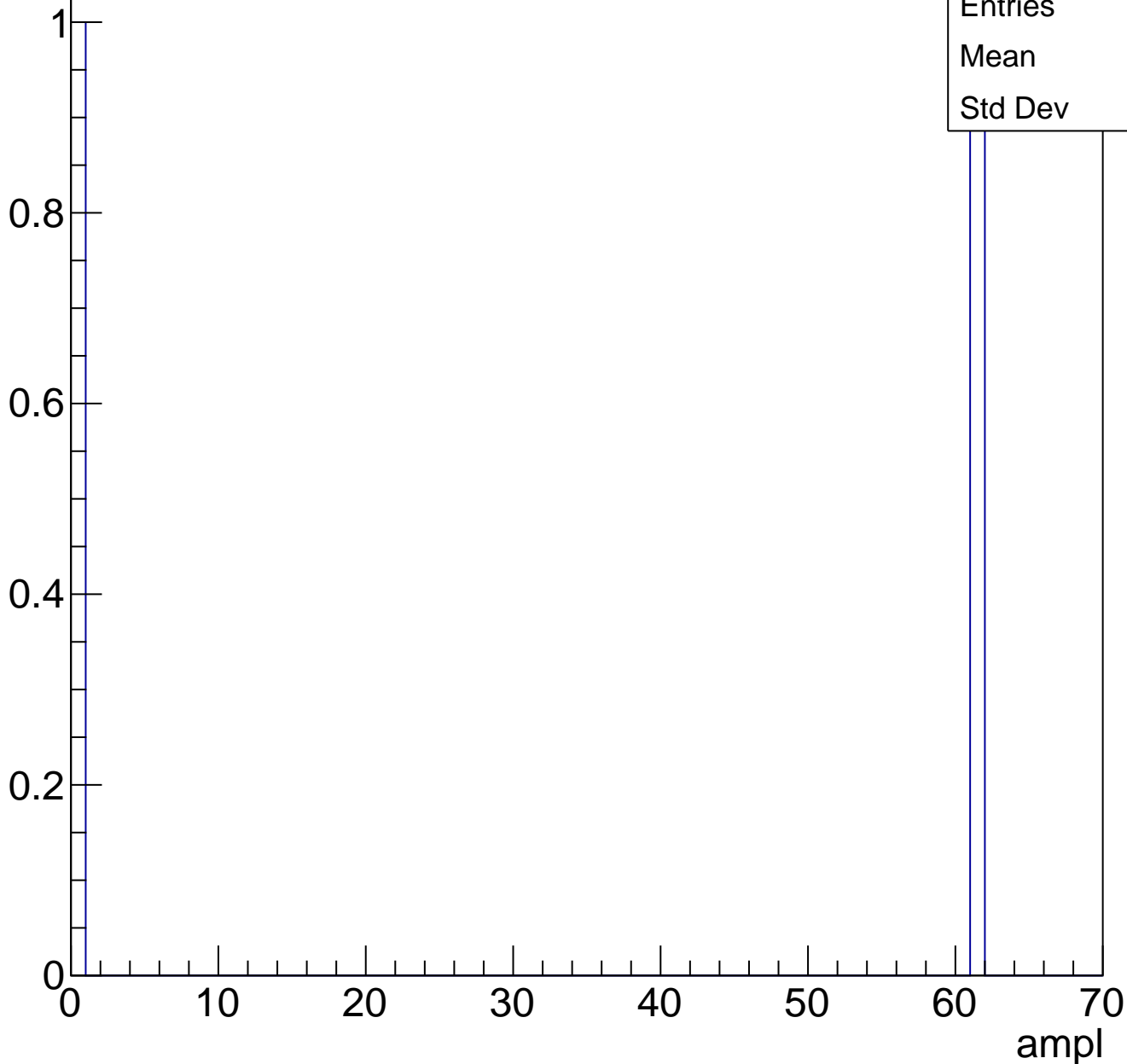
| | |
|---------|-------|
| Entries | 22 |
| Mean | 59.05 |
| Std Dev | 12.96 |



B1L102S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch45, adc0

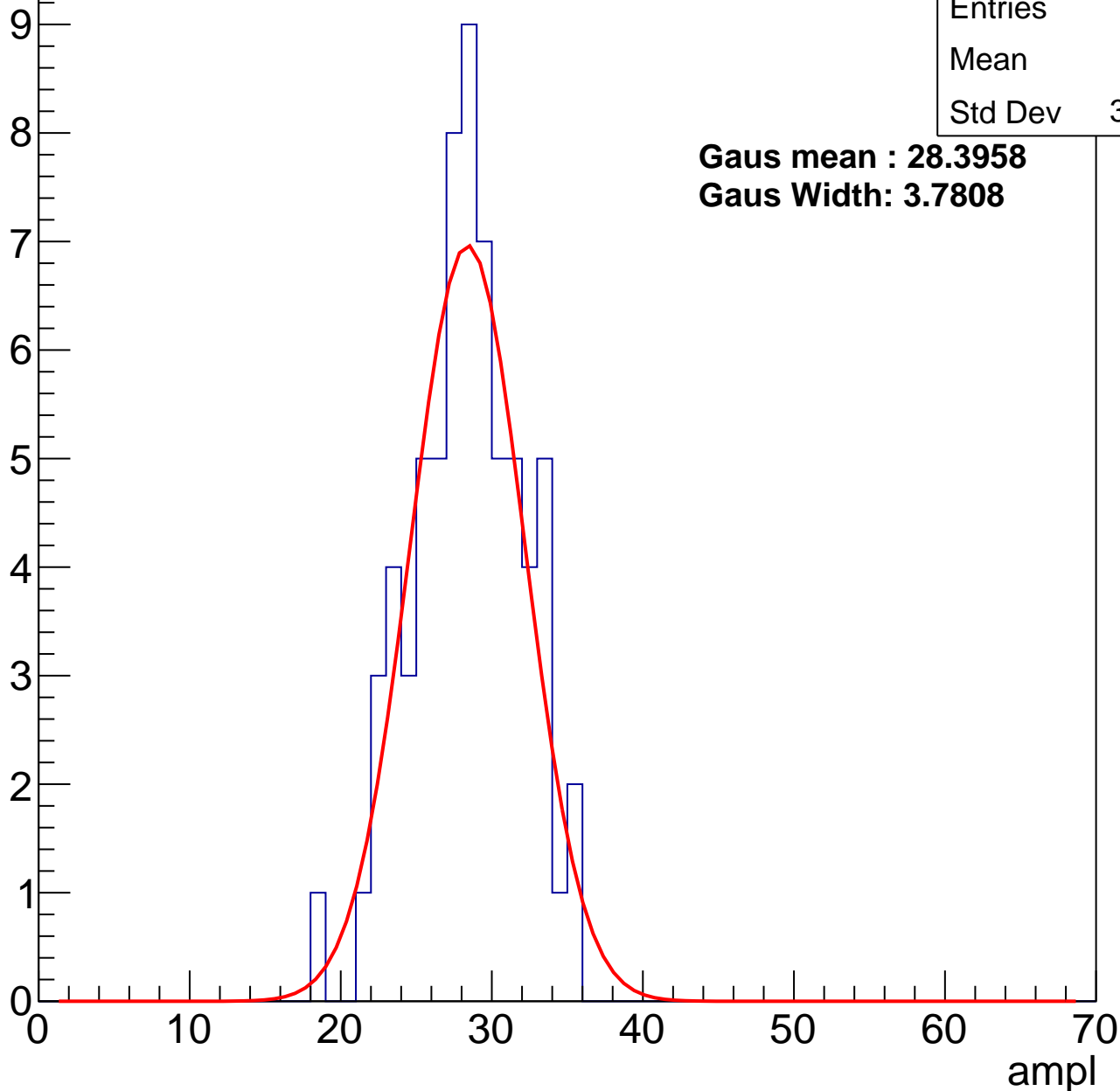
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 27.9 |
| Std Dev | 3.582 |

Gaus mean : 28.3958

Gaus Width: 3.7808



B1L102S, U15-ch45, adc1

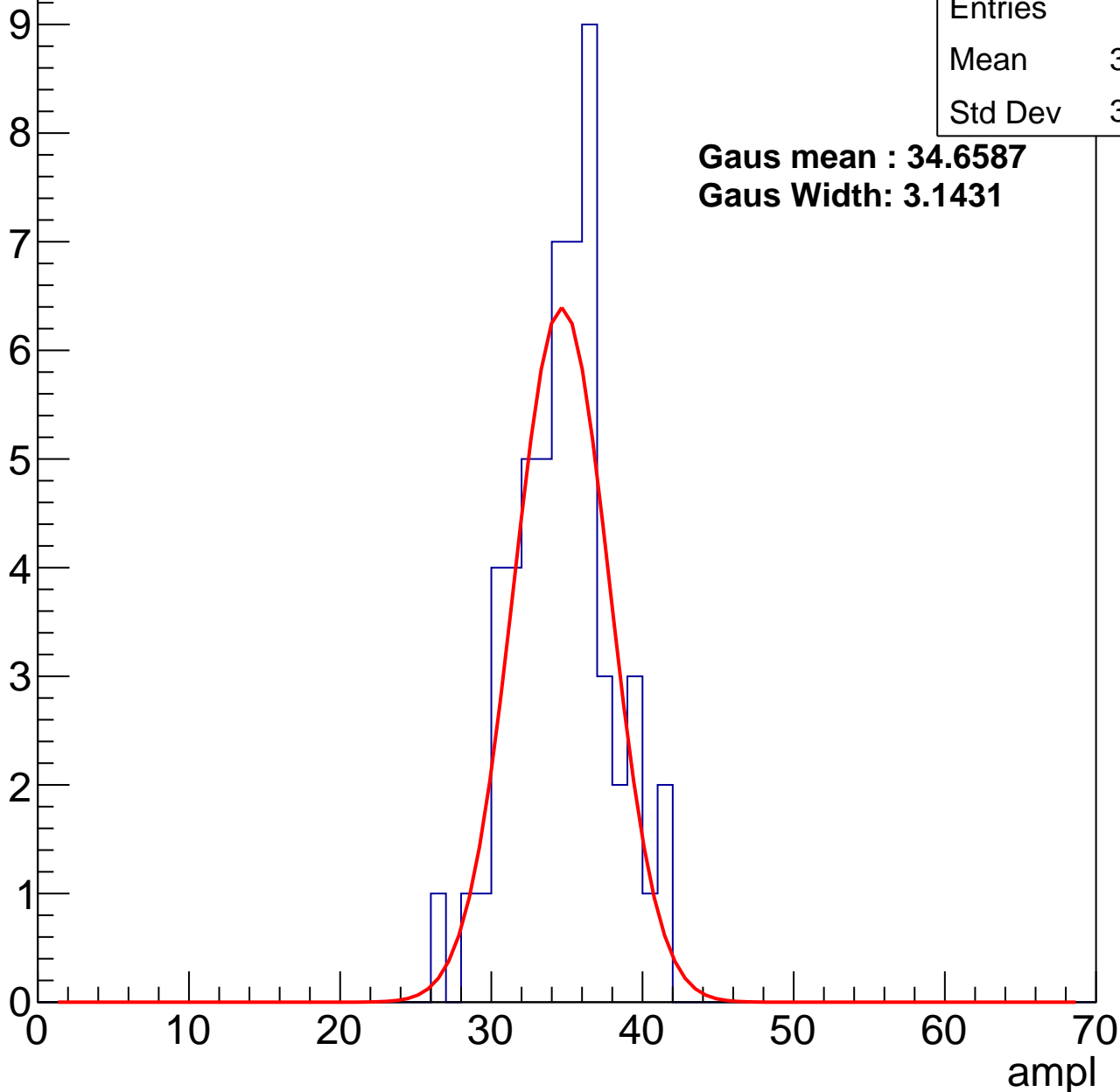
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 34.27 |
| Std Dev | 3.182 |

Gaus mean : 34.6587

Gaus Width: 3.1431



B1L102S, U15-ch45, adc2

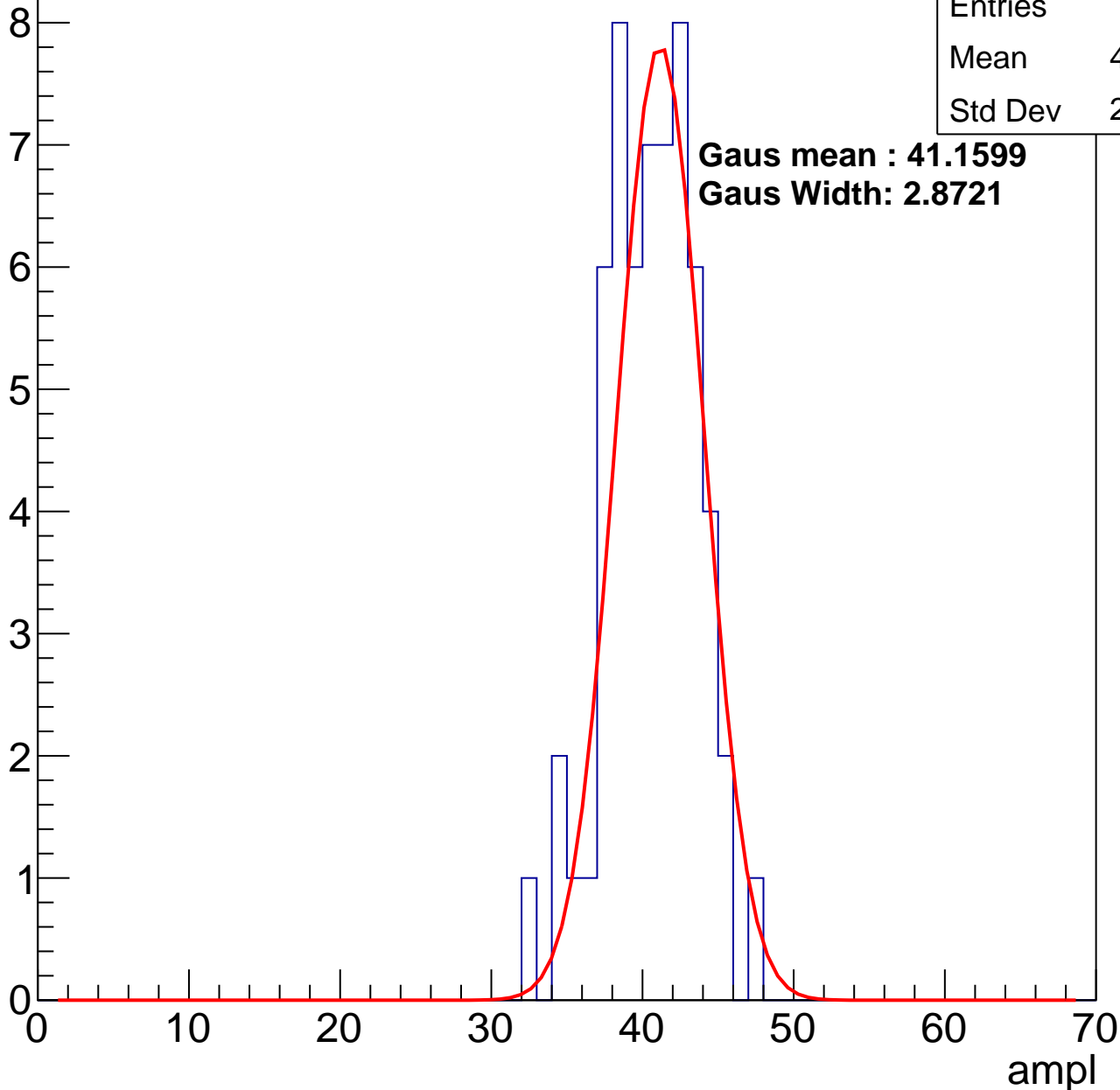
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 40.08 |
| Std Dev | 2.957 |

Gaus mean : 41.1599

Gaus Width: 2.8721

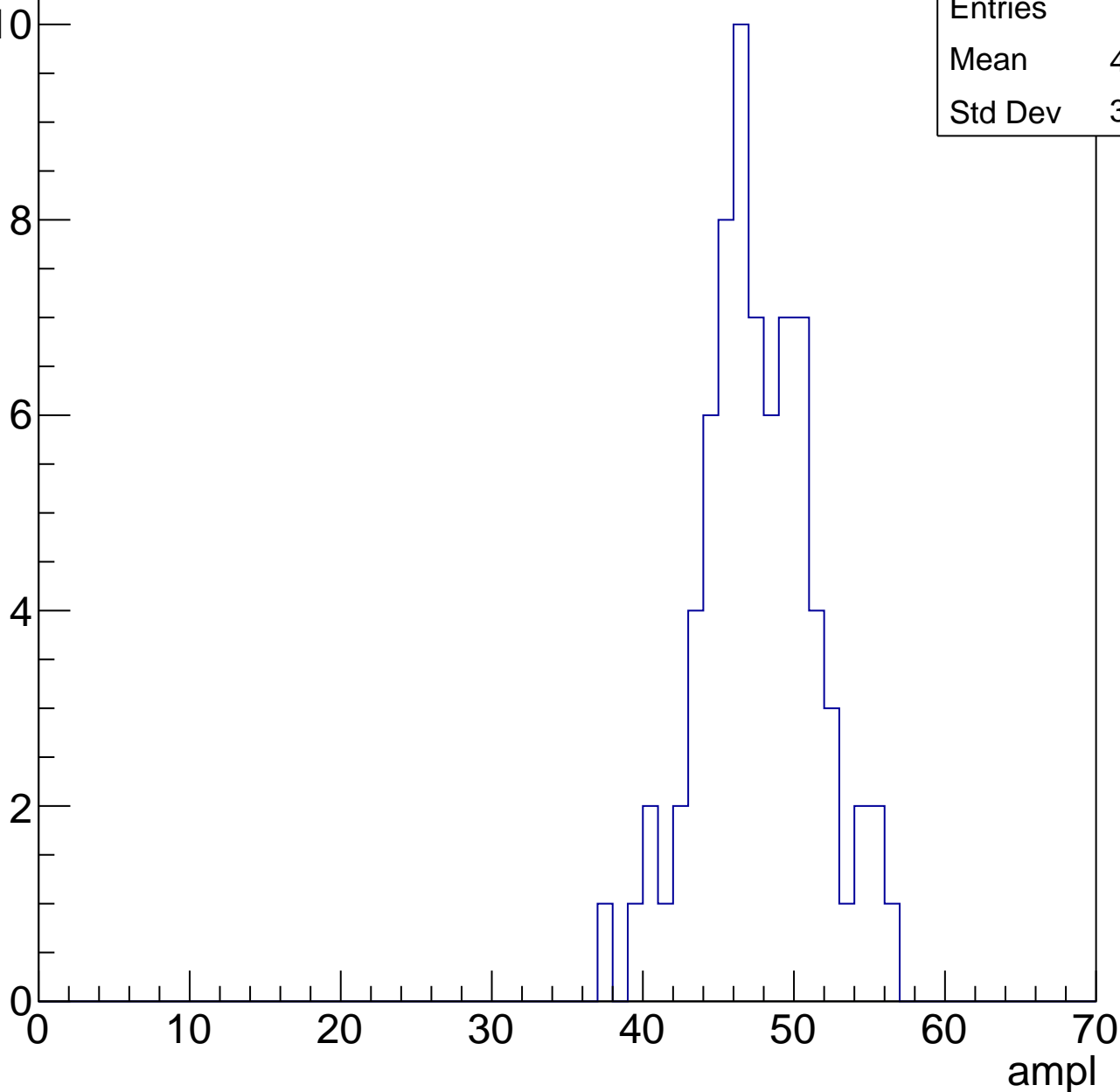


B1L102S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 47.12 |
| Std Dev | 3.805 |

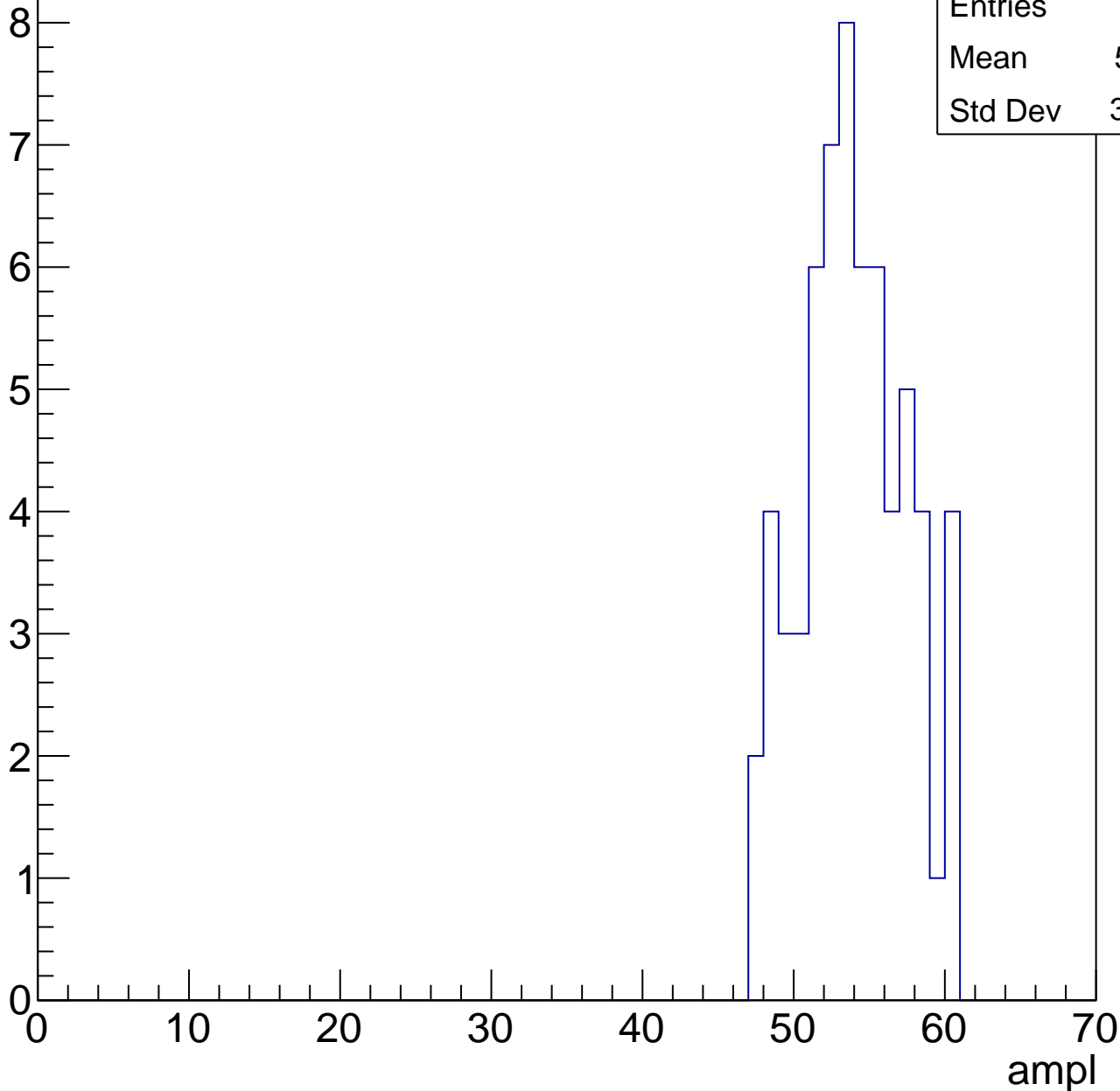


B1L102S, U15-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 53.51 |
| Std Dev | 3.427 |



B1L102S, U15-ch45, adc5

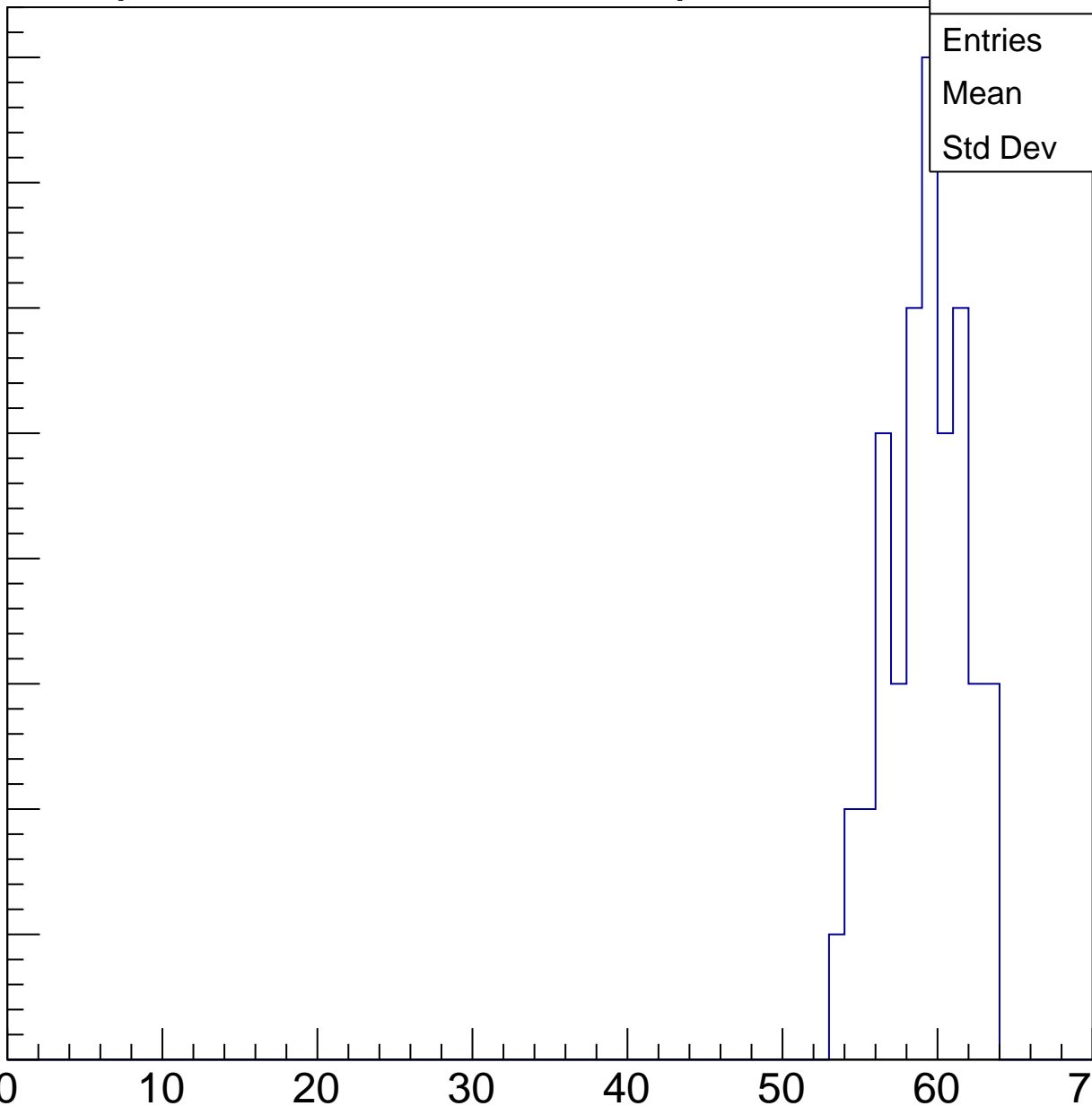
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 44 |
| Mean | 58.7 |
| Std Dev | 2.528 |

ampl

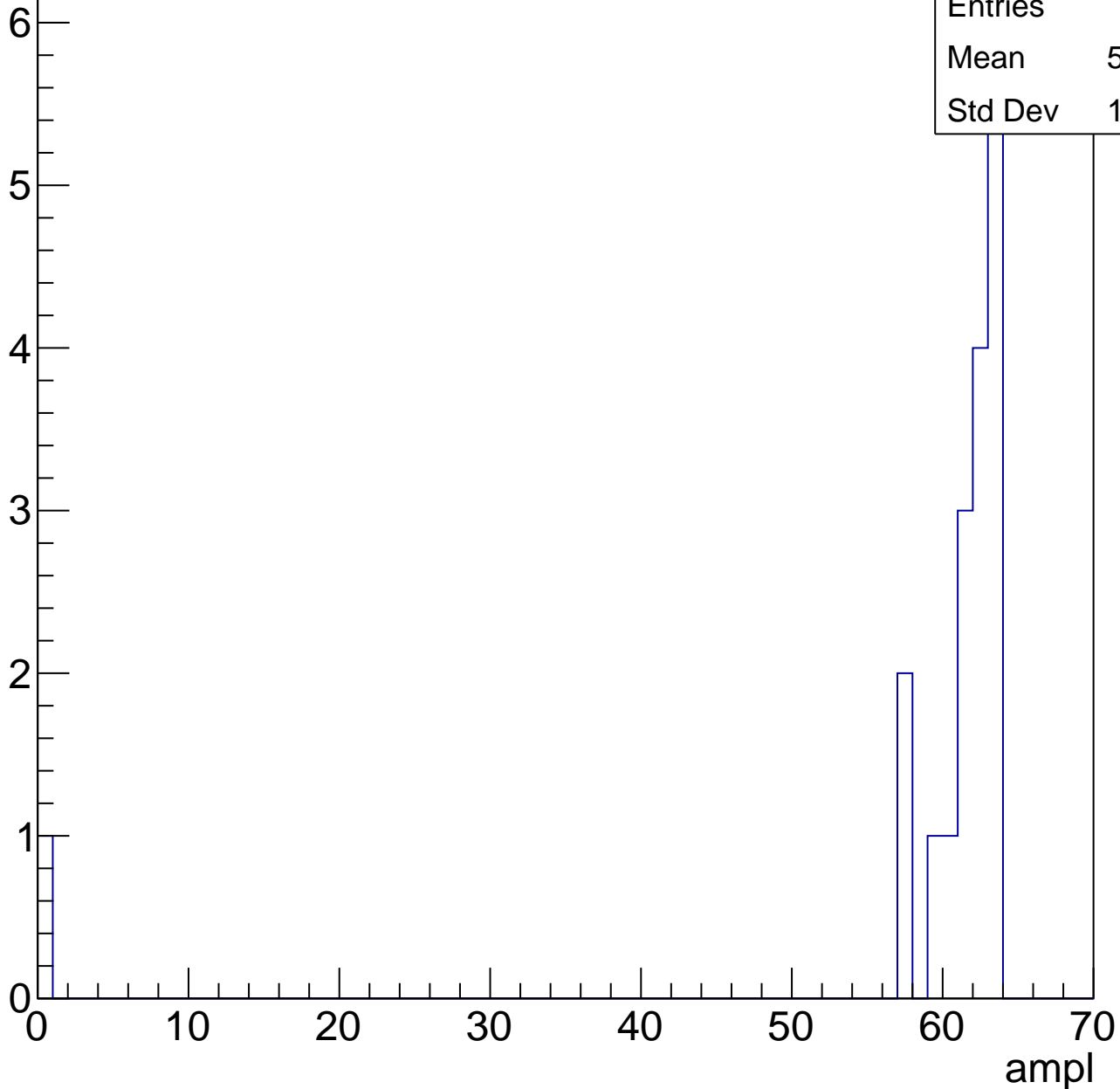


B1L102S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

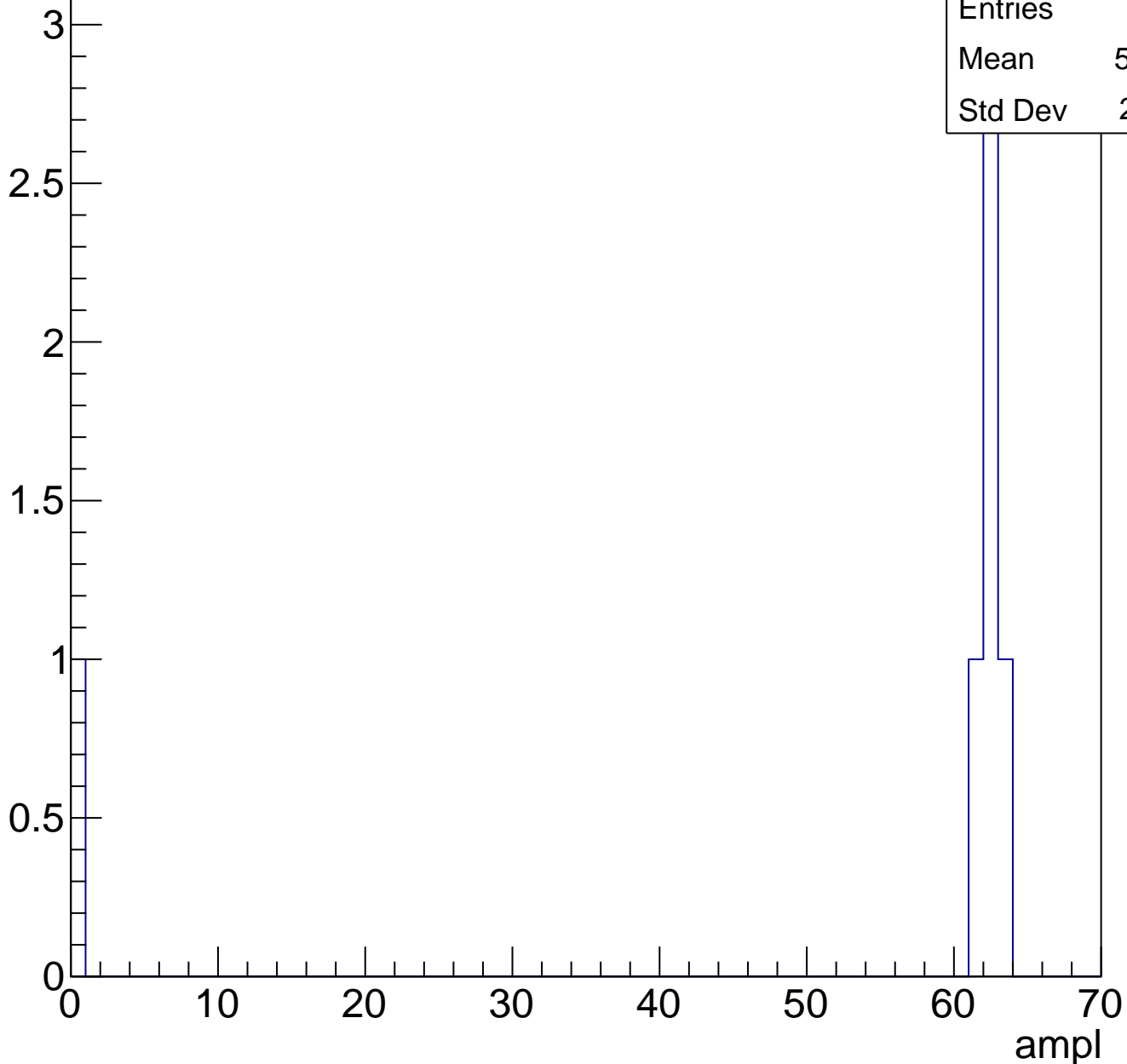
| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.89 |
| Std Dev | 14.17 |



B1L102S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 6 |
| Mean | 51.67 |
| Std Dev | 23.11 |

B1L102S, U15-ch46, adc0

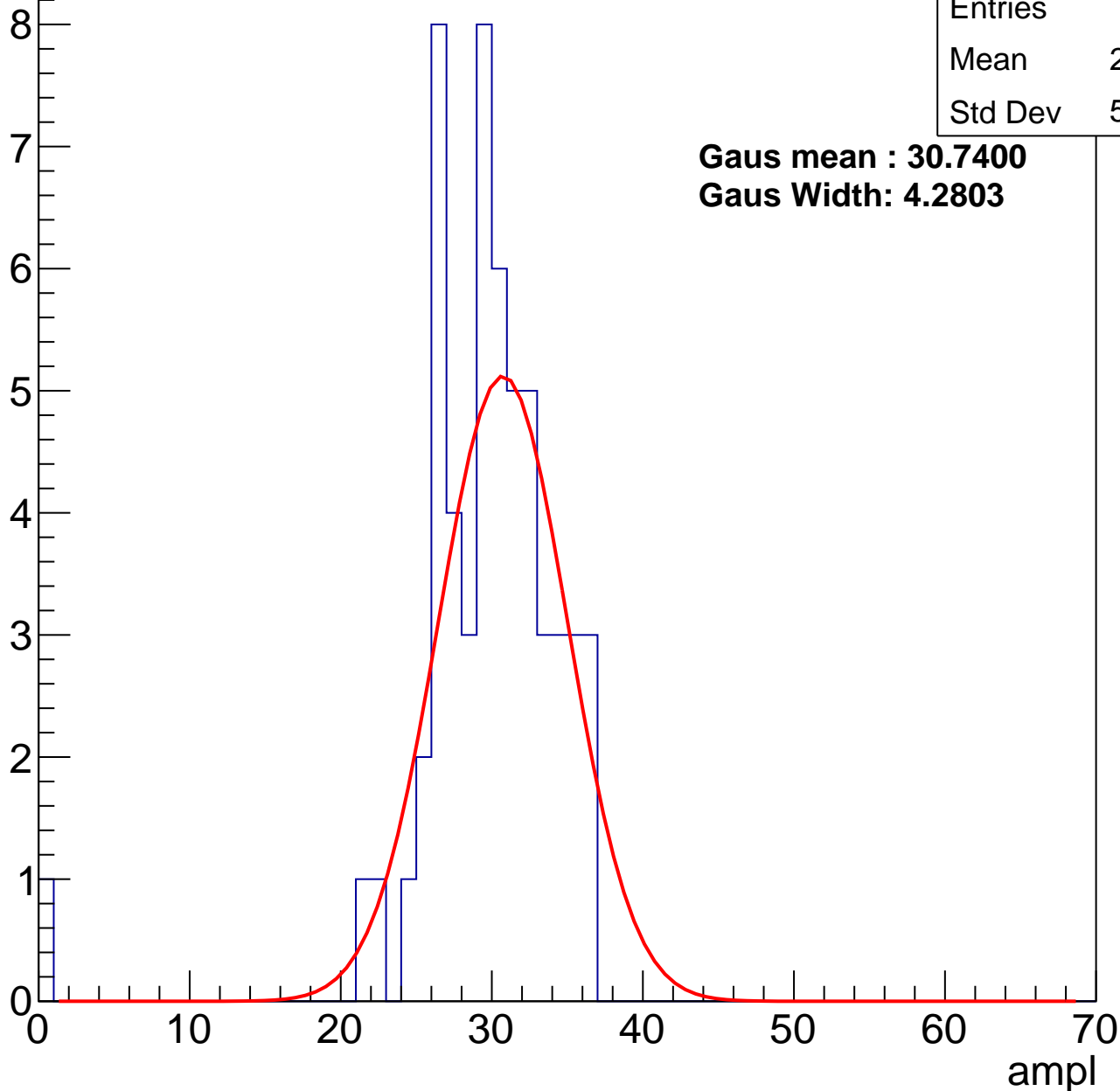
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 29.09 |
| Std Dev | 5.216 |

Gaus mean : 30.7400

Gaus Width: 4.2803



B1L102S, U15-ch46, adc1

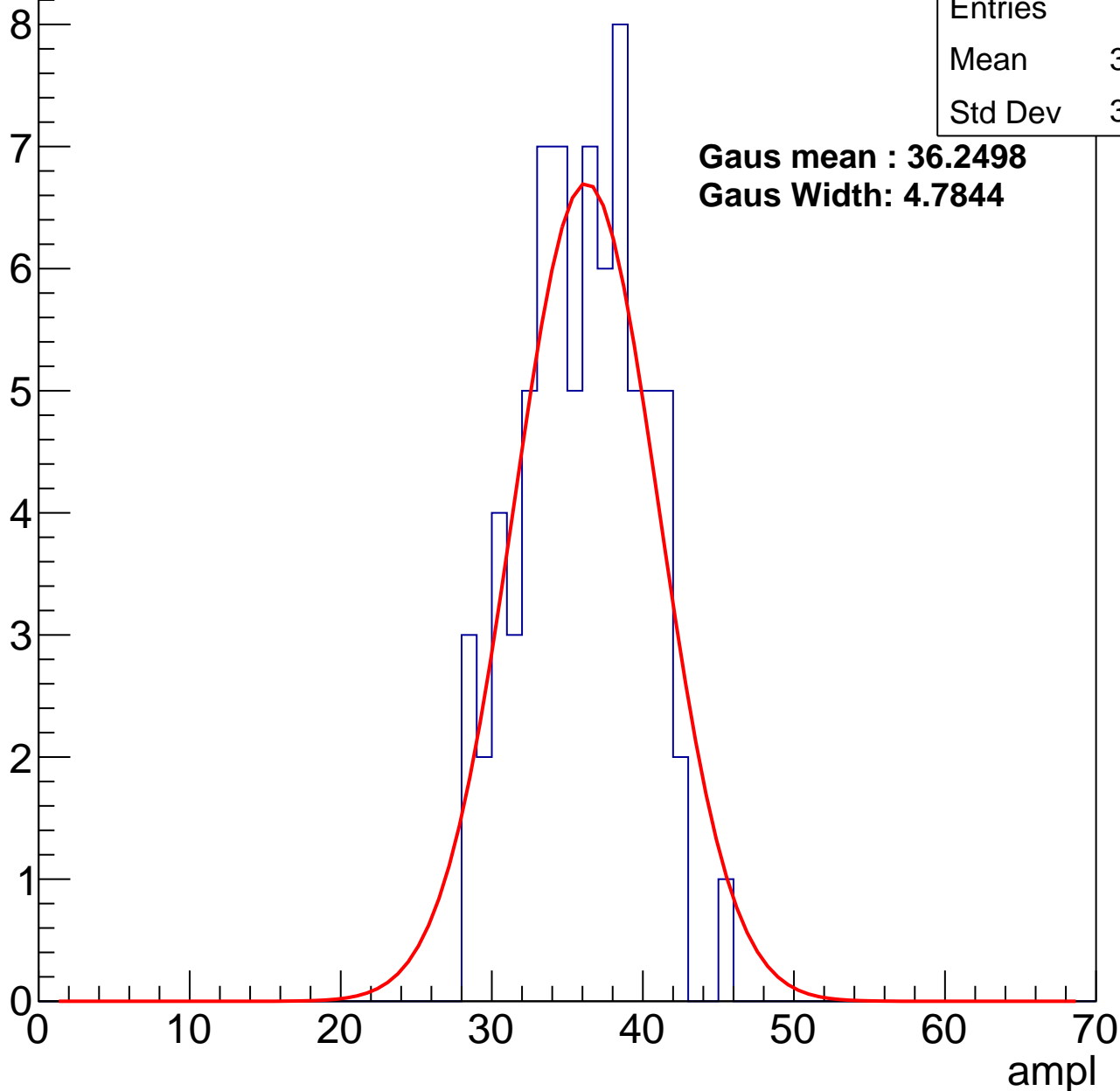
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 35.55 |
| Std Dev | 3.848 |

Gaus mean : 36.2498

Gaus Width: 4.7844



B1L102S, U15-ch46, adc2

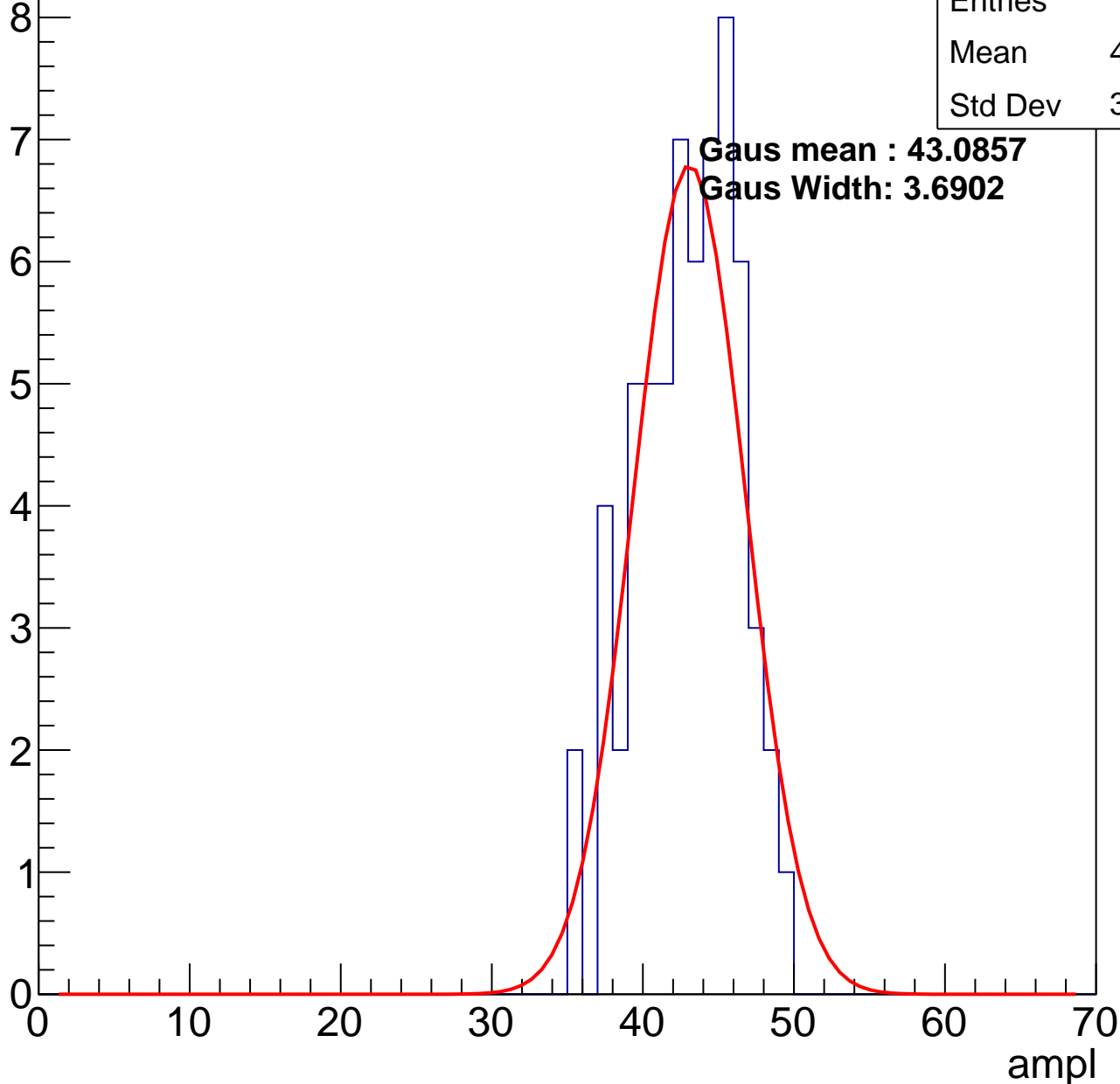
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 42.48 |
| Std Dev | 3.294 |

Gaus mean : 43.0857

Gaus Width: 3.6902

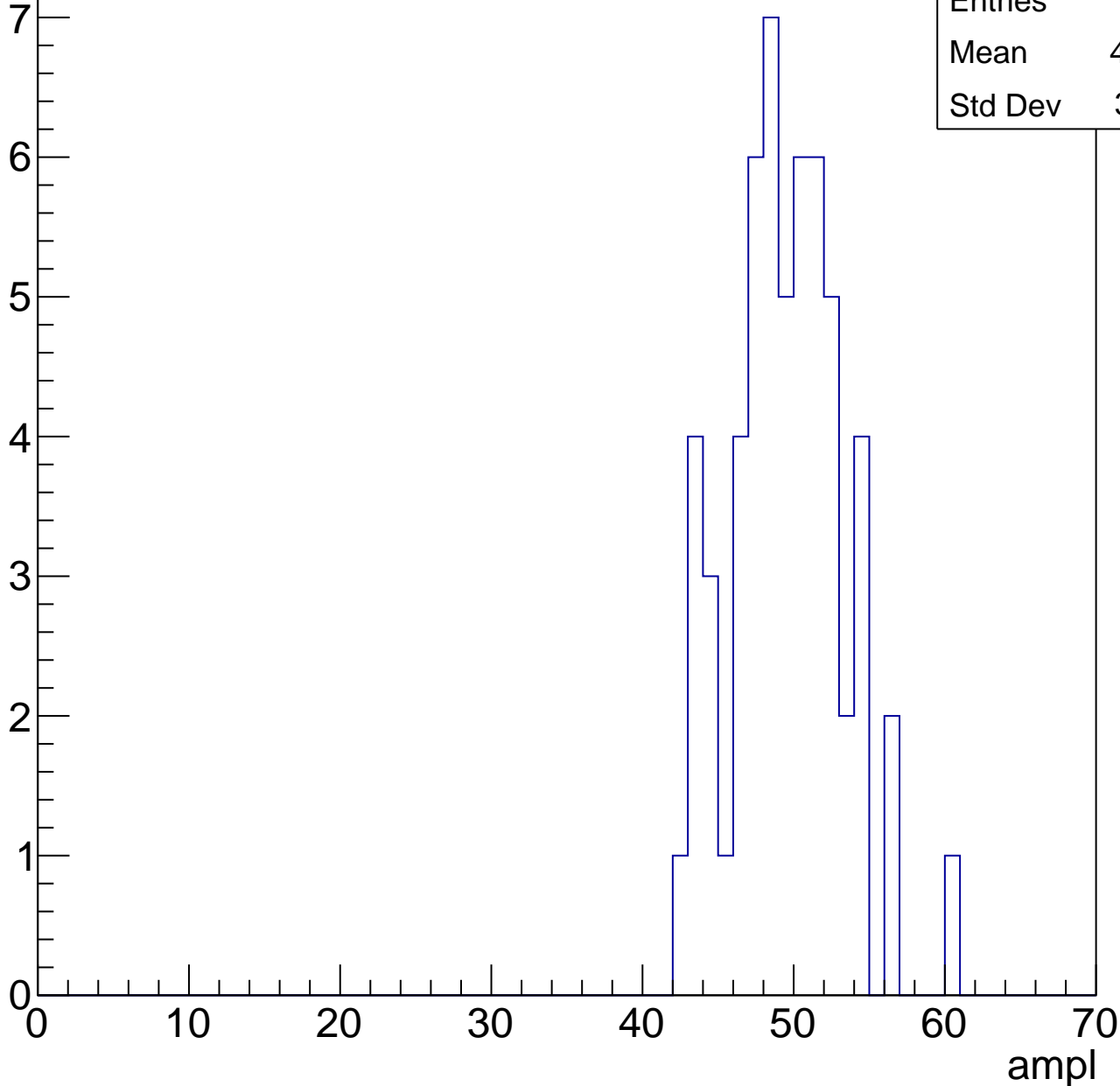


B1L102S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 49.09 |
| Std Dev | 3.691 |

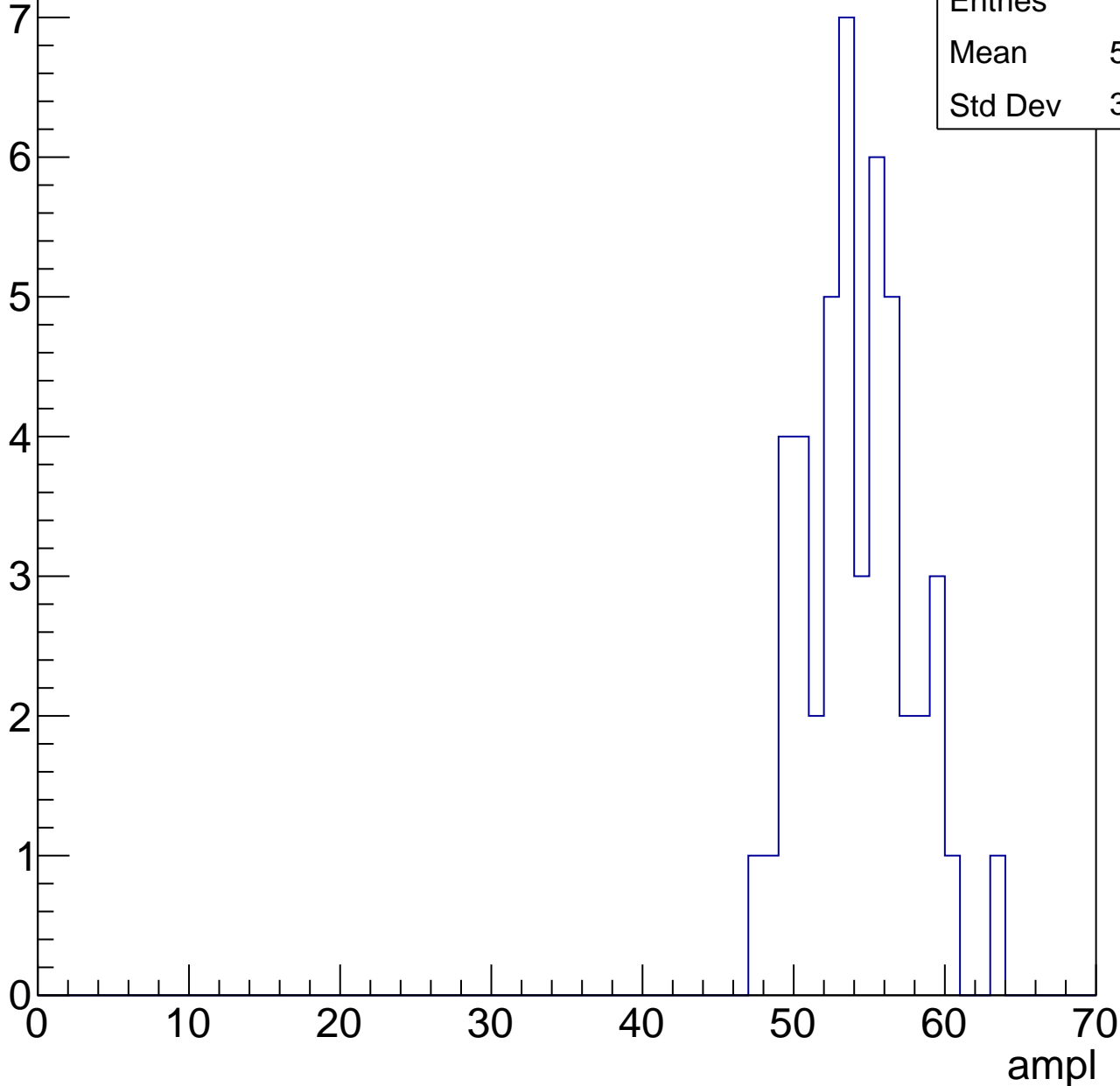


B1L102S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 53.74 |
| Std Dev | 3.449 |

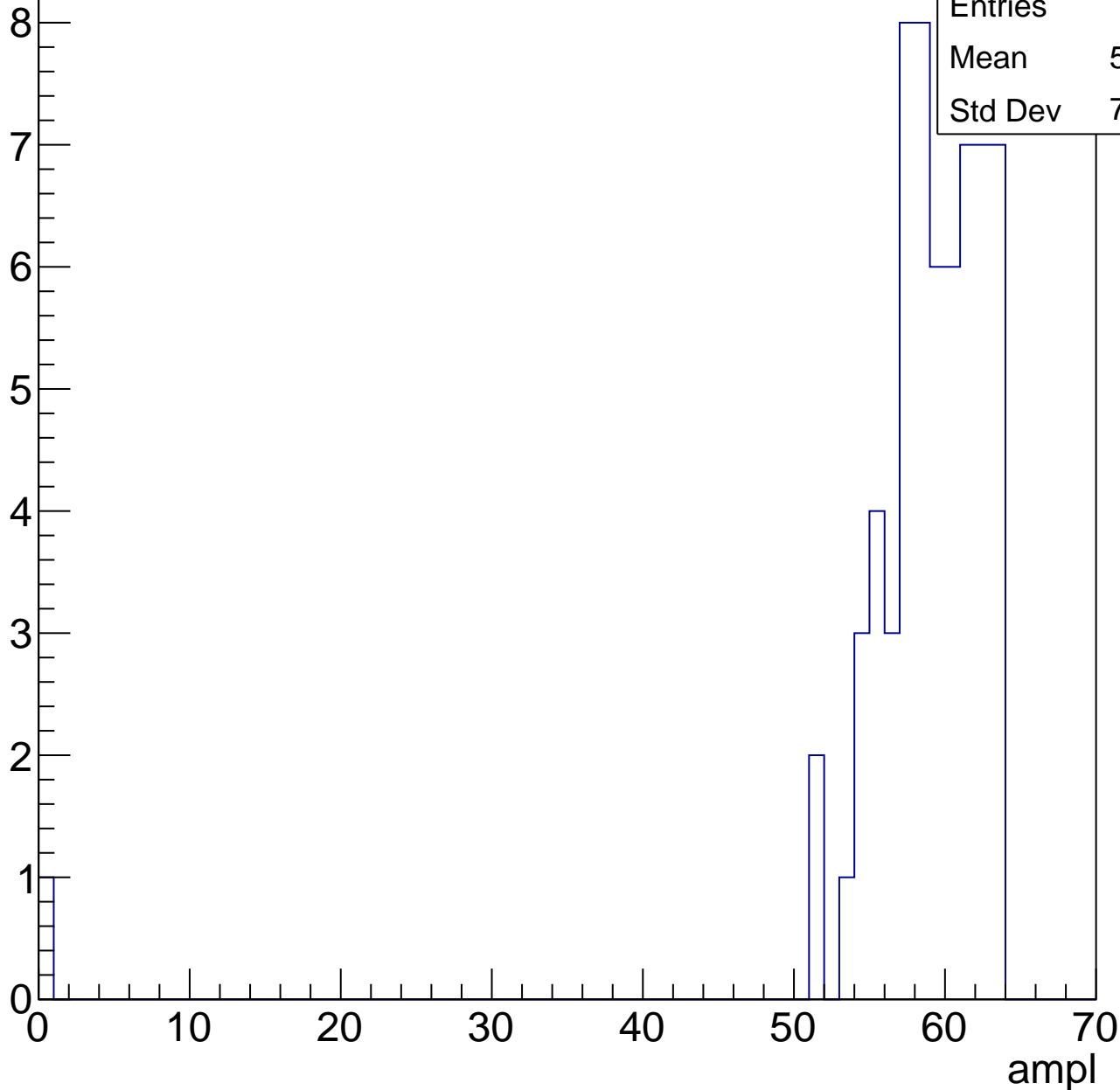


B1L102S, U15-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

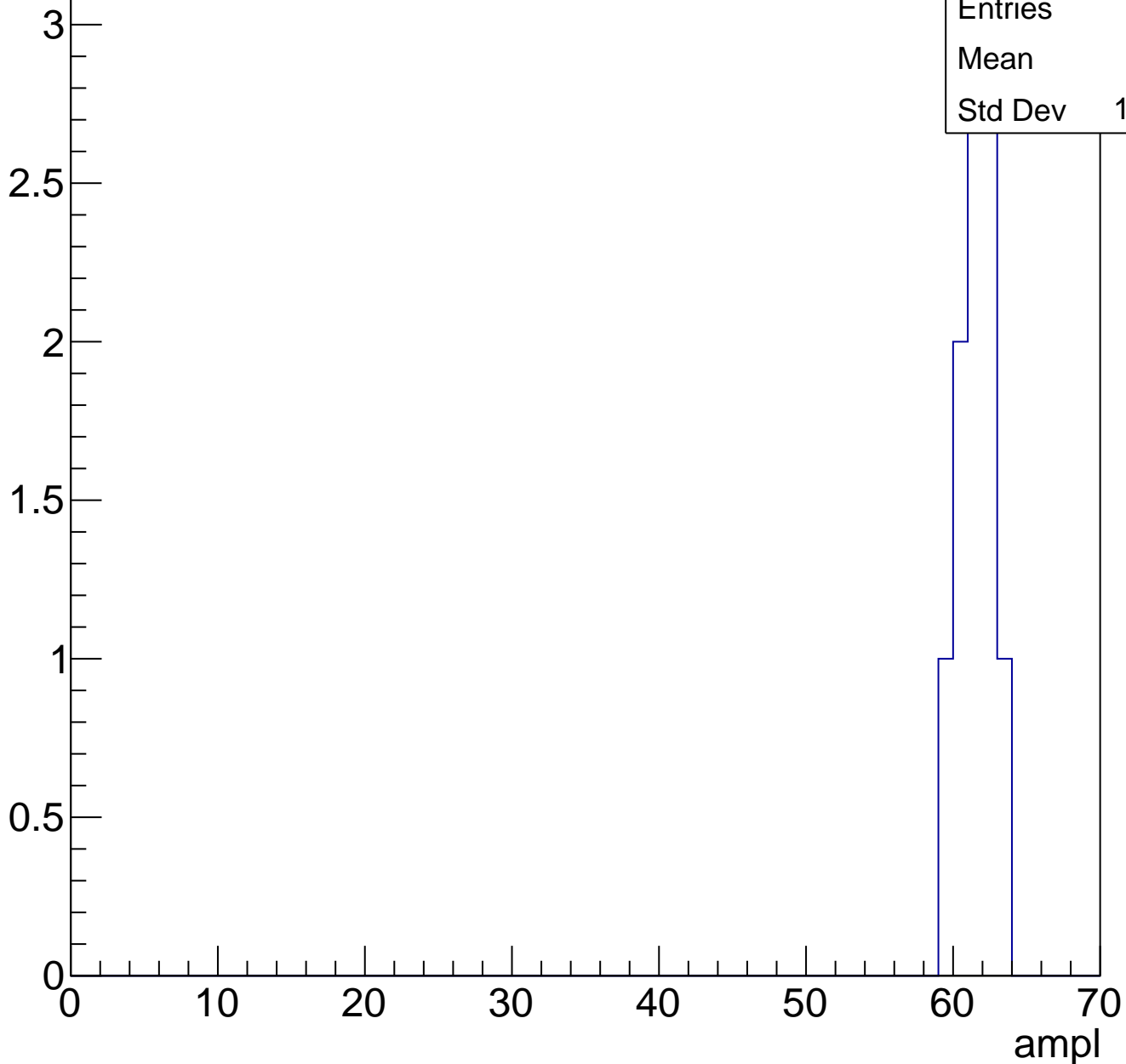
| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.79 |
| Std Dev | 7.937 |



B1L102S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

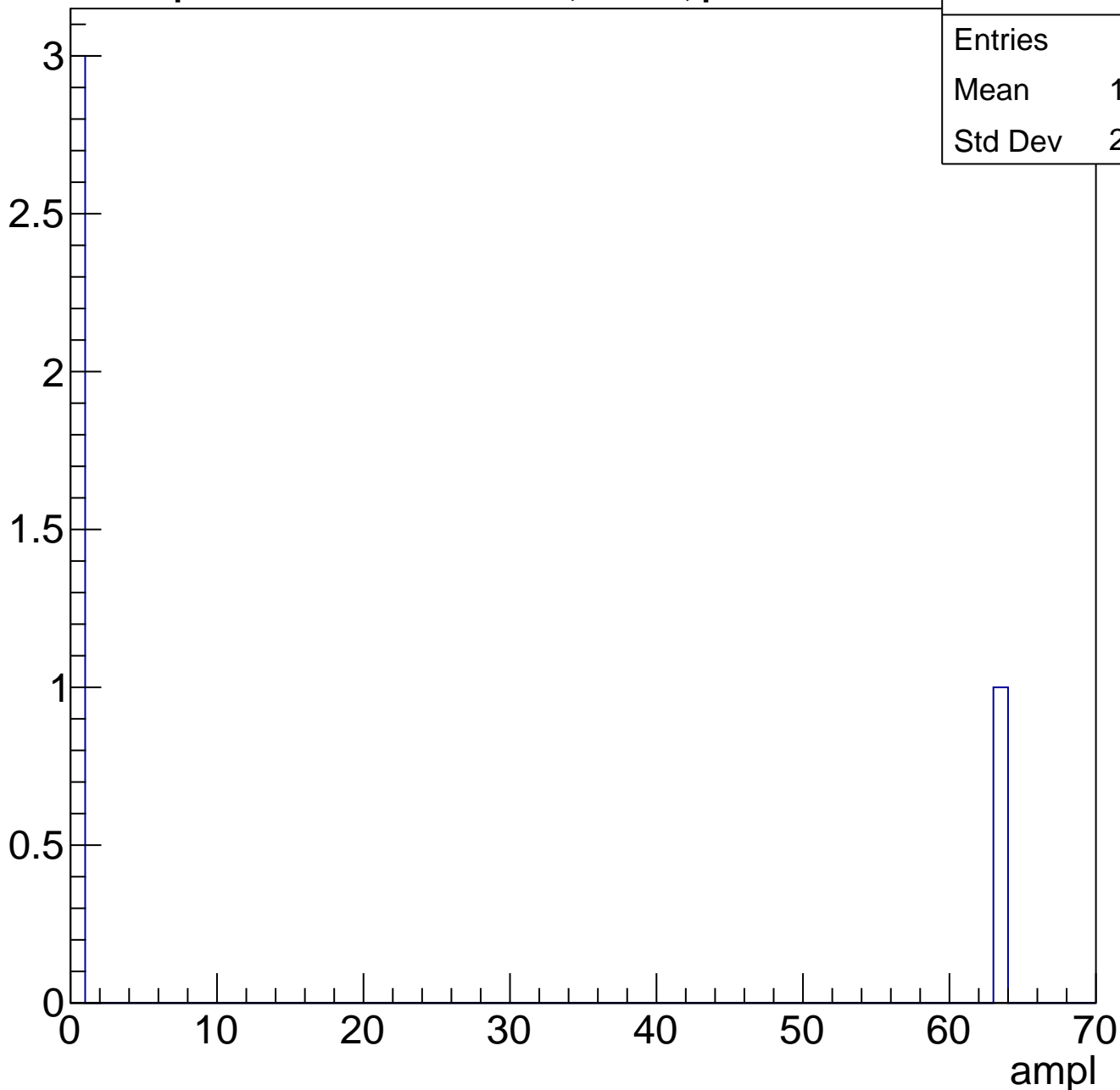
Entry



B1L102S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch47, adc0

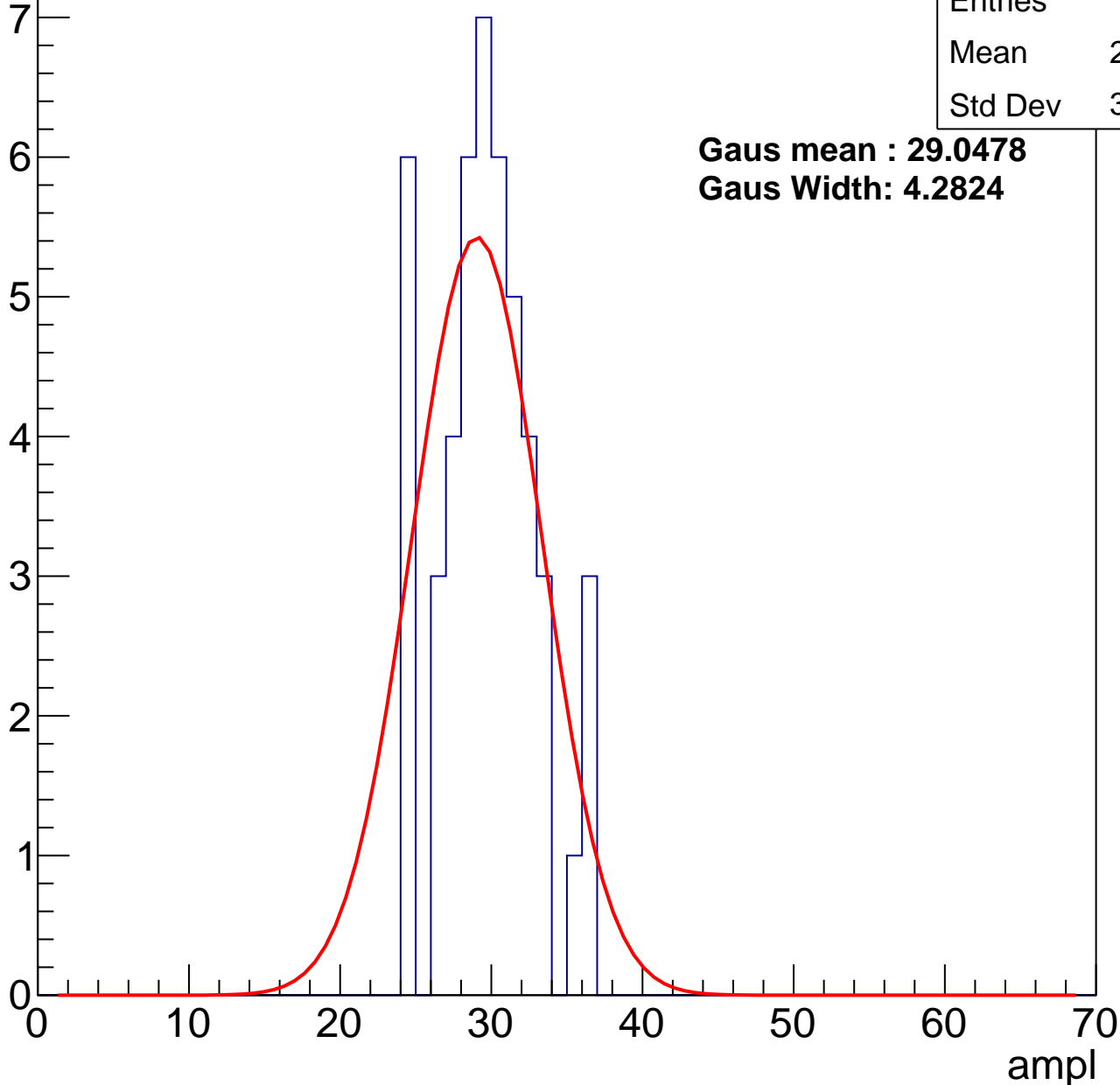
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 29.29 |
| Std Dev | 3.188 |

Gaus mean : 29.0478

Gaus Width: 4.2824



B1L102S, U15-ch47, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 34.9 |
| Std Dev | 3.595 |

Gaus mean : 35.6422

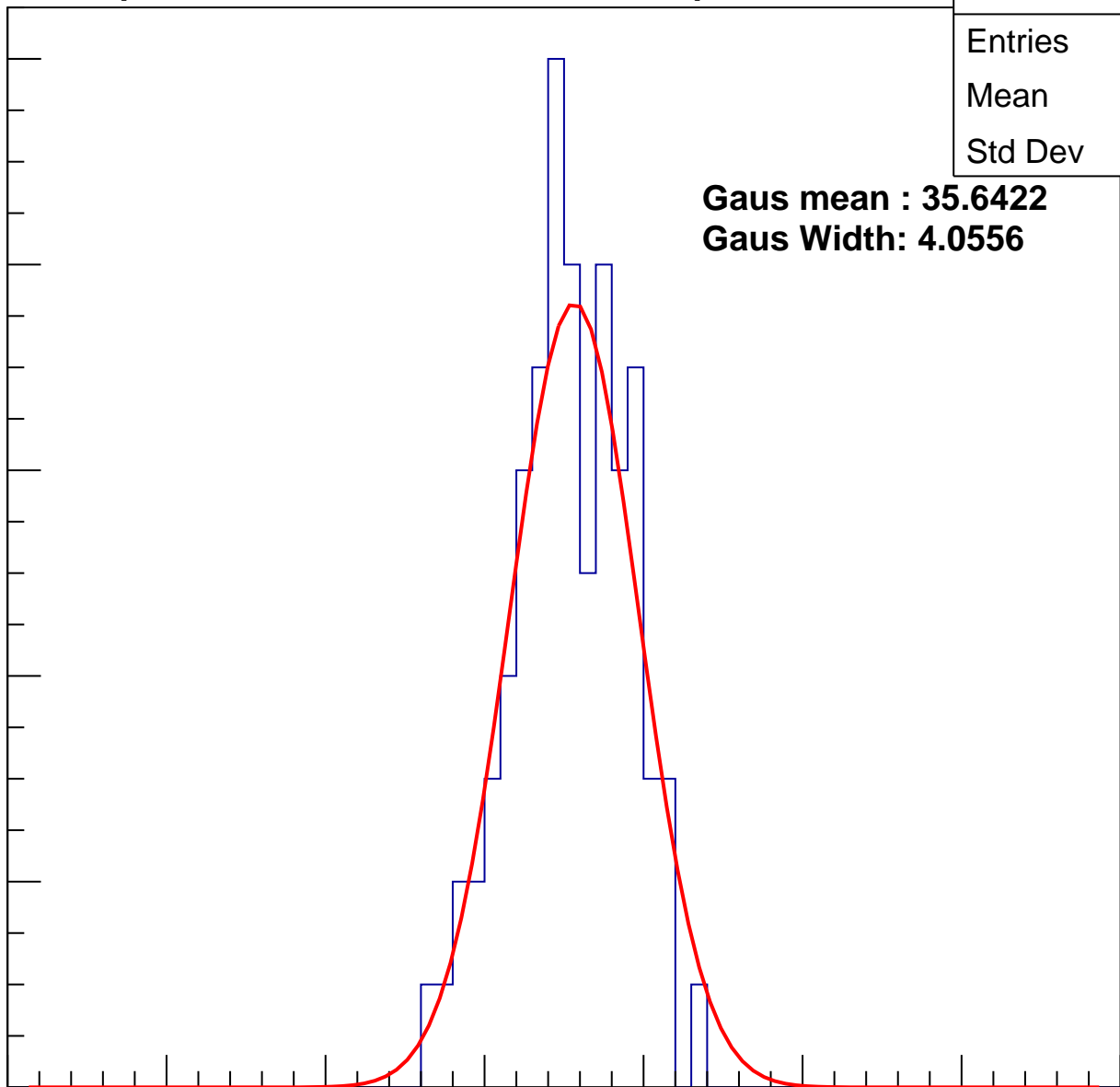
Gaus Width: 4.0556

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch47, adc2

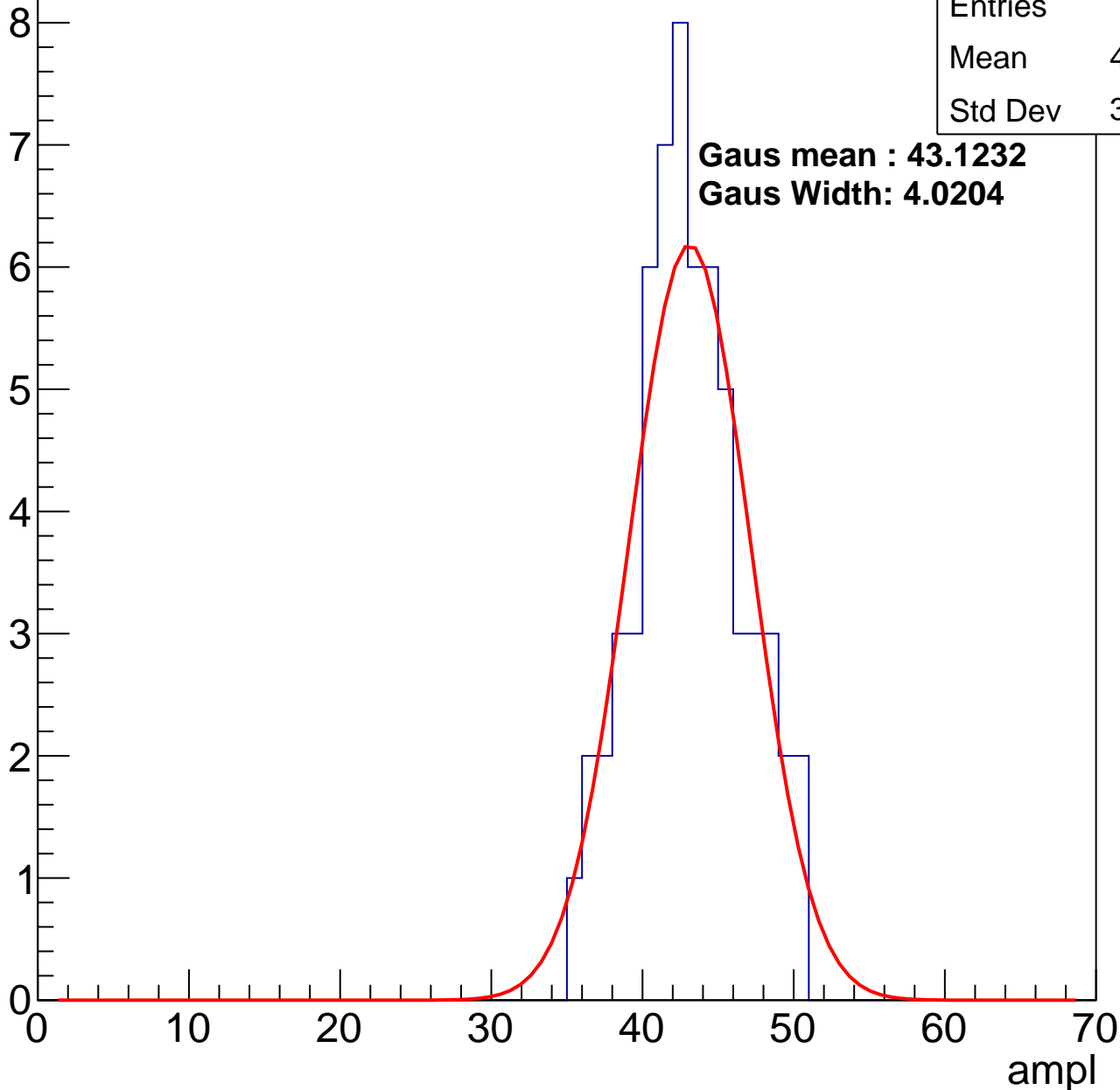
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.63 |
| Std Dev | 3.557 |

Gaus mean : 43.1232

Gaus Width: 4.0204

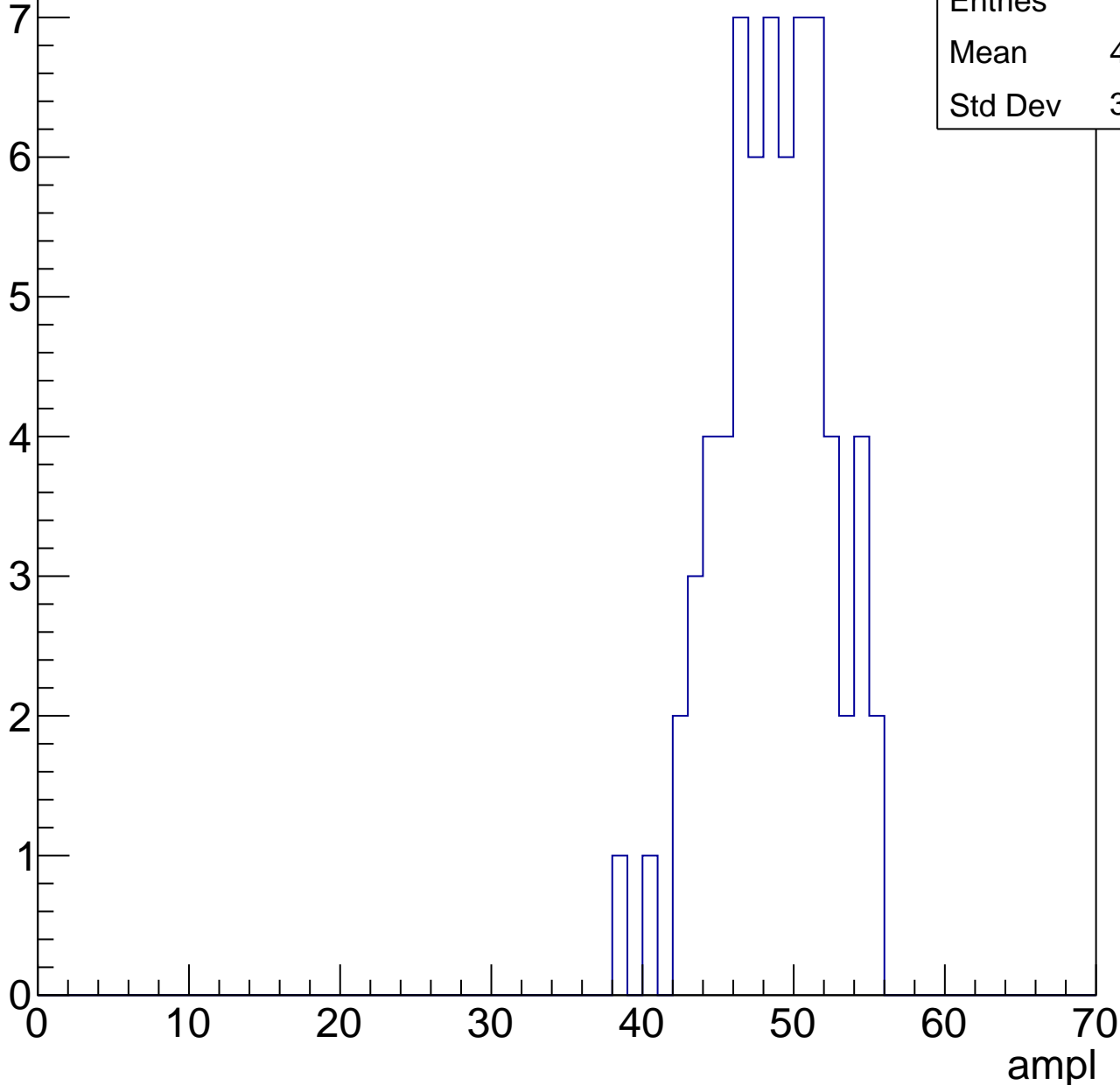


B1L102S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 48.18 |
| Std Dev | 3.657 |

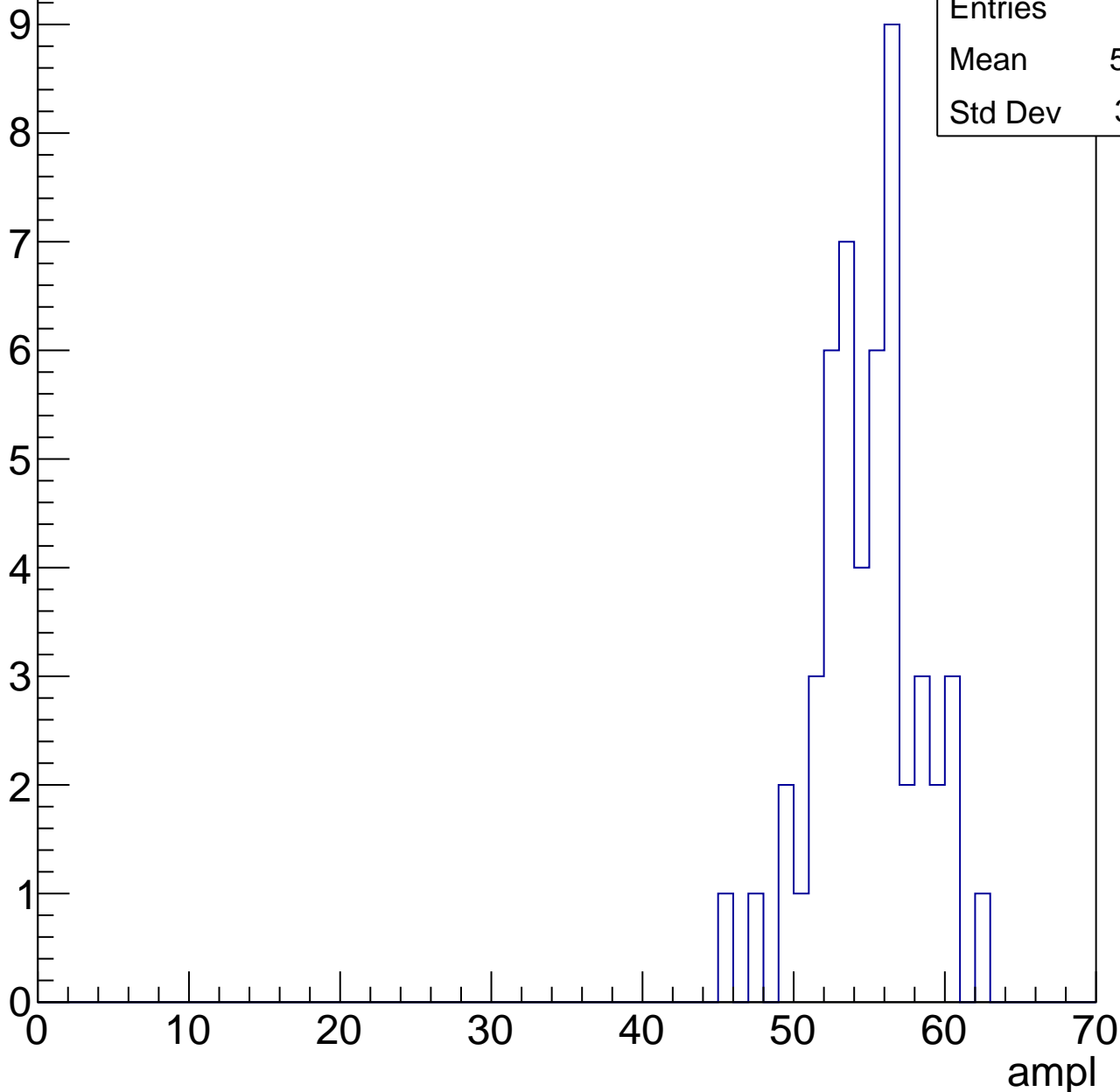


B1L102S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

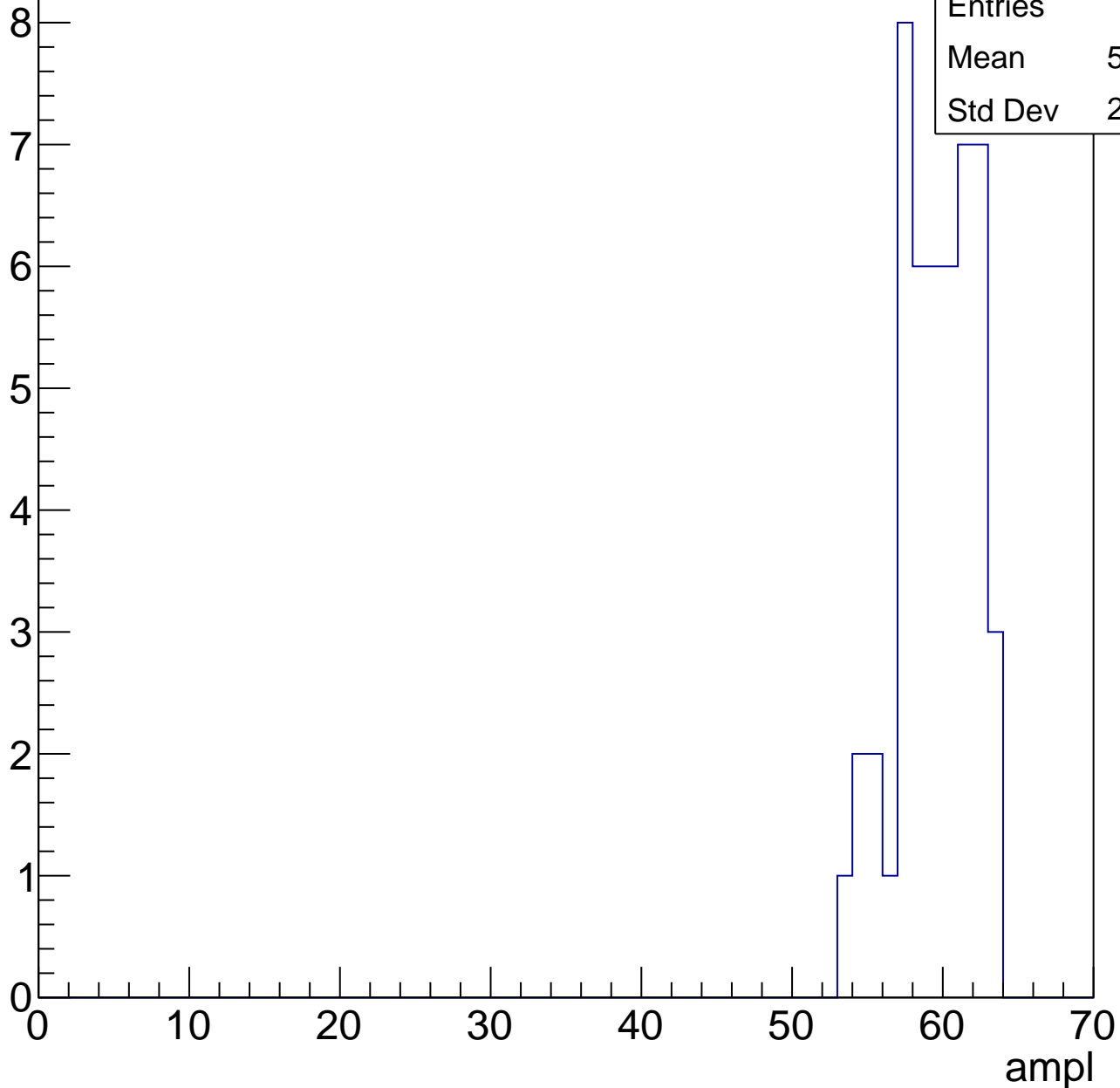
| | |
|---------|-------|
| Entries | 51 |
| Mean | 54.39 |
| Std Dev | 3.361 |



B1L102S, U15-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

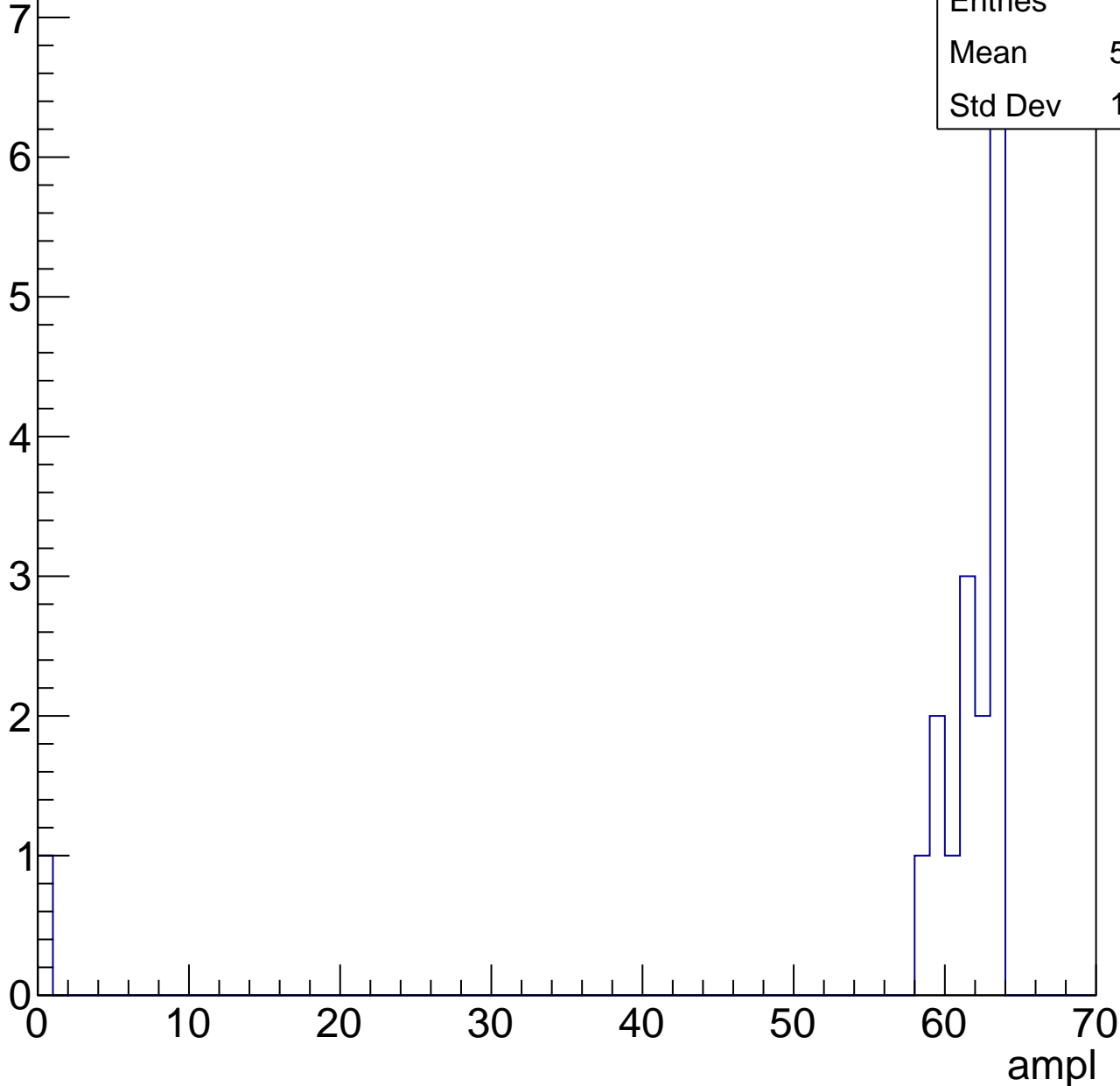


B1L102S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 57.88 |
| Std Dev | 14.56 |



B1L102S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 3 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch48, adc0

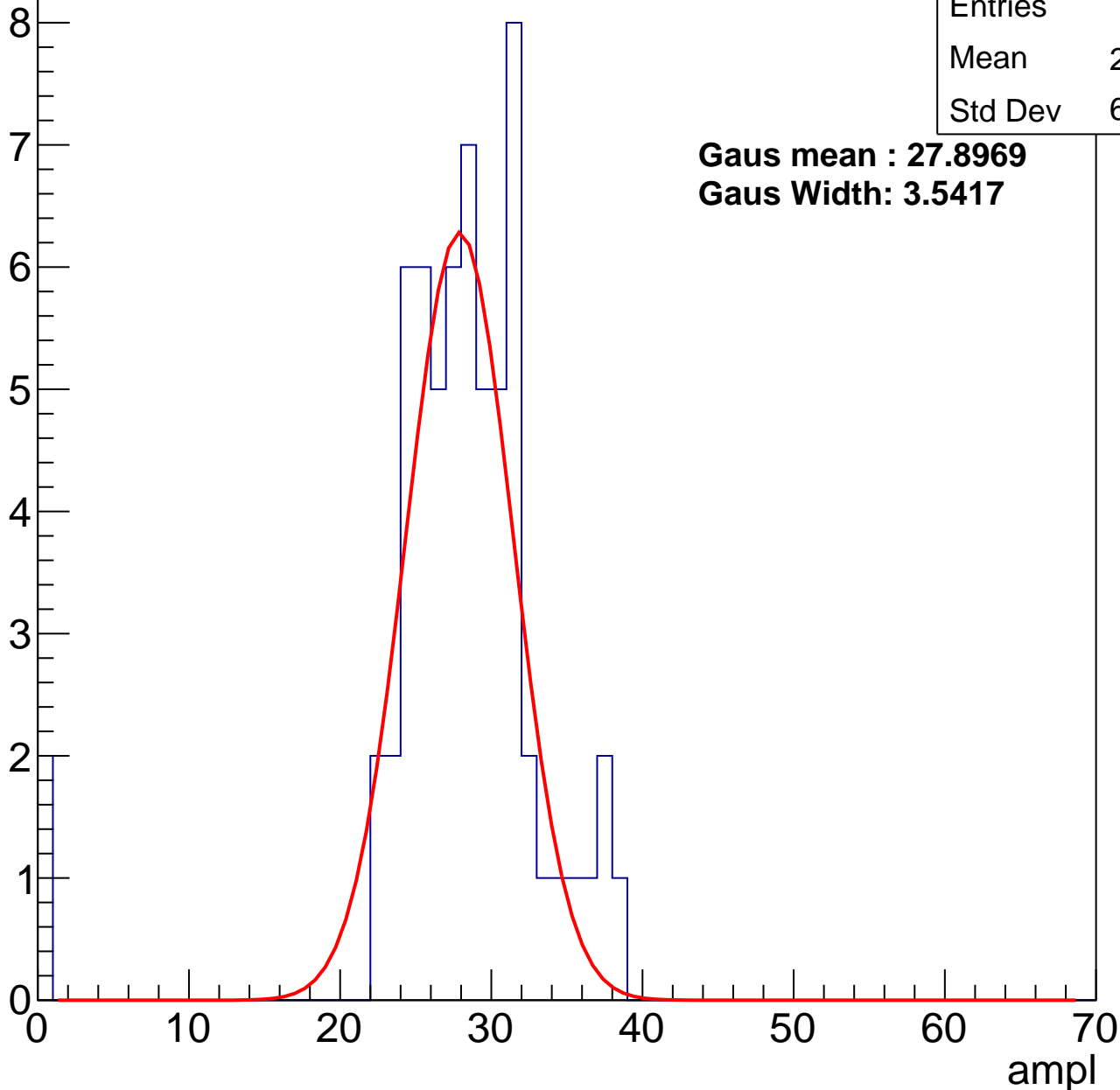
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 27.44 |
| Std Dev | 6.187 |

Gaus mean : 27.8969

Gaus Width: 3.5417



B1L102S, U15-ch48, adc1

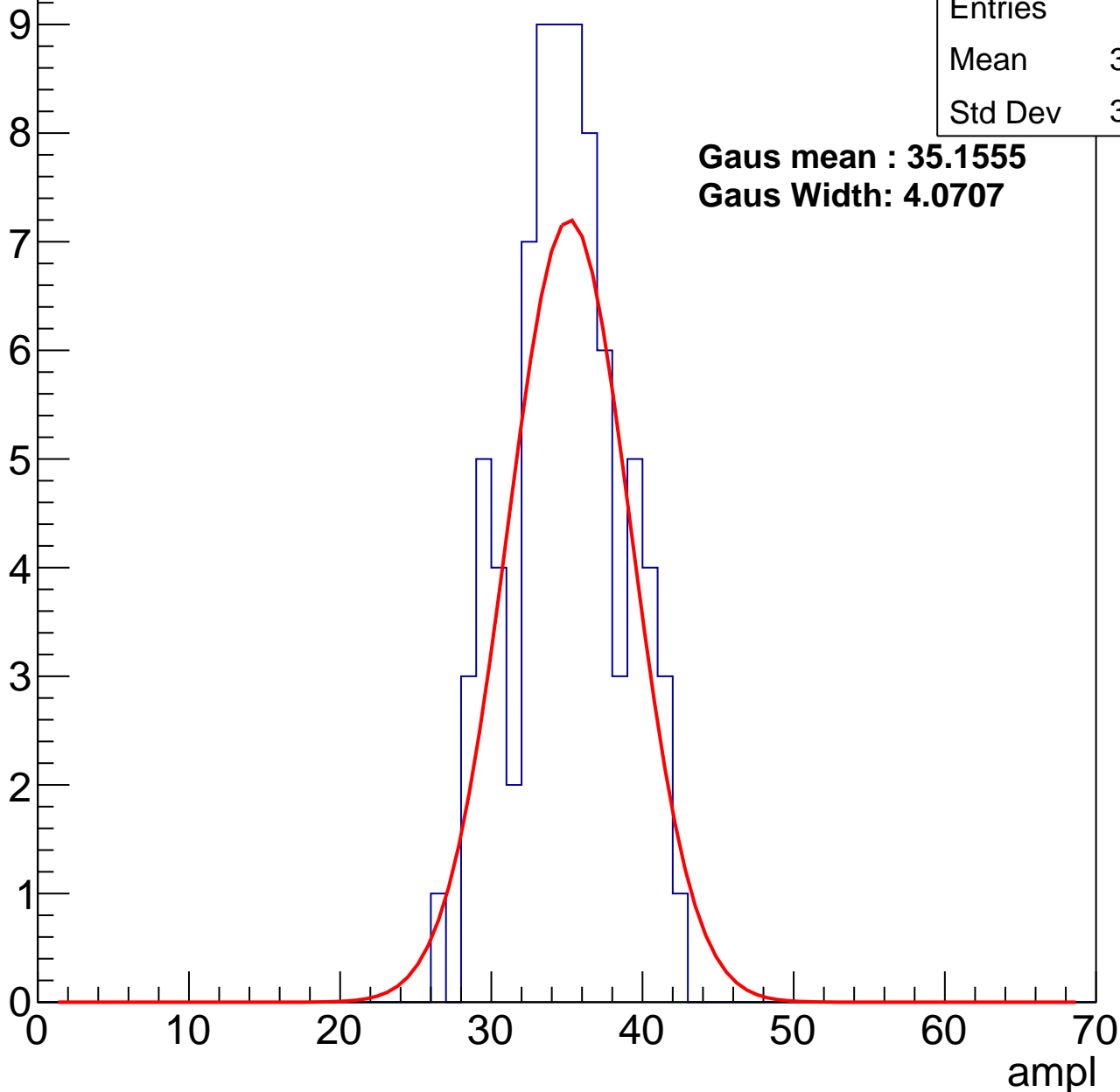
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 34.47 |
| Std Dev | 3.596 |

Gaus mean : 35.1555

Gaus Width: 4.0707



B1L102S, U15-ch48, adc2

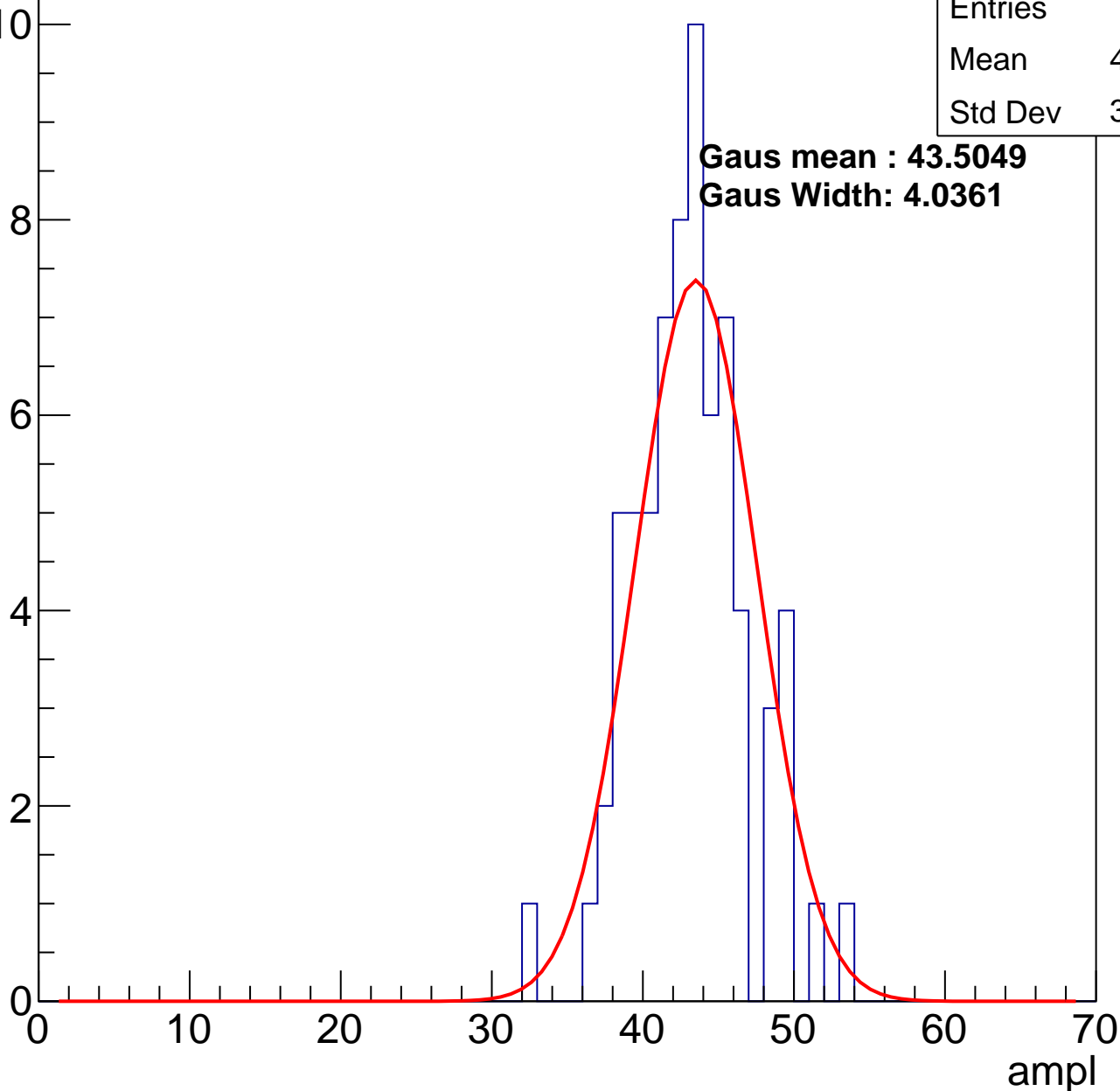
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 42.67 |
| Std Dev | 3.729 |

Gaus mean : 43.5049

Gaus Width: 4.0361

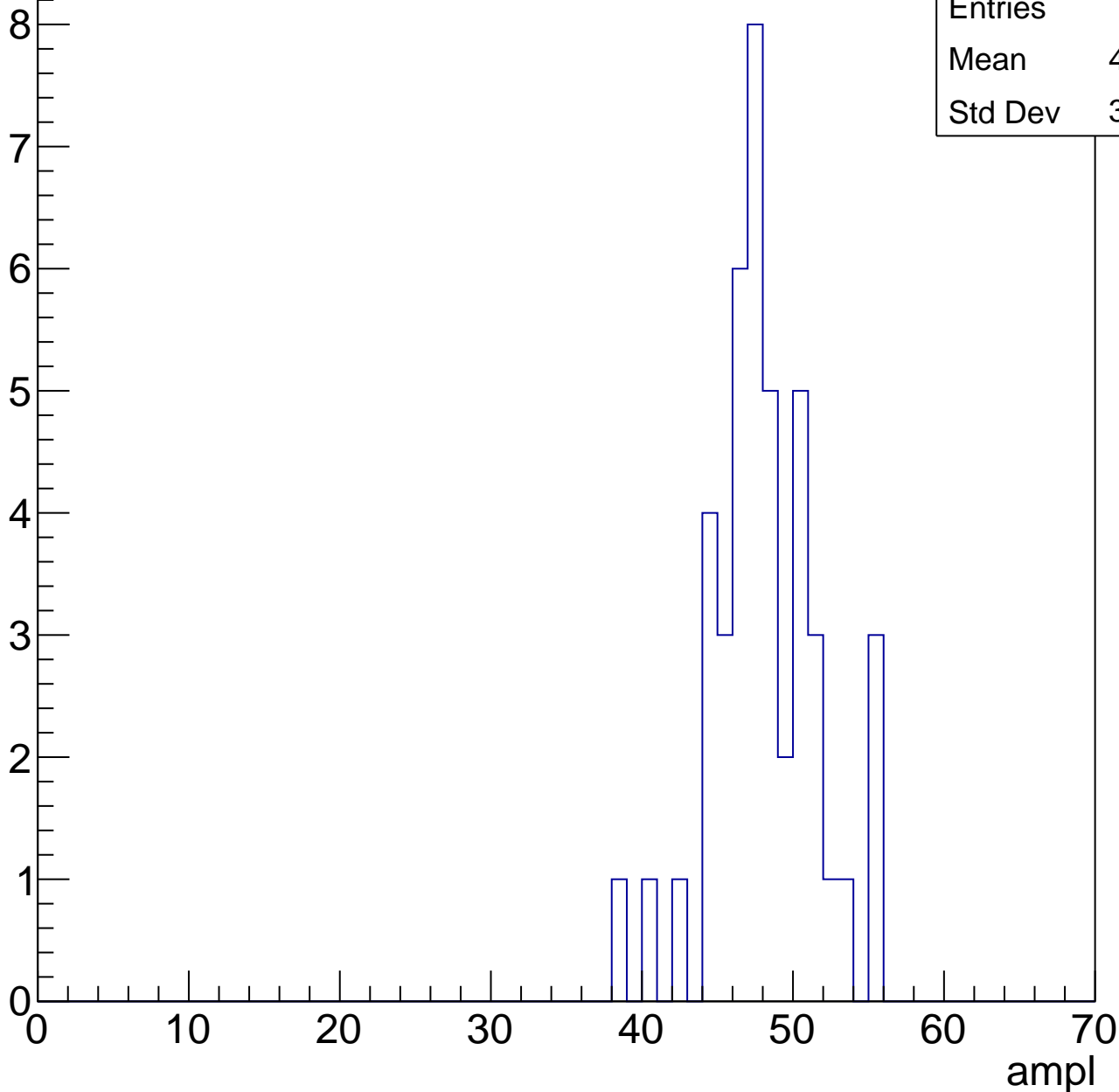


B1L102S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 47.59 |
| Std Dev | 3.544 |

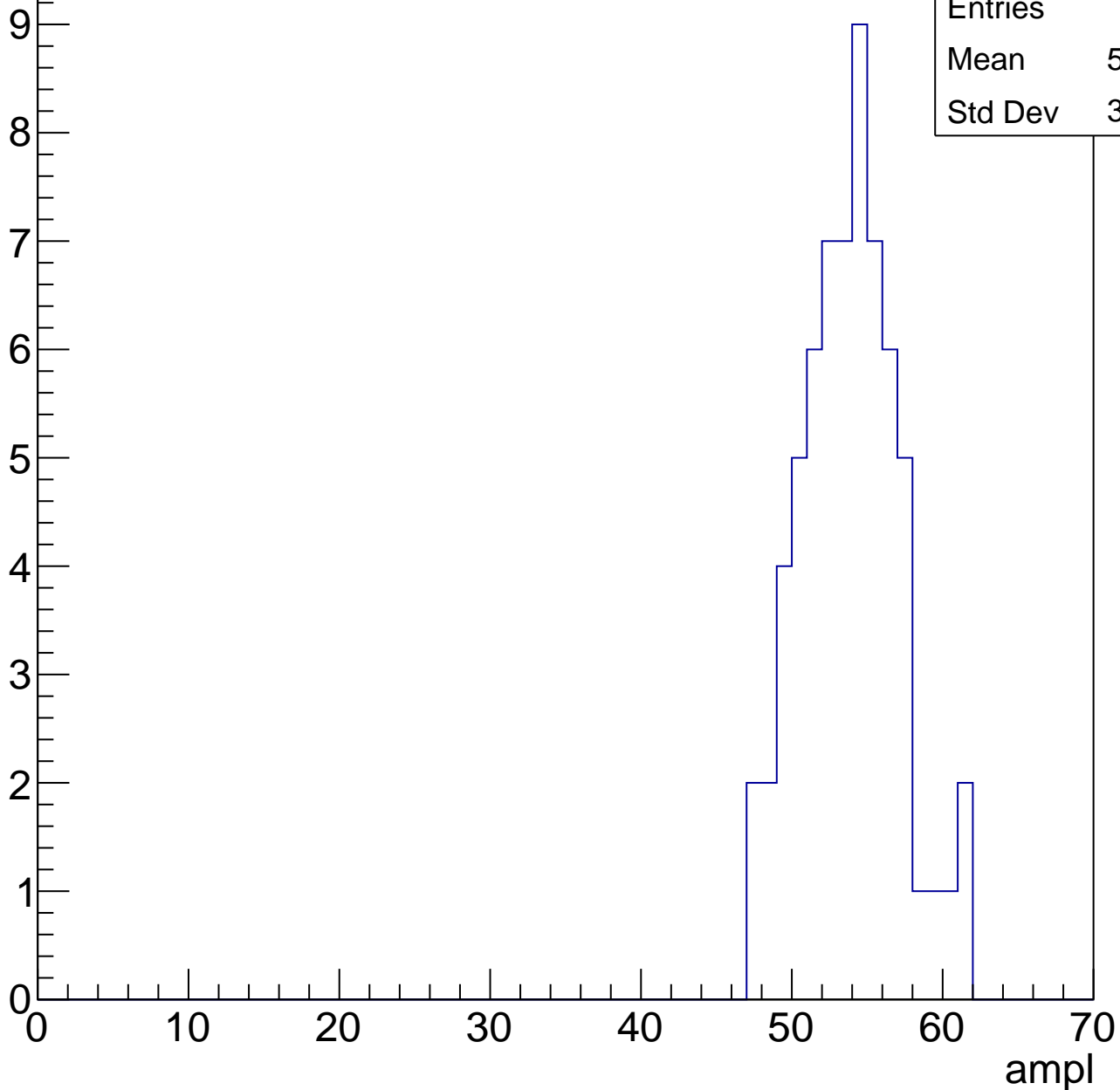


B1L102S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 53.35 |
| Std Dev | 3.193 |

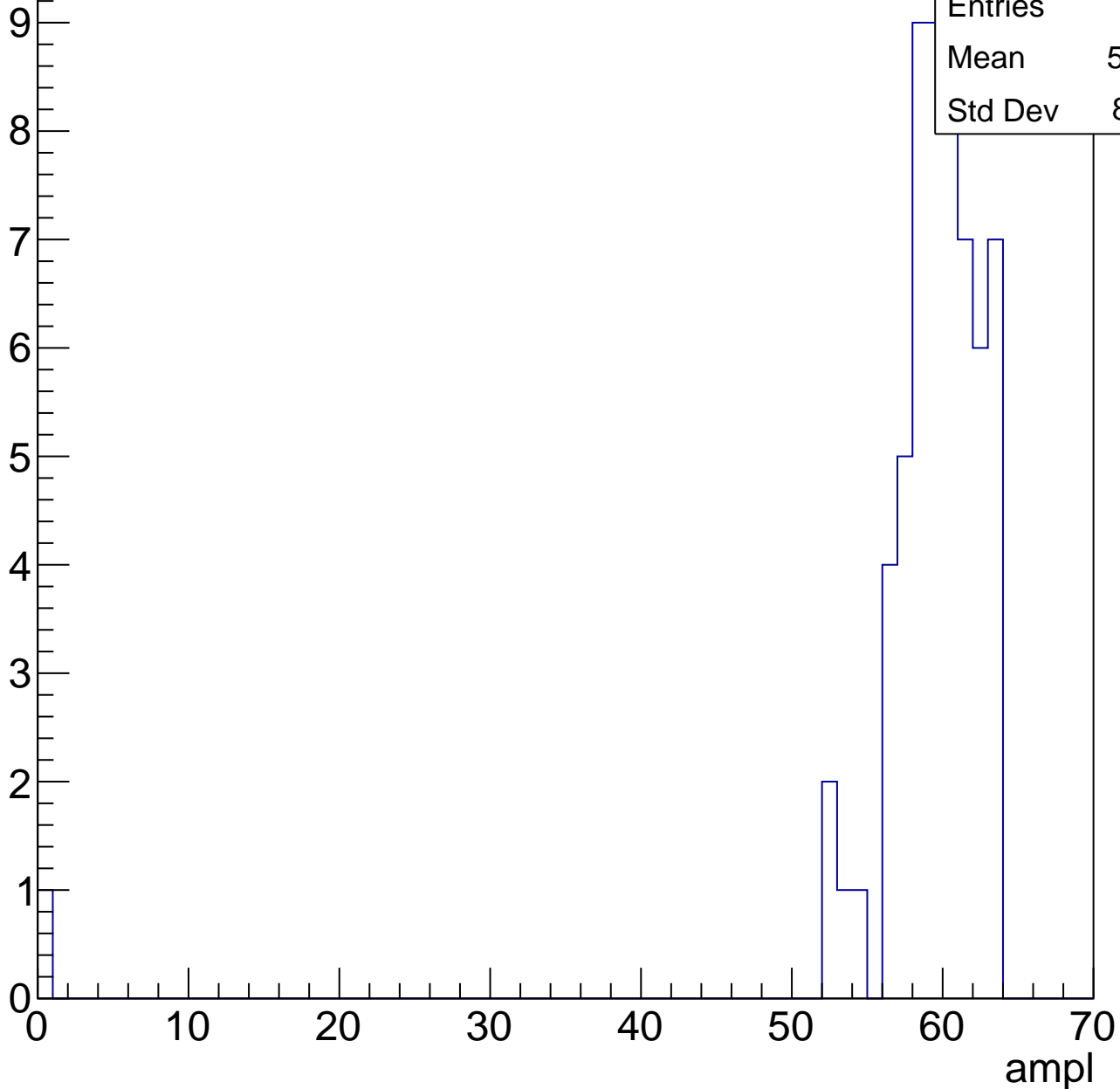


B1L102S, U15-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 58.22 |
| Std Dev | 8.031 |



B1L102S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62

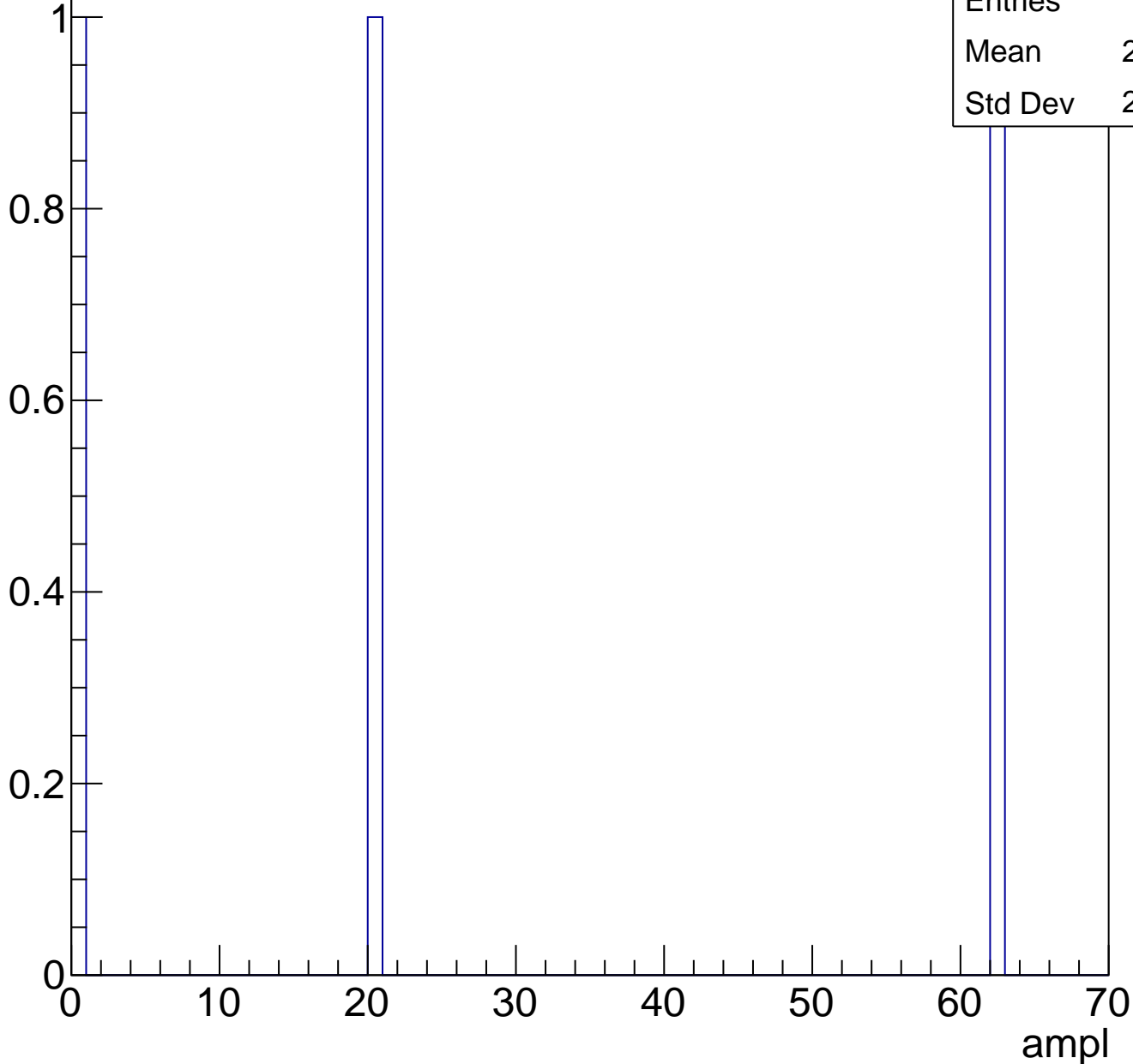
Std Dev

1

B1L102S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 27.33 |
| Std Dev | 25.84 |

B1L102S, U15-ch49, adc0

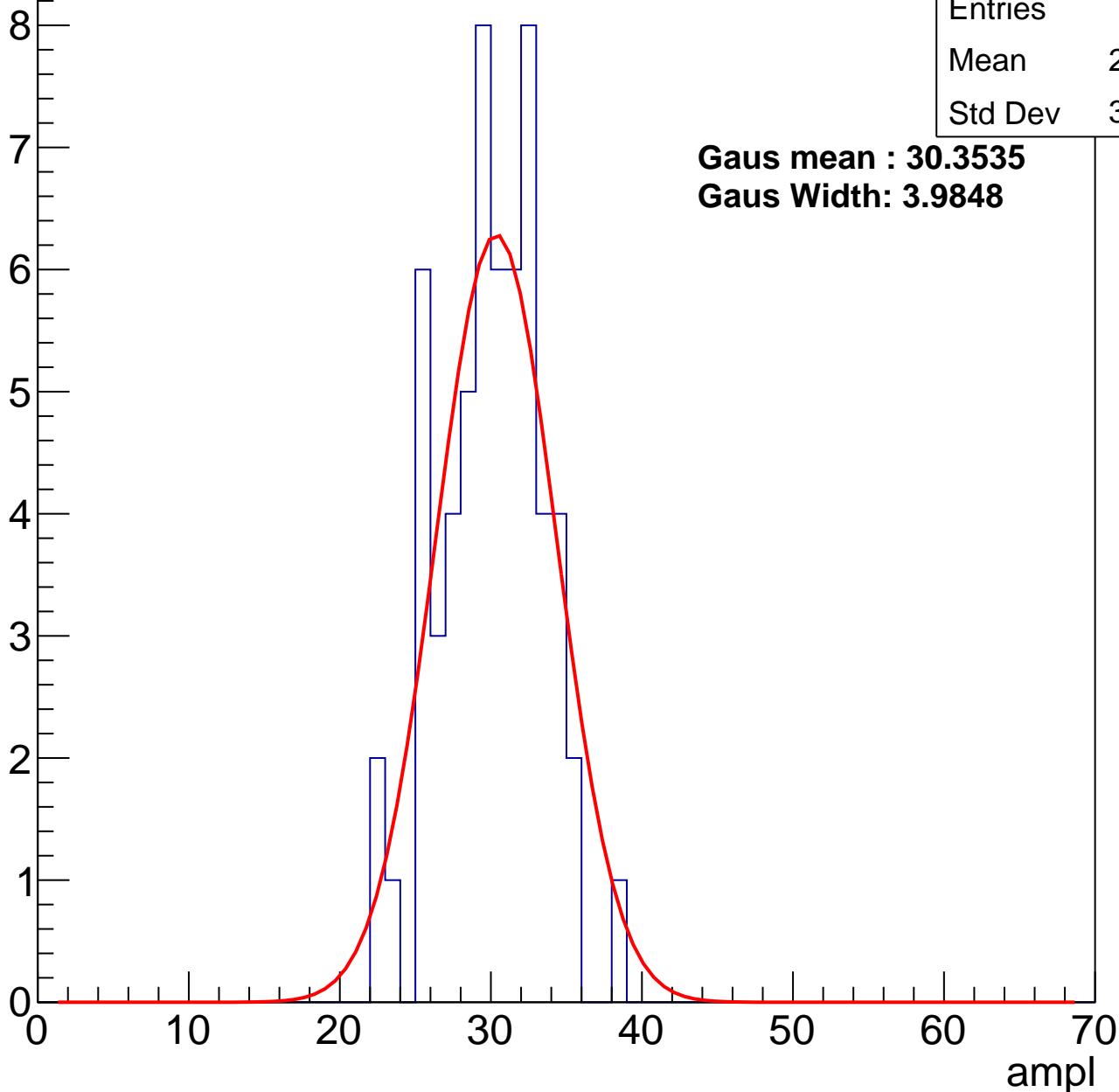
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 29.55 |
| Std Dev | 3.364 |

Gaus mean : 30.3535

Gaus Width: 3.9848



B1L102S, U15-ch49, adc1

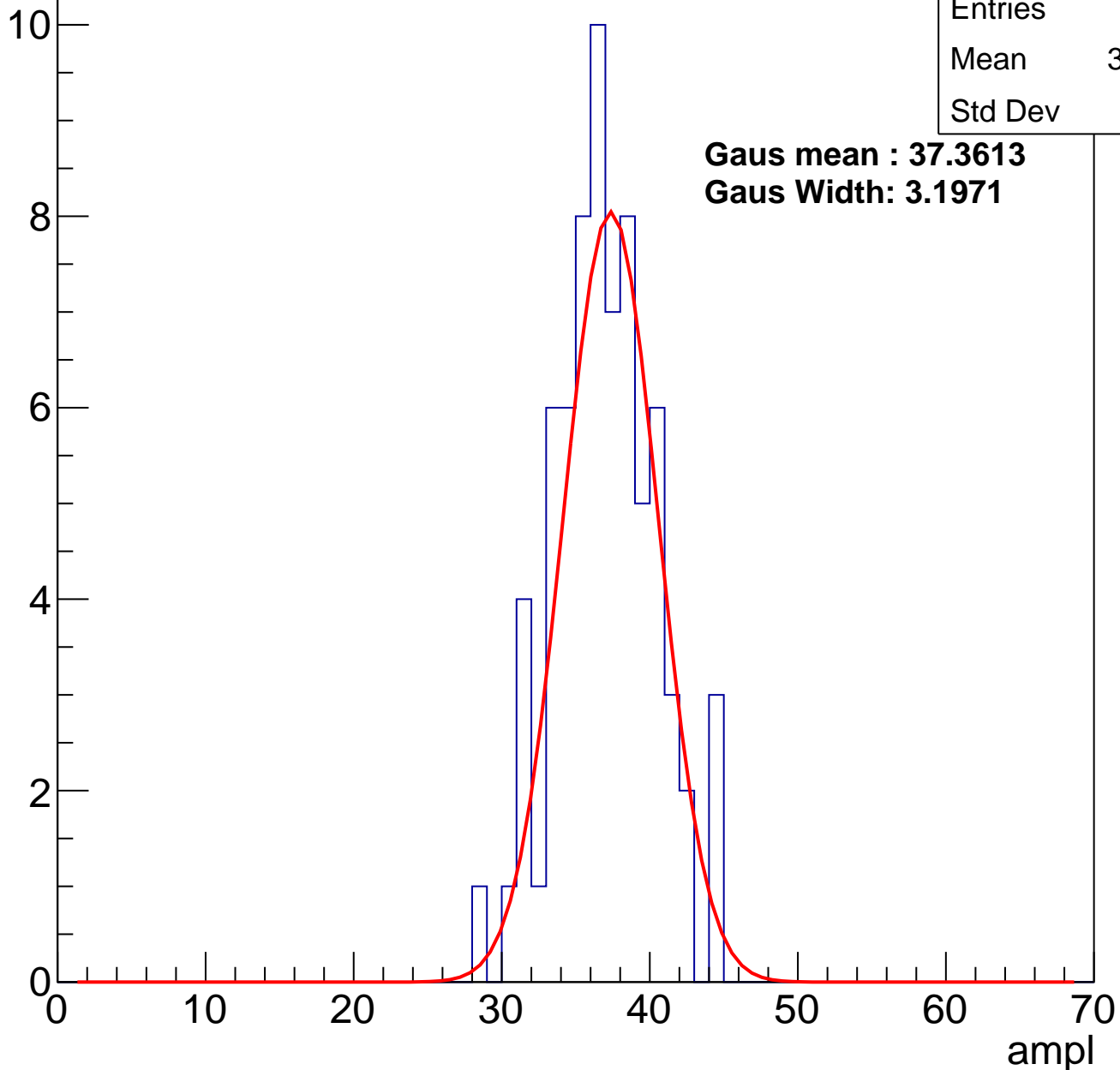
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 71 |
| Mean | 36.52 |
| Std Dev | 3.36 |

Gaus mean : 37.3613

Gaus Width: 3.1971

Entry



B1L102S, U15-ch49, adc2

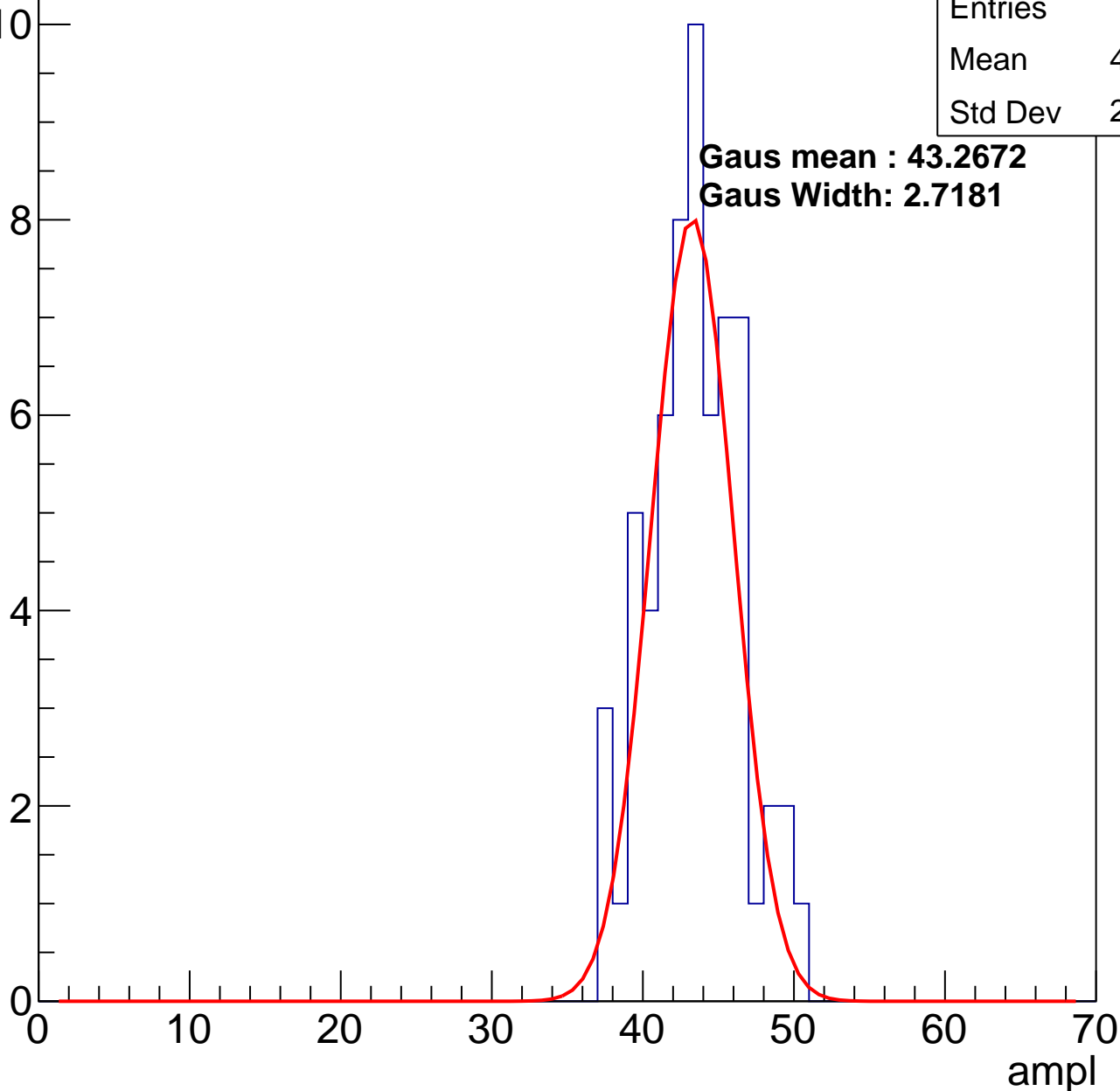
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 42.98 |
| Std Dev | 2.995 |

Gaus mean : 43.2672

Gaus Width: 2.7181

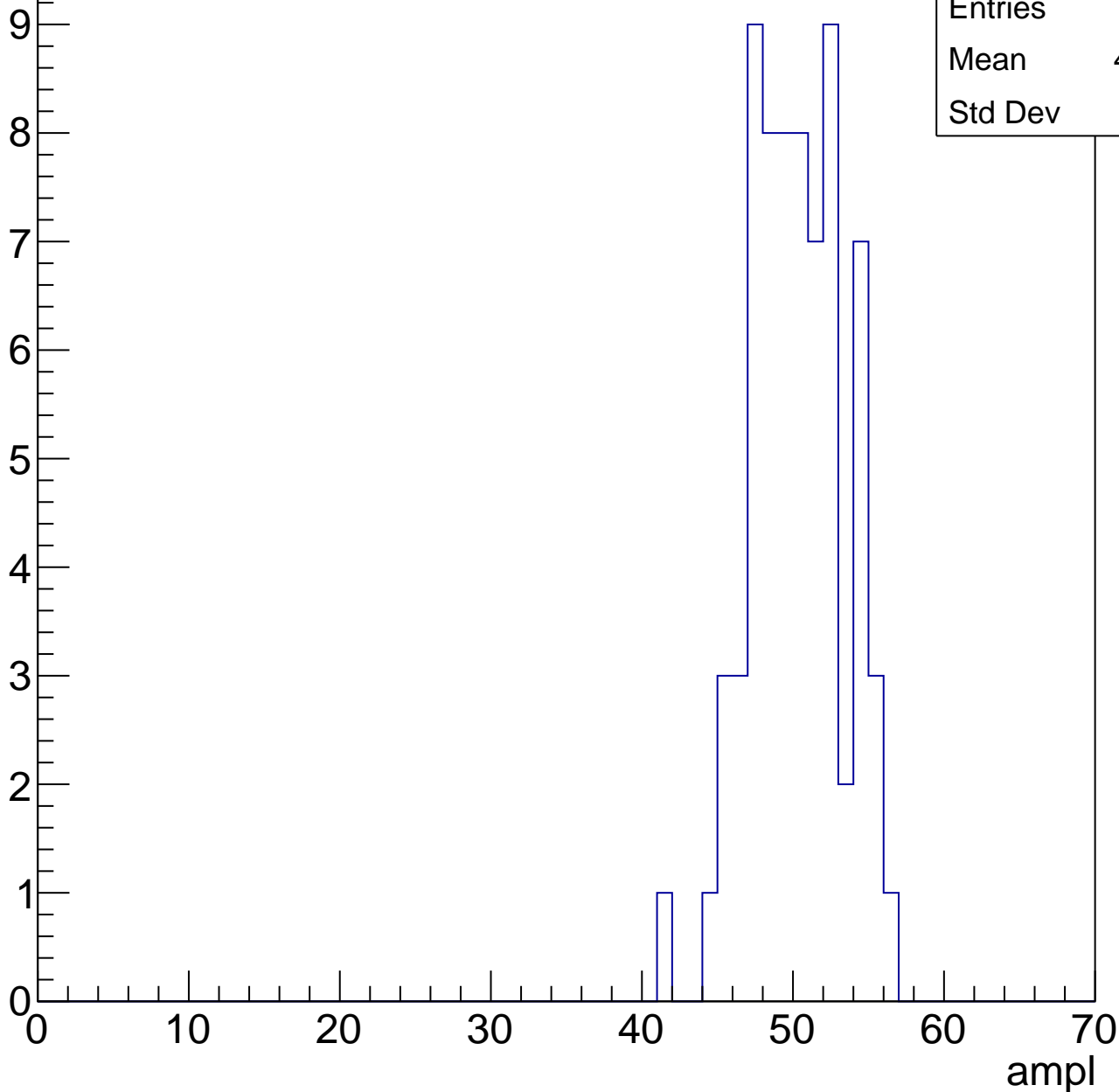


B1L102S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

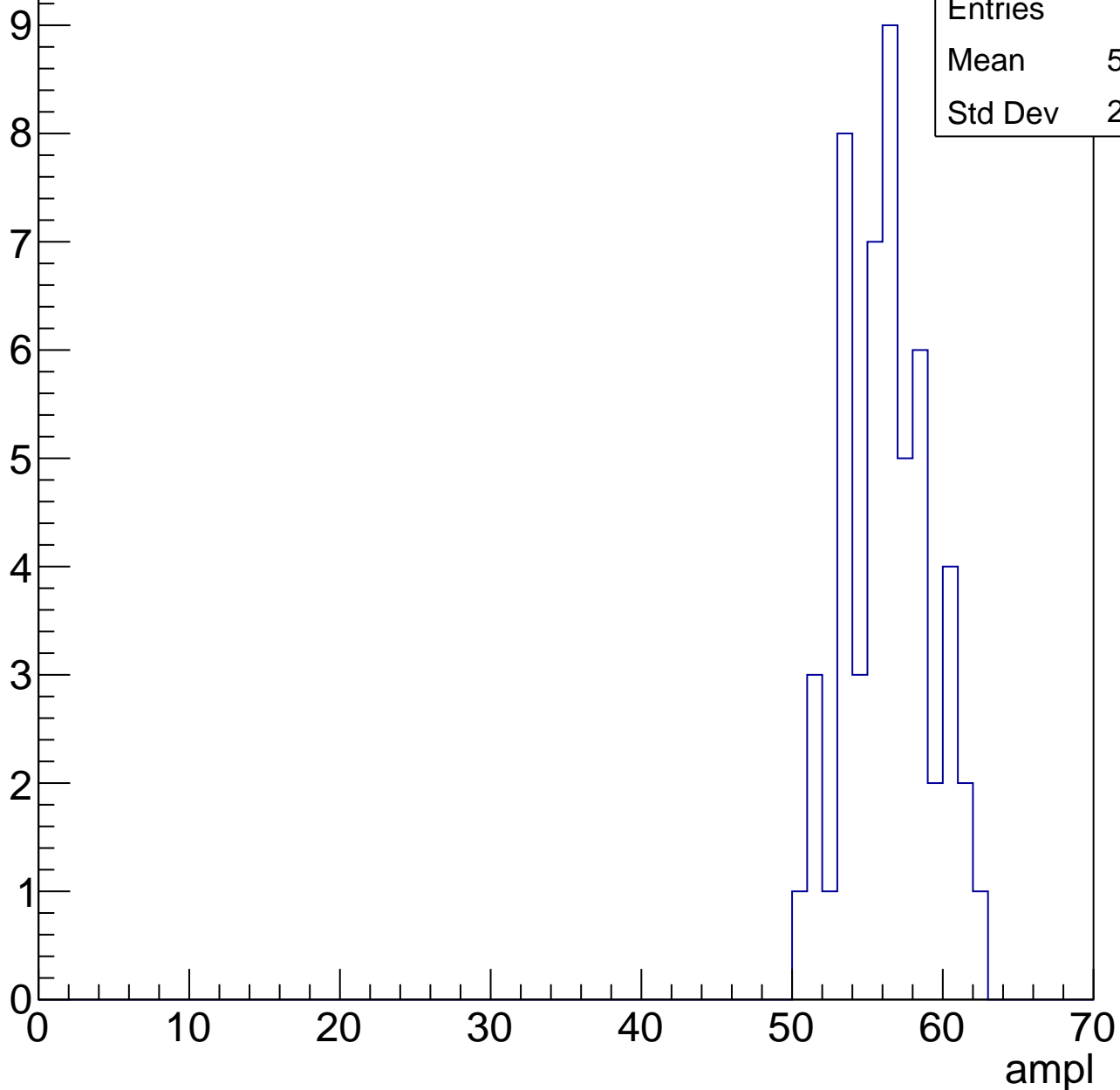
| | |
|---------|-------|
| Entries | 70 |
| Mean | 49.81 |
| Std Dev | 3.03 |



B1L102S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 52 |
| Mean | 55.87 |
| Std Dev | 2.822 |

B1L102S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

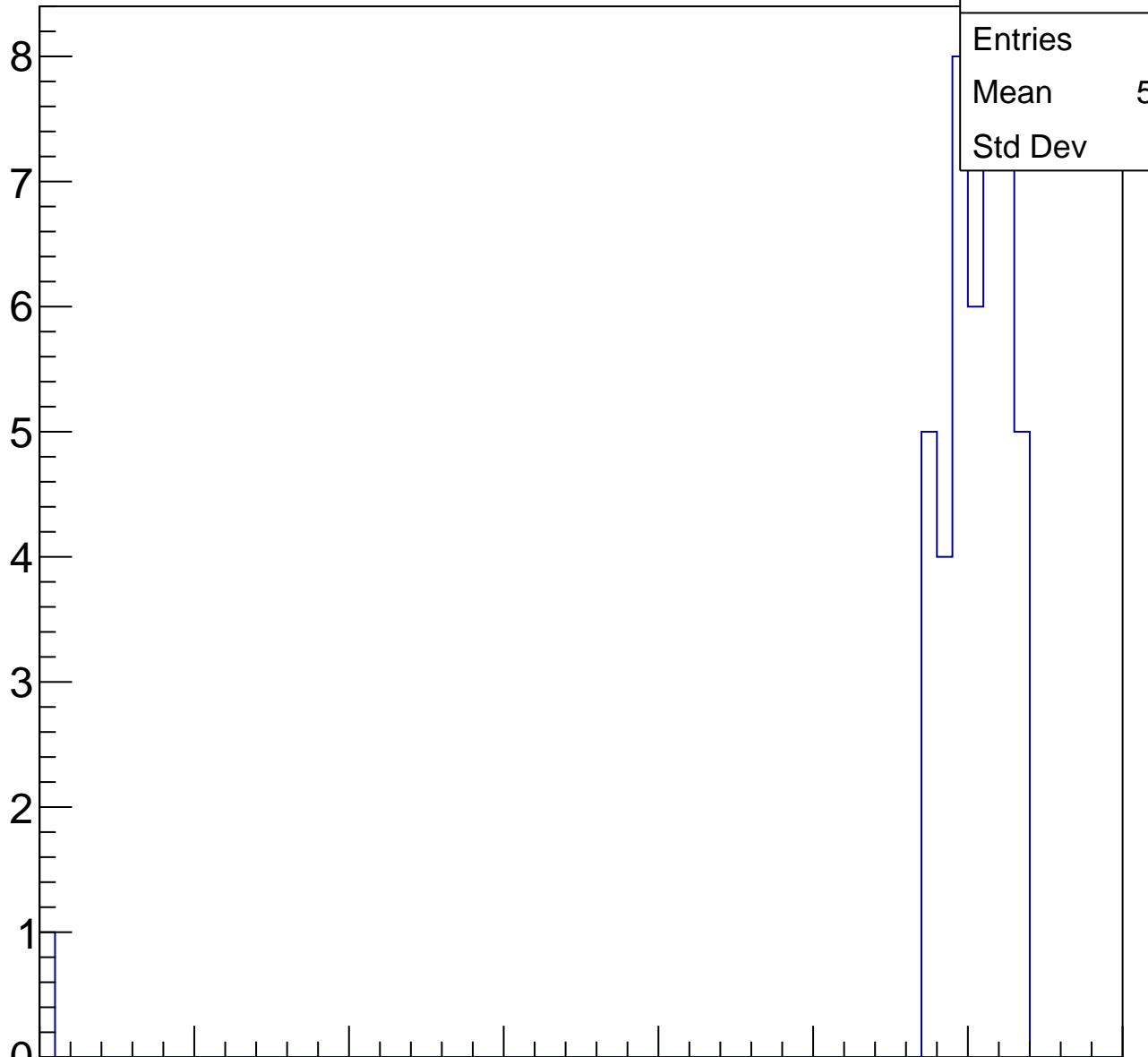
Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.84 |
| Std Dev | 9.06 |

ampl

0 10 20 30 40 50 60 70

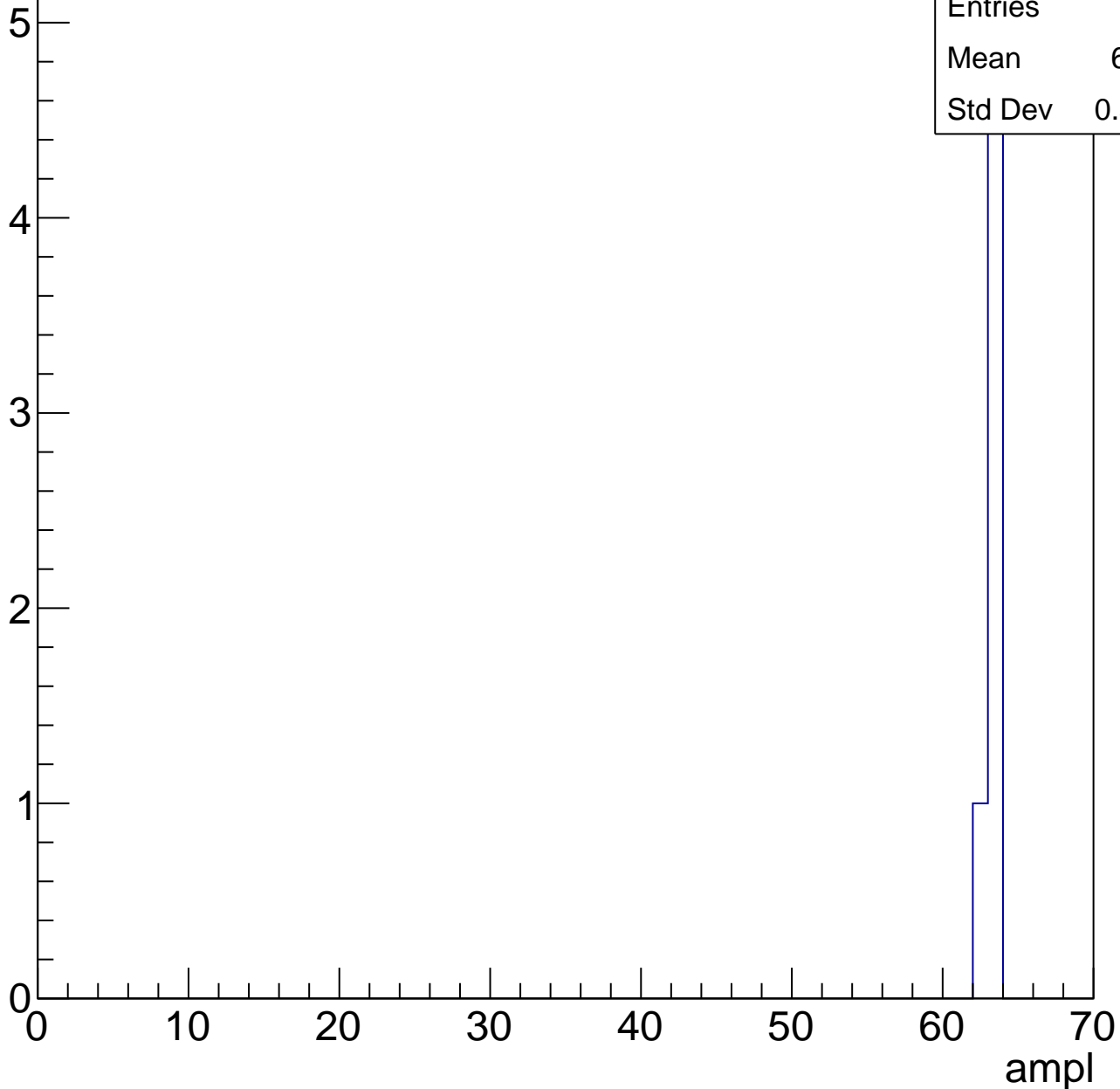


B1L102S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

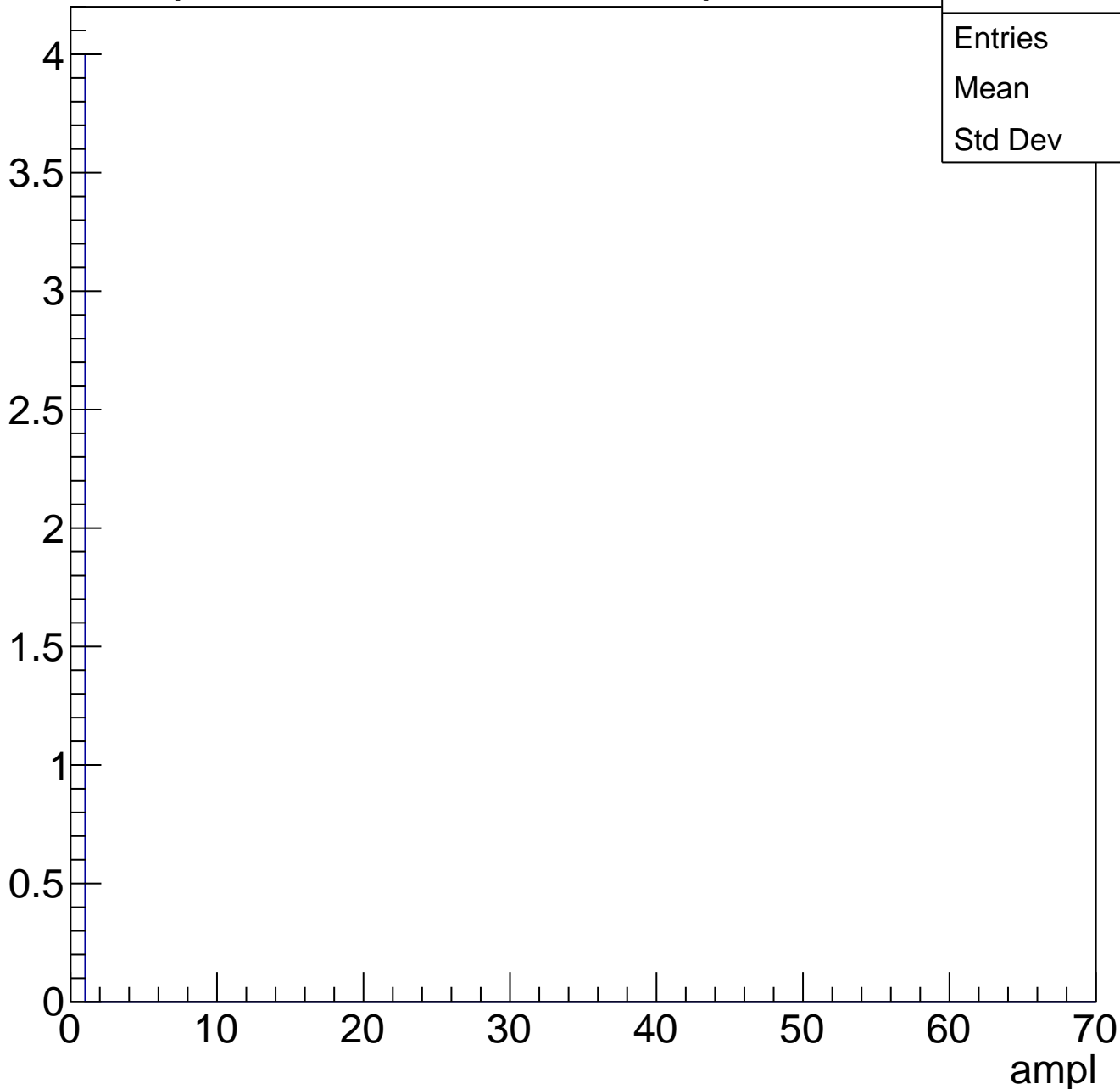
| | |
|---------|--------|
| Entries | 6 |
| Mean | 62.83 |
| Std Dev | 0.3727 |



B1L102S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch50, adc0

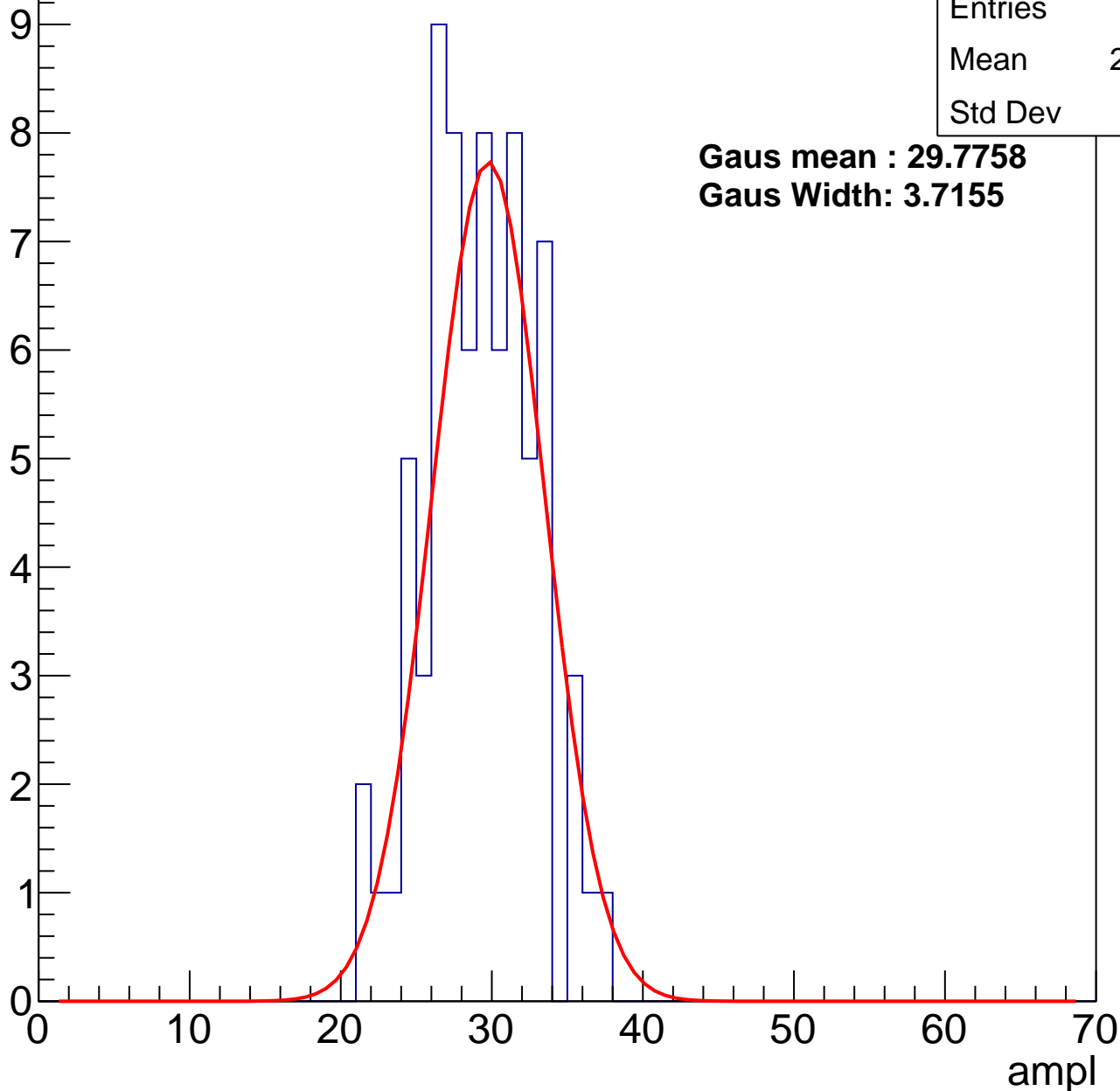
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 28.77 |
| Std Dev | 3.52 |

Gaus mean : 29.7758

Gaus Width: 3.7155



B1L102S, U15-ch50, adc1

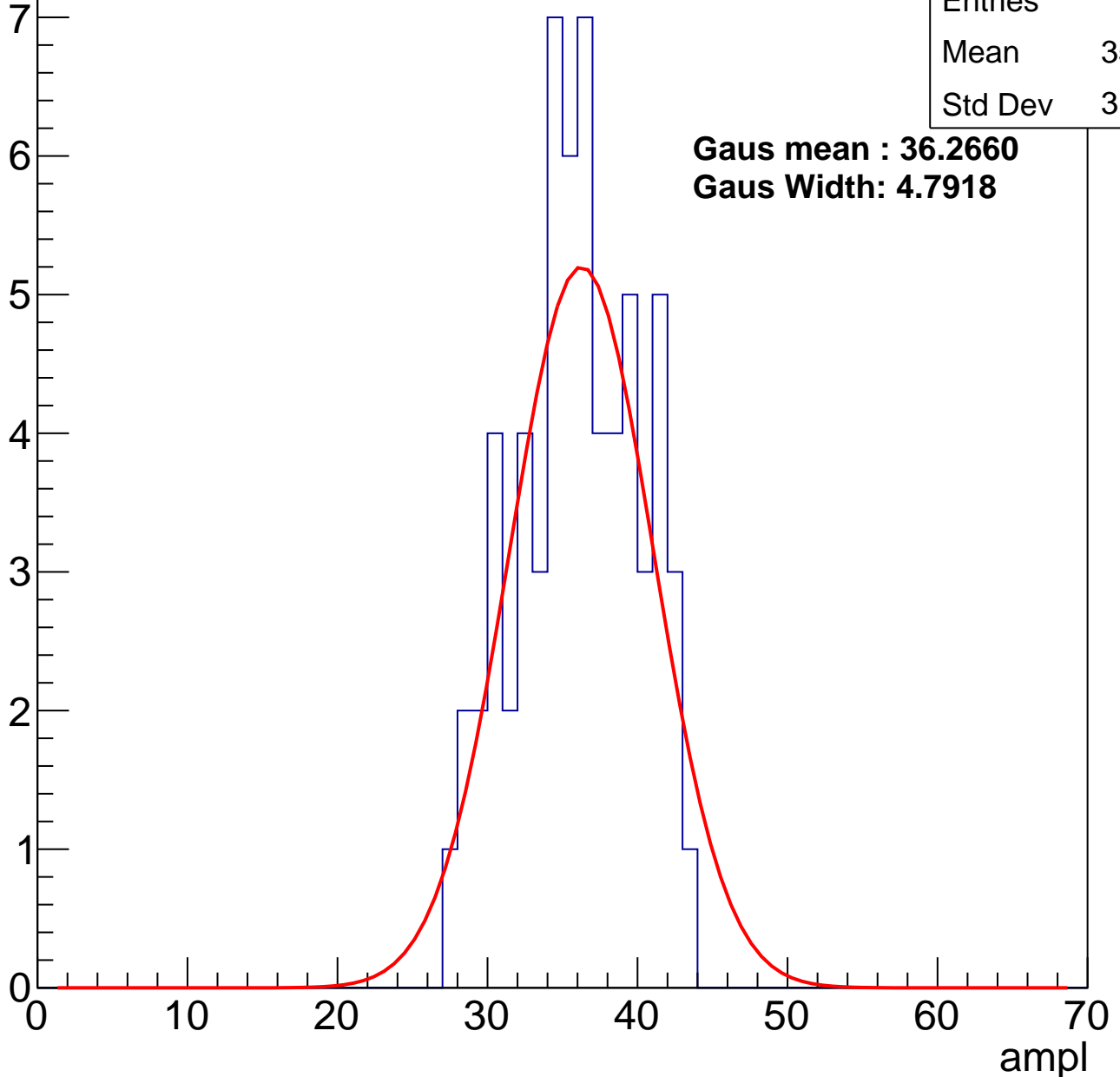
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 35.54 |
| Std Dev | 3.999 |

Gaus mean : 36.2660

Gaus Width: 4.7918



B1L102S, U15-ch50, adc2

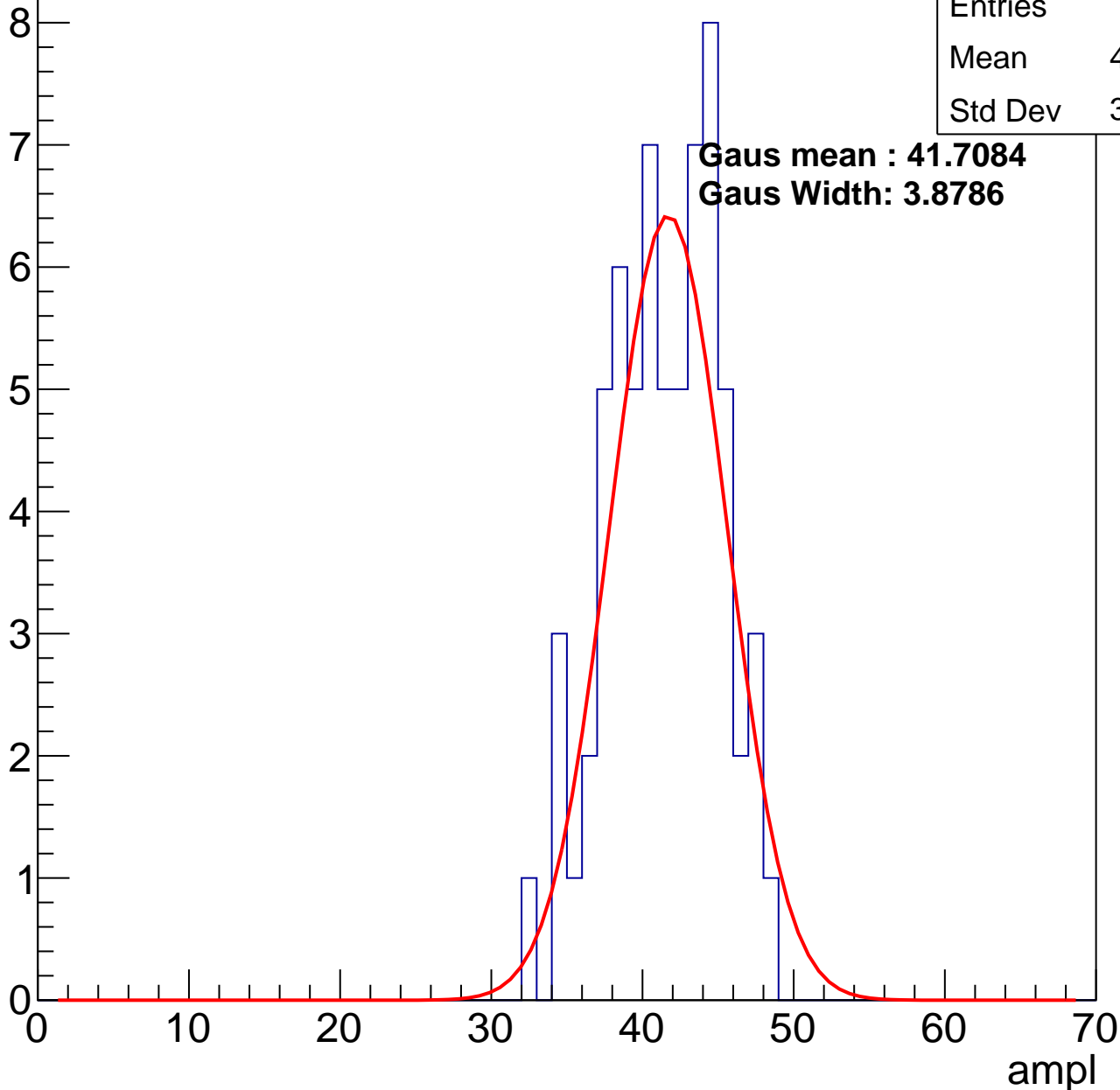
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 40.95 |
| Std Dev | 3.624 |

Gaus mean : 41.7084

Gaus Width: 3.8786

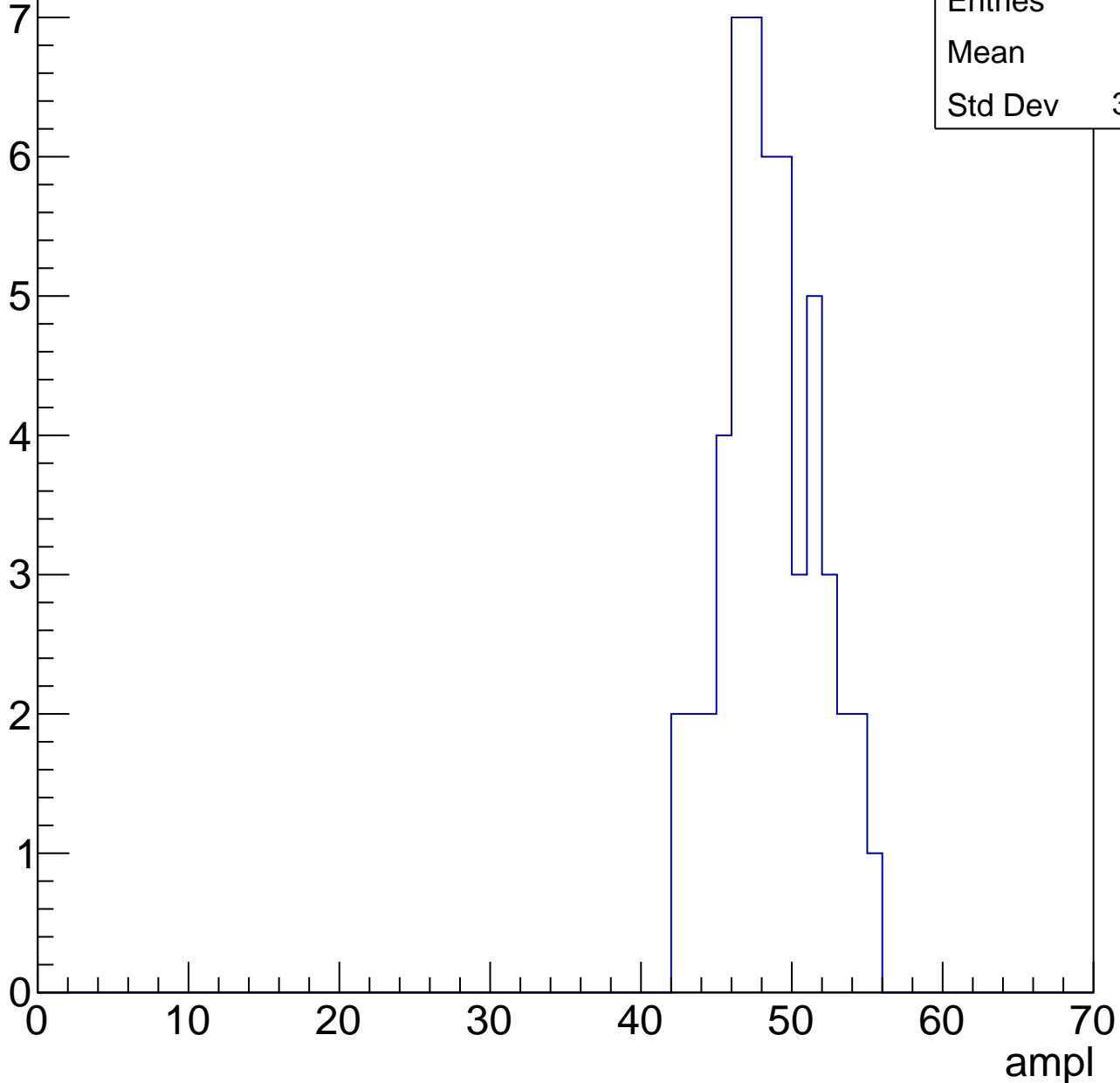


B1L102S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 48.1 |
| Std Dev | 3.121 |

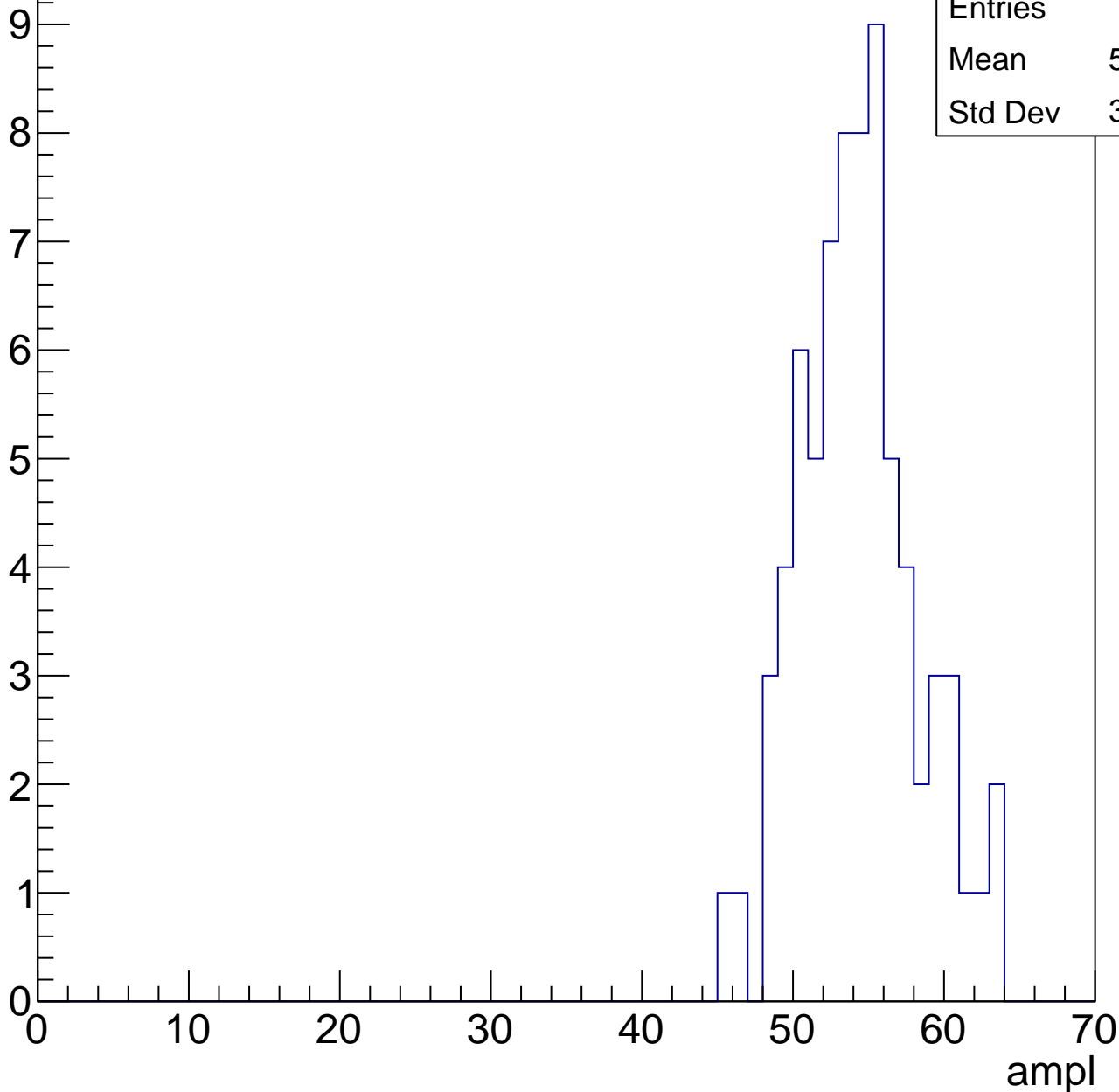


B1L102S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 53.85 |
| Std Dev | 3.852 |

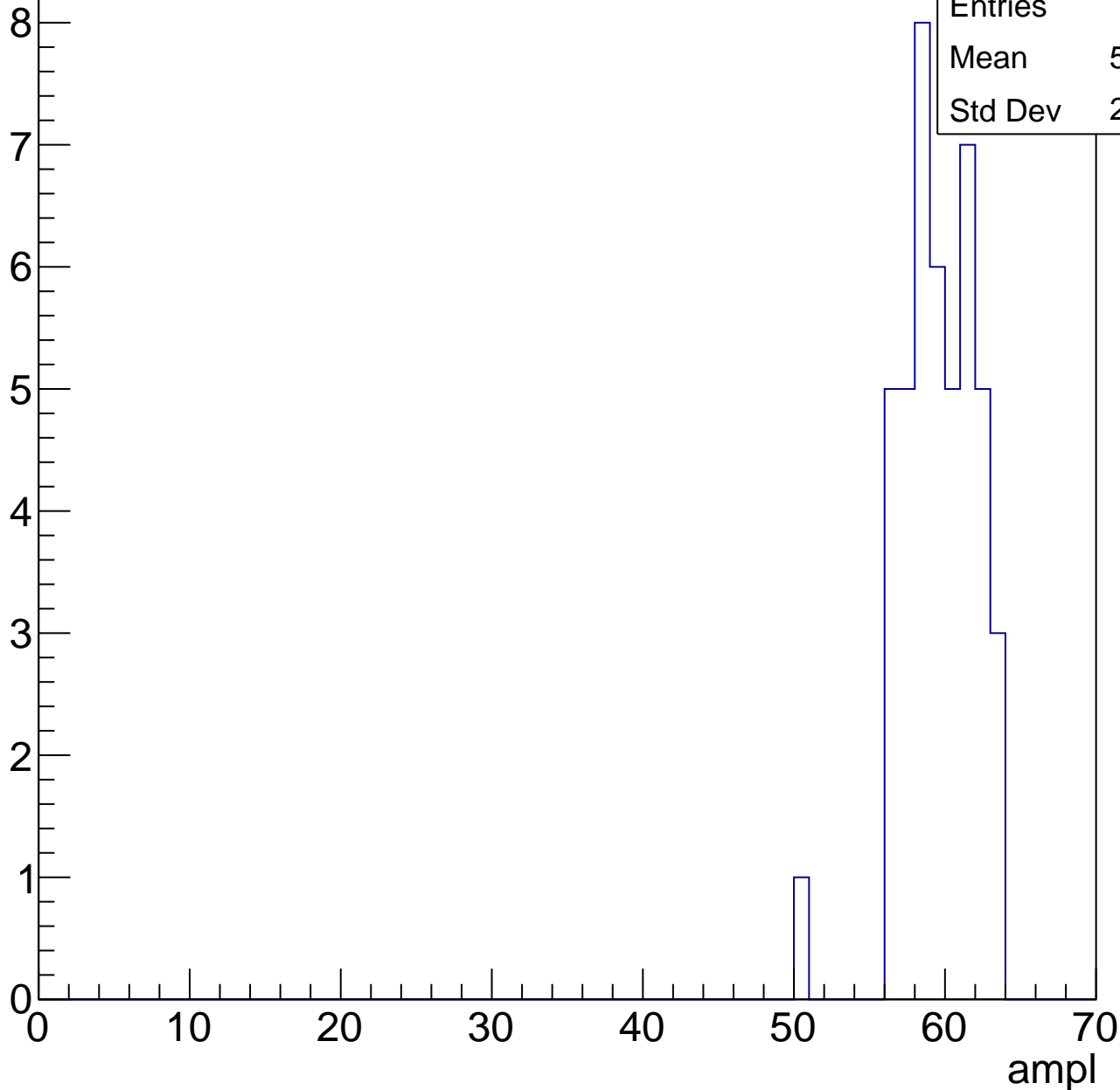


B1L102S, U15-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

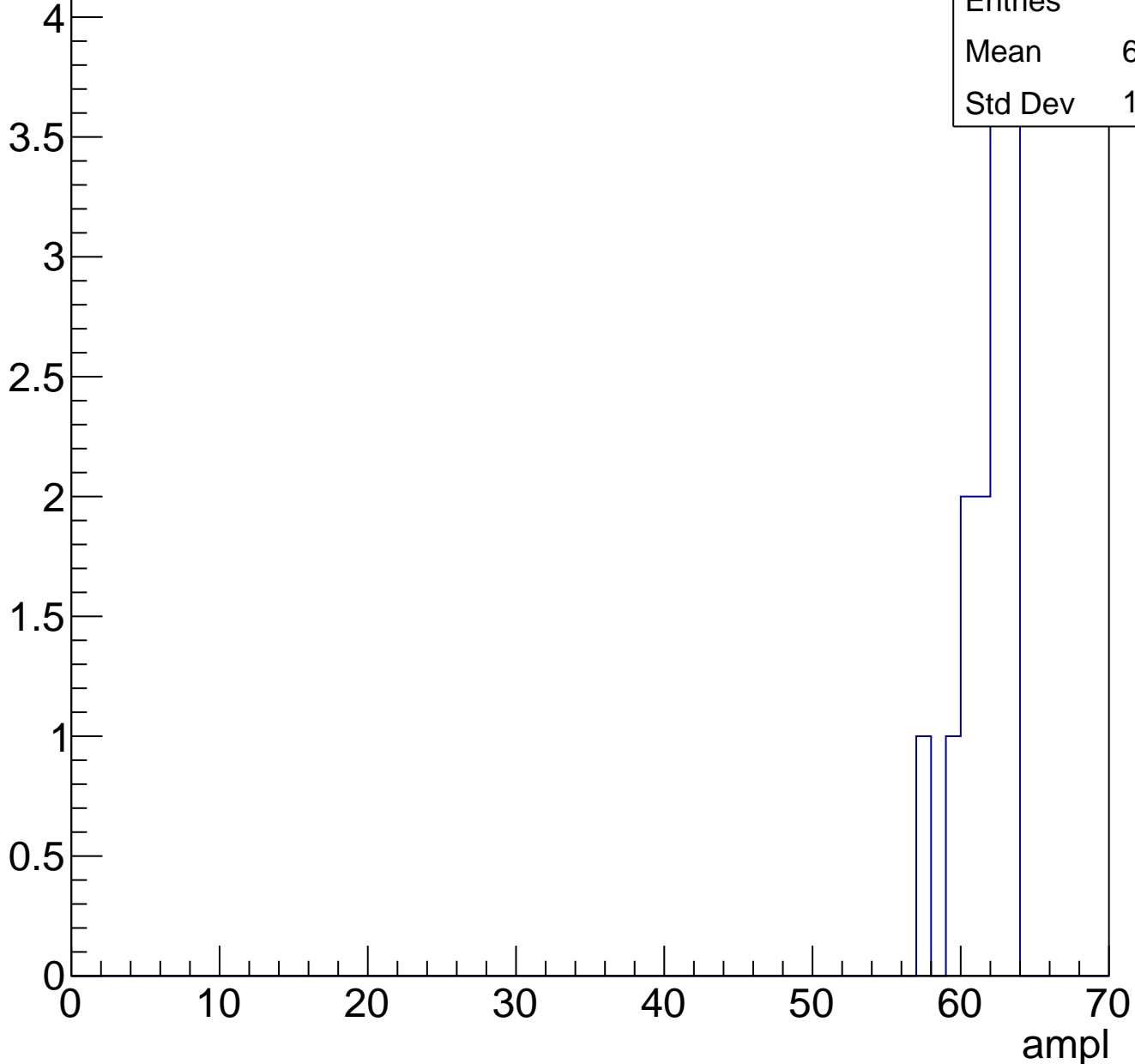
| | |
|---------|-------|
| Entries | 45 |
| Mean | 59.09 |
| Std Dev | 2.493 |



B1L102S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 14 |
| Mean | 61.29 |
| Std Dev | 1.708 |

B1L102S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B1L102S, U15-ch51, adc0

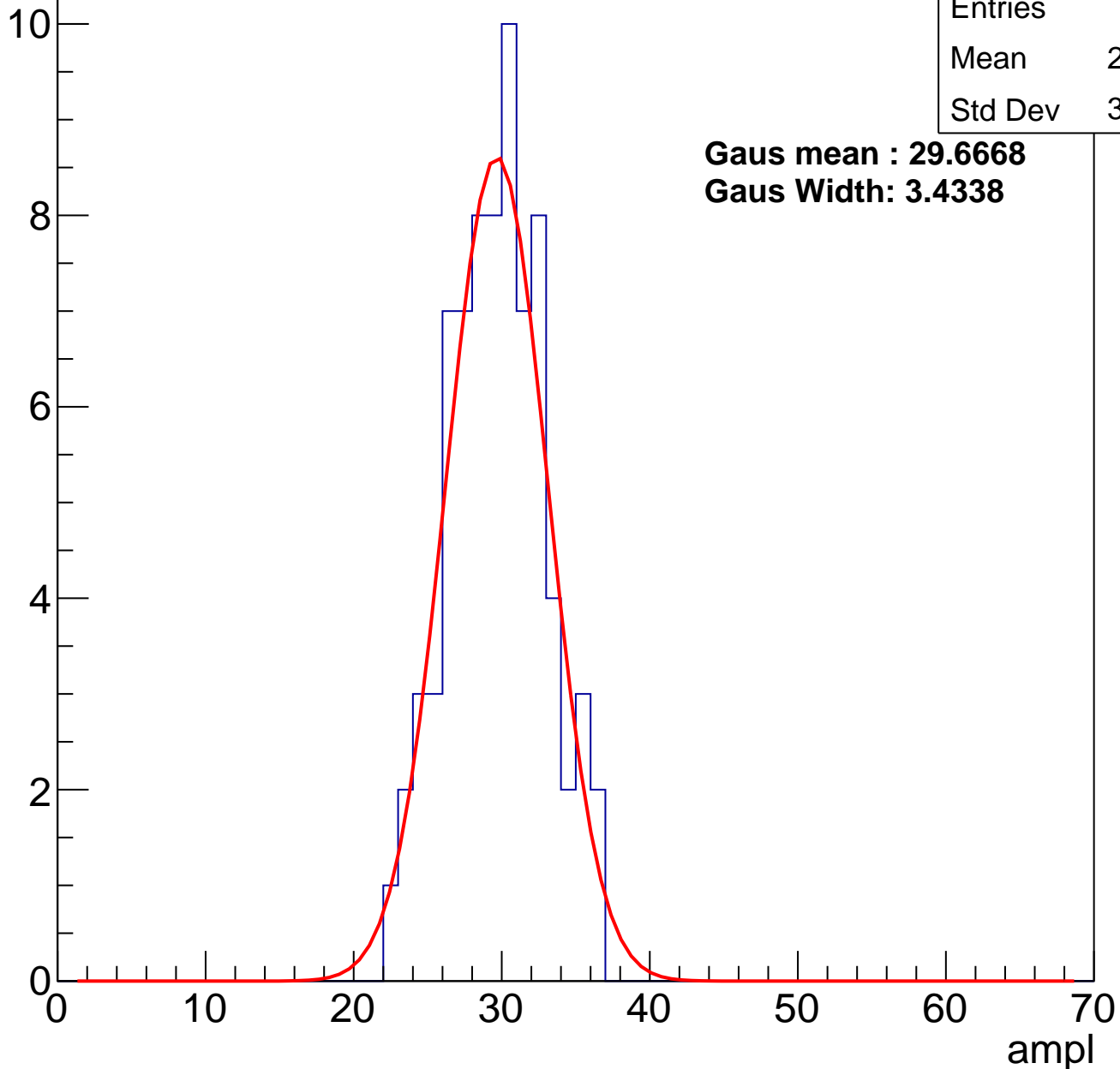
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 75 |
| Mean | 29.23 |
| Std Dev | 3.202 |

Gaus mean : 29.6668

Gaus Width: 3.4338

Entry



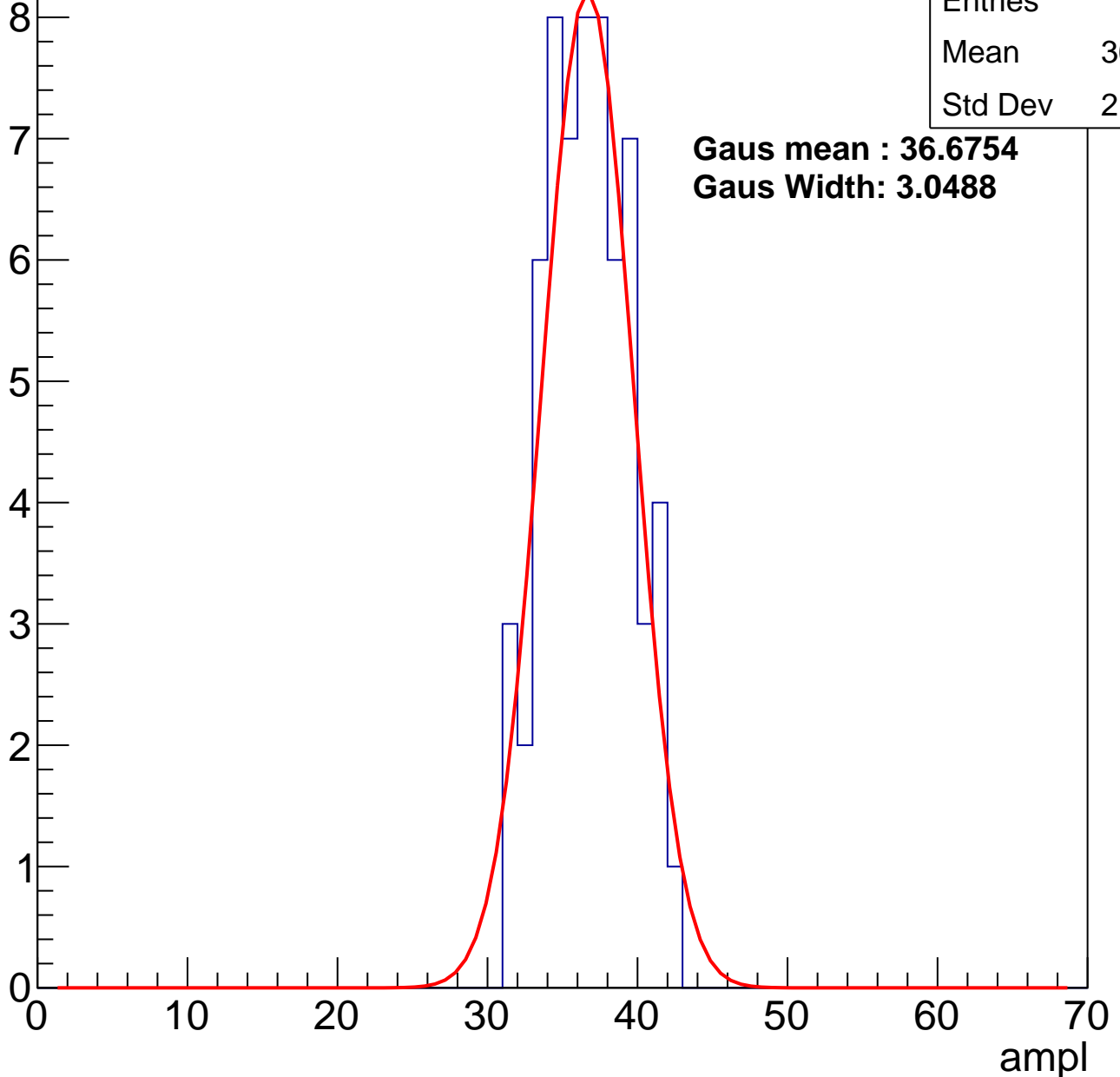
B1L102S, U15-ch51, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.24 |
| Std Dev | 2.747 |

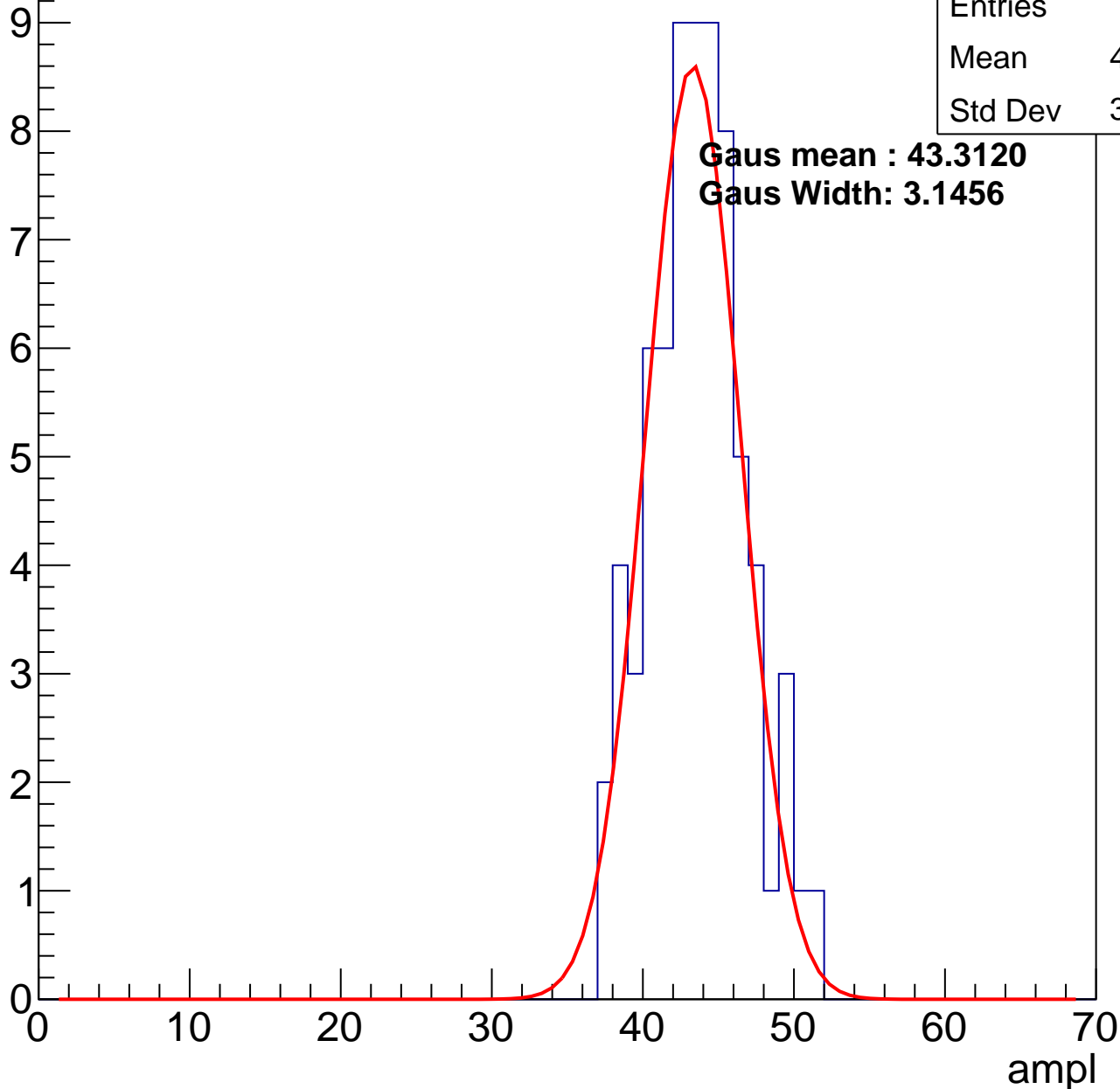
Gaus mean : 36.6754
Gaus Width: 3.0488



B1L102S, U15-ch51, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

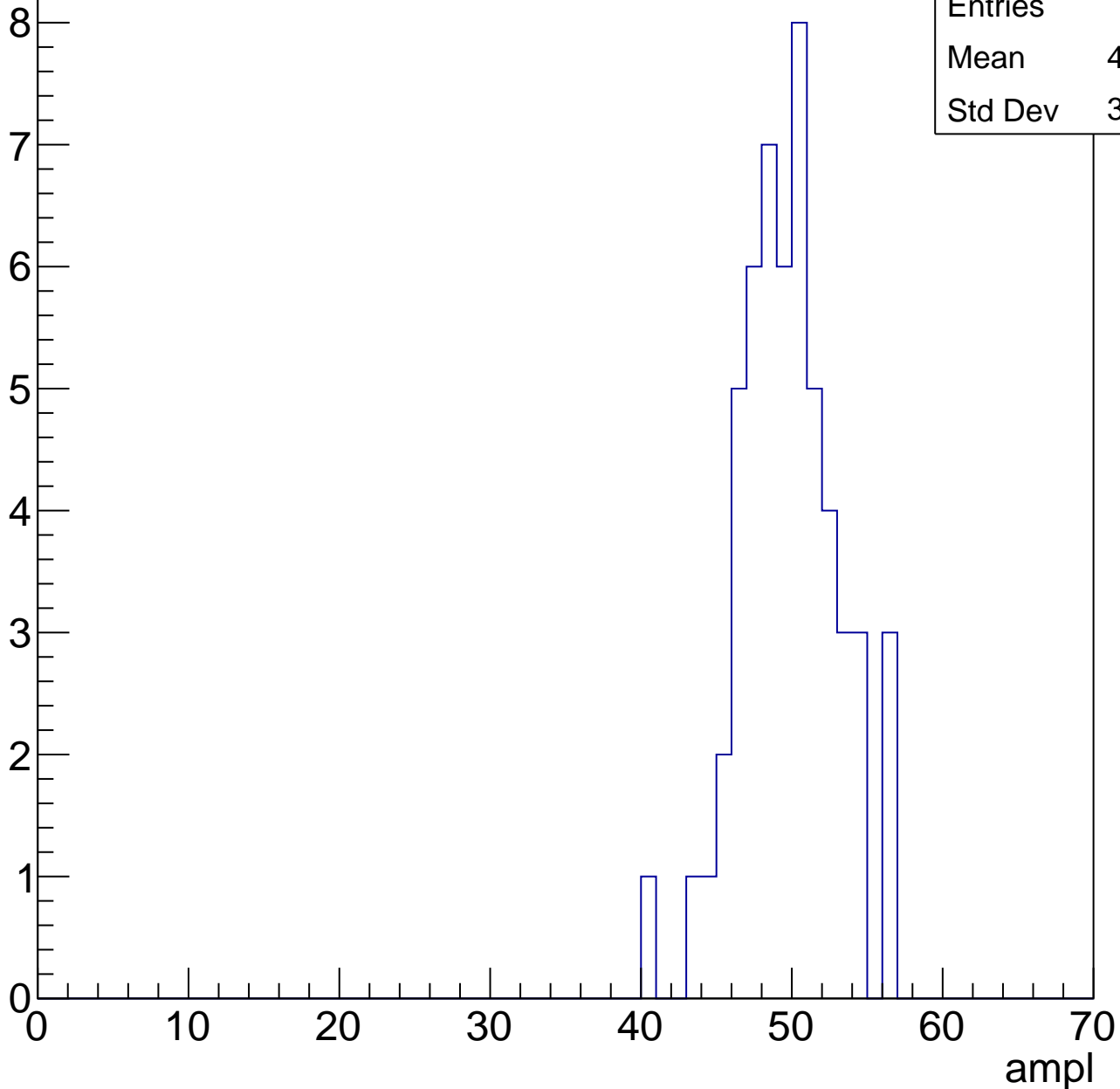


B1L102S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

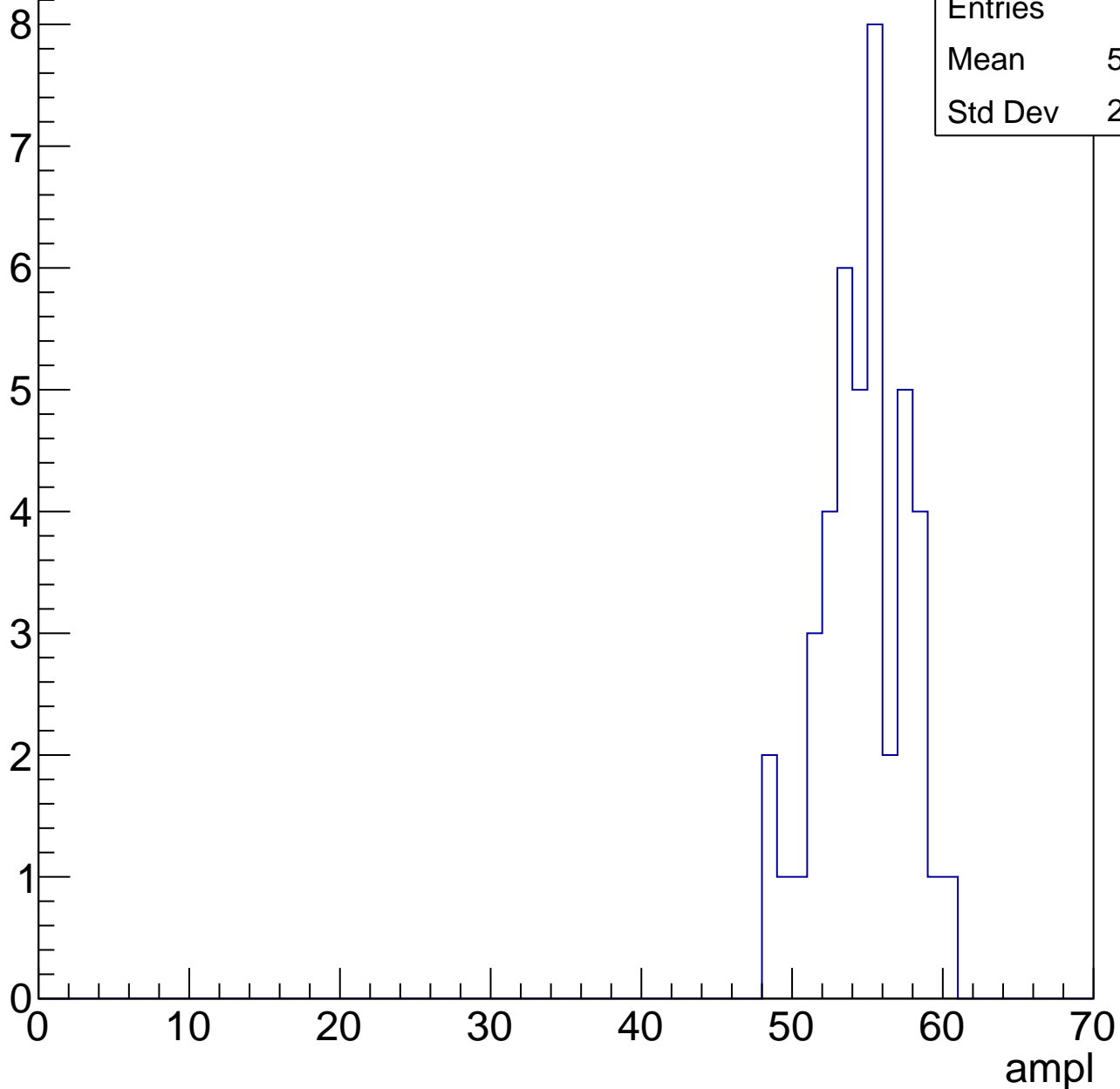
| | |
|---------|-------|
| Entries | 55 |
| Mean | 49.29 |
| Std Dev | 3.246 |



B1L102S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

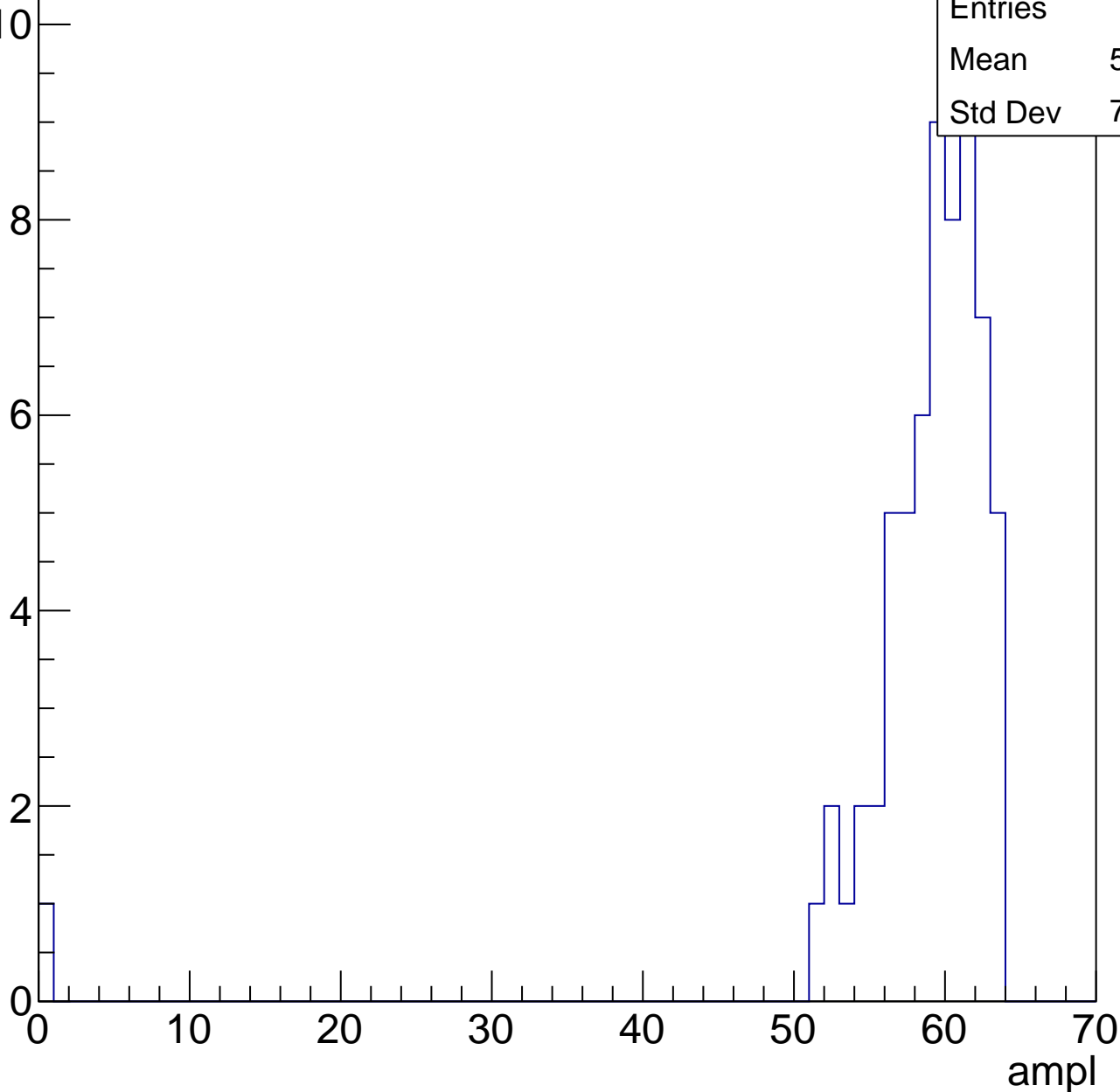


B1L102S, U15-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

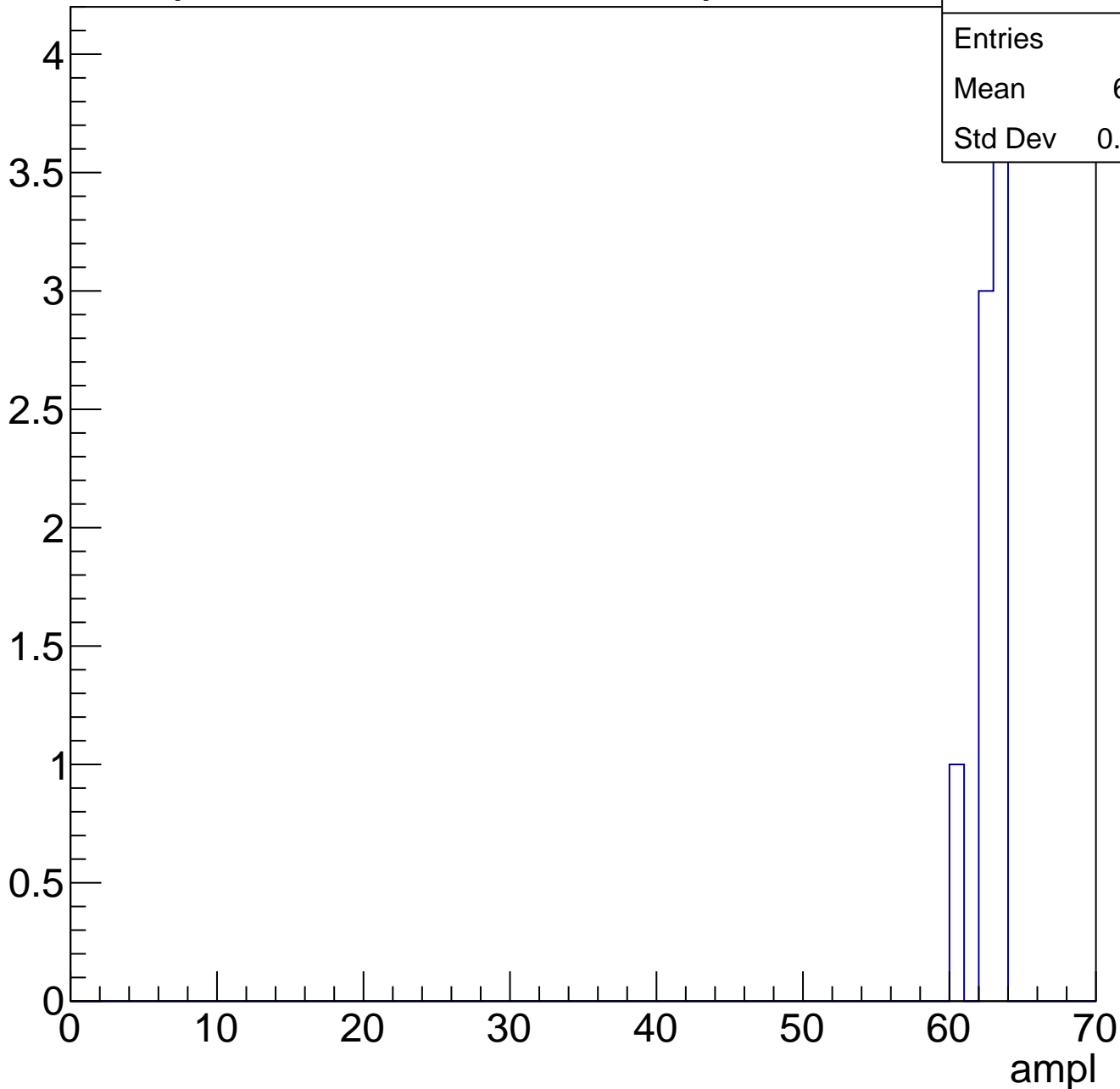
| | |
|---------|-------|
| Entries | 64 |
| Mean | 57.95 |
| Std Dev | 7.859 |



B1L102S, U15-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

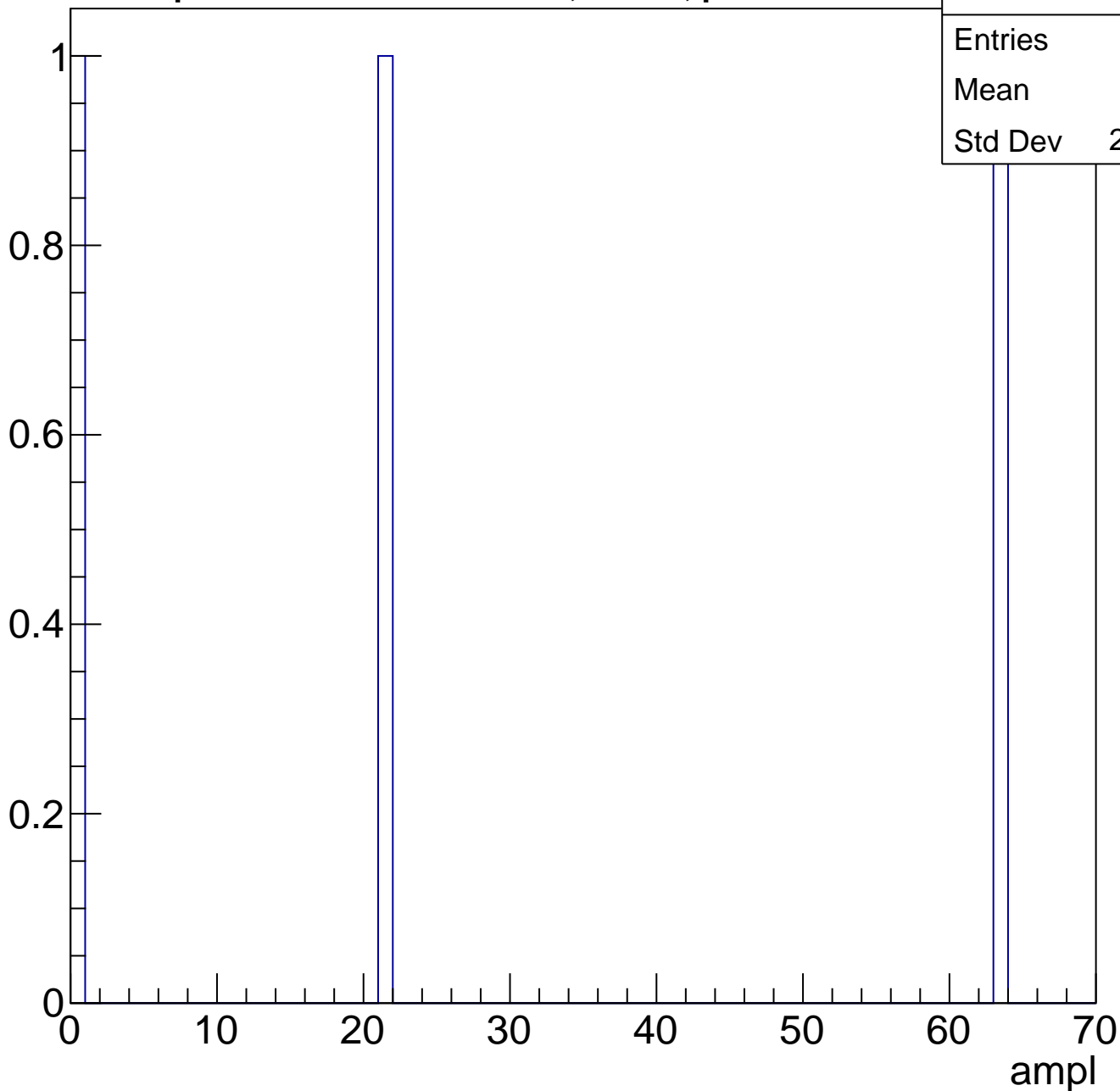
Entry



B1L102S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 28 |
| Std Dev | 26.19 |

B1L102S, U15-ch52, adc0

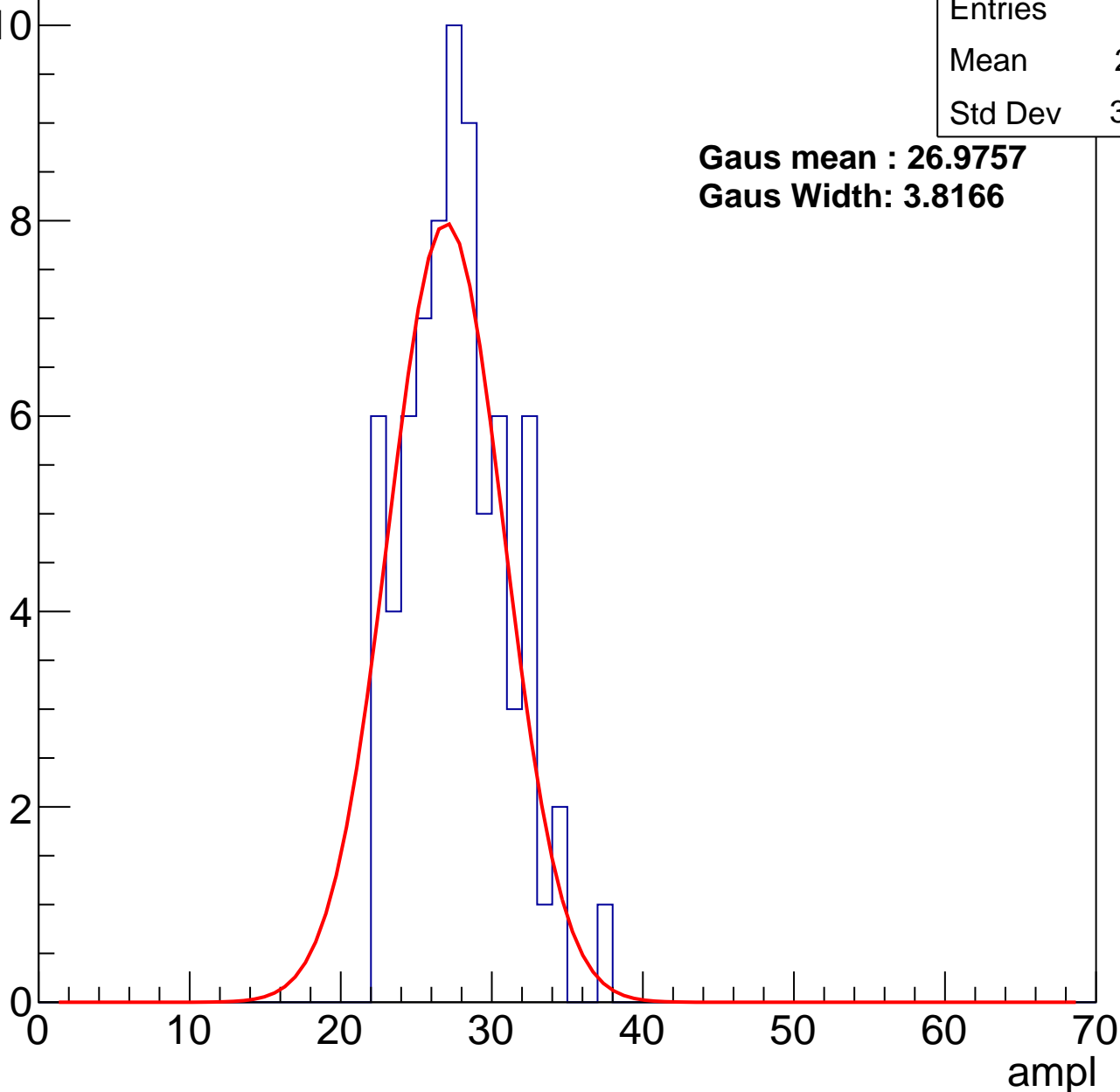
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 27.31 |
| Std Dev | 3.312 |

Gaus mean : 26.9757

Gaus Width: 3.8166



B1L102S, U15-ch52, adc1

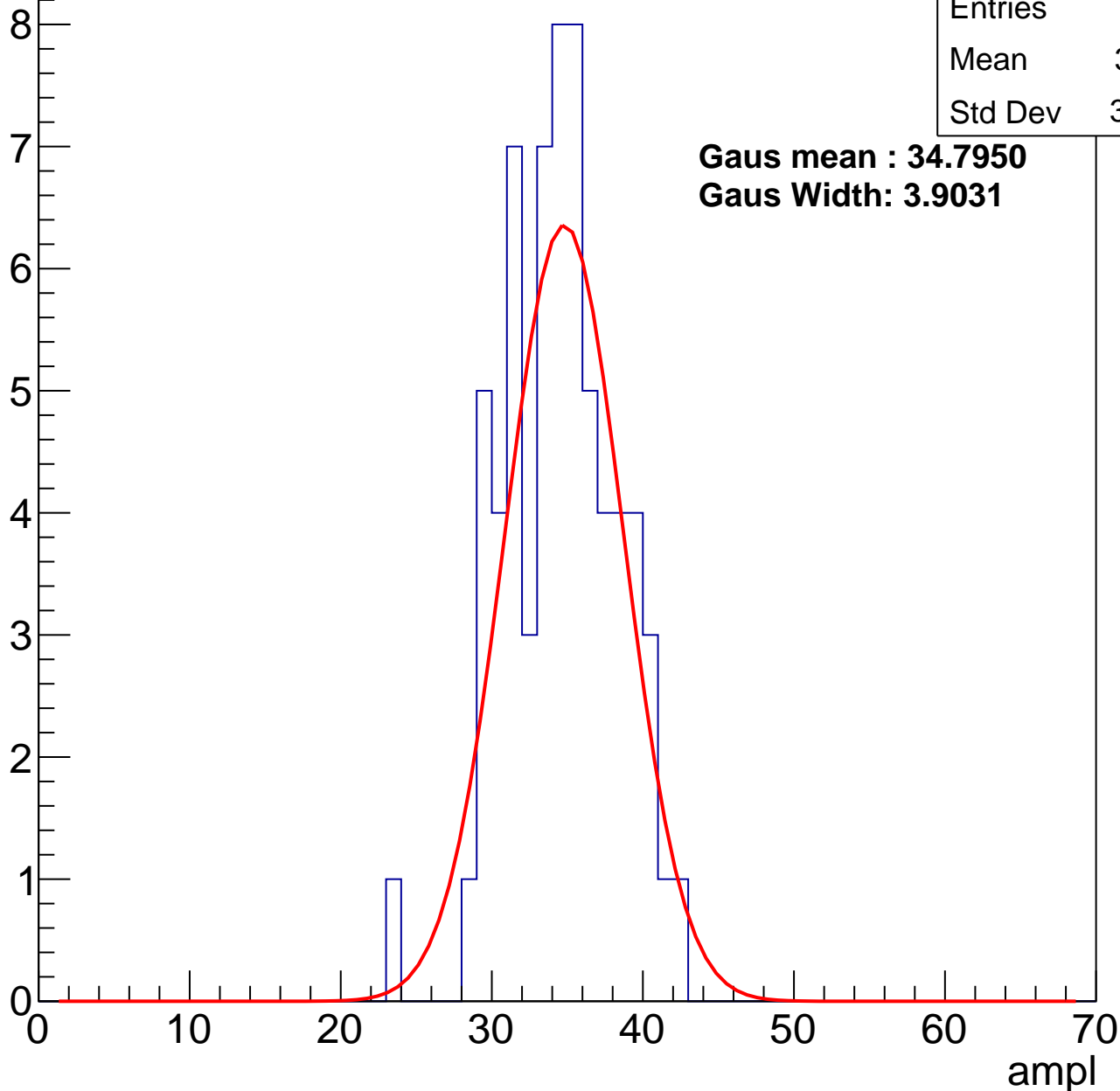
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 34.11 |
| Std Dev | 3.644 |

Gaus mean : 34.7950

Gaus Width: 3.9031



B1L102S, U15-ch52, adc2

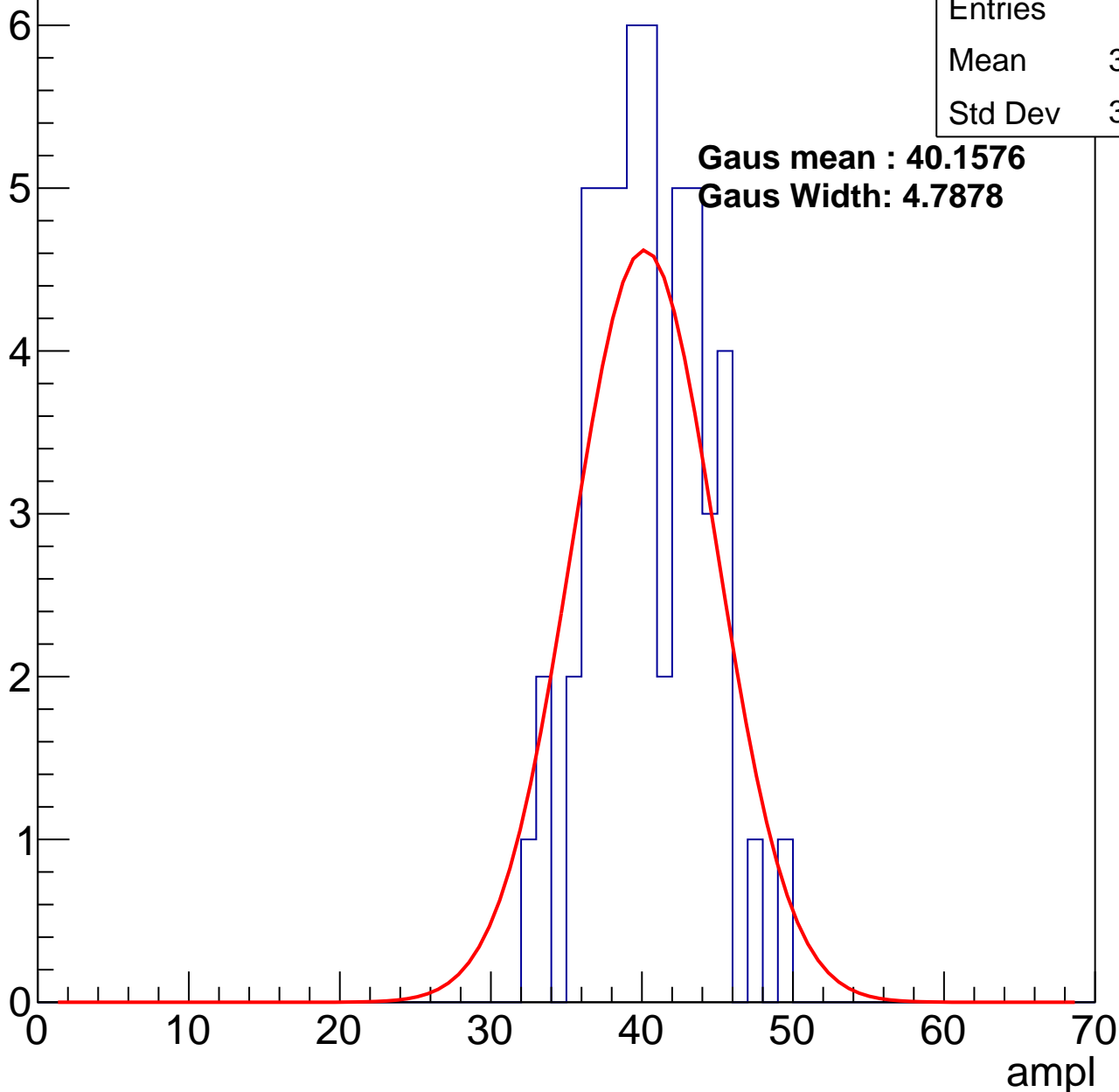
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 39.85 |
| Std Dev | 3.652 |

Gaus mean : 40.1576

Gaus Width: 4.7878

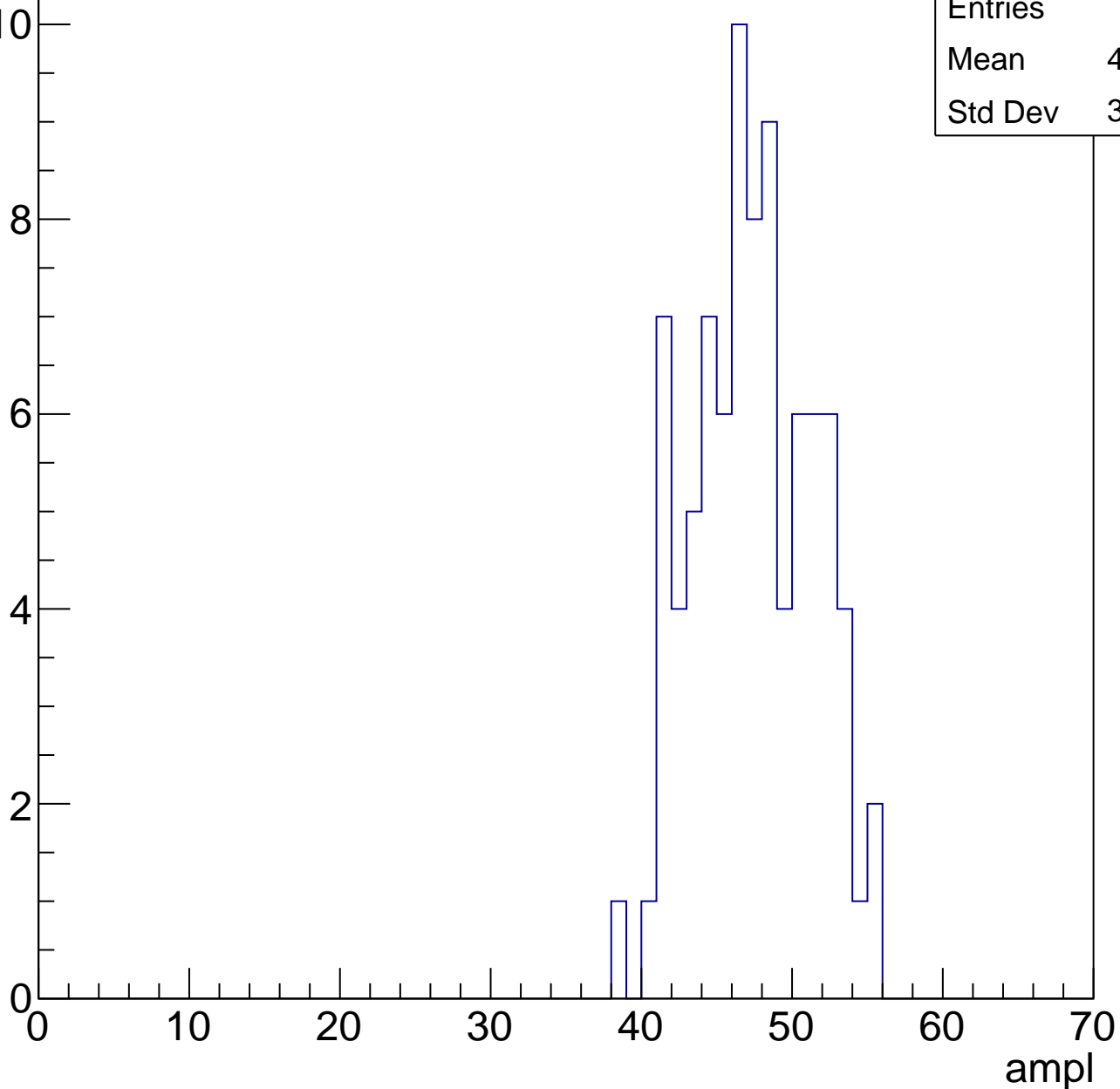


B1L102S, U15-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

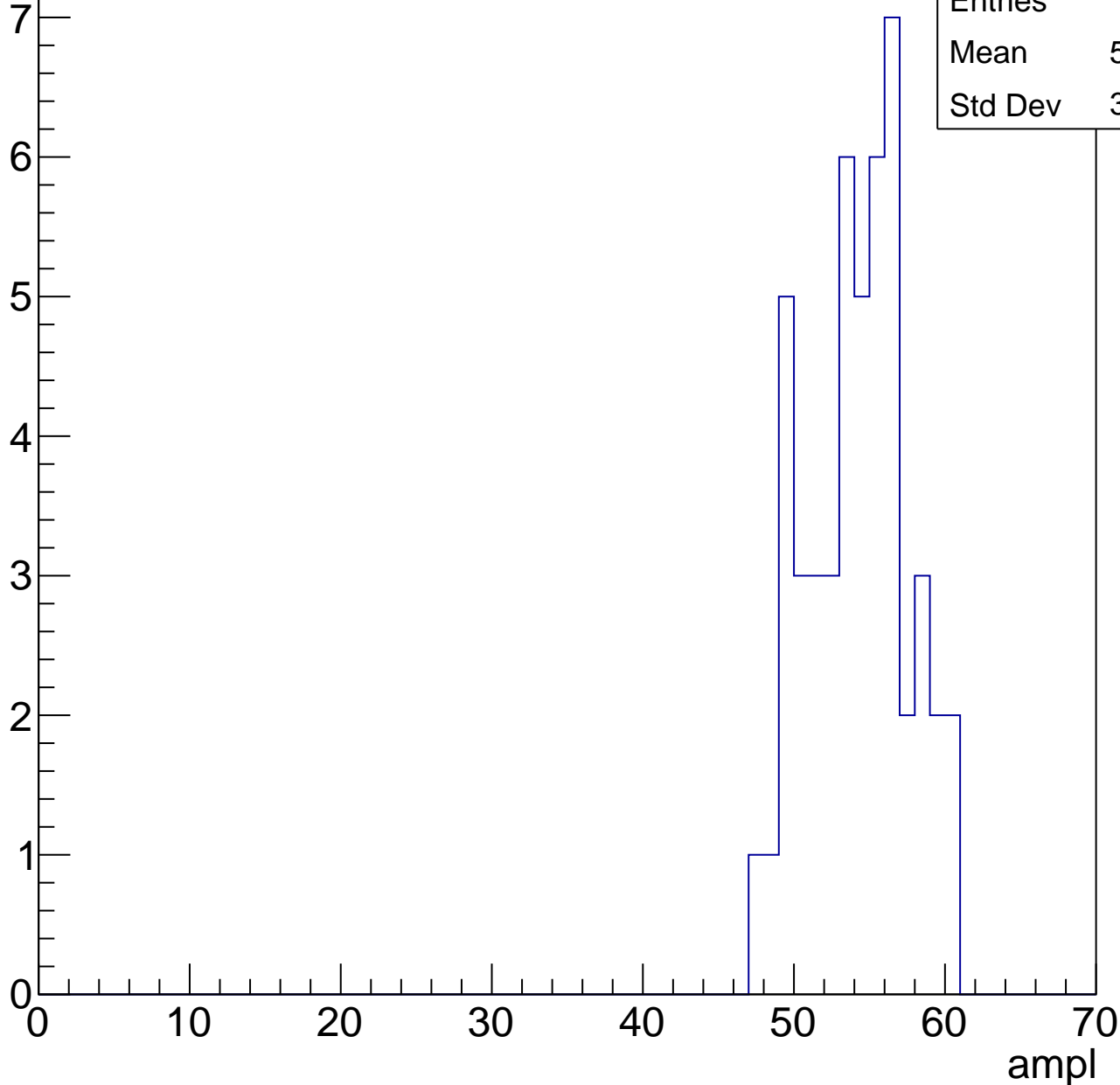
| | |
|---------|-------|
| Entries | 87 |
| Mean | 46.94 |
| Std Dev | 3.873 |



B1L102S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

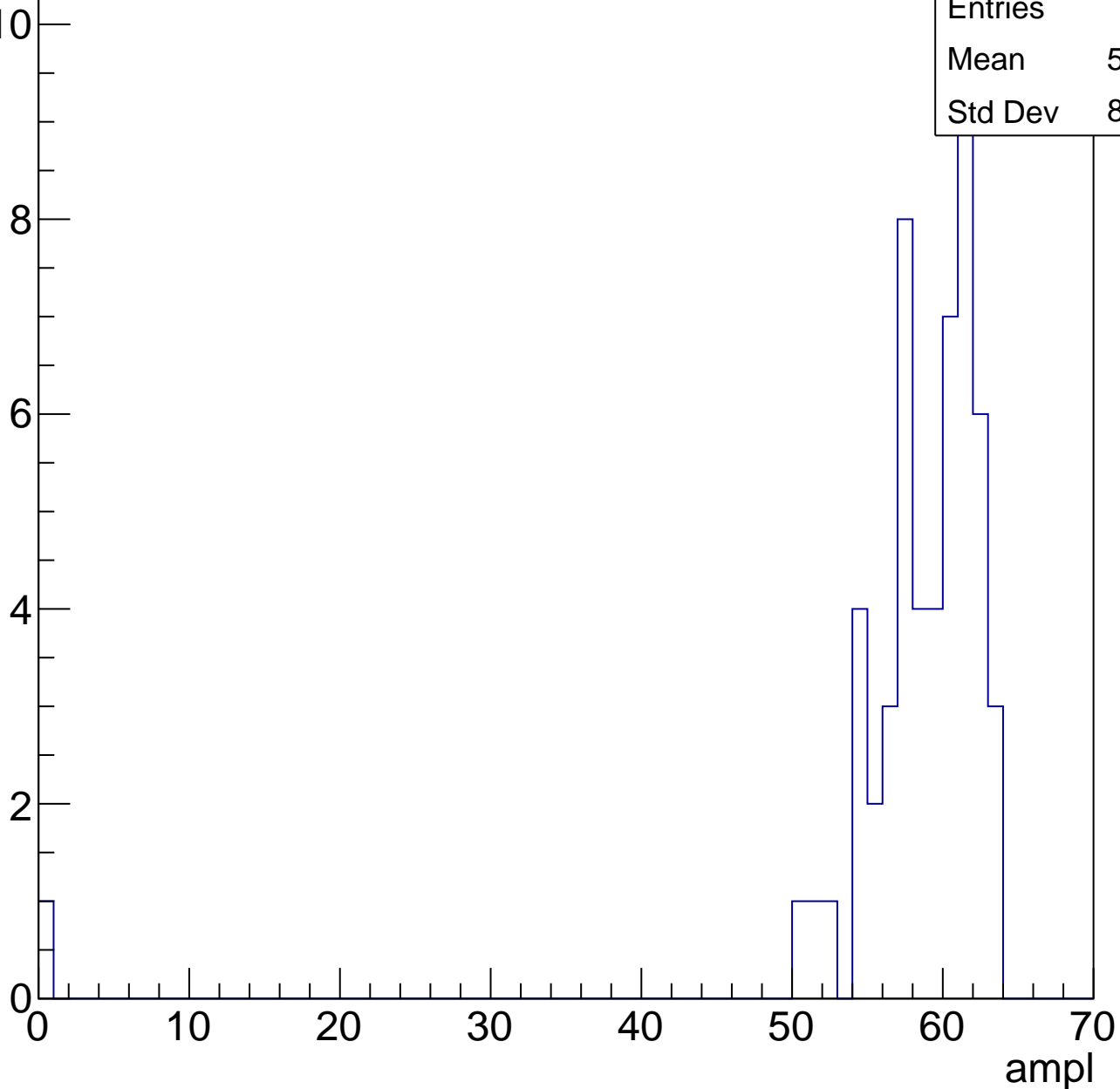


B1L102S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 57.49 |
| Std Dev | 8.414 |

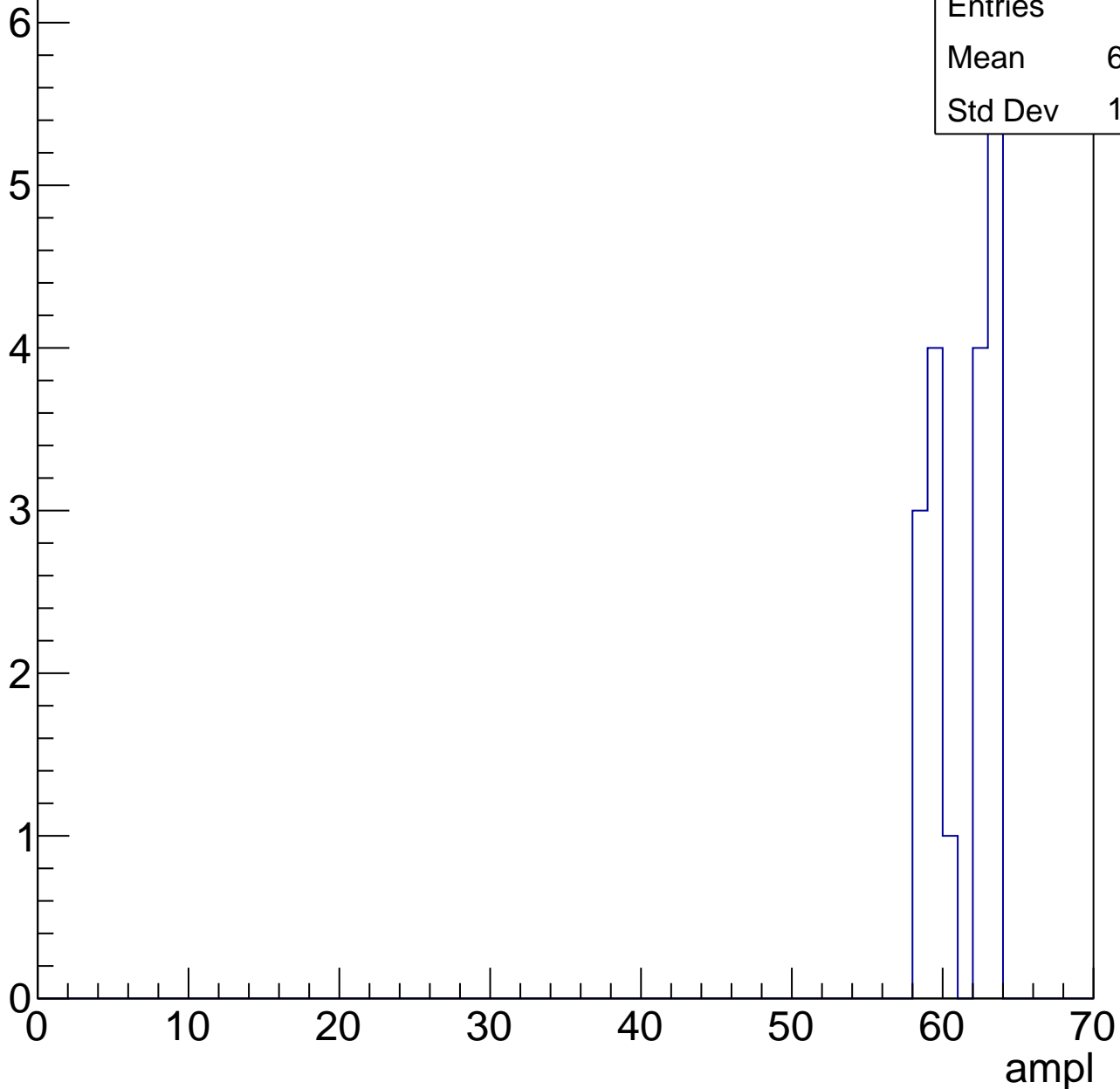


B1L102S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

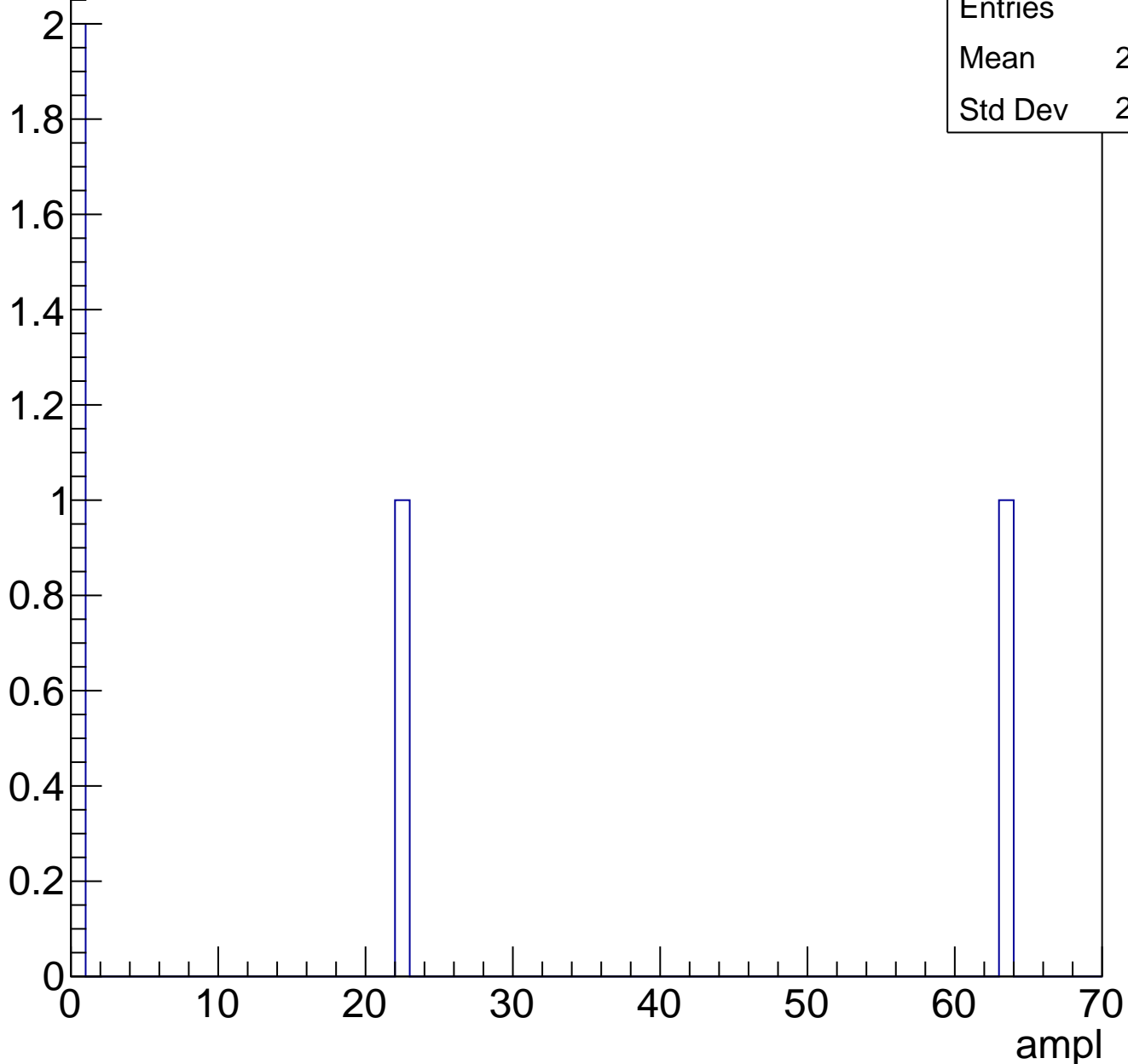
| | |
|---------|-------|
| Entries | 18 |
| Mean | 60.89 |
| Std Dev | 1.997 |



B1L102S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 21.25 |
| Std Dev | 25.72 |

B1L102S, U15-ch53, adc0

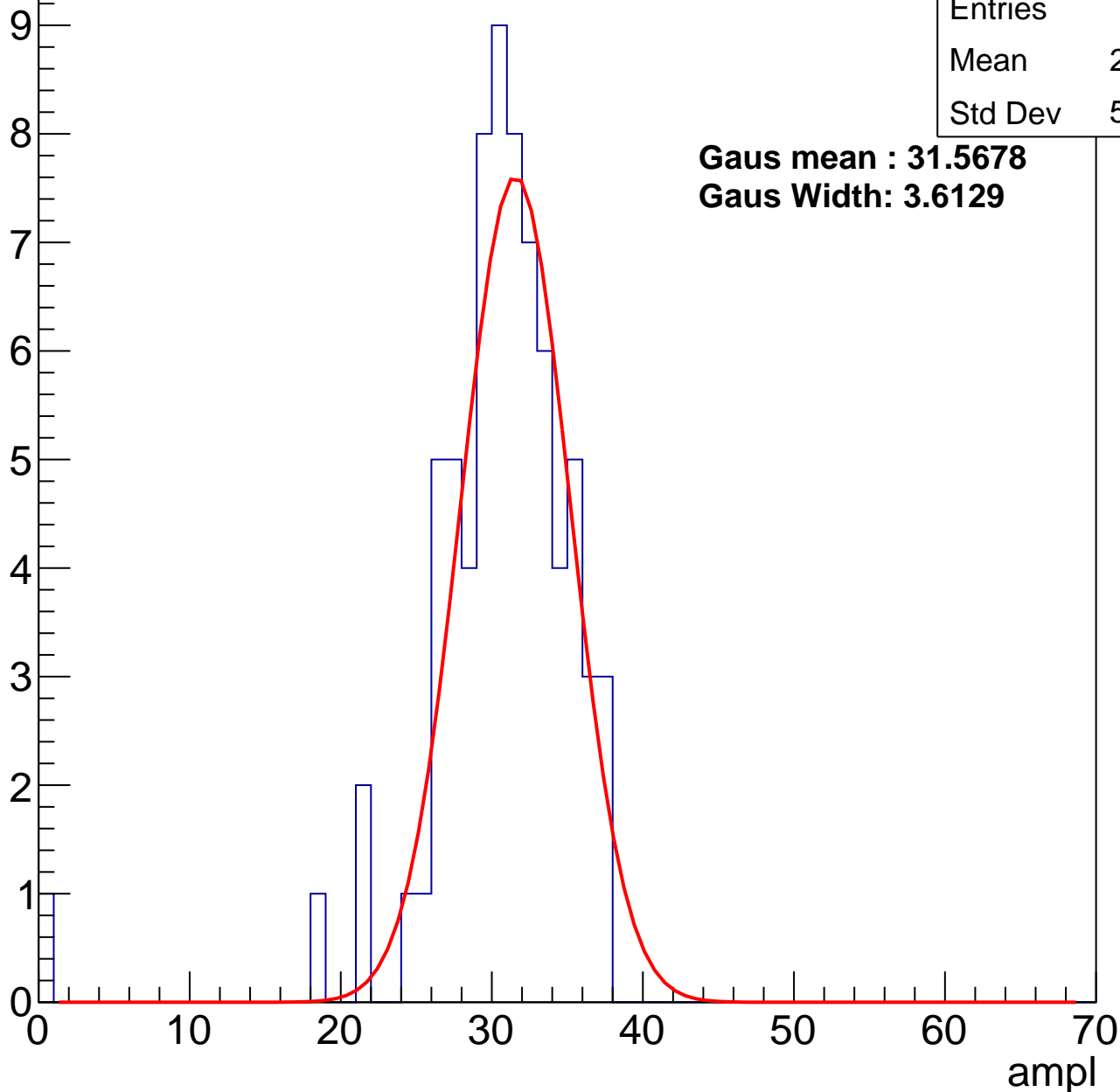
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 29.97 |
| Std Dev | 5.168 |

Gaus mean : 31.5678

Gaus Width: 3.6129



B1L102S, U15-ch53, adc1

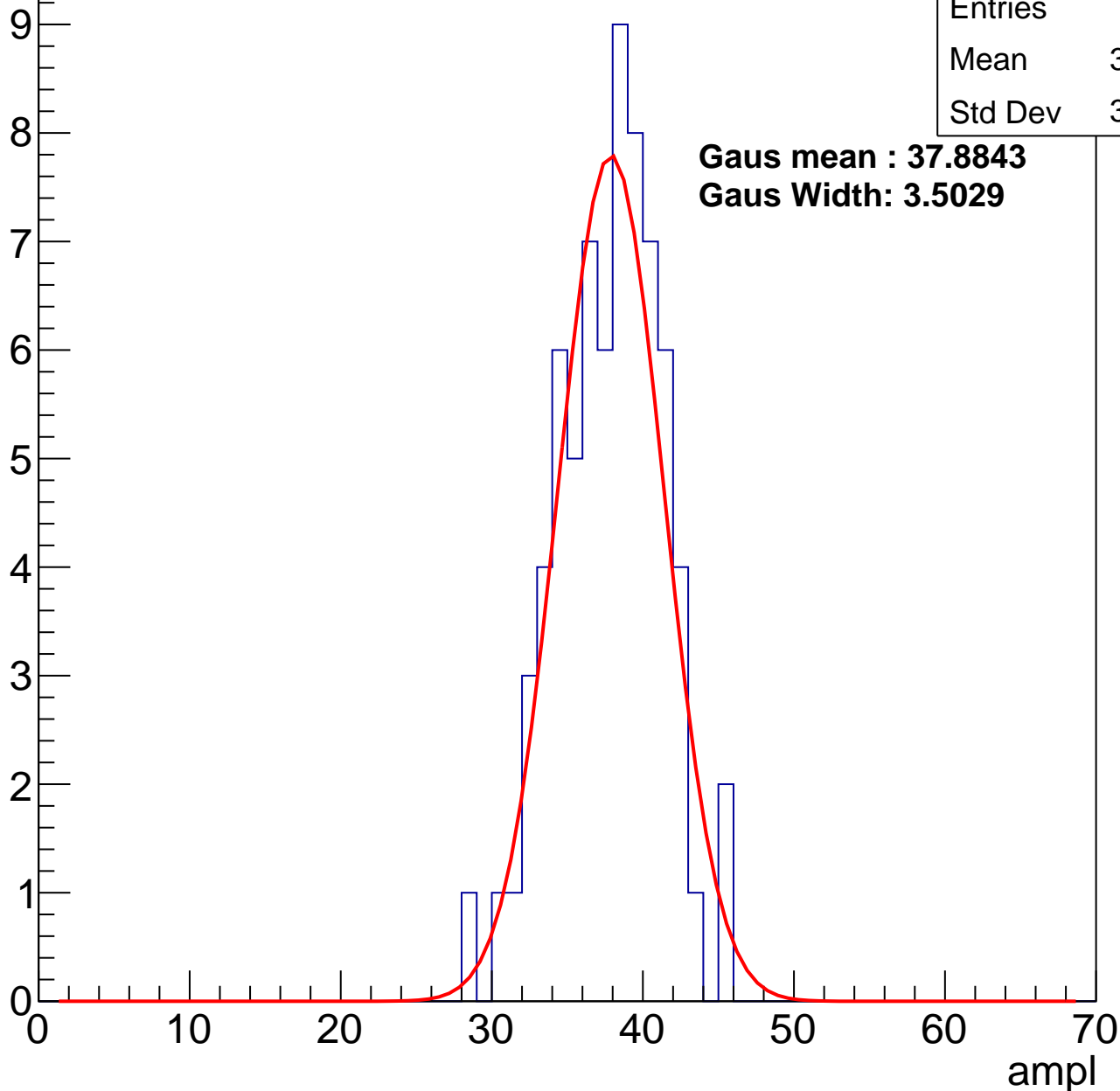
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 37.34 |
| Std Dev | 3.443 |

Gaus mean : 37.8843

Gaus Width: 3.5029



B1L102S, U15-ch53, adc2

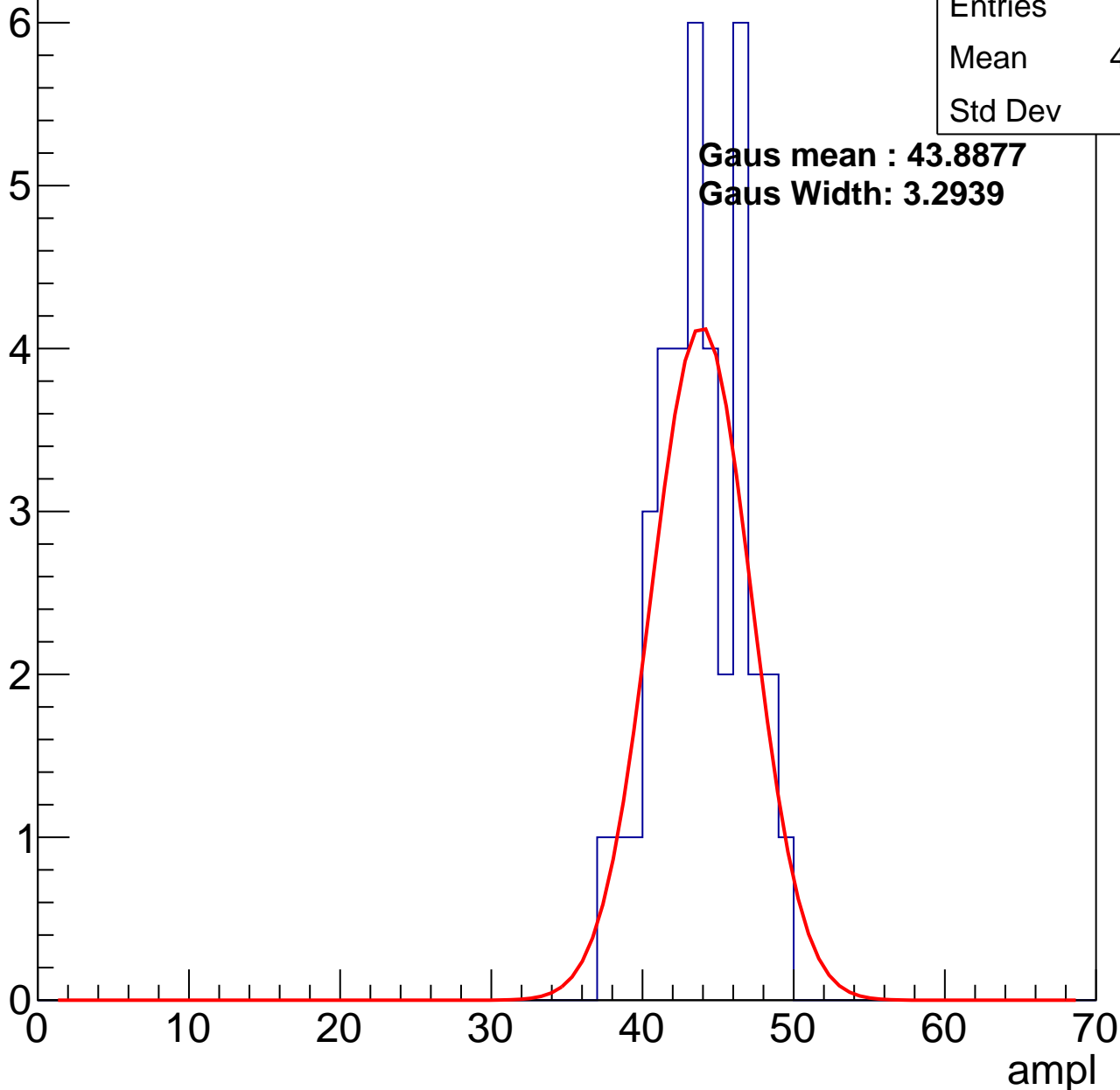
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 37 |
| Mean | 43.38 |
| Std Dev | 2.86 |

Gaus mean : 43.8877

Gaus Width: 3.2939

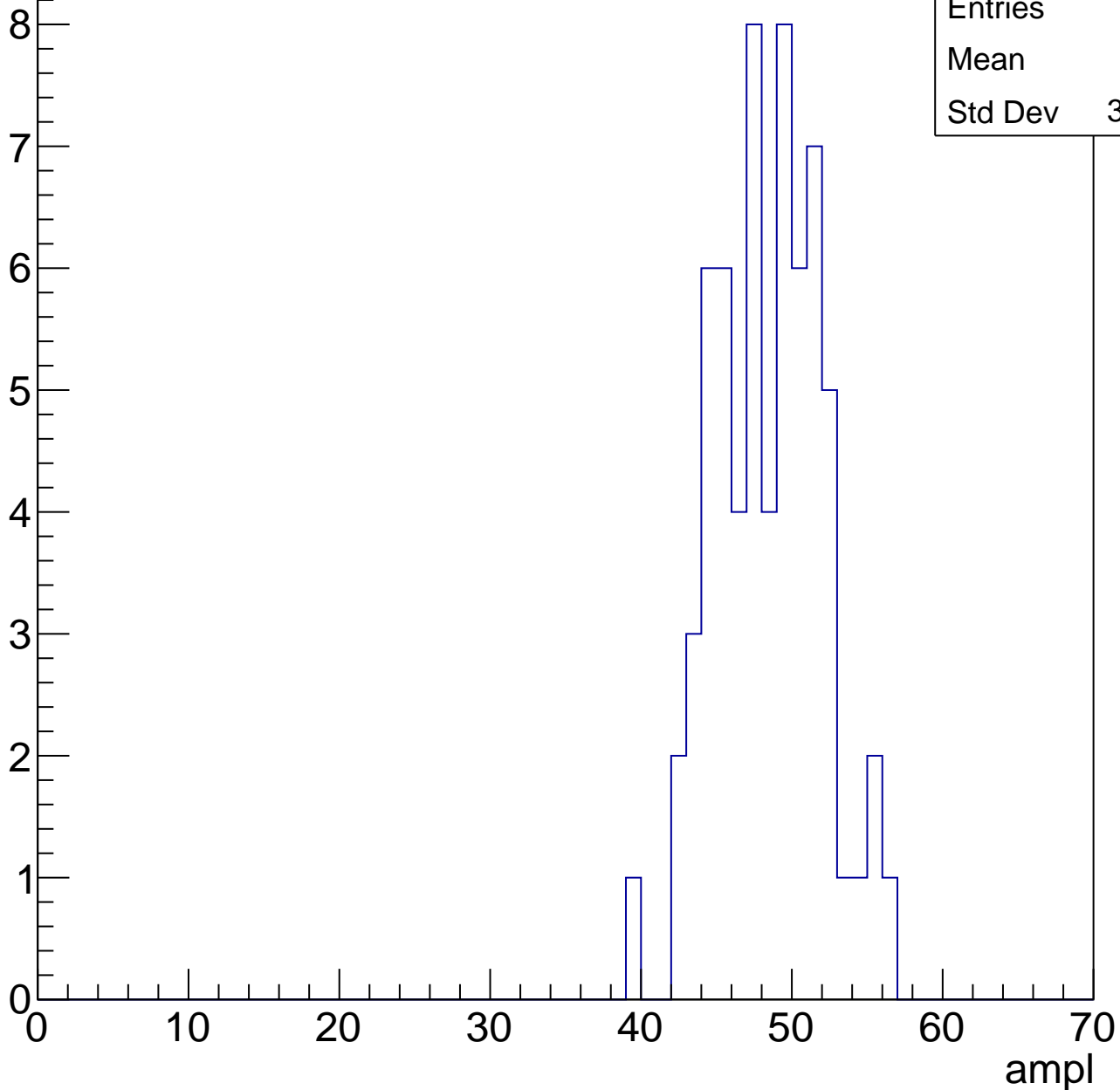


B1L102S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

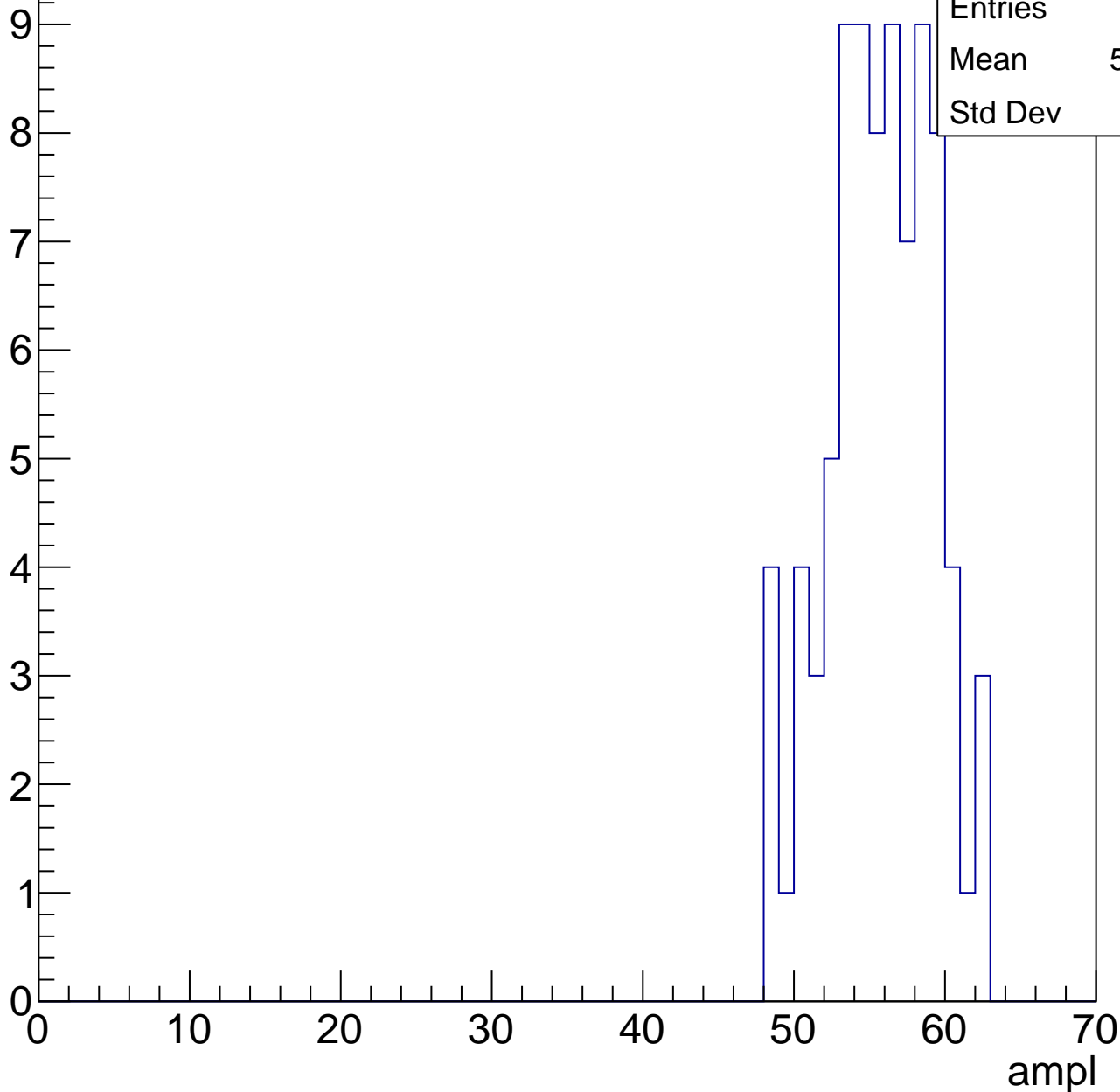
| | |
|---------|-------|
| Entries | 65 |
| Mean | 48 |
| Std Dev | 3.508 |



B1L102S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

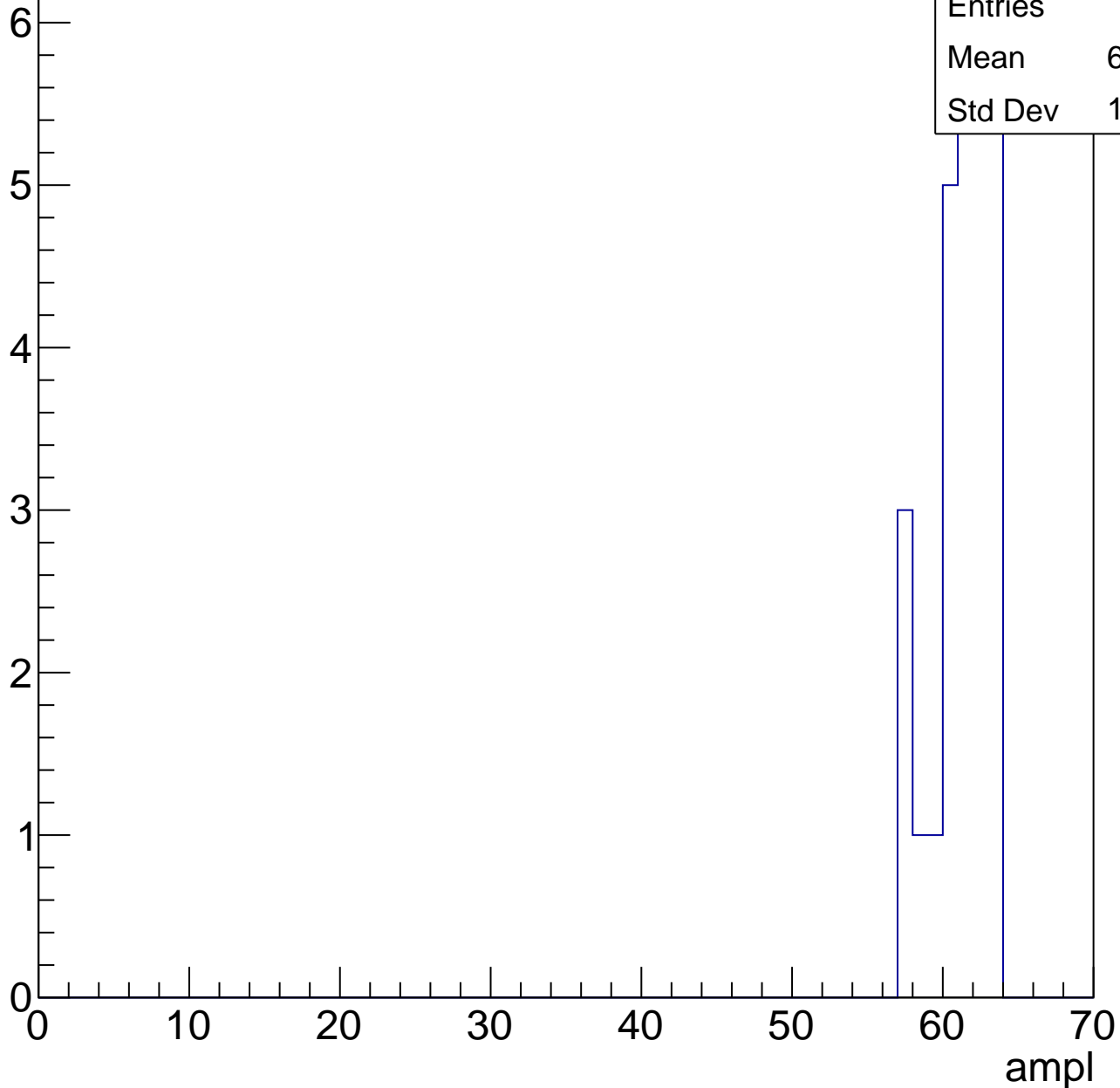
Entry



B1L102S, U15-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

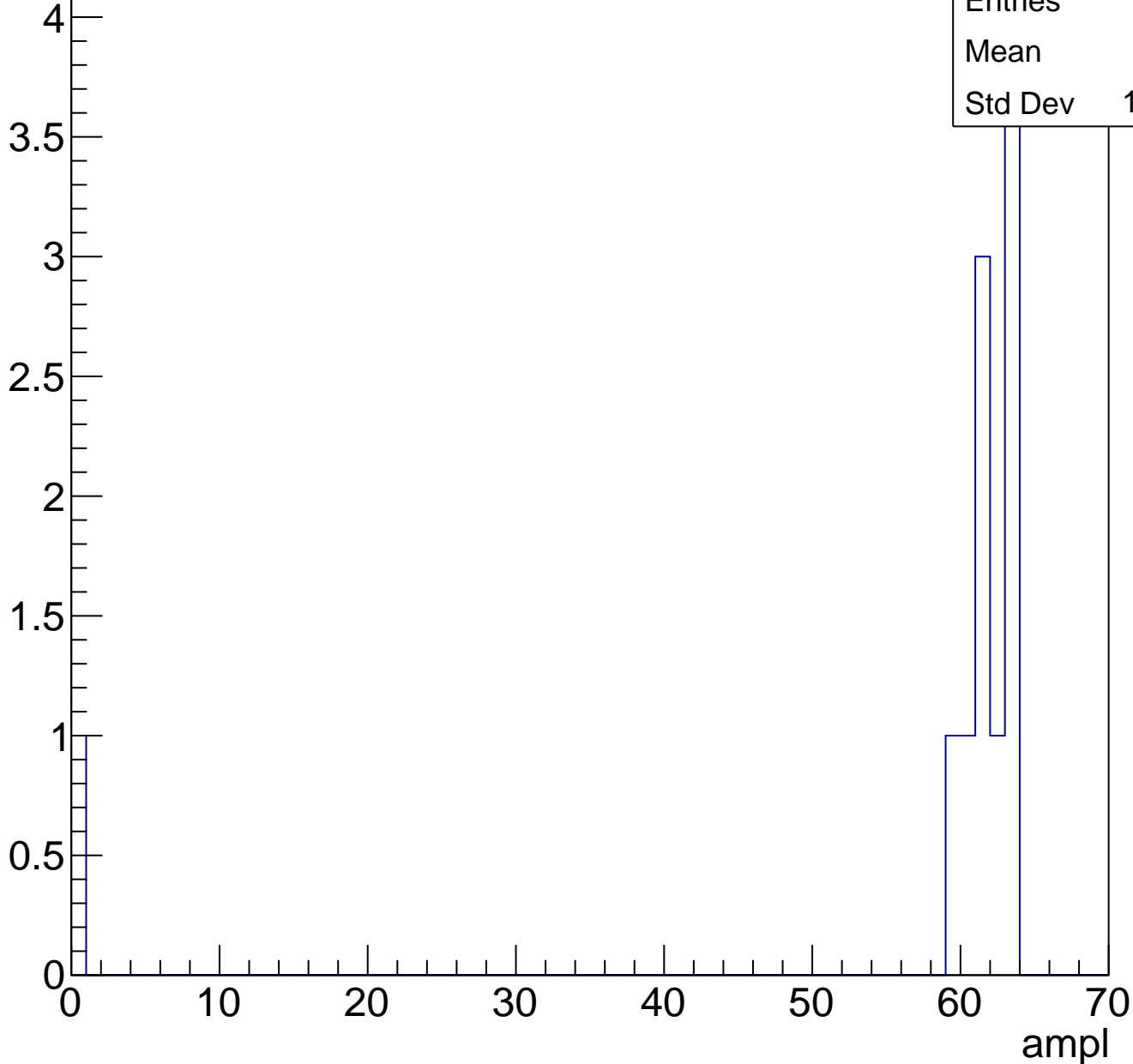
Entry



B1L102S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 11 |
| Mean | 56 |
| Std Dev | 17.76 |

B1L102S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch54, adc0

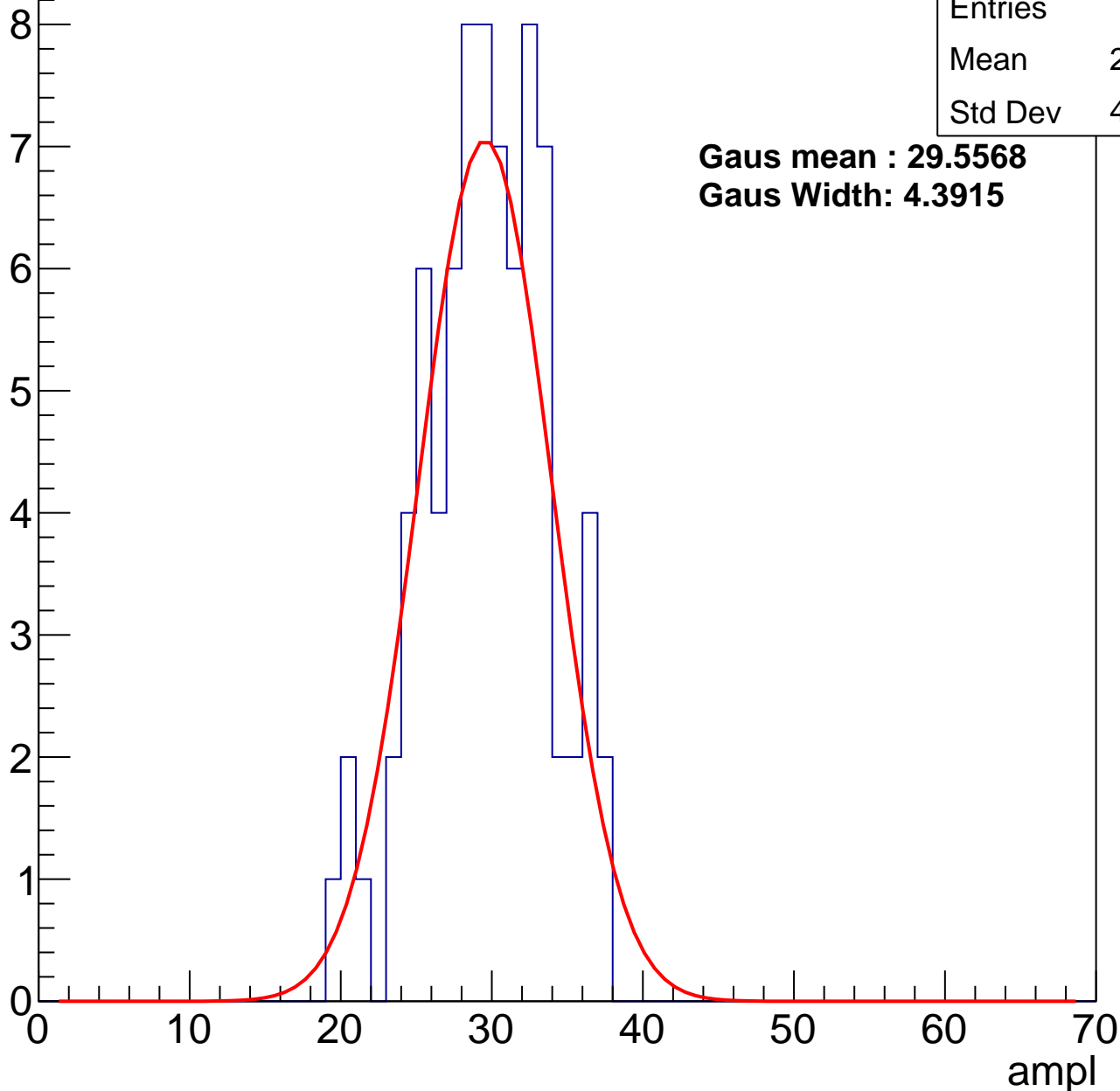
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 29.16 |
| Std Dev | 4.063 |

Gaus mean : 29.5568

Gaus Width: 4.3915



B1L102S, U15-ch54, adc1

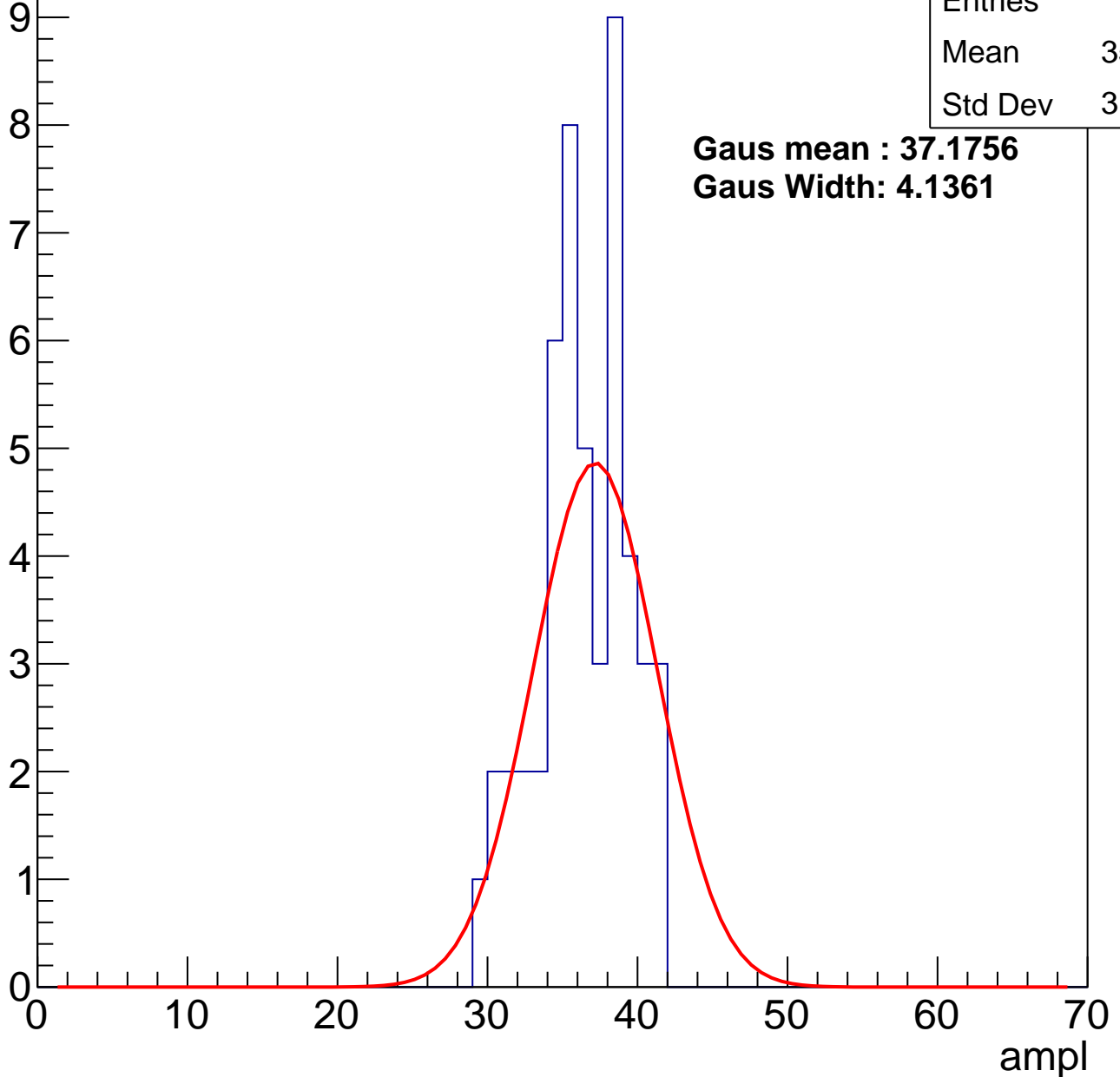
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 35.94 |
| Std Dev | 3.003 |

Gaus mean : 37.1756

Gaus Width: 4.1361



B1L102S, U15-ch54, adc2

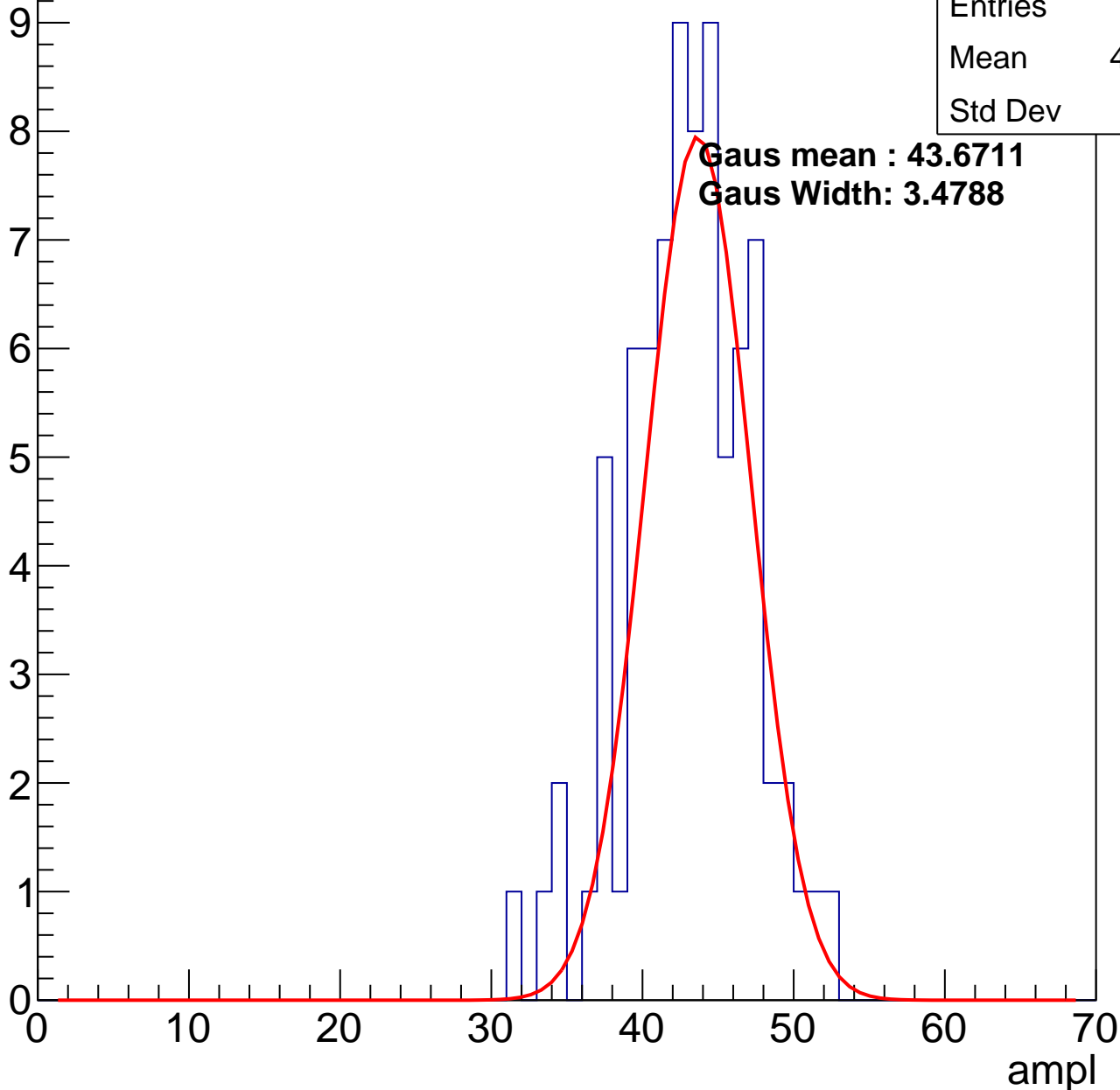
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 42.56 |
| Std Dev | 4.07 |

Gaus mean : 43.6711

Gaus Width: 3.4788

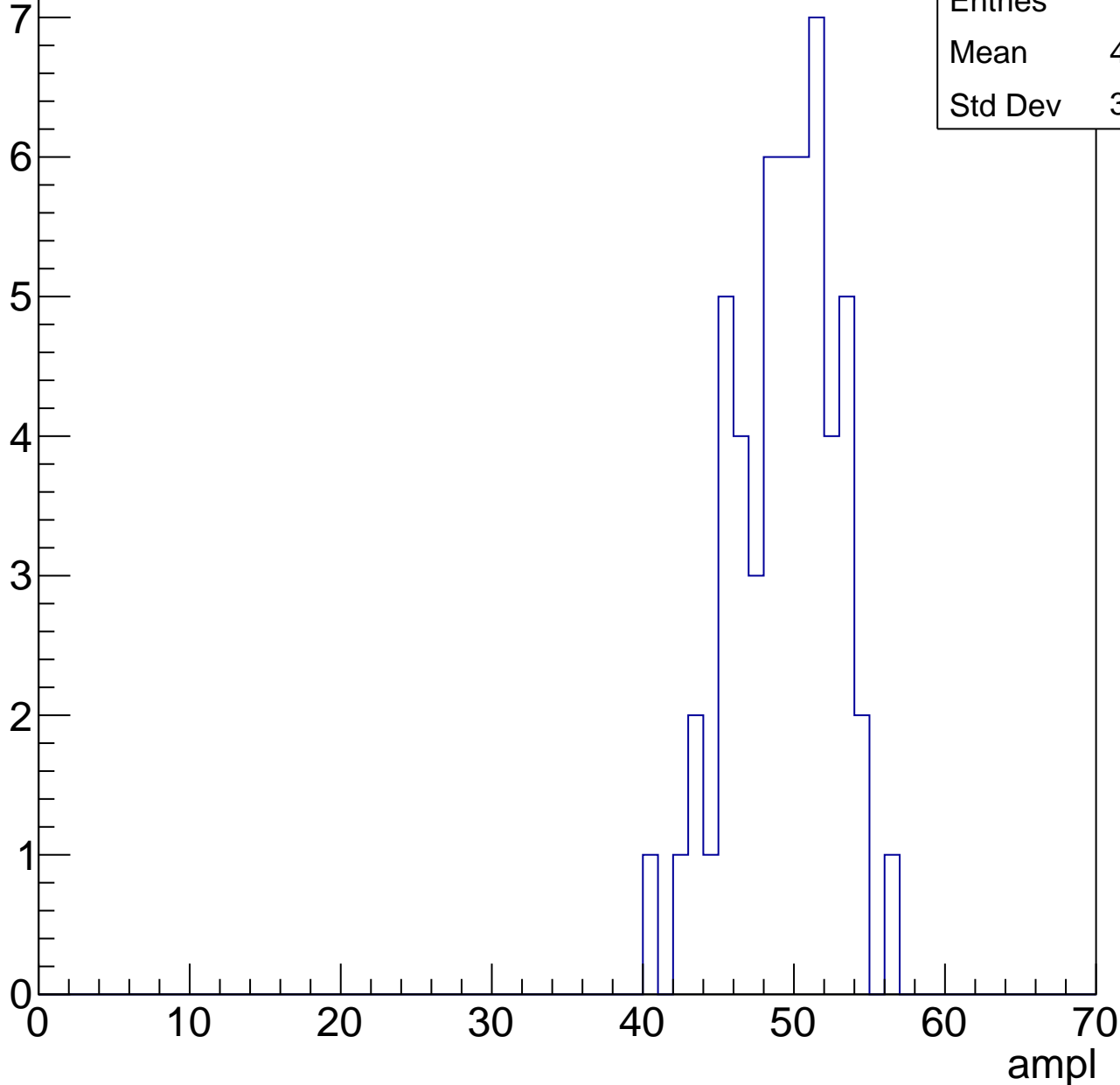


B1L102S, U15-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 48.85 |
| Std Dev | 3.358 |

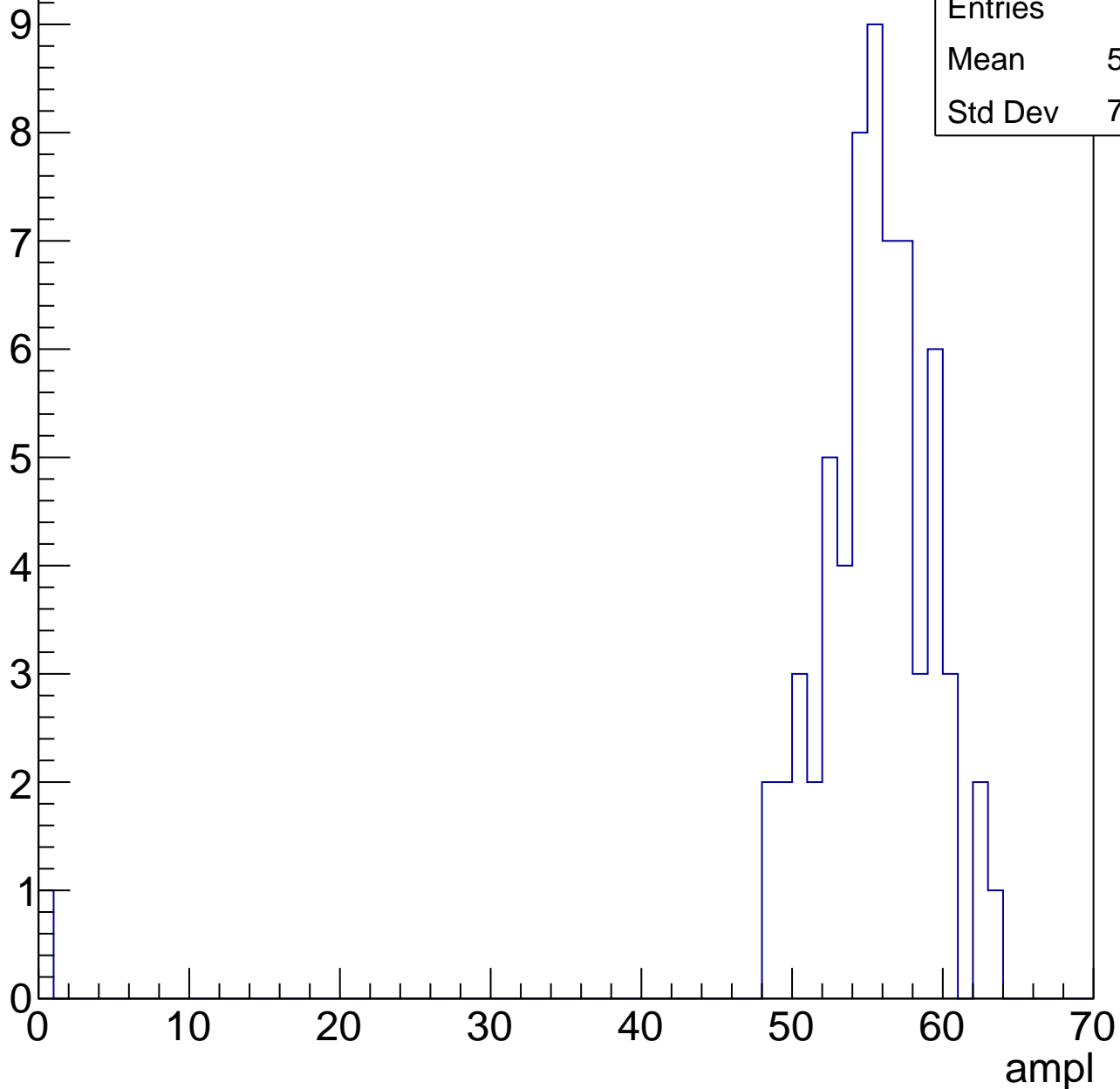


B1L102S, U15-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.32 |
| Std Dev | 7.582 |

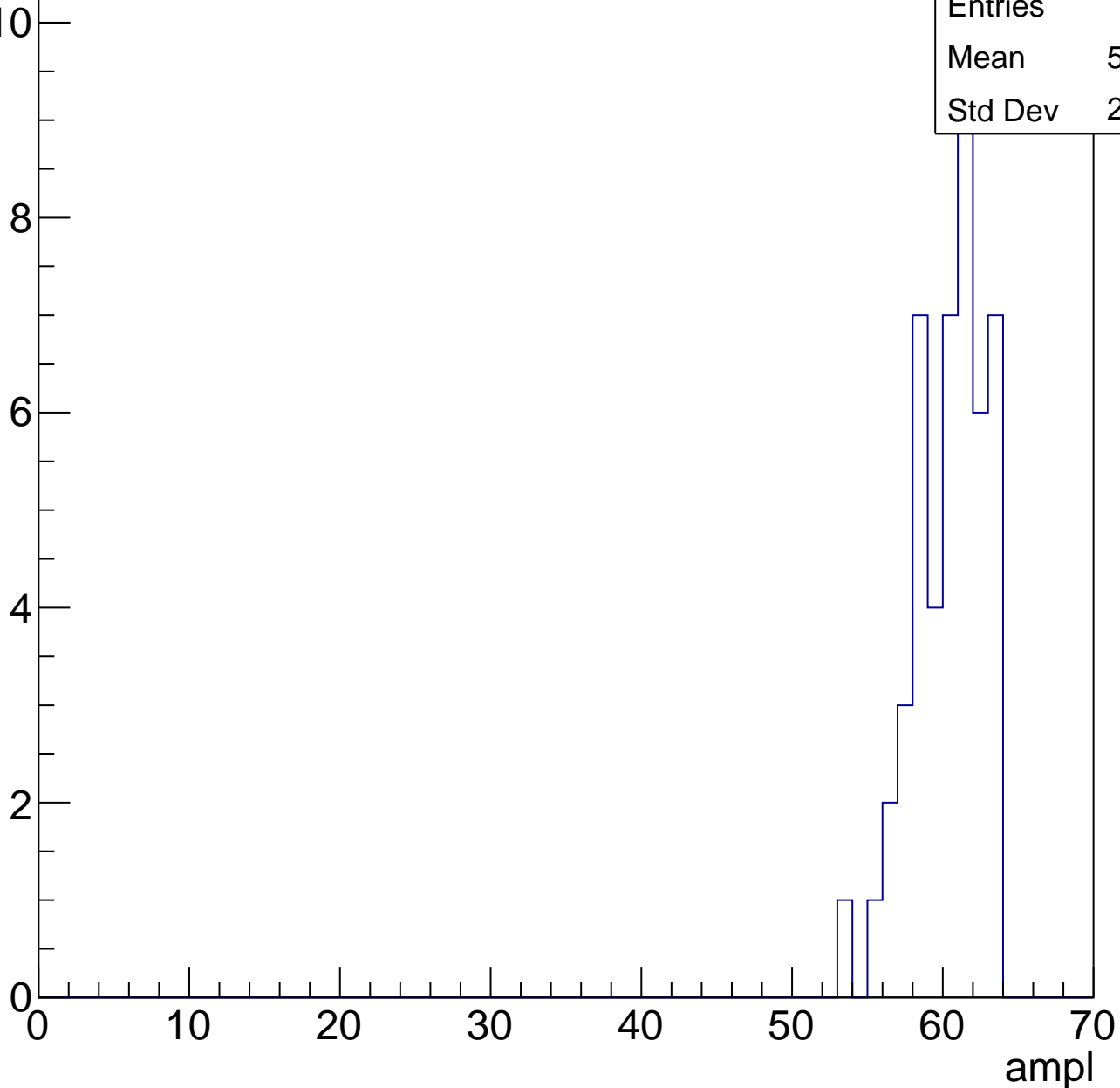


B1L102S, U15-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 59.92 |
| Std Dev | 2.335 |



B1L102S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch55, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 30.47 |
| Std Dev | 4.902 |

Gaus mean : 31.3213

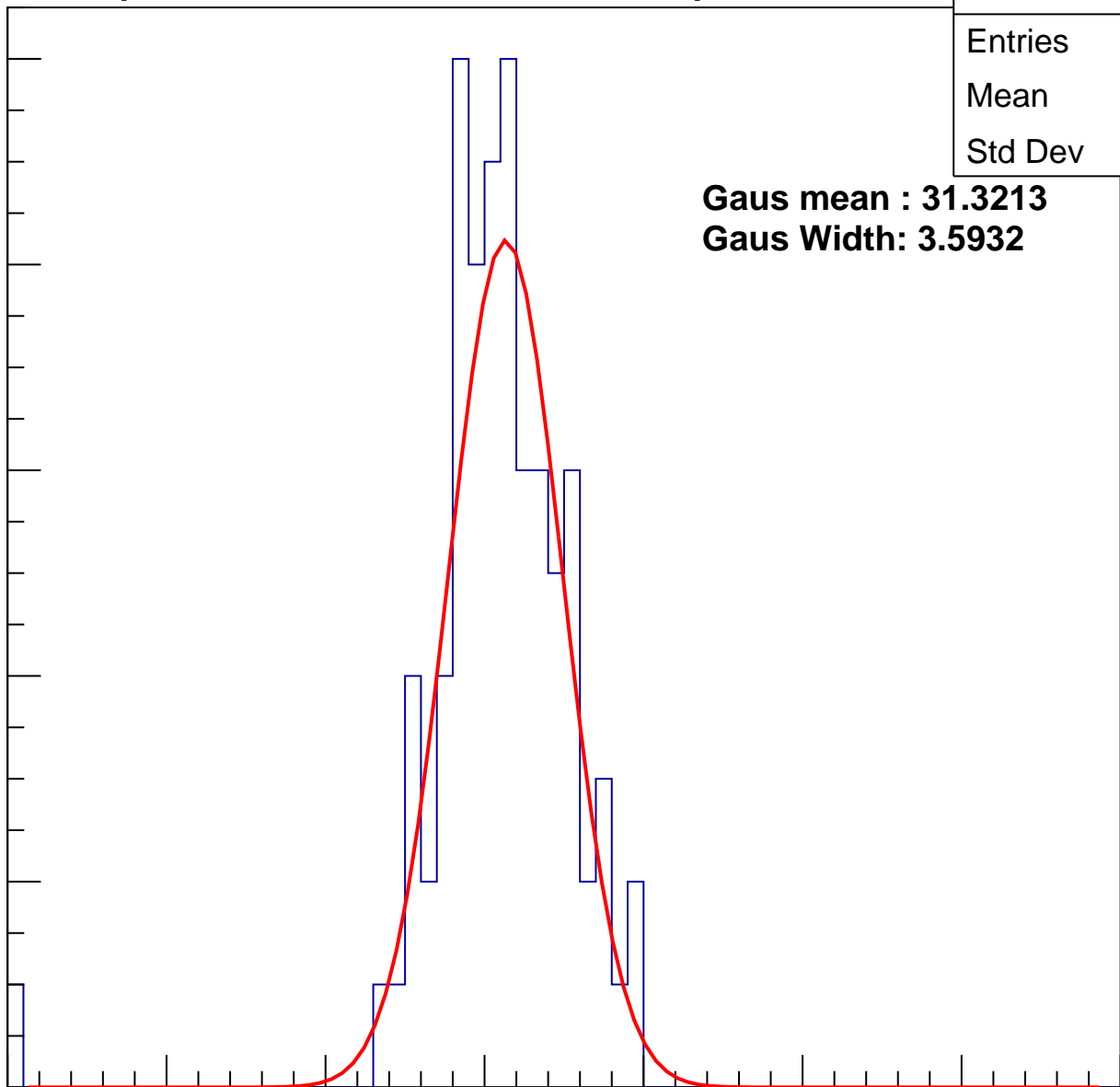
Gaus Width: 3.5932

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



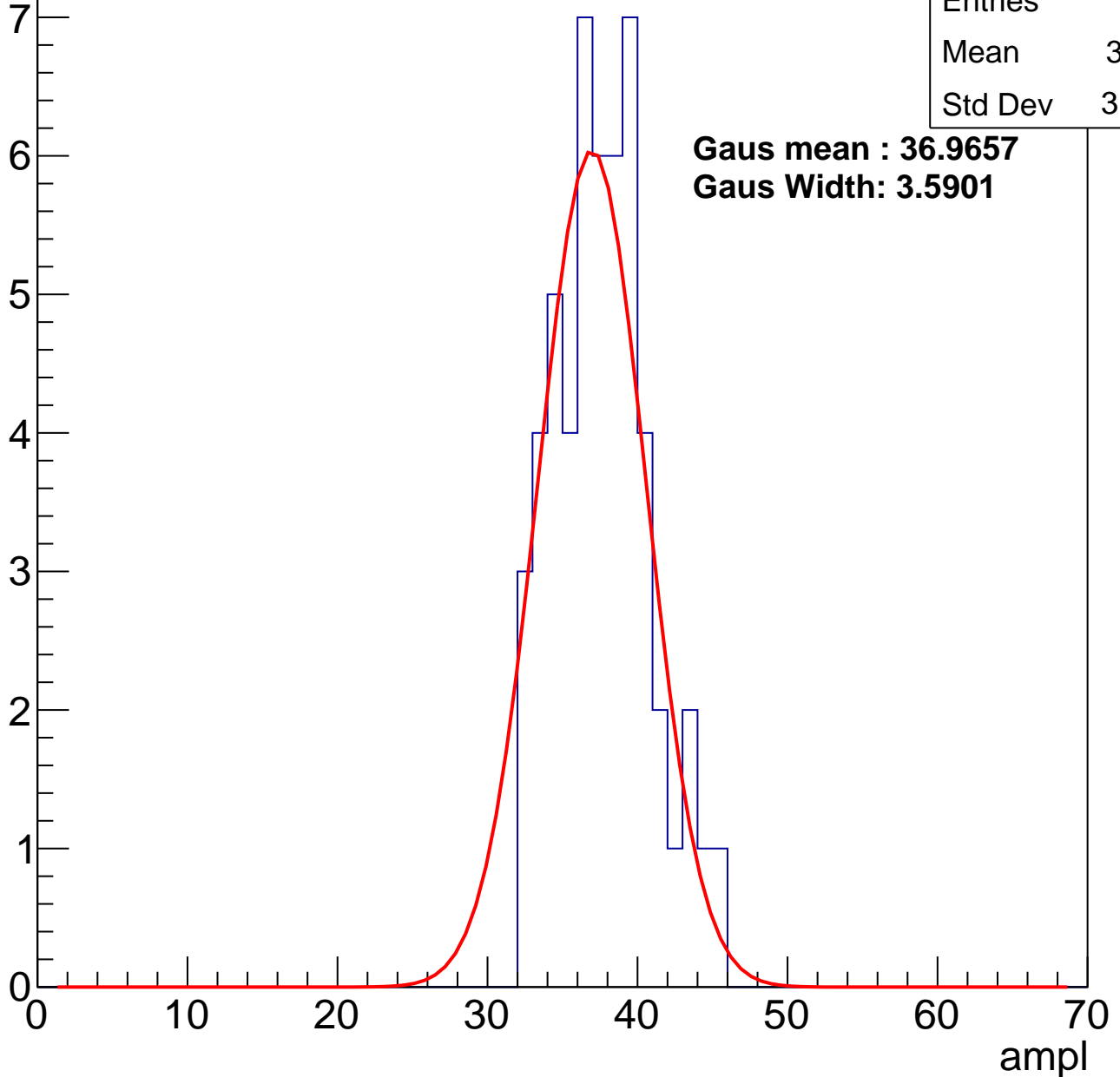
B1L102S, U15-ch55, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

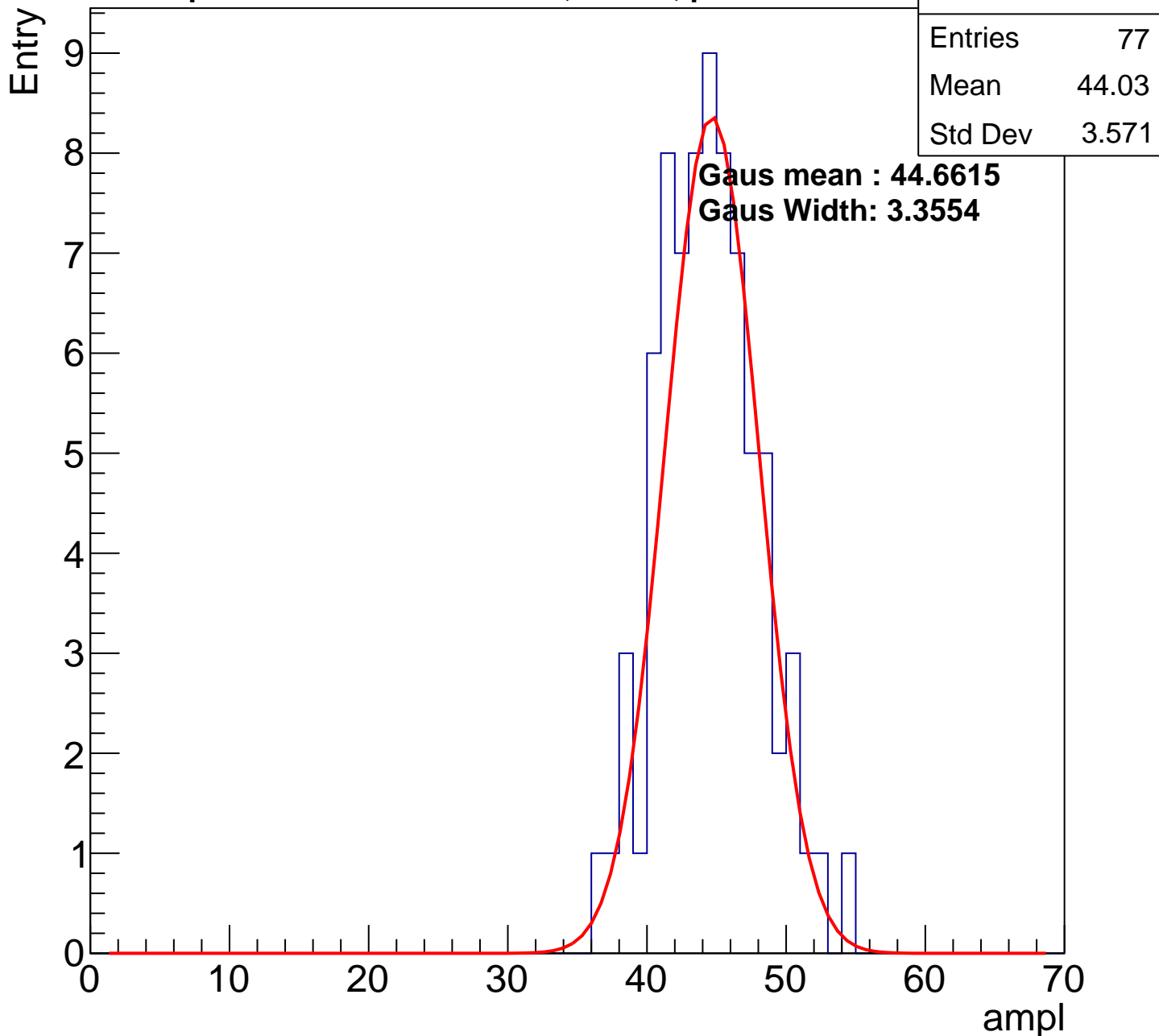
| | |
|---------|-------|
| Entries | 53 |
| Mean | 37.21 |
| Std Dev | 3.122 |

Gaus mean : 36.9657
Gaus Width: 3.5901



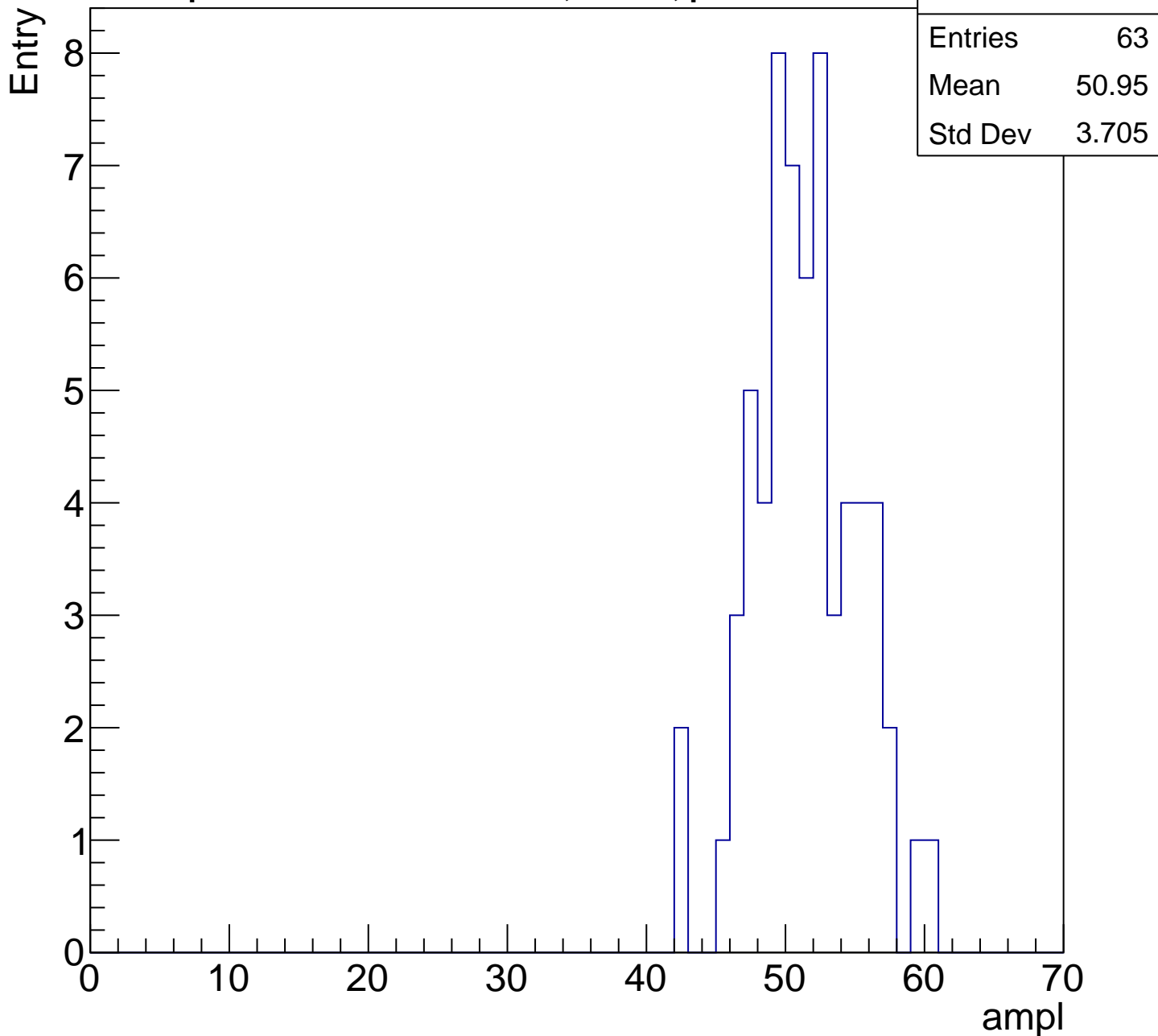
B1L102S, U15-ch55, adc2

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

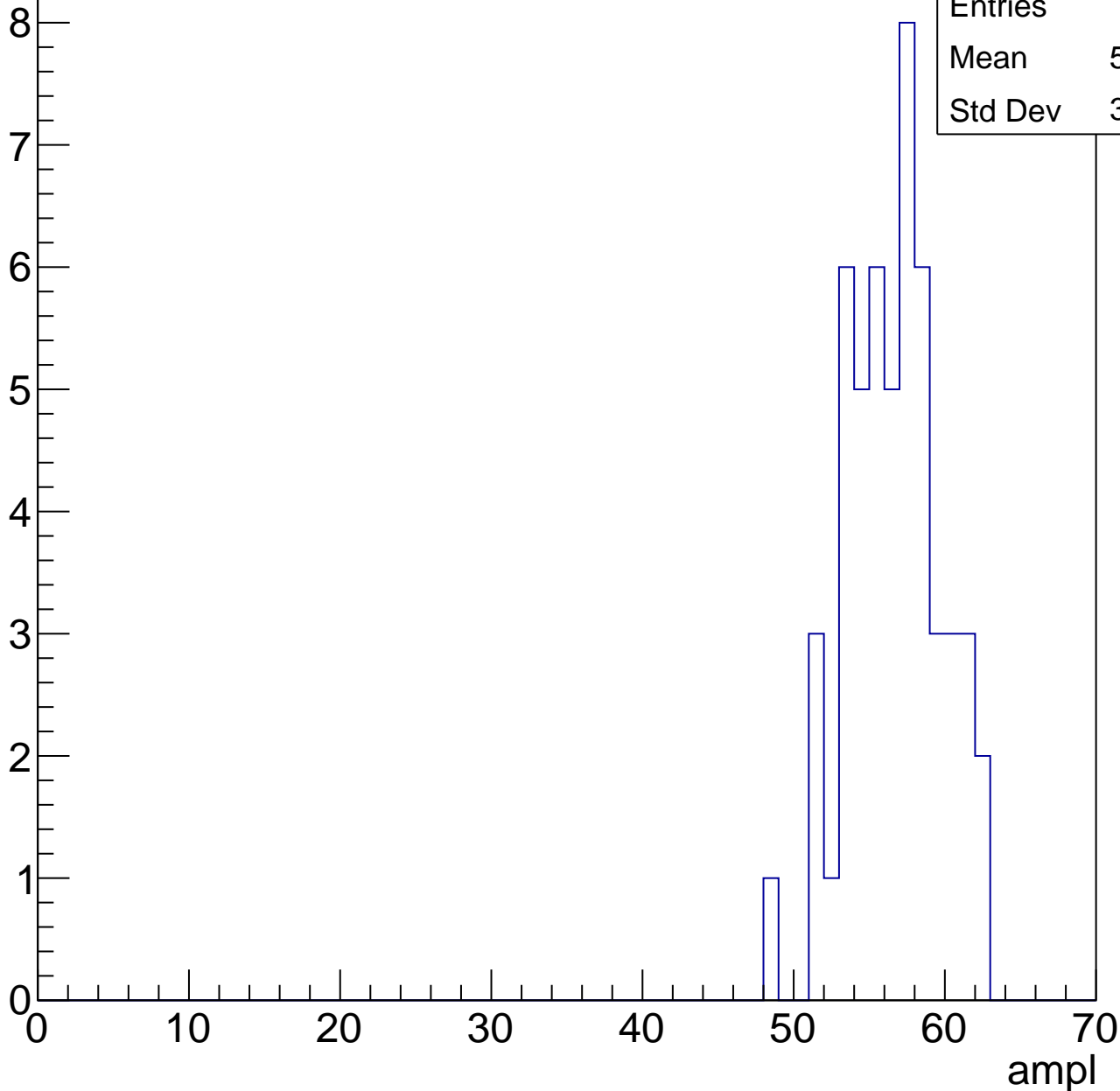


B1L102S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 56.13 |
| Std Dev | 3.064 |

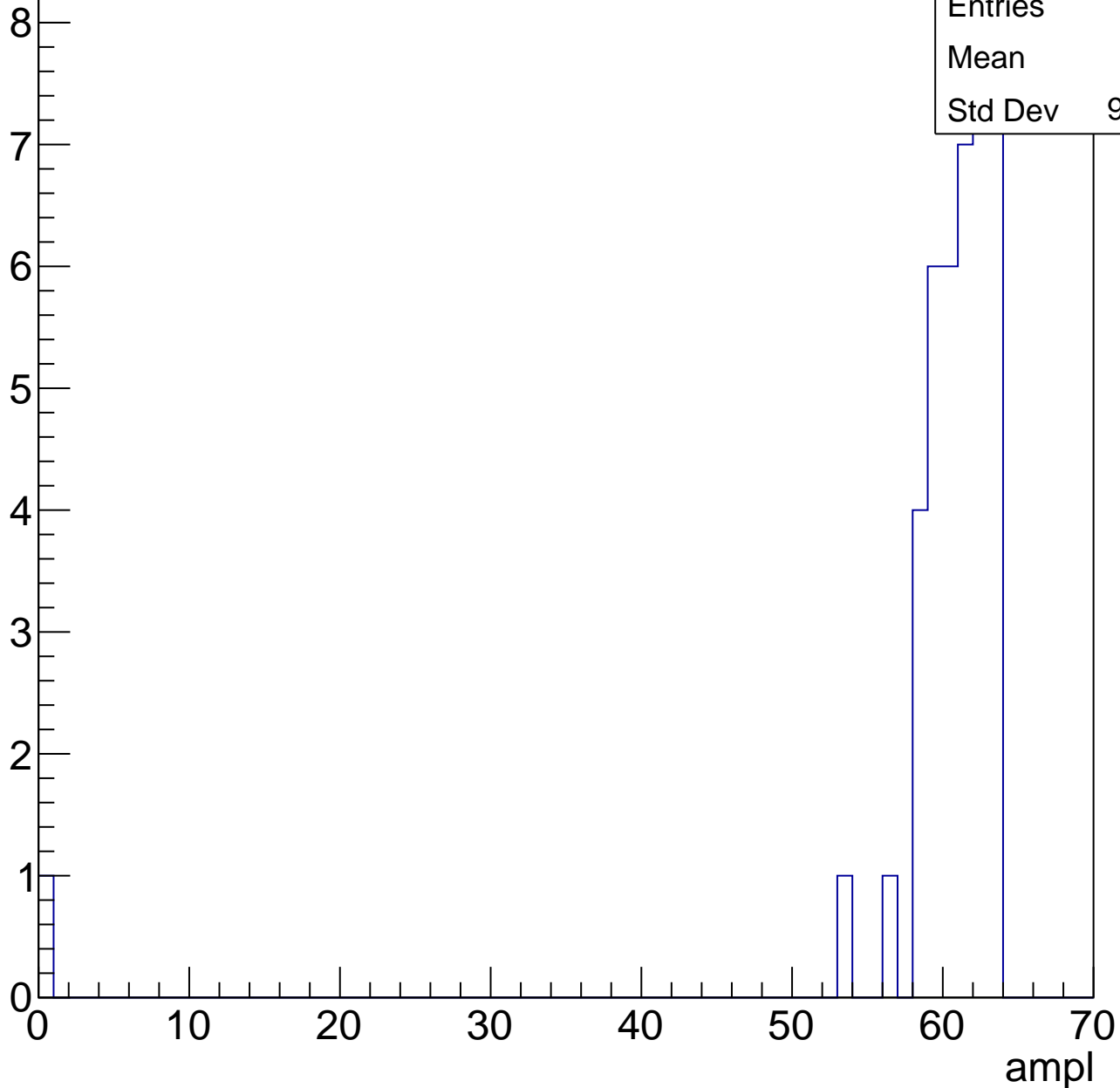


B1L102S, U15-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.1 |
| Std Dev | 9.466 |



B1L102S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch56, adc0

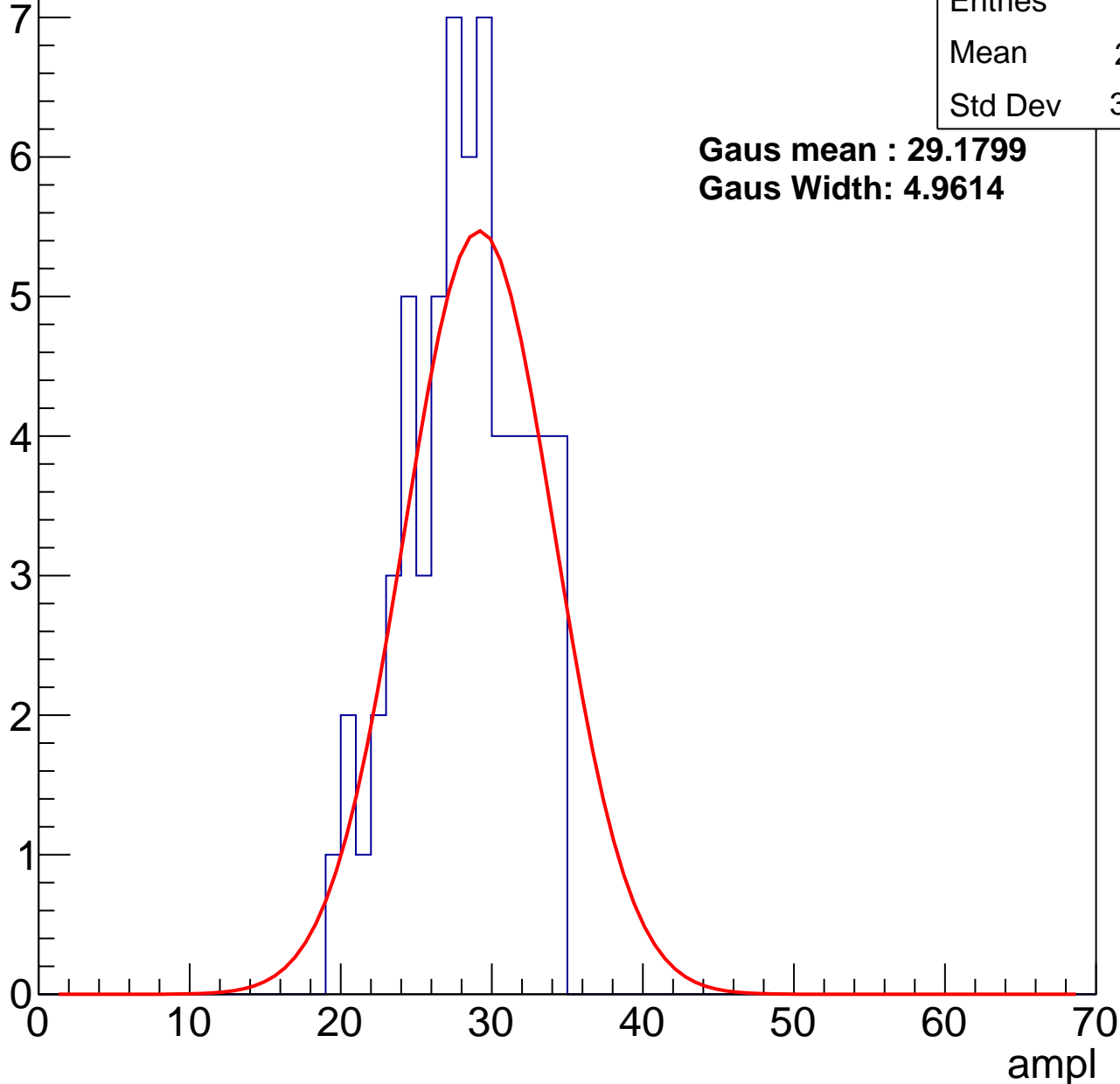
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 27.71 |
| Std Dev | 3.812 |

Gaus mean : 29.1799

Gaus Width: 4.9614



B1L102S, U15-ch56, adc1

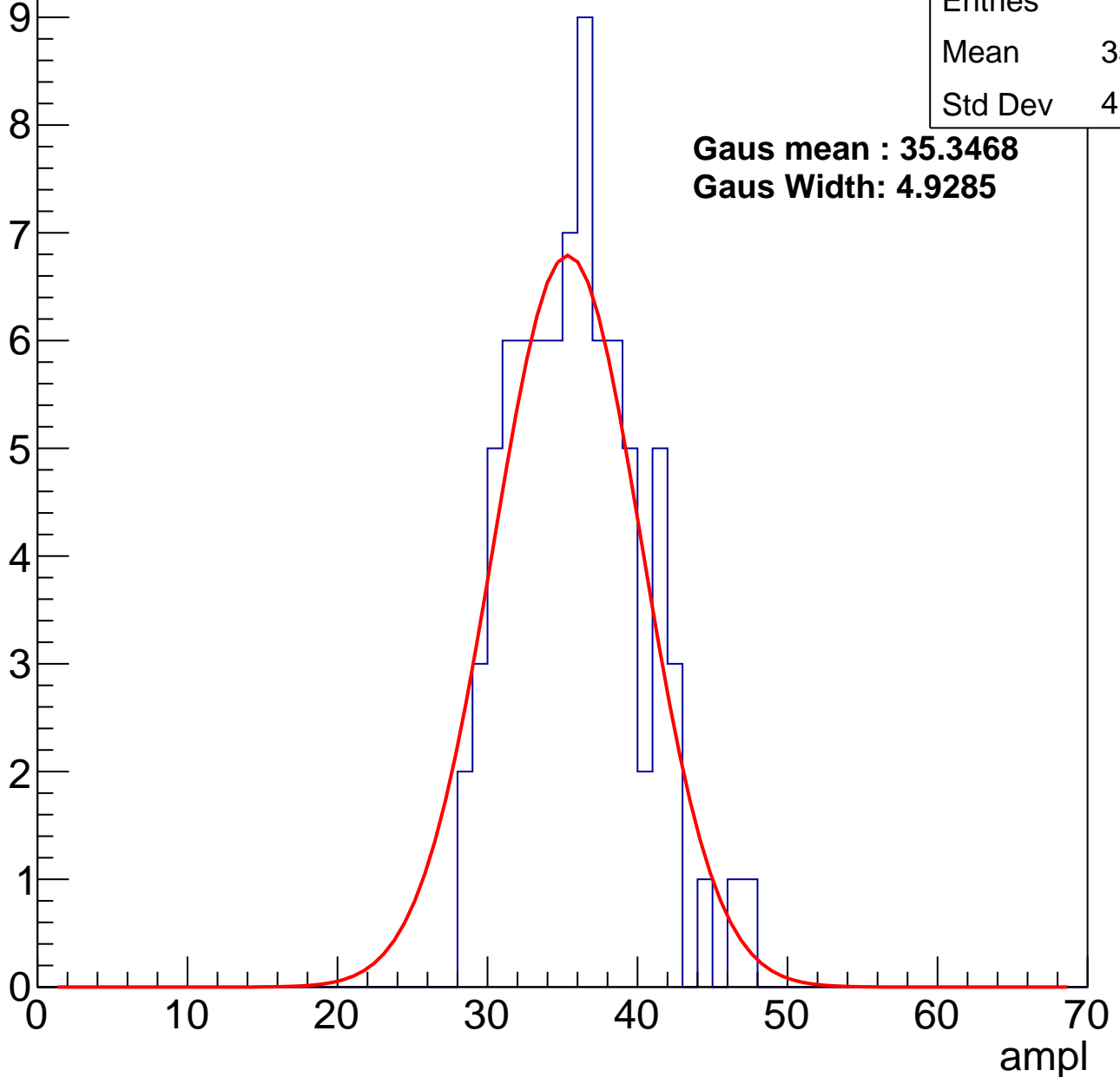
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 35.44 |
| Std Dev | 4.162 |

Gaus mean : 35.3468

Gaus Width: 4.9285



B1L102S, U15-ch56, adc2

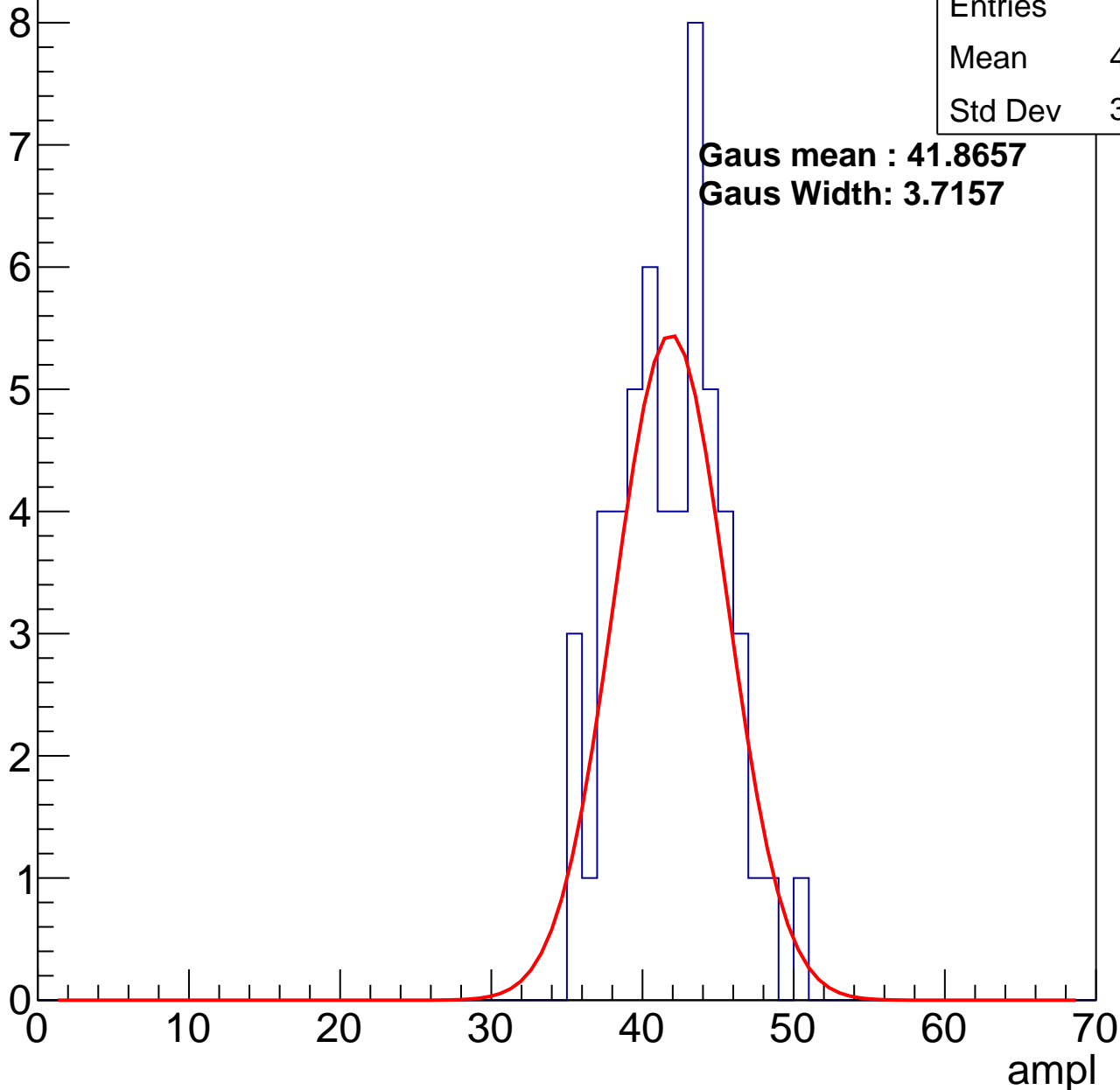
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 41.39 |
| Std Dev | 3.434 |

Gaus mean : 41.8657

Gaus Width: 3.7157

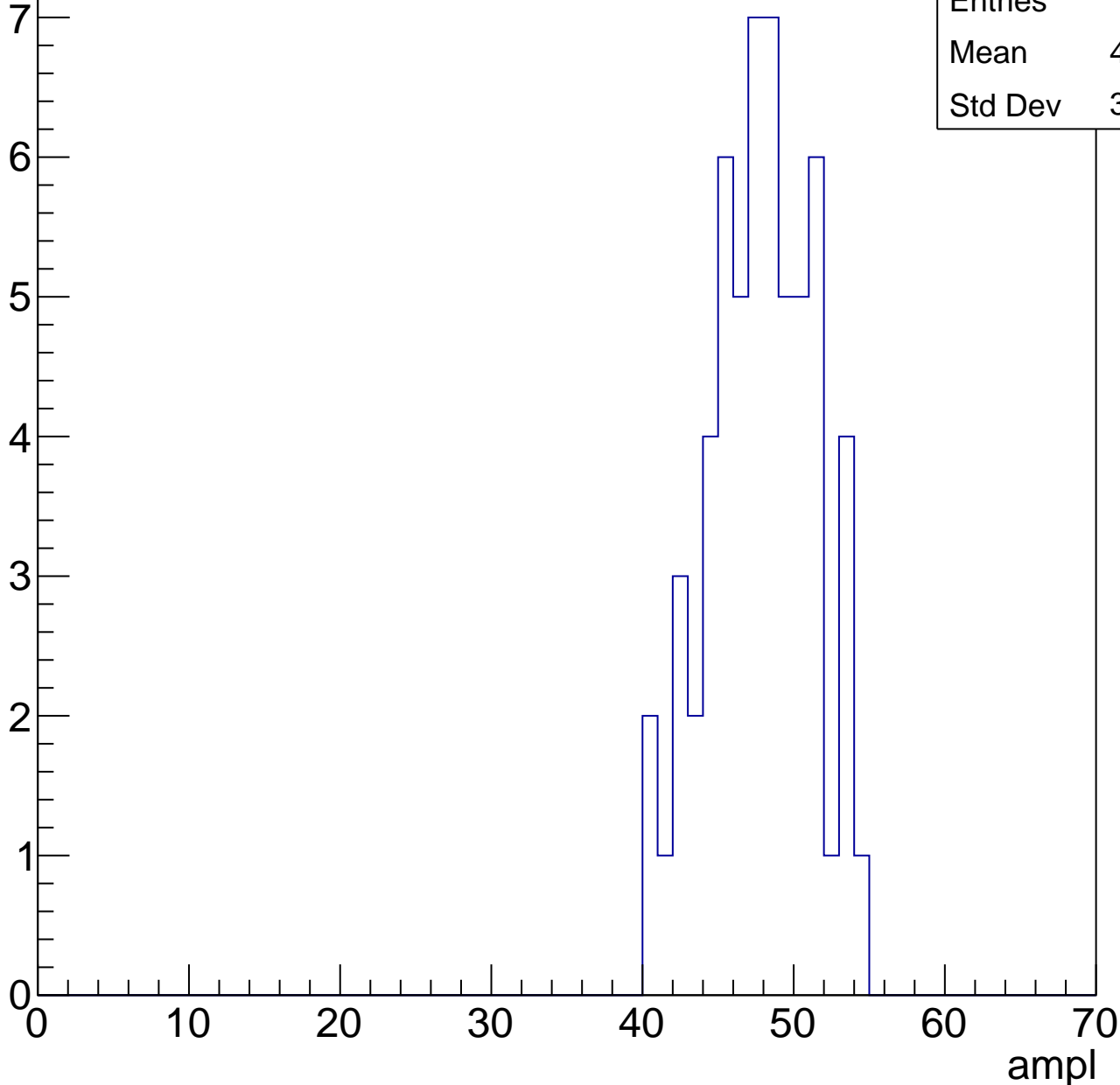


B1L102S, U15-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

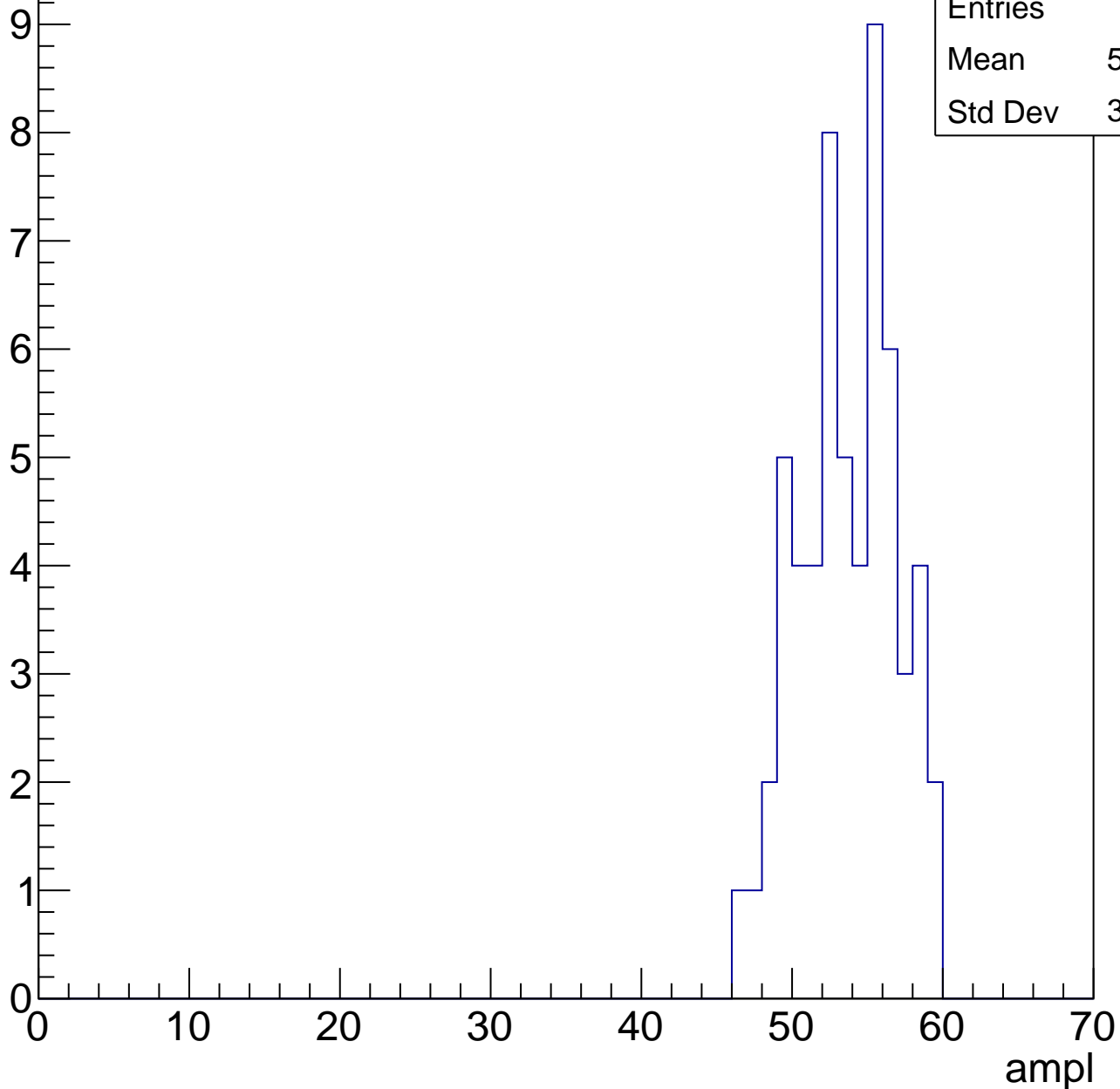
| | |
|---------|-------|
| Entries | 59 |
| Mean | 47.34 |
| Std Dev | 3.408 |



B1L102S, U15-ch56, adc4

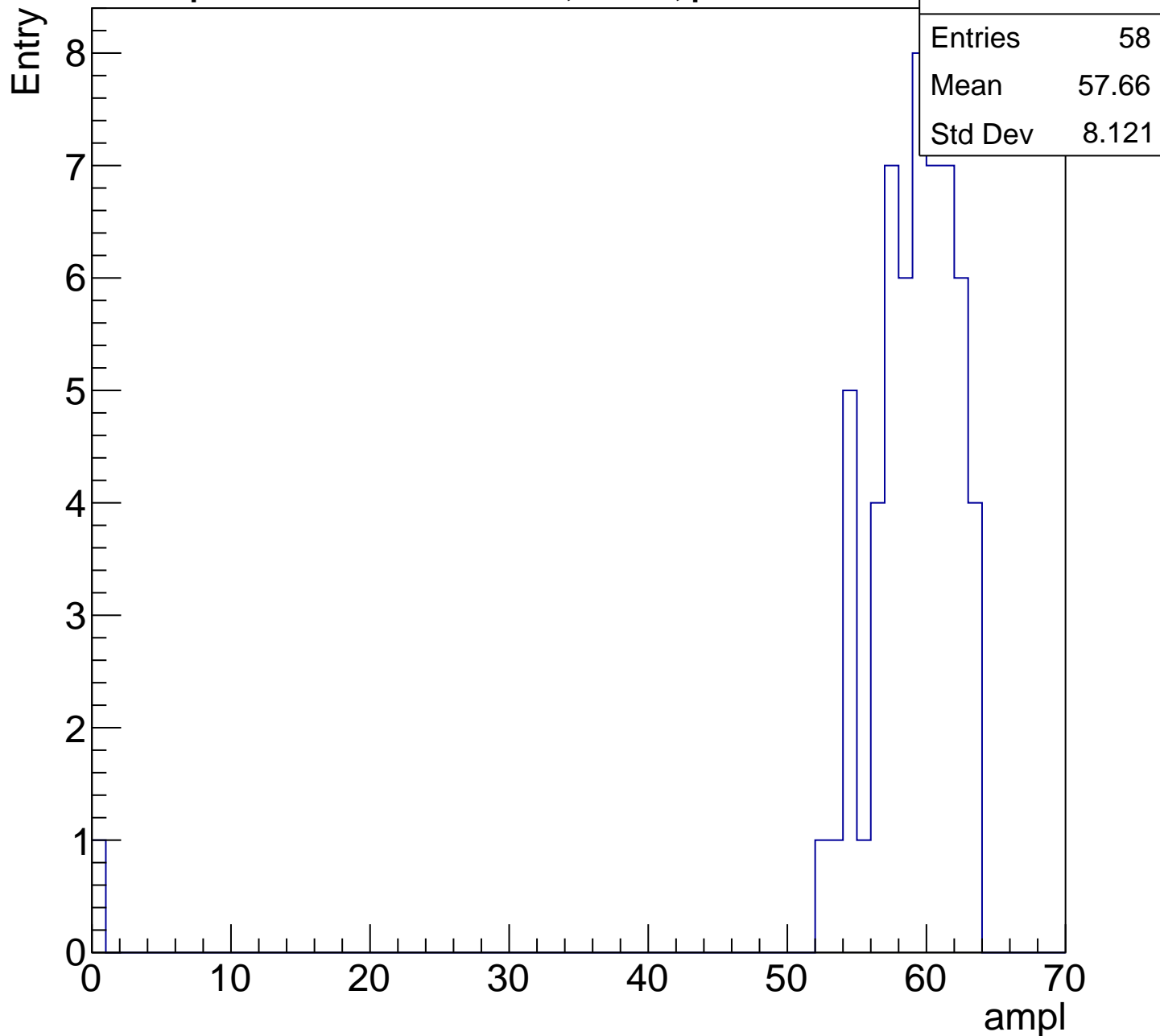
calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

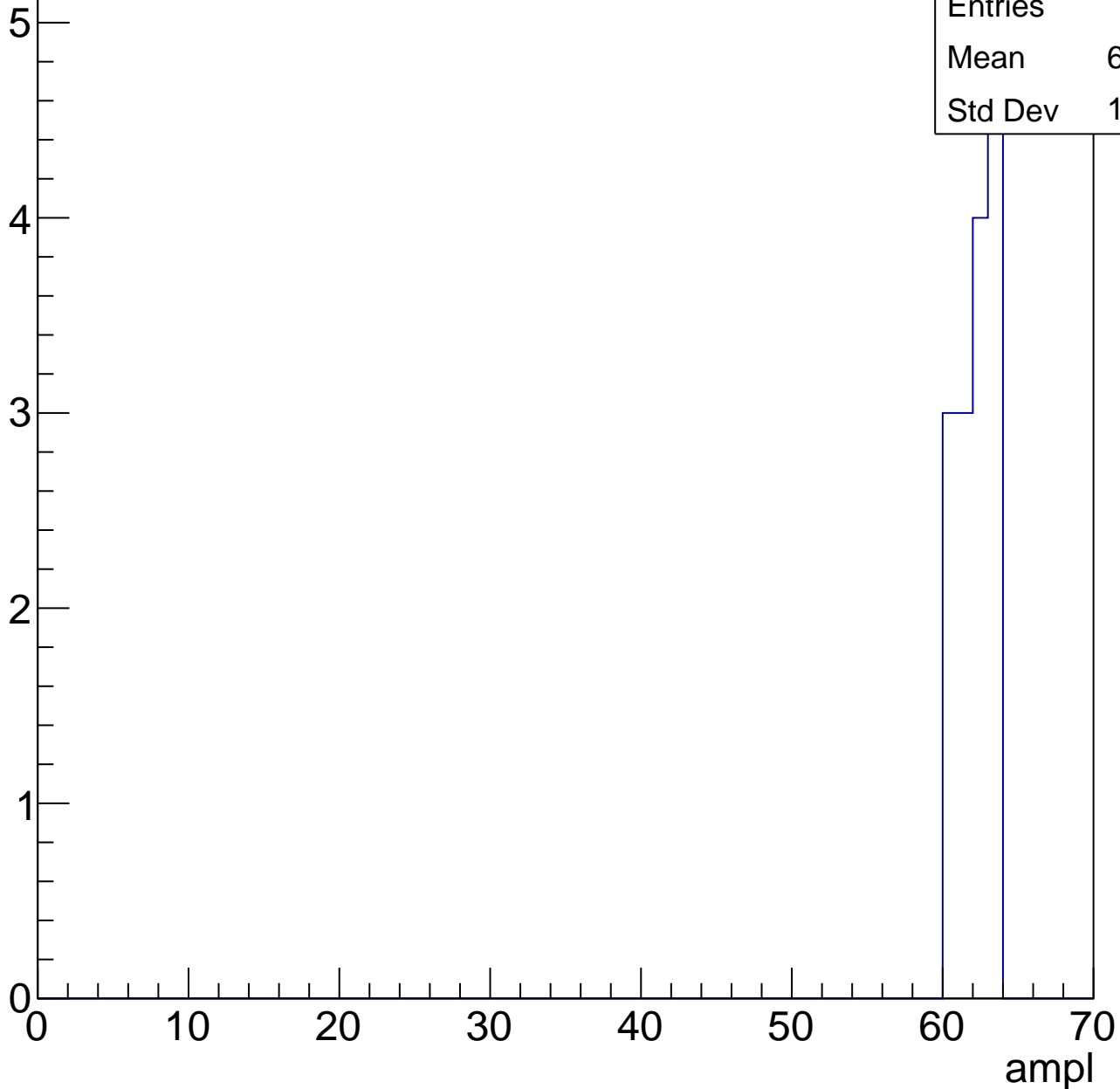


B1L102S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.73 |
| Std Dev | 1.123 |



B1L102S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch57, adc0

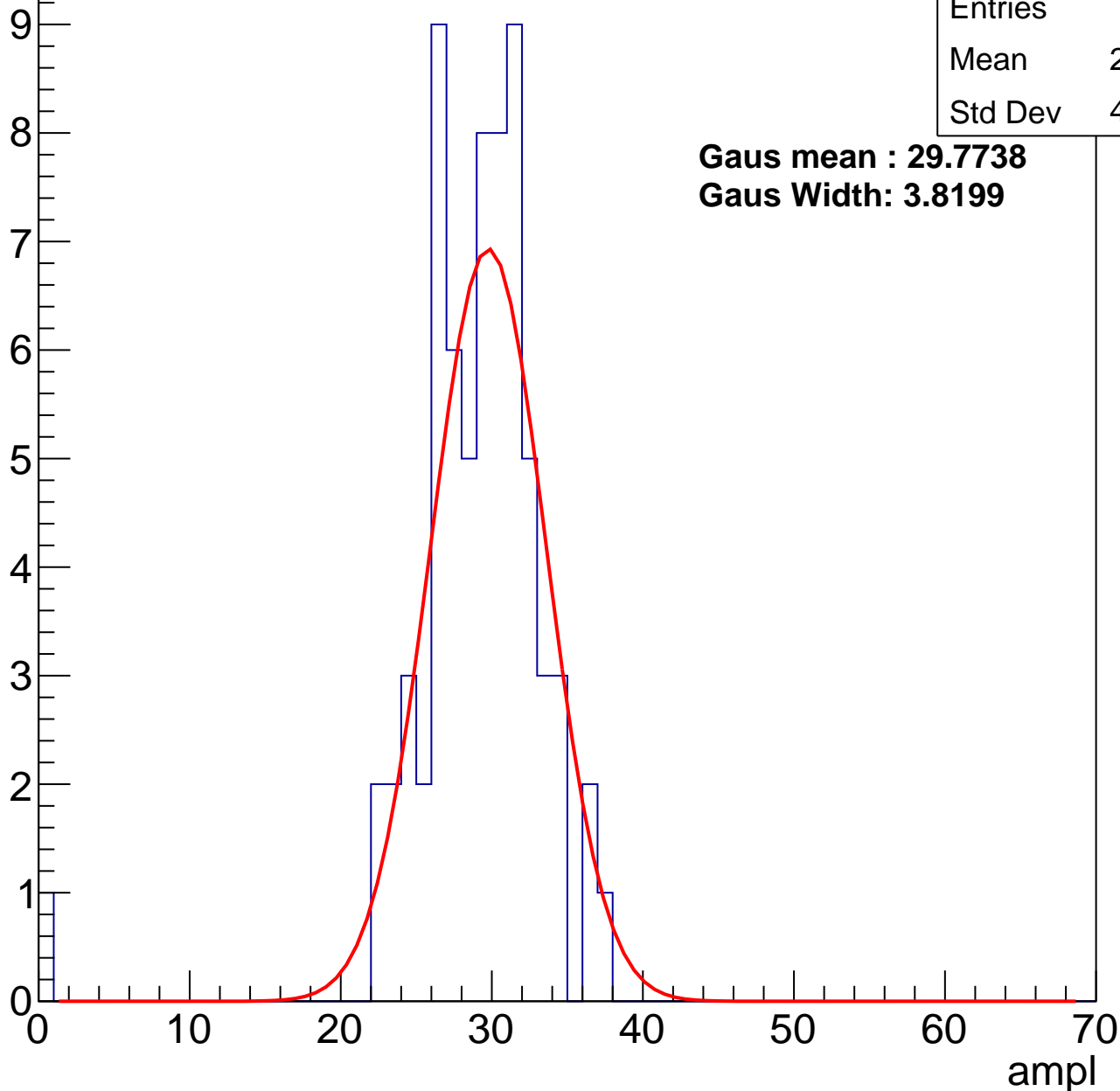
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 28.54 |
| Std Dev | 4.796 |

Gaus mean : 29.7738

Gaus Width: 3.8199



B1L102S, U15-ch57, adc1

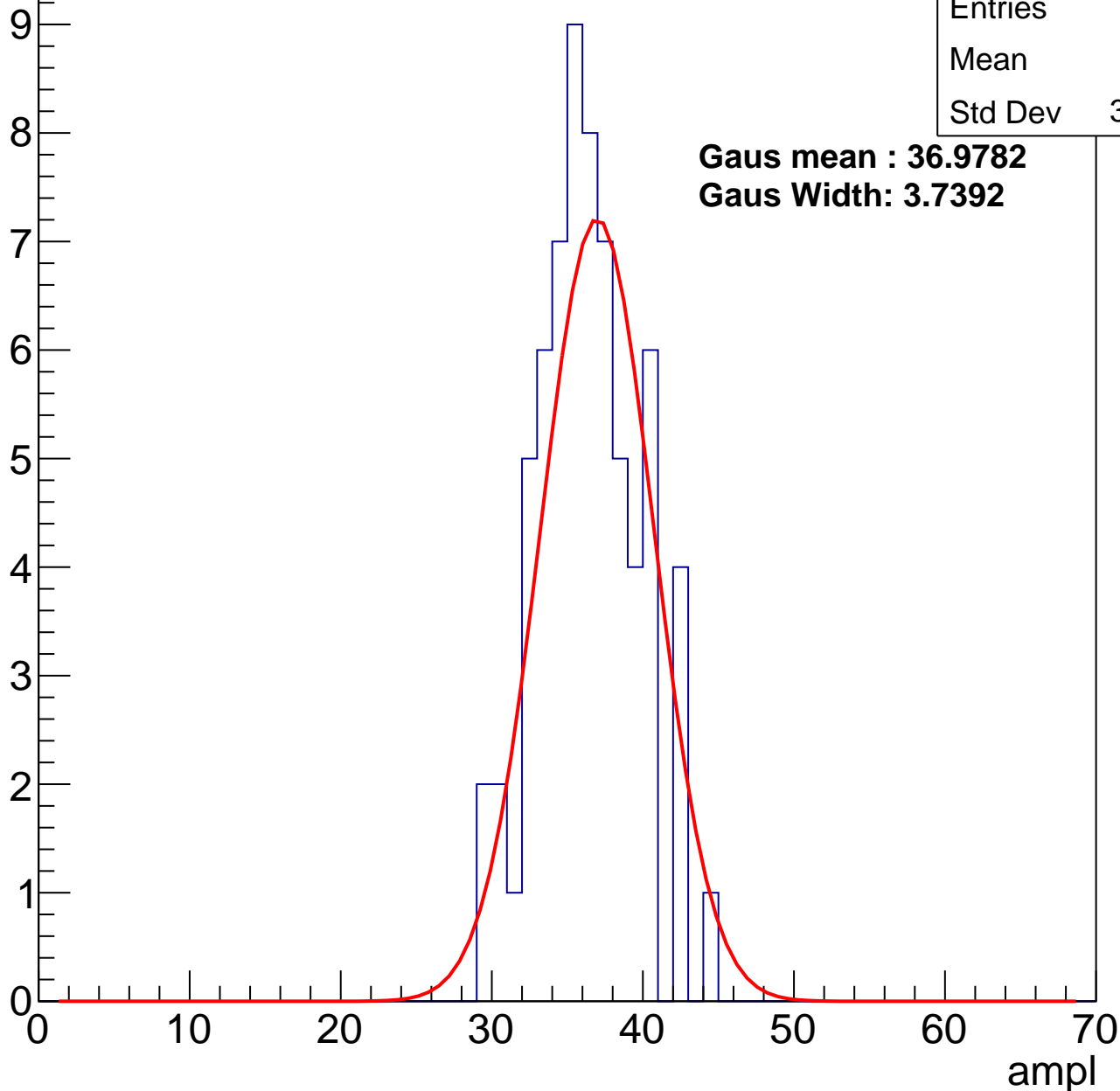
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 35.9 |
| Std Dev | 3.306 |

Gaus mean : 36.9782

Gaus Width: 3.7392



B1L102S, U15-ch57, adc2

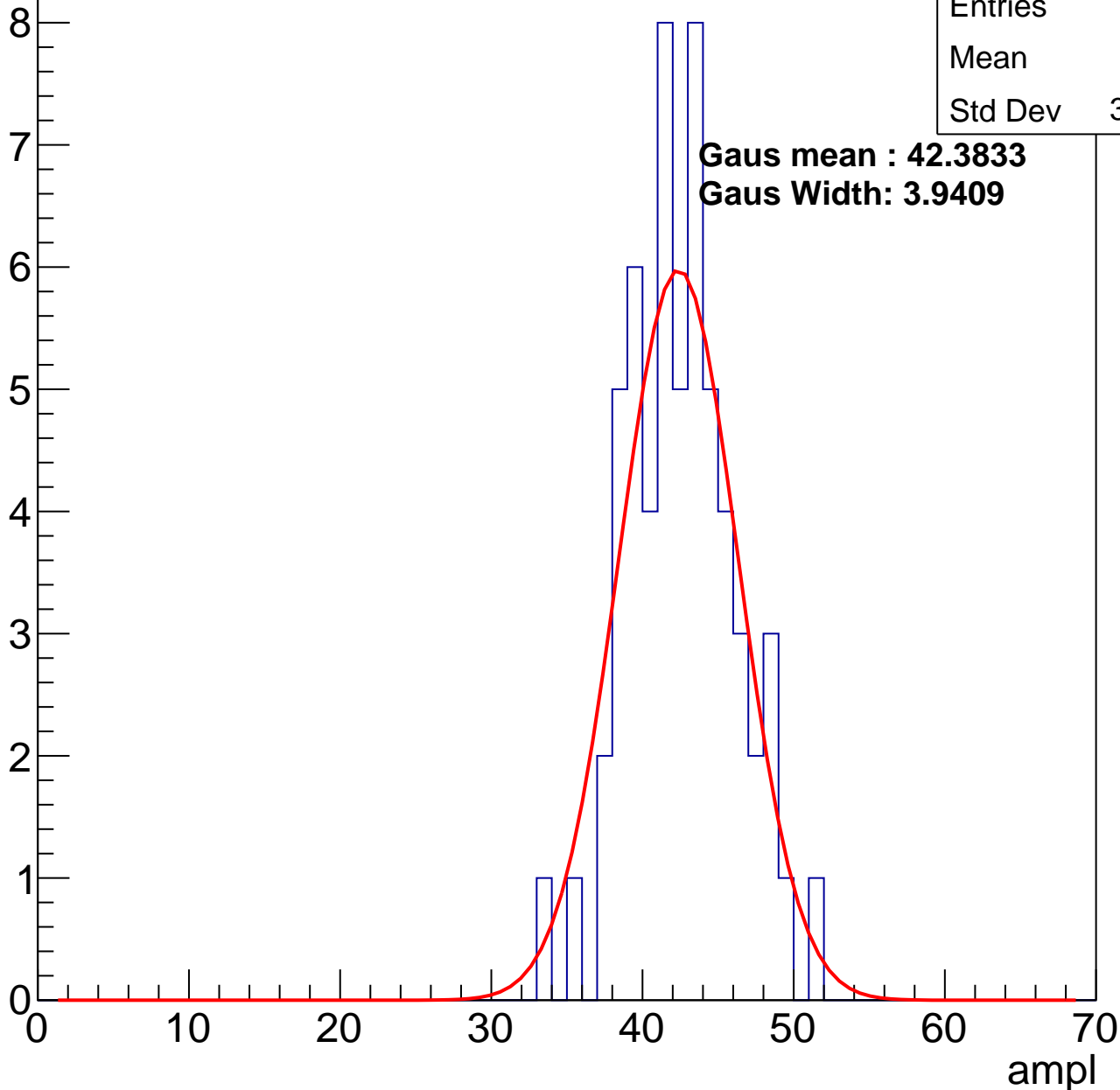
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 42.1 |
| Std Dev | 3.535 |

Gaus mean : 42.3833

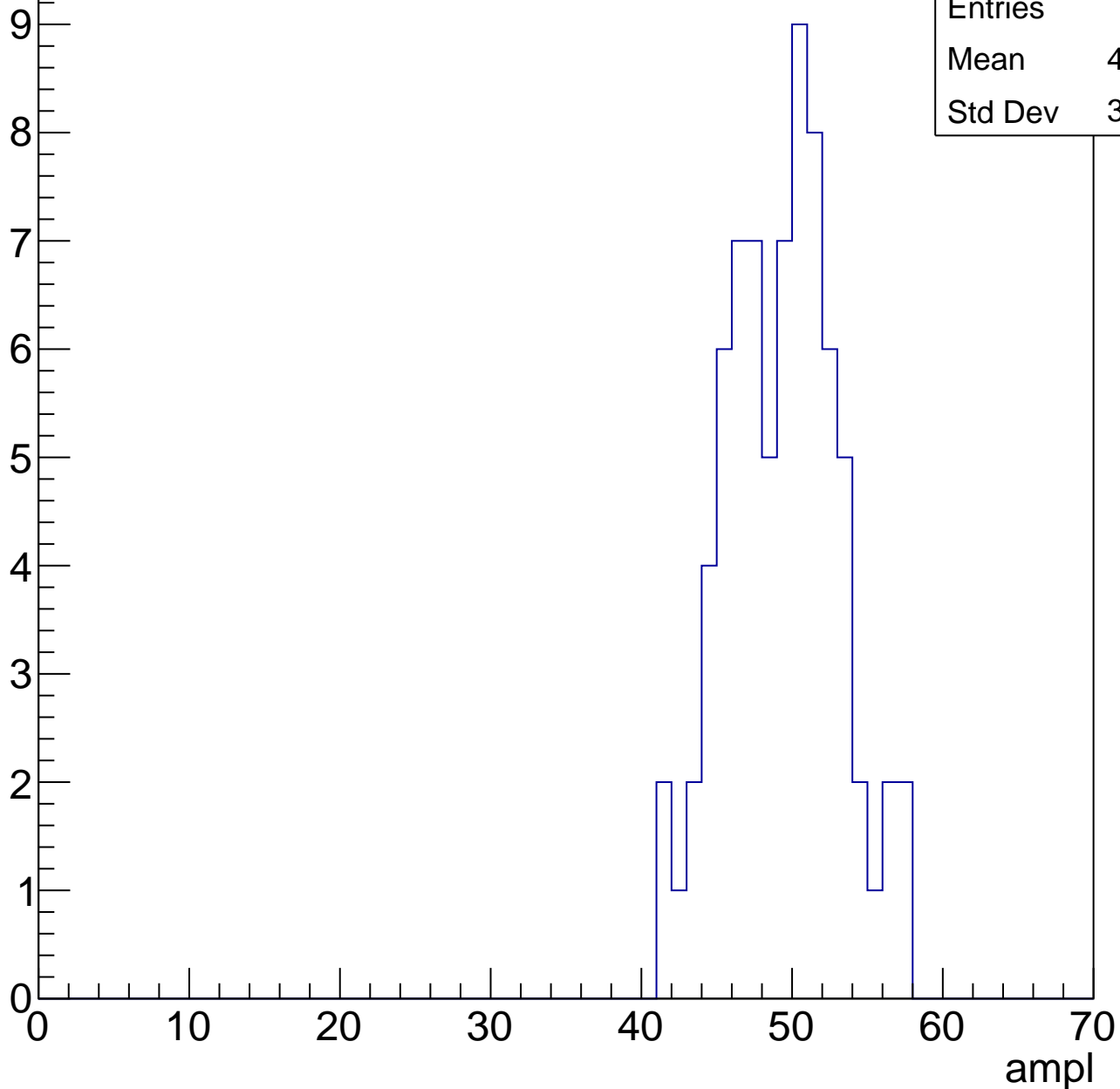
Gaus Width: 3.9409



B1L102S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



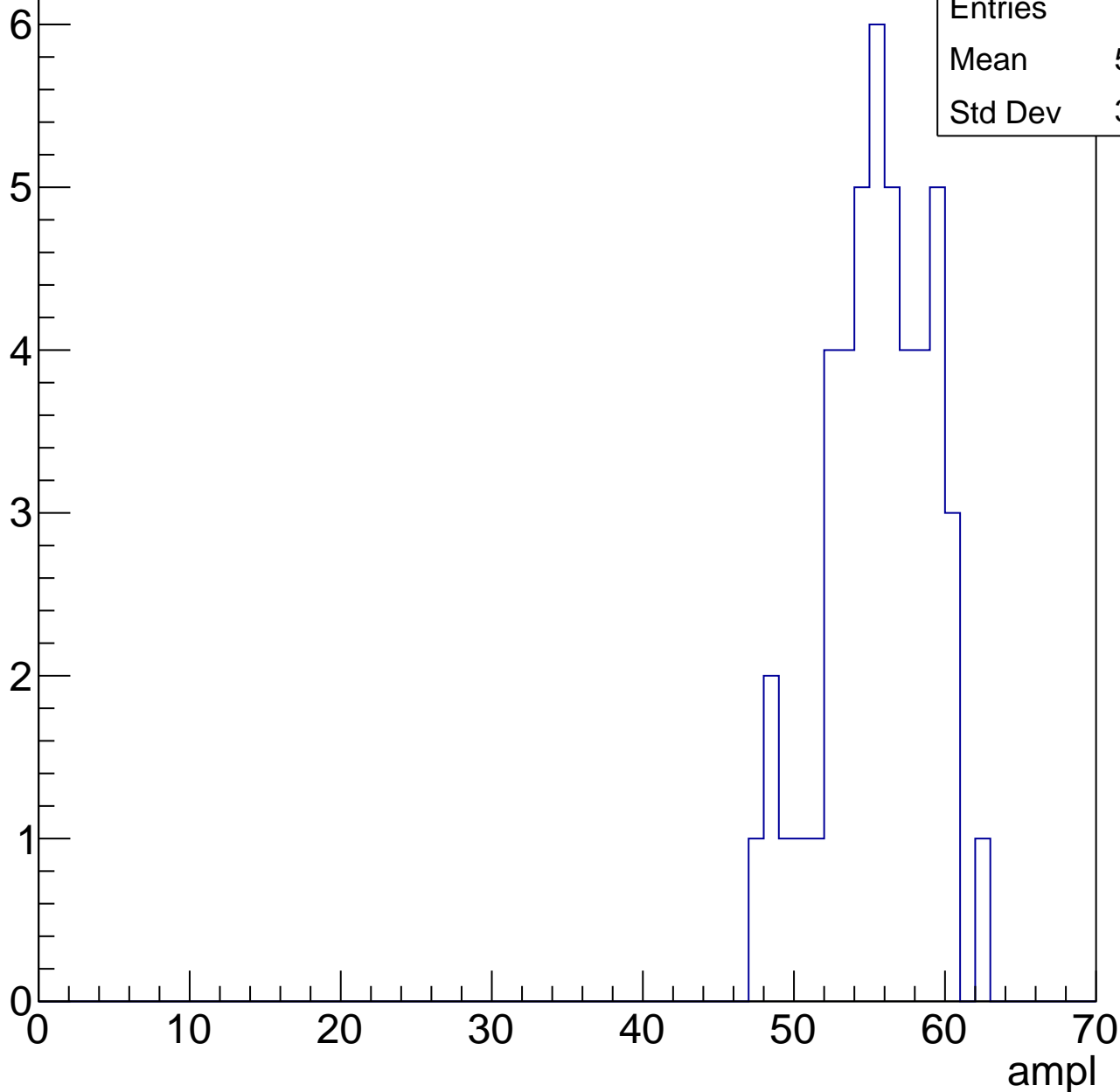
| | |
|---------|-------|
| Entries | 76 |
| Mean | 48.87 |
| Std Dev | 3.675 |

B1L102S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.11 |
| Std Dev | 3.441 |

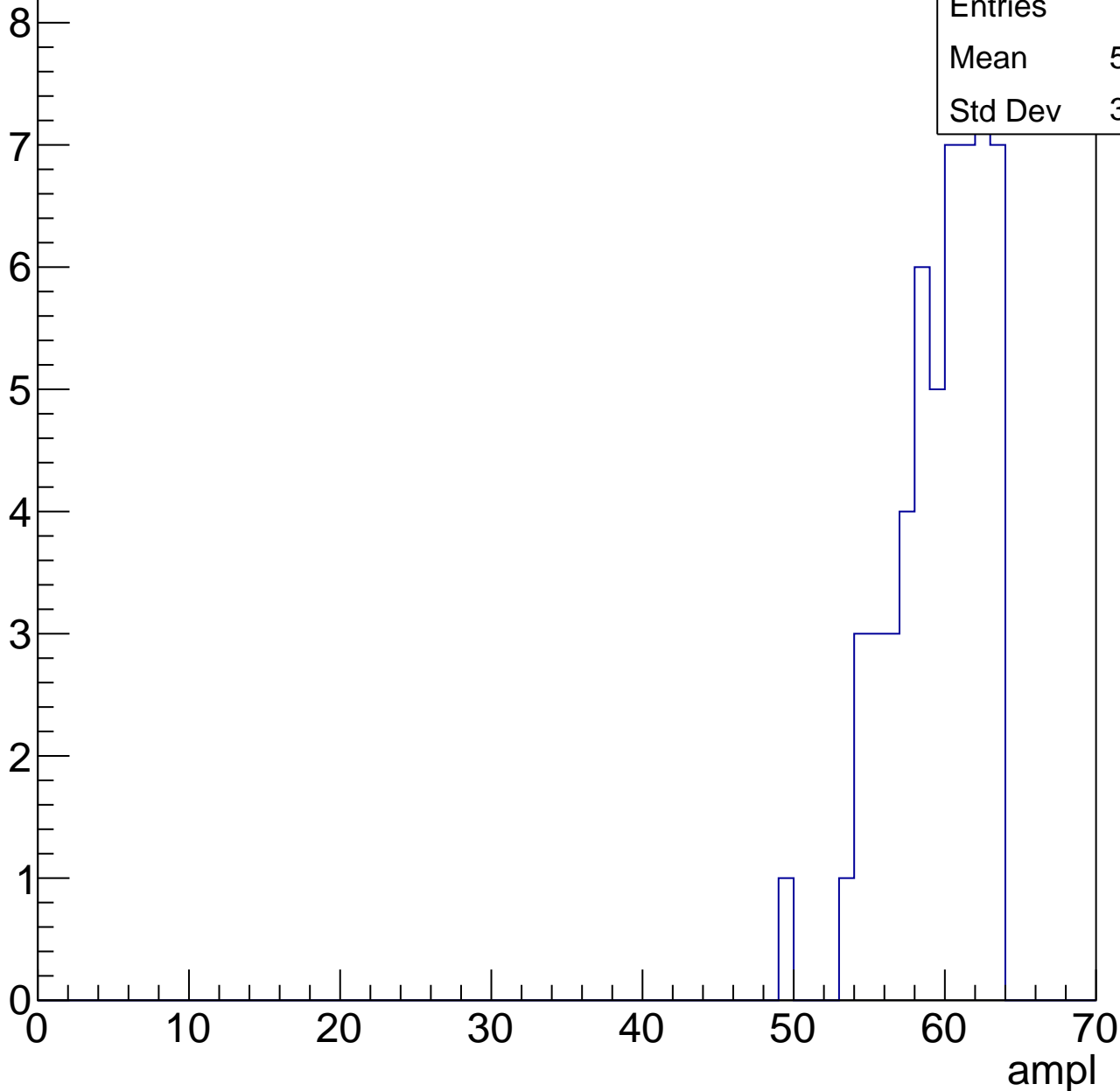


B1L102S, U15-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

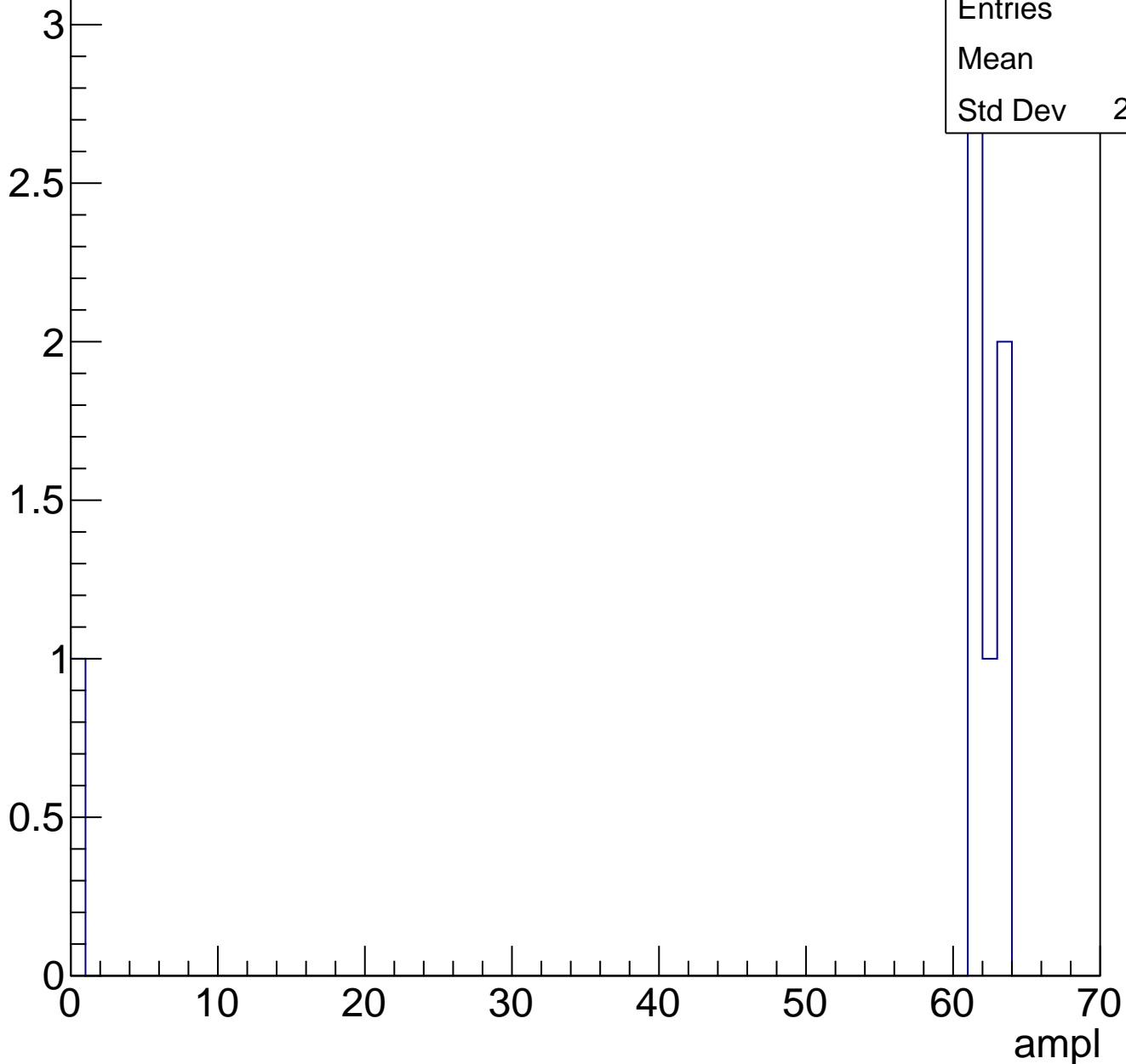
| | |
|---------|-------|
| Entries | 55 |
| Mean | 59.13 |
| Std Dev | 3.093 |



B1L102S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

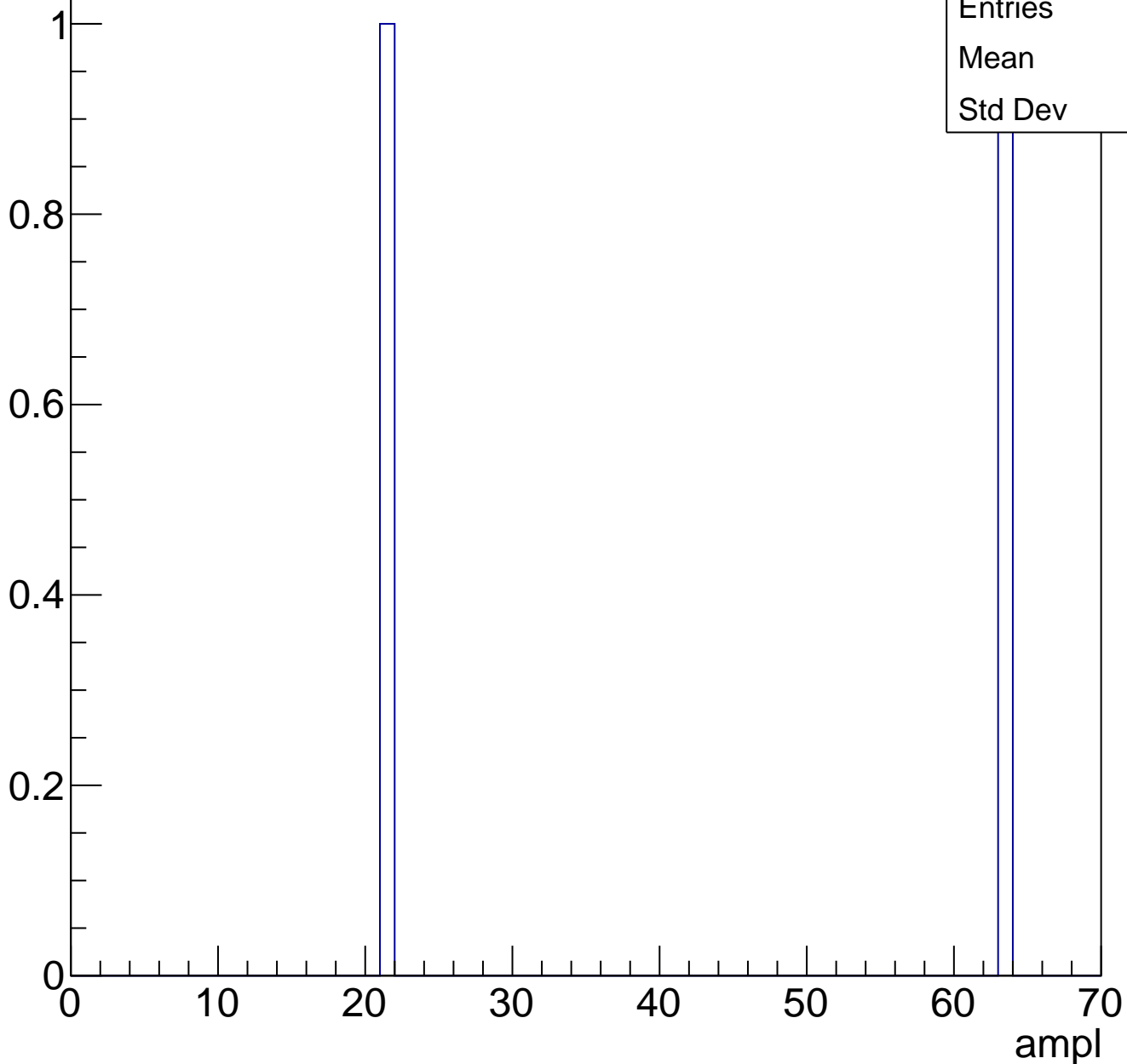
Entry



B1L102S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch58, adc0

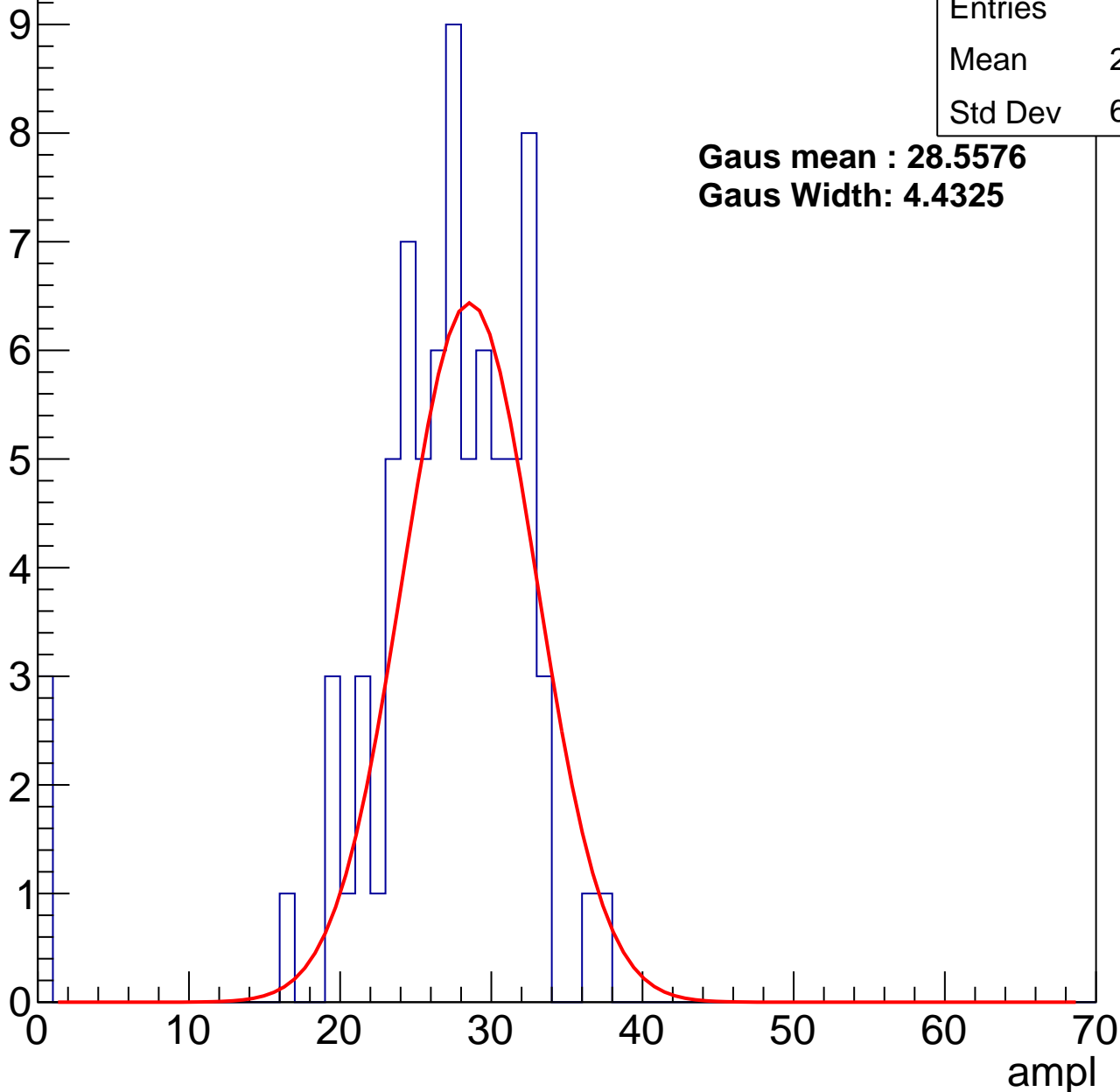
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 26.05 |
| Std Dev | 6.625 |

Gaus mean : 28.5576

Gaus Width: 4.4325



B1L102S, U15-ch58, adc1

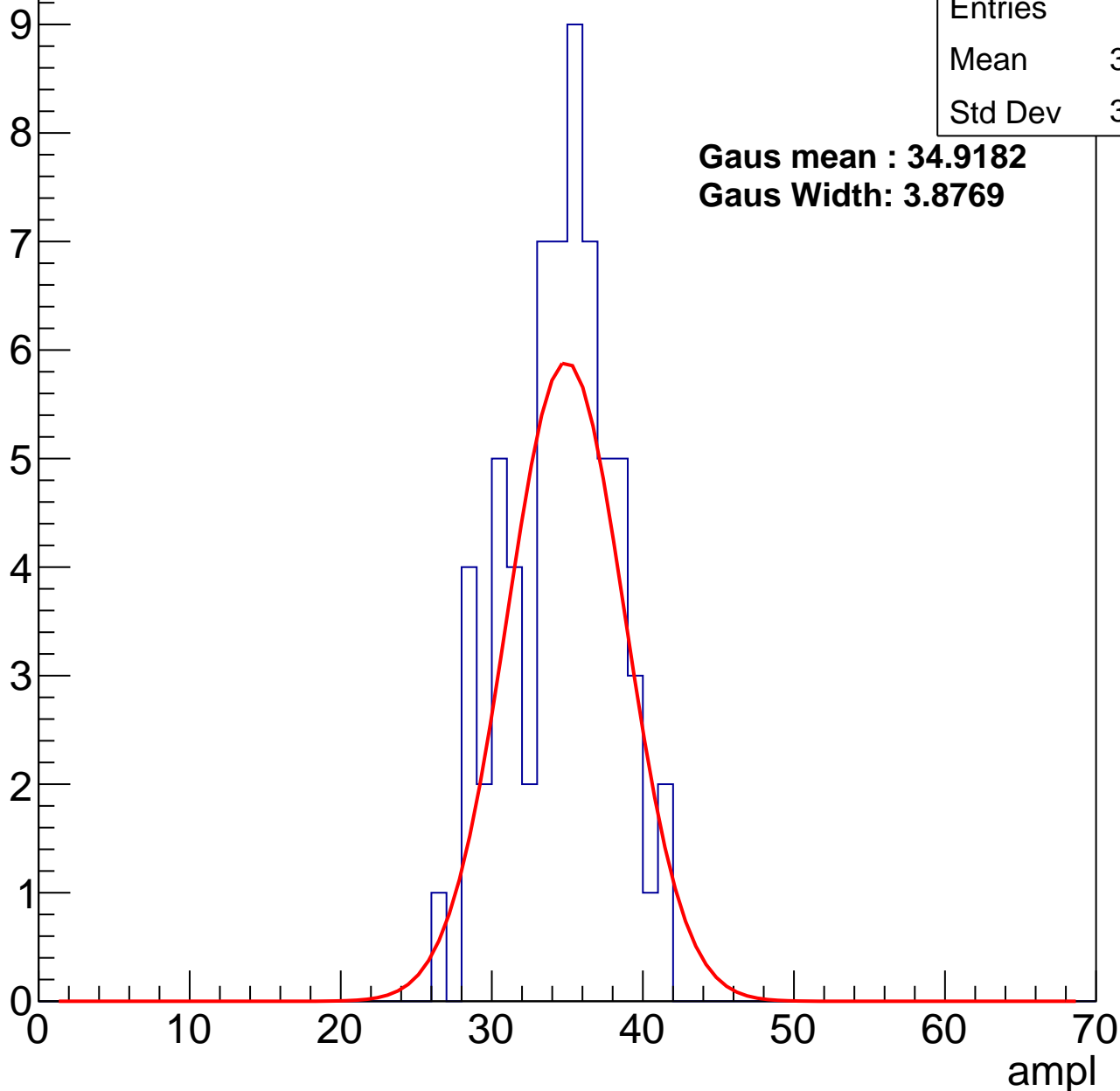
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 34.12 |
| Std Dev | 3.444 |

Gaus mean : 34.9182

Gaus Width: 3.8769



B1L102S, U15-ch58, adc2

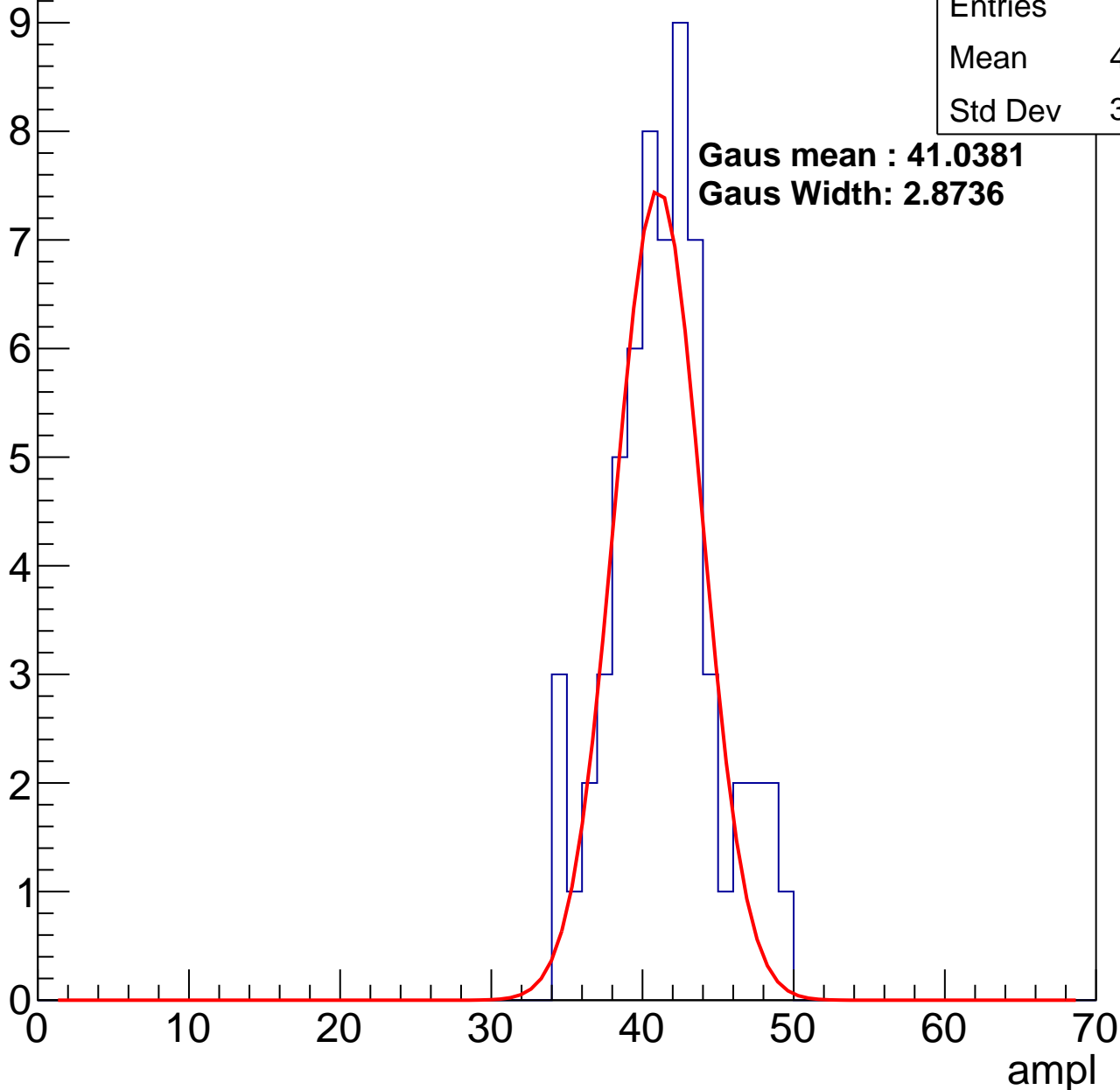
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 40.94 |
| Std Dev | 3.412 |

Gaus mean : 41.0381

Gaus Width: 2.8736

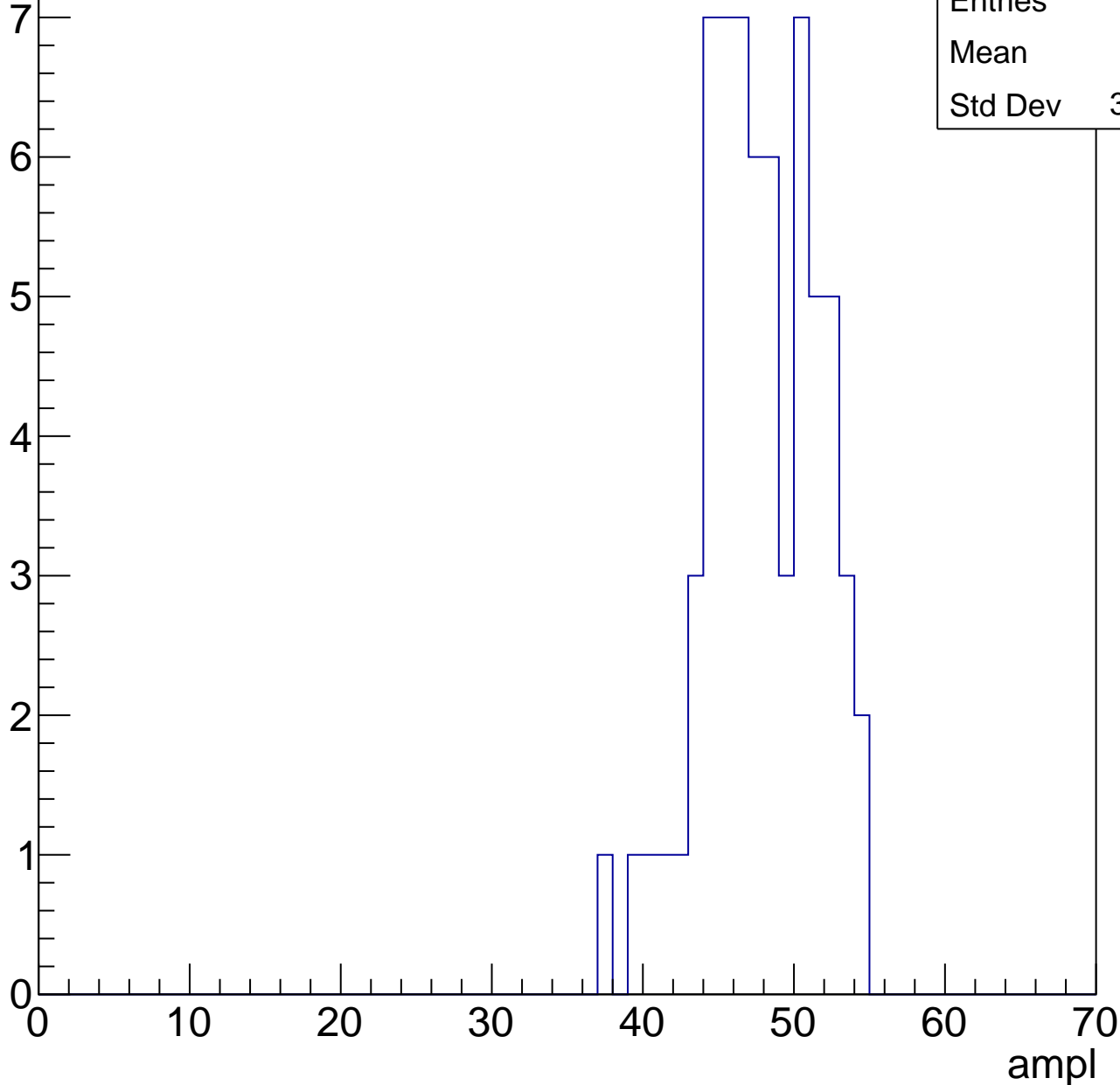


B1L102S, U15-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 47.3 |
| Std Dev | 3.705 |

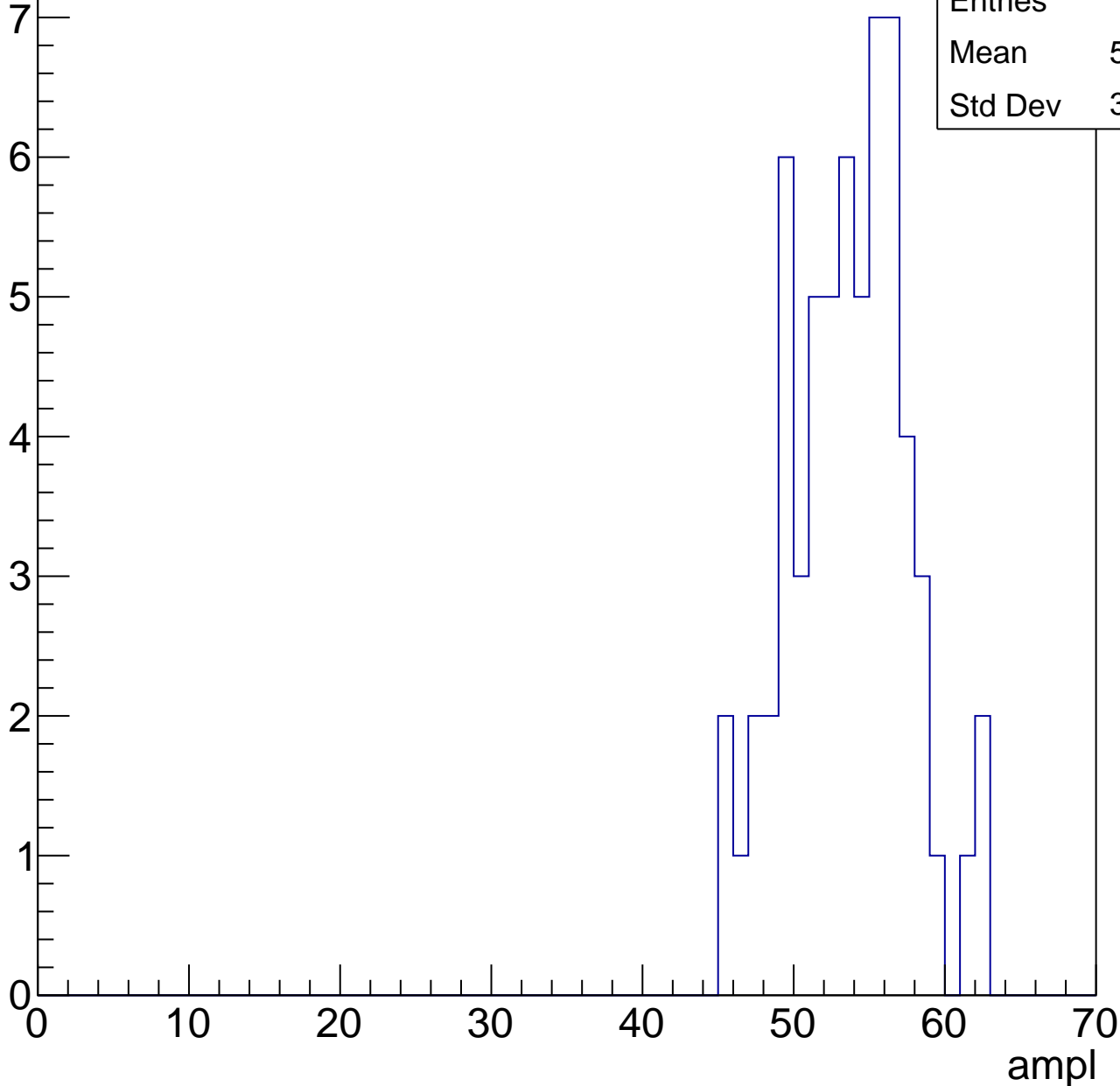


B1L102S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 53.16 |
| Std Dev | 3.899 |



B1L102S, U15-ch58, adc5

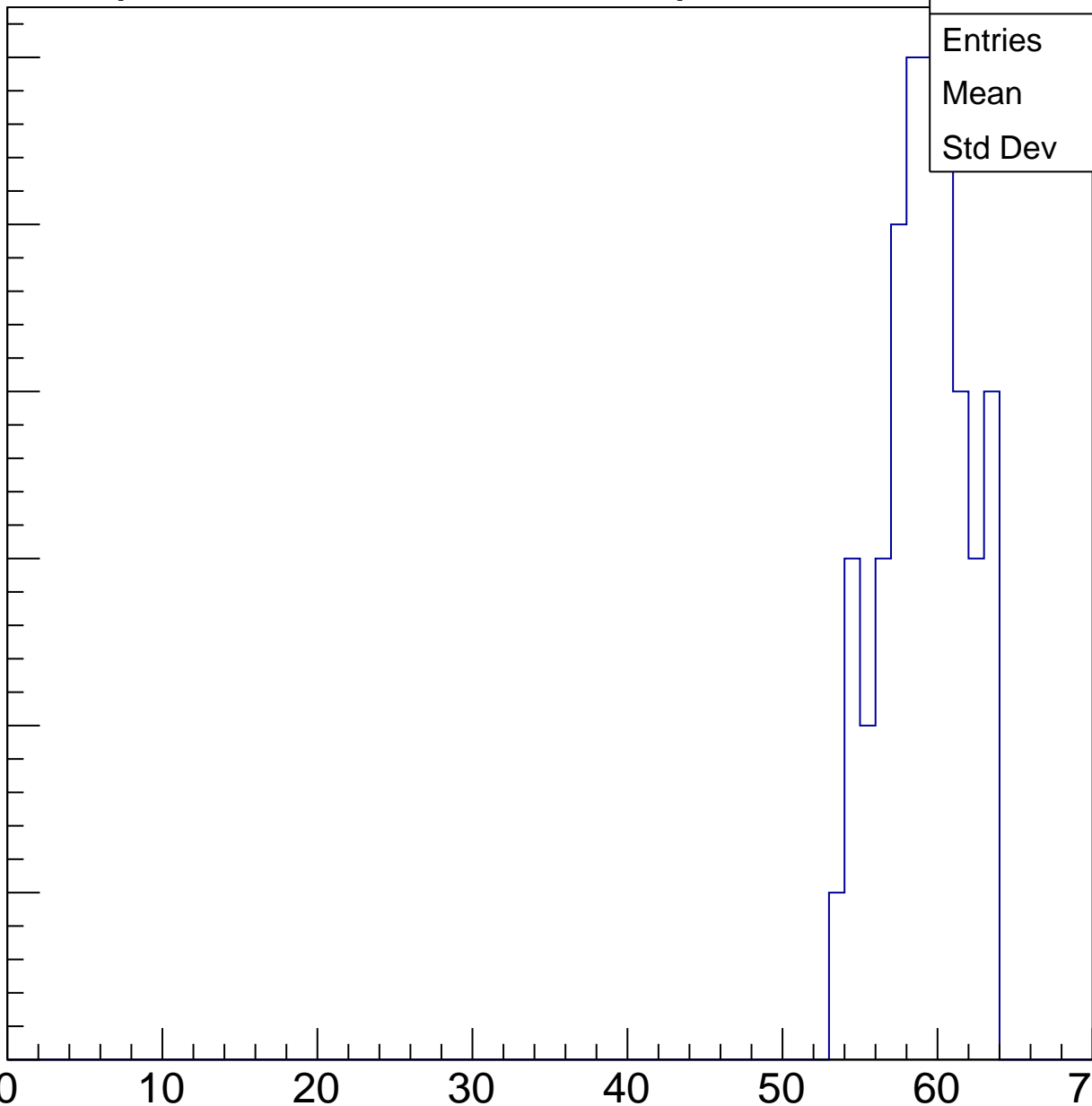
calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 43 |
| Mean | 58.65 |
| Std Dev | 2.658 |

ampl

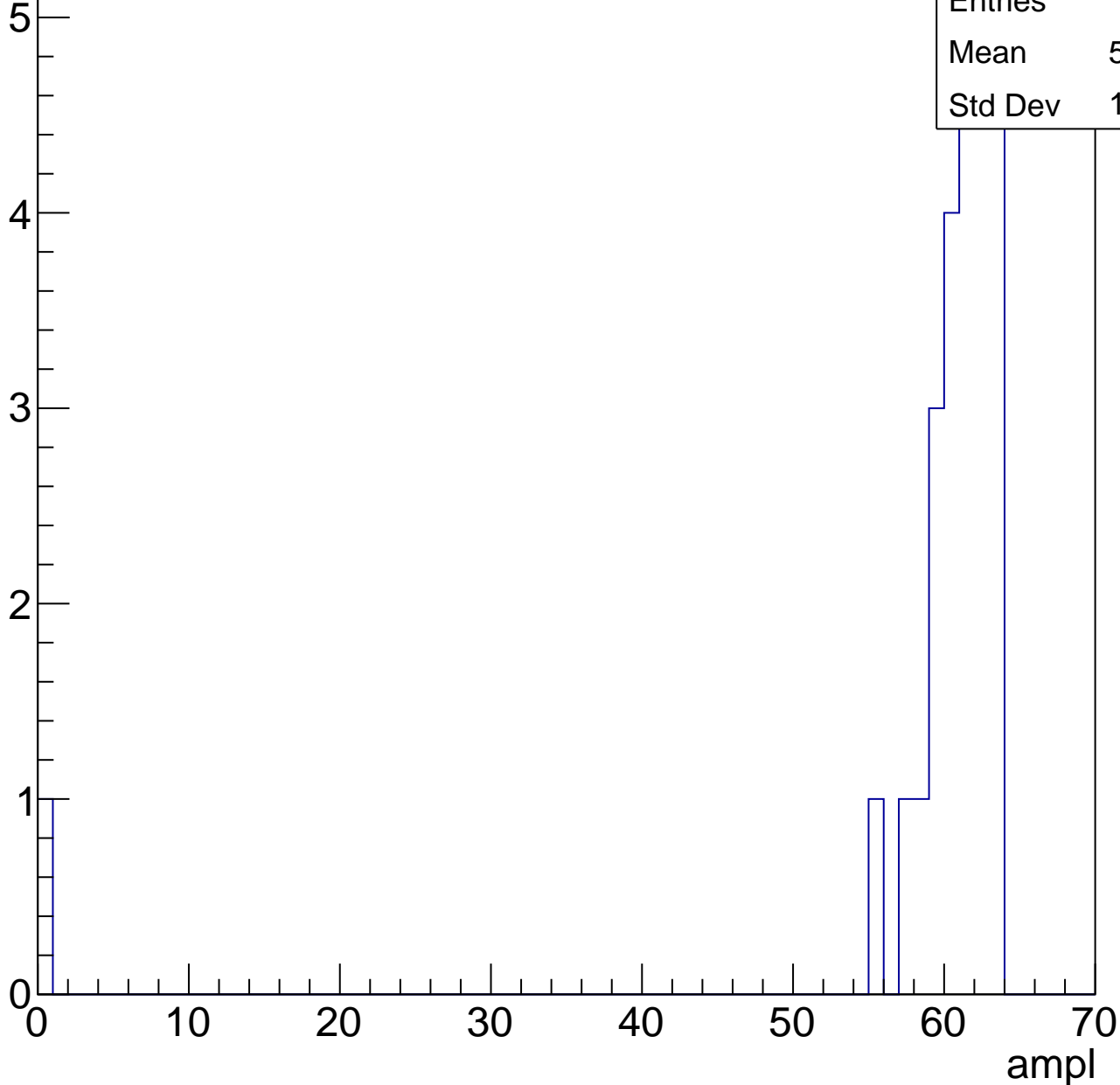


B1L102S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 26 |
| Mean | 58.35 |
| Std Dev | 11.83 |



B1L102S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch59, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 28.14 |
| Std Dev | 4.78 |

Gaus mean : 29.2080

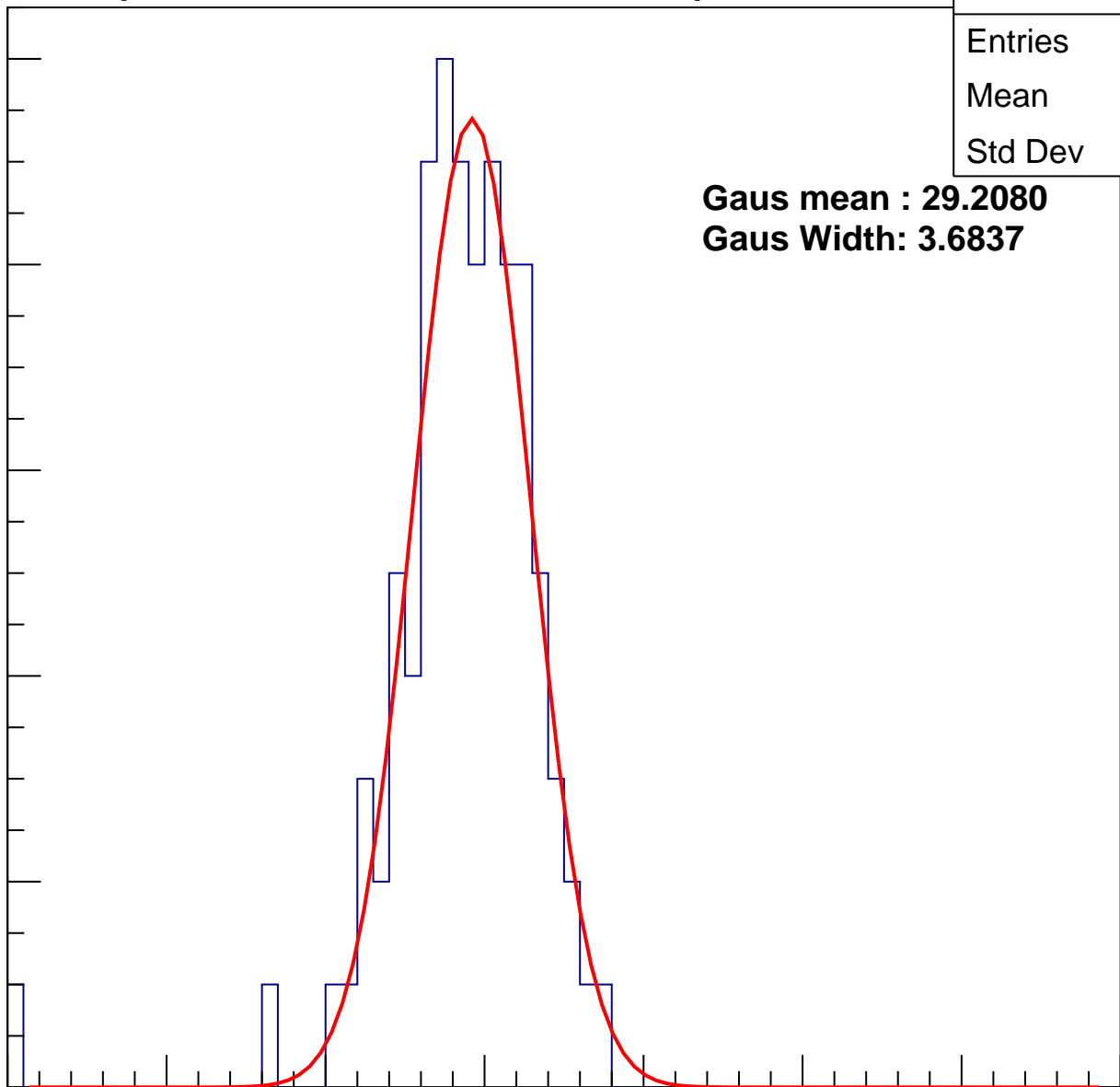
Gaus Width: 3.6837

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch59, adc1

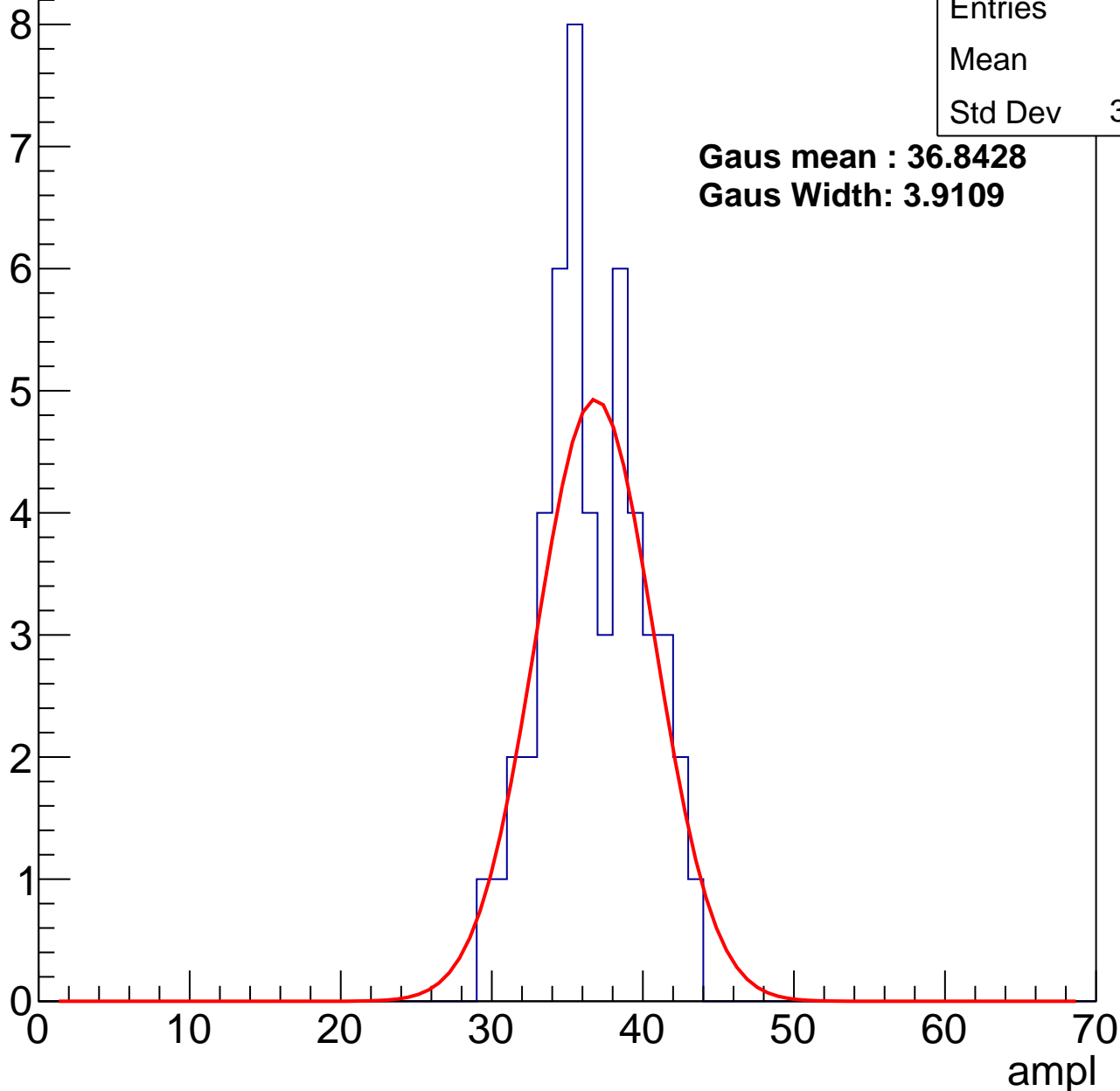
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 36.2 |
| Std Dev | 3.286 |

Gaus mean : 36.8428

Gaus Width: 3.9109



B1L102S, U15-ch59, adc2

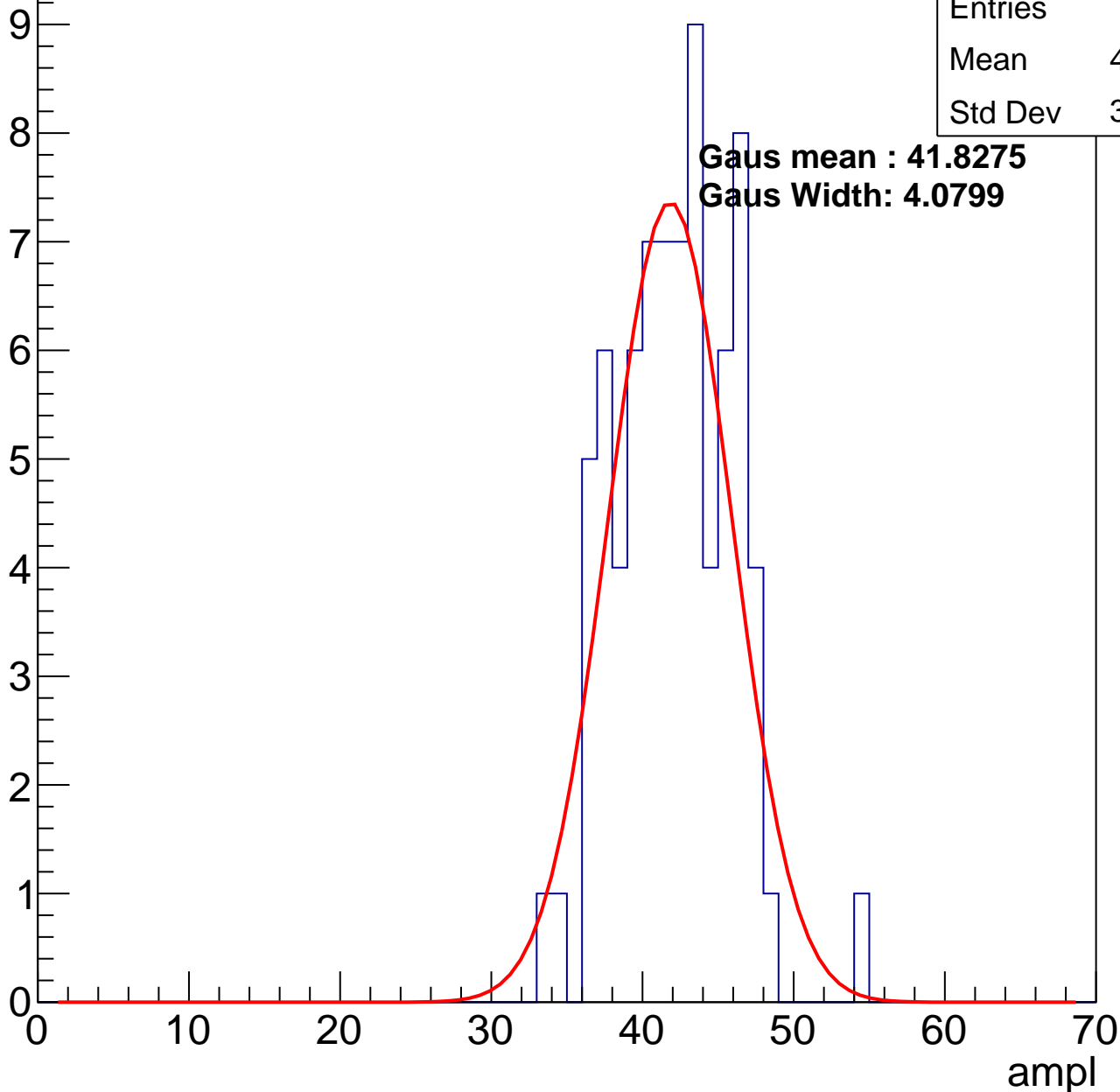
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 41.65 |
| Std Dev | 3.789 |

Gaus mean : 41.8275

Gaus Width: 4.0799

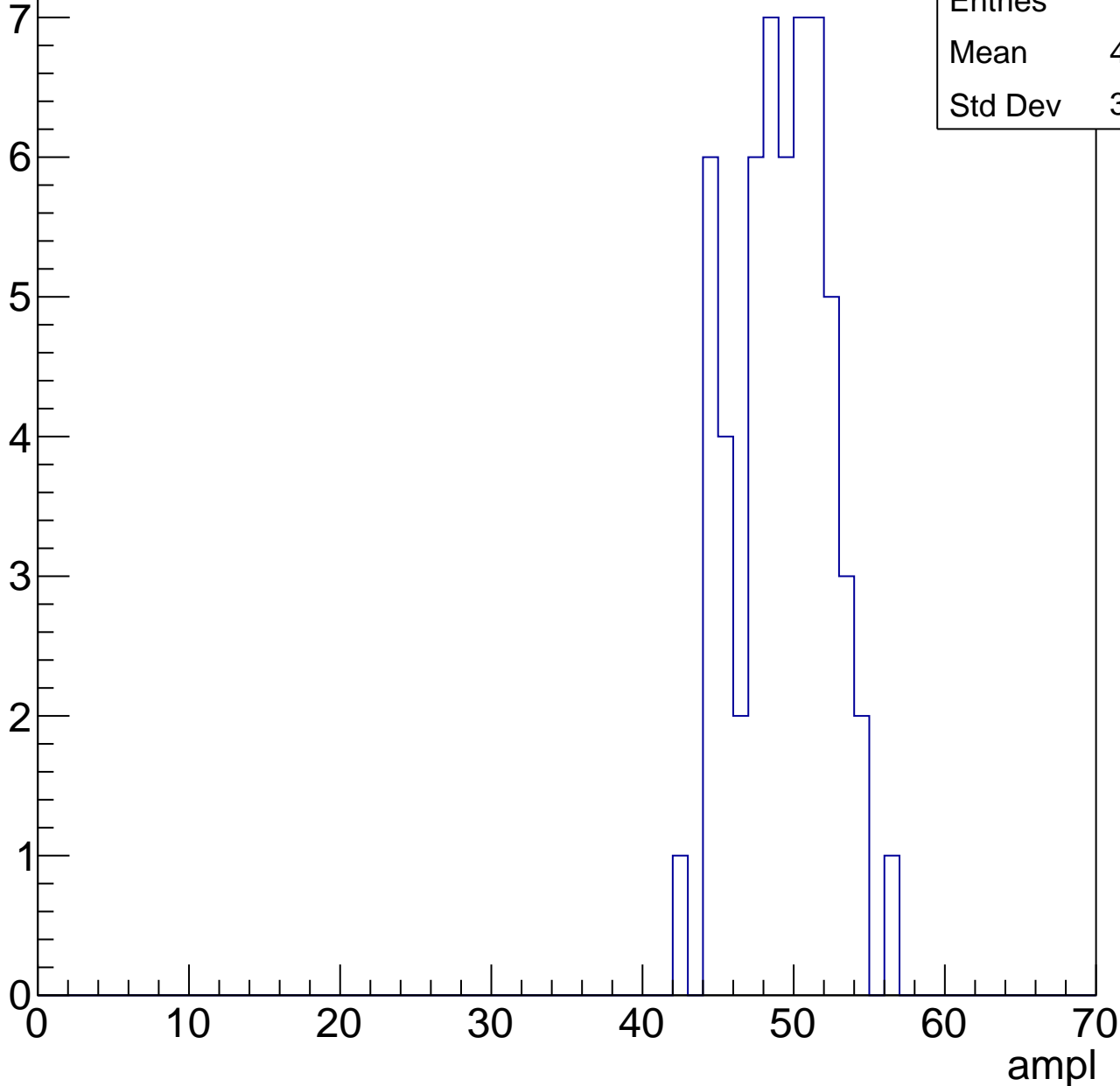


B1L102S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 48.77 |
| Std Dev | 3.067 |

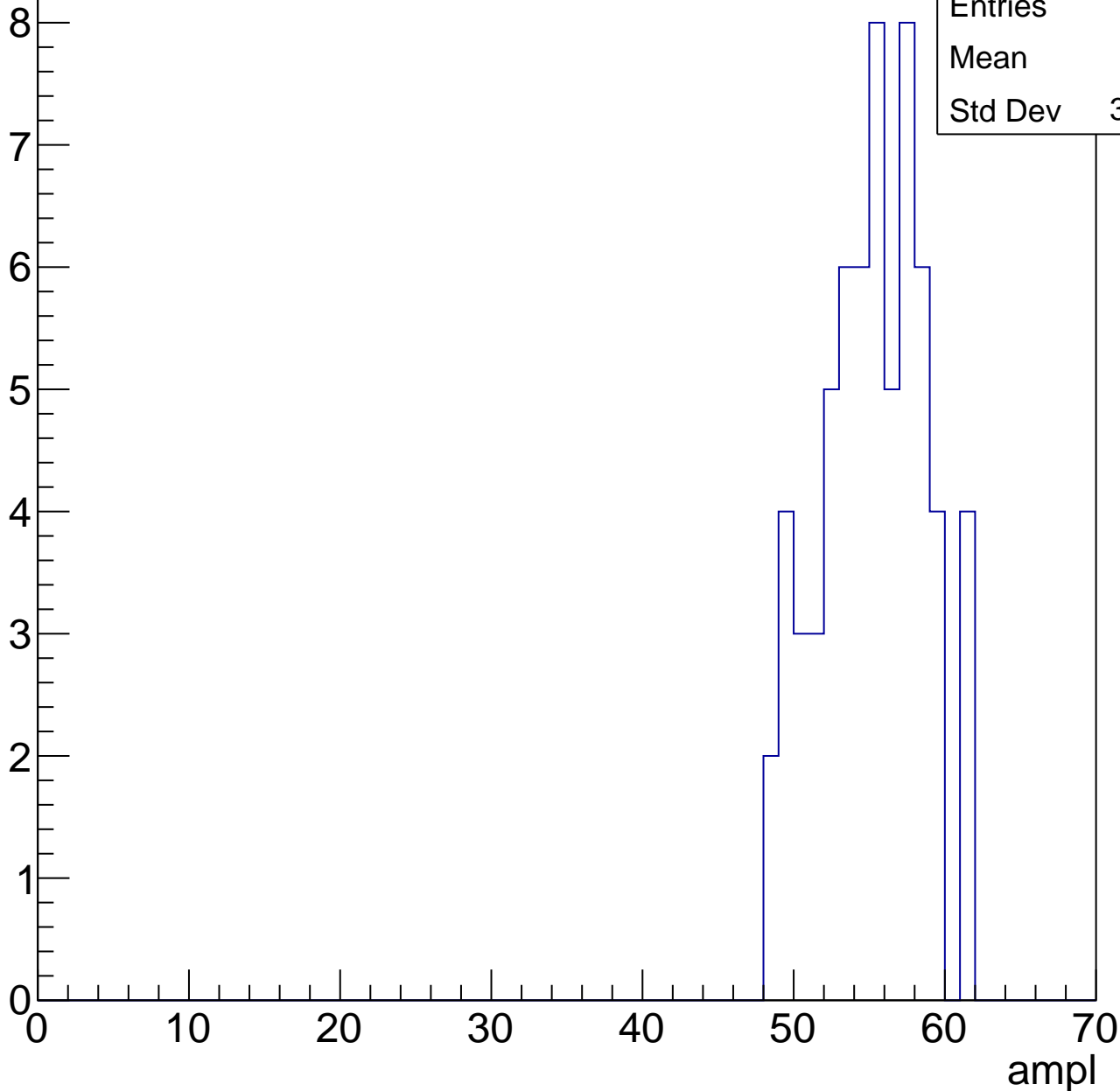


B1L102S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 54.7 |
| Std Dev | 3.385 |



B1L102S, U15-ch59, adc5

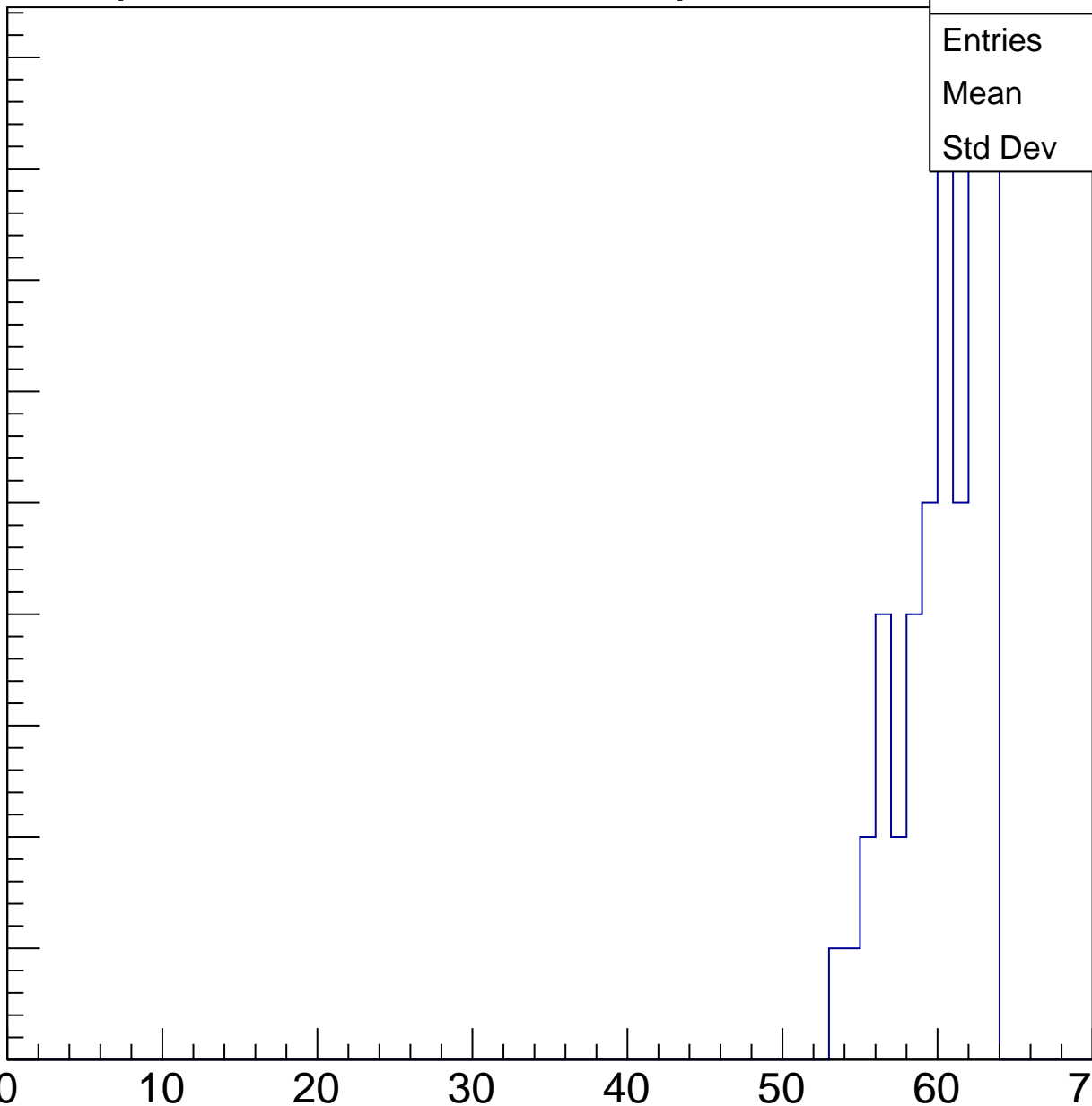
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 49 |
| Mean | 59.78 |
| Std Dev | 2.667 |

ampl



B1L102S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

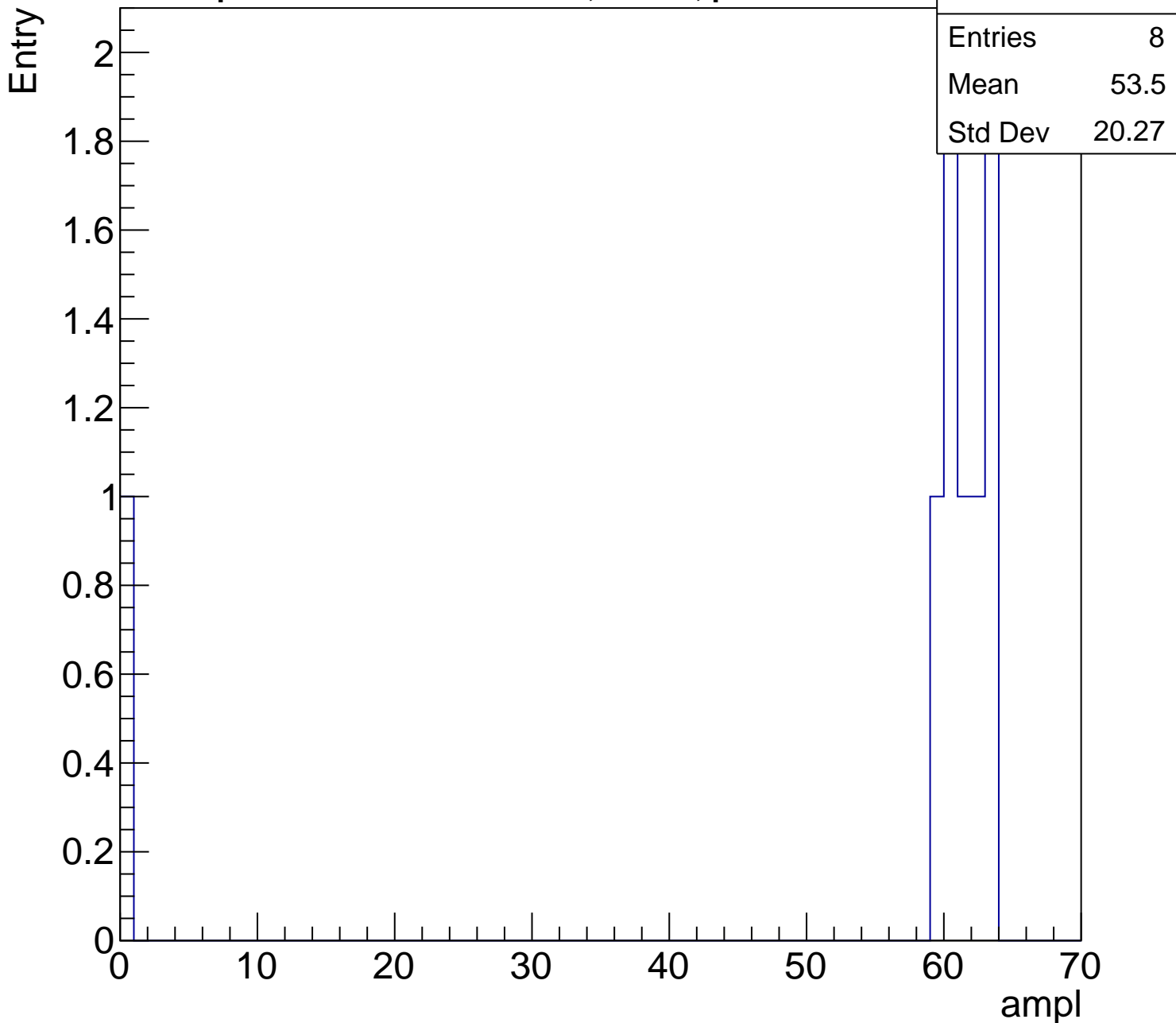
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 8 |
| Mean | 53.5 |
| Std Dev | 20.27 |

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch60, adc0

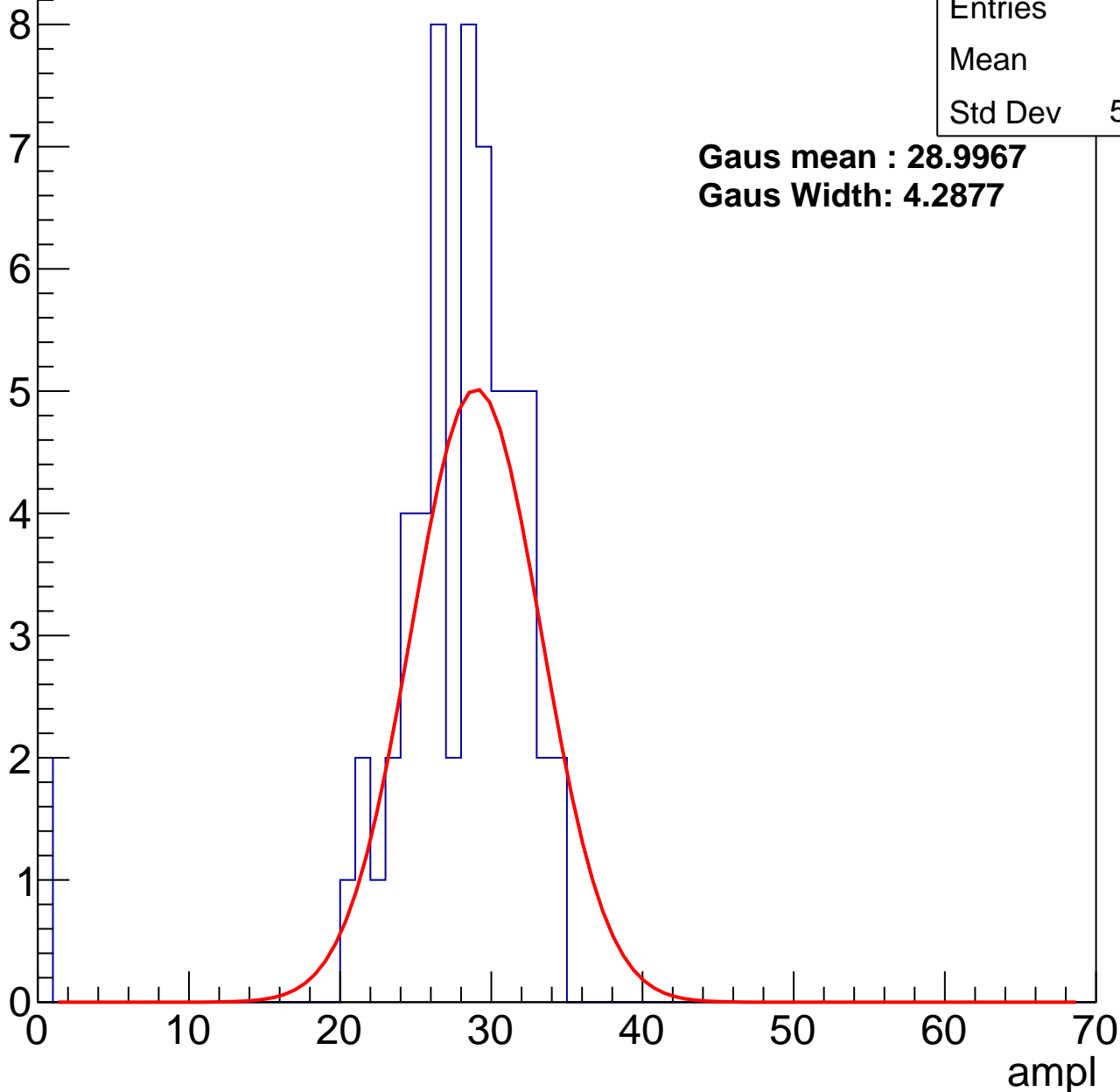
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 26.9 |
| Std Dev | 5.988 |

Gaus mean : 28.9967

Gaus Width: 4.2877



B1L102S, U15-ch60, adc1

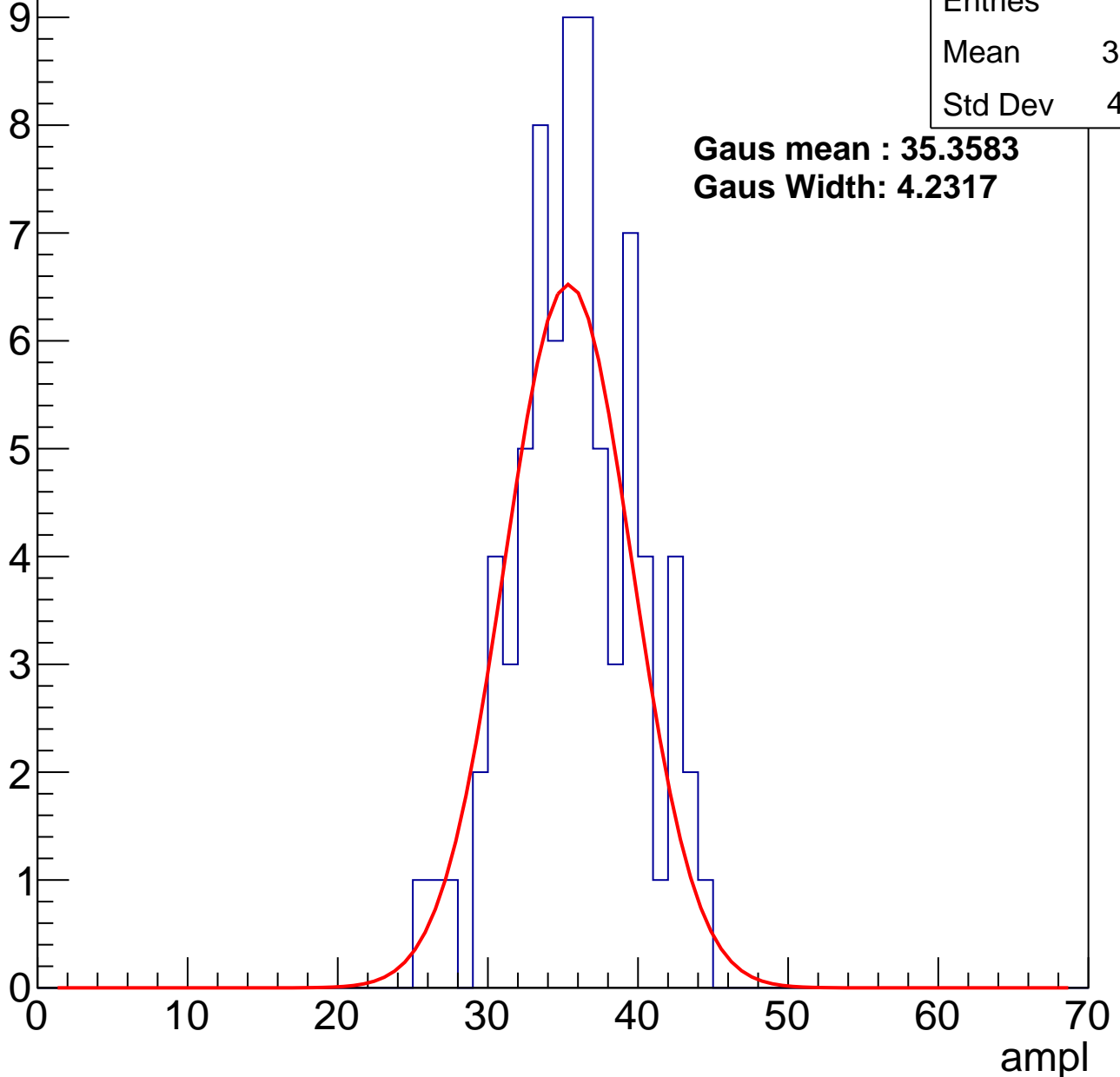
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.36 |
| Std Dev | 4.051 |

Gaus mean : 35.3583

Gaus Width: 4.2317



B1L102S, U15-ch60, adc2

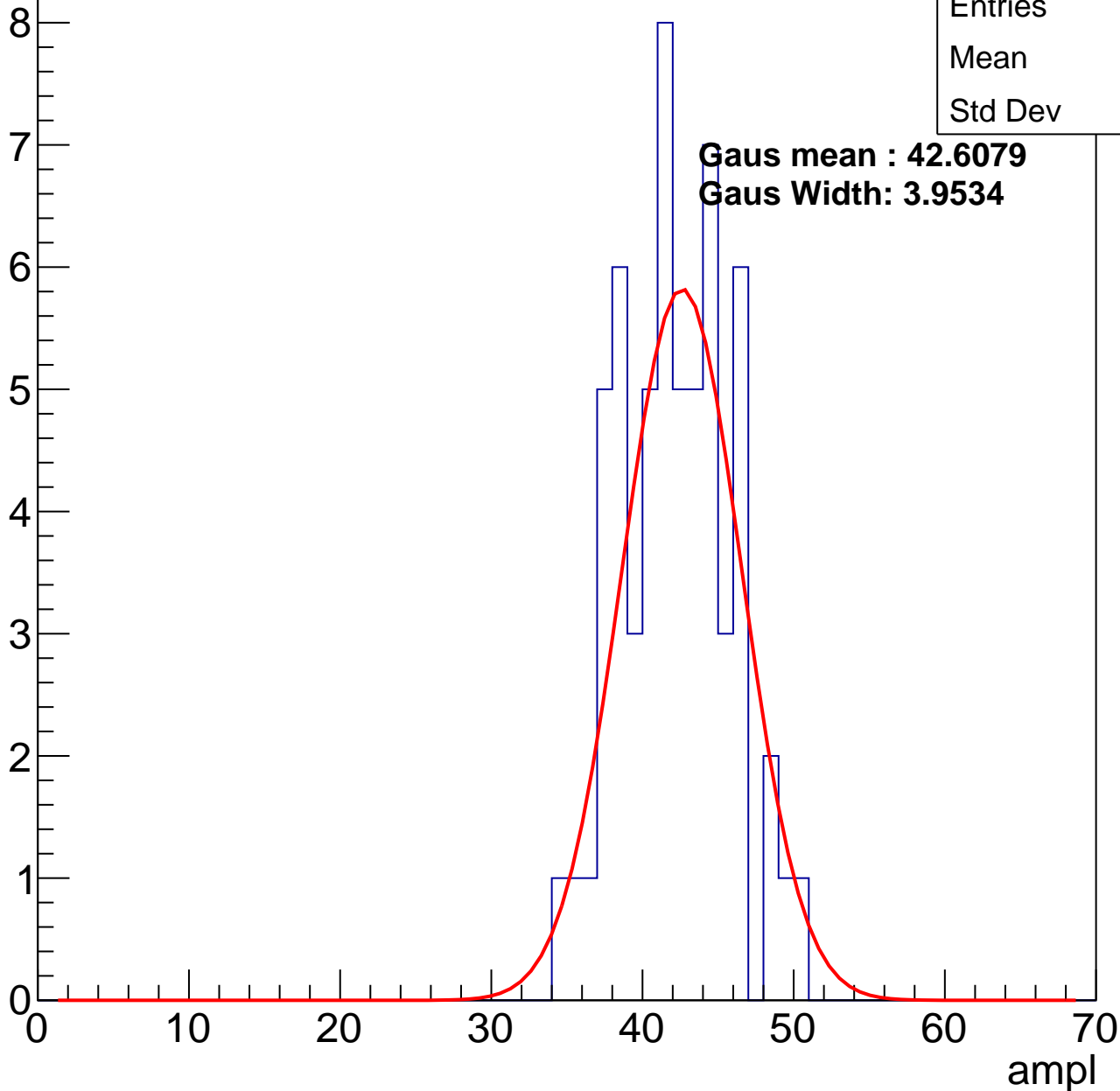
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 60 |
| Mean | 41.7 |
| Std Dev | 3.56 |

Gaus mean : 42.6079

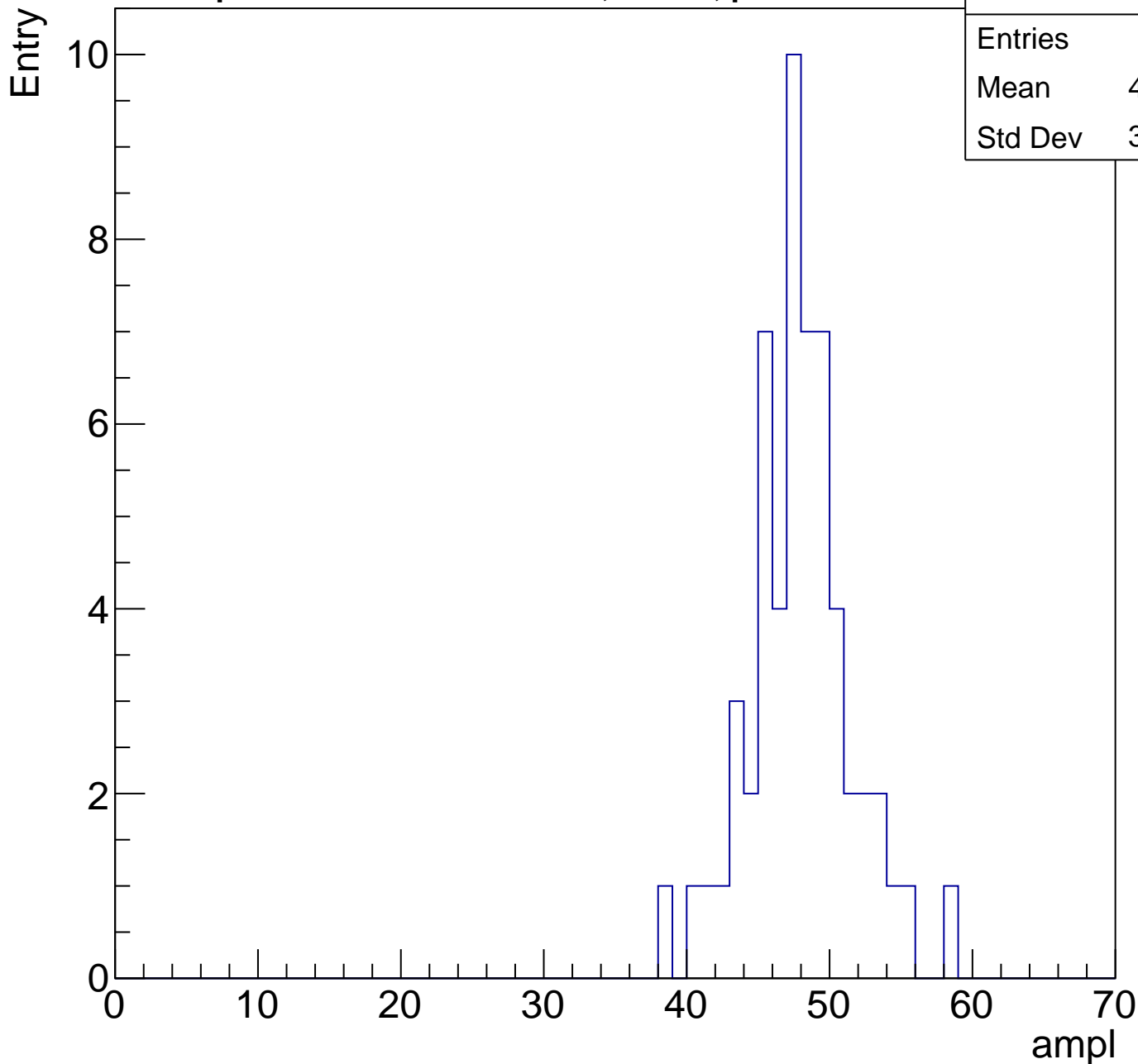
Gaus Width: 3.9534



B1L102S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 57 |
| Mean | 47.46 |
| Std Dev | 3.594 |

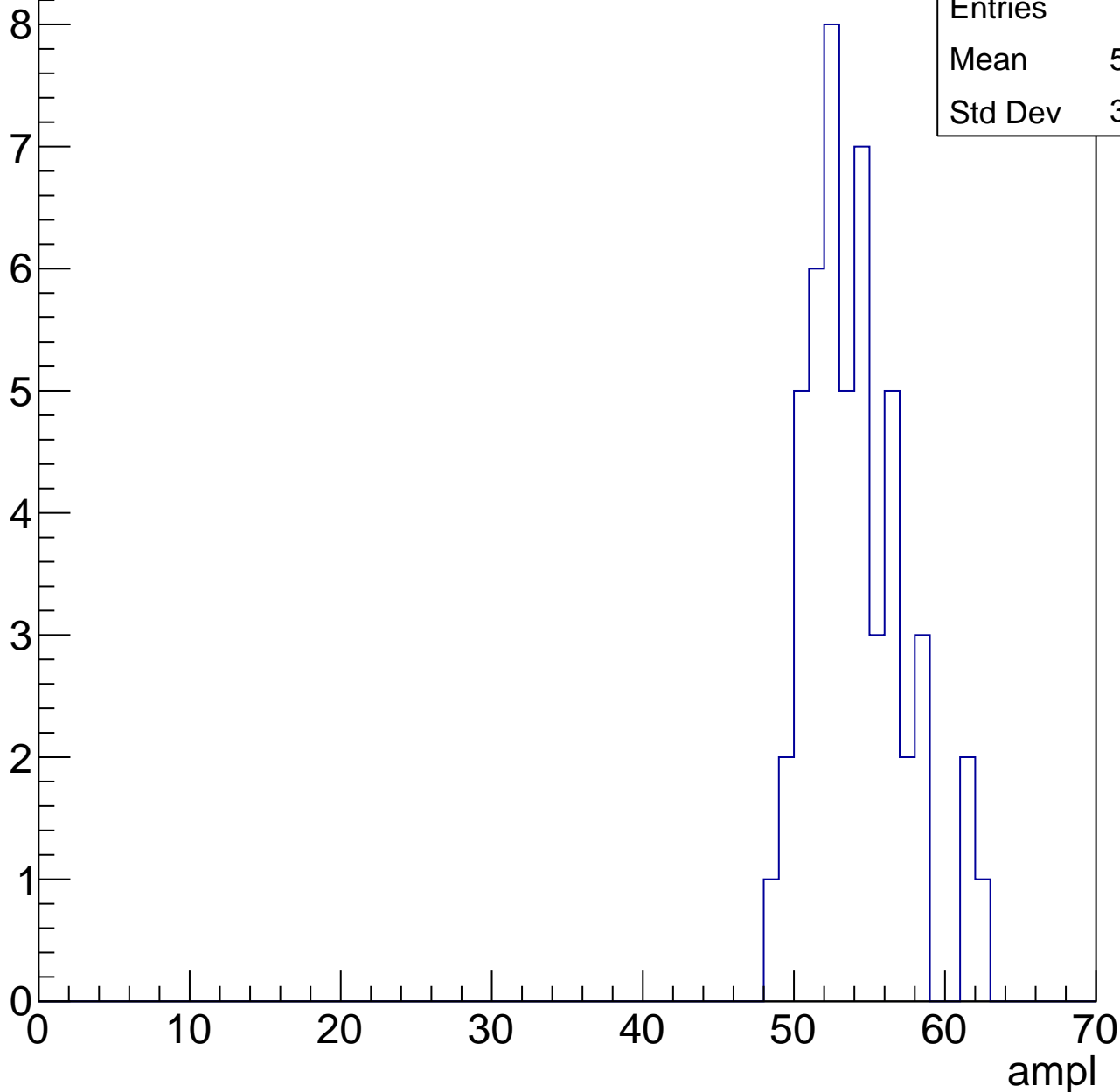


B1L102S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 53.56 |
| Std Dev | 3.157 |

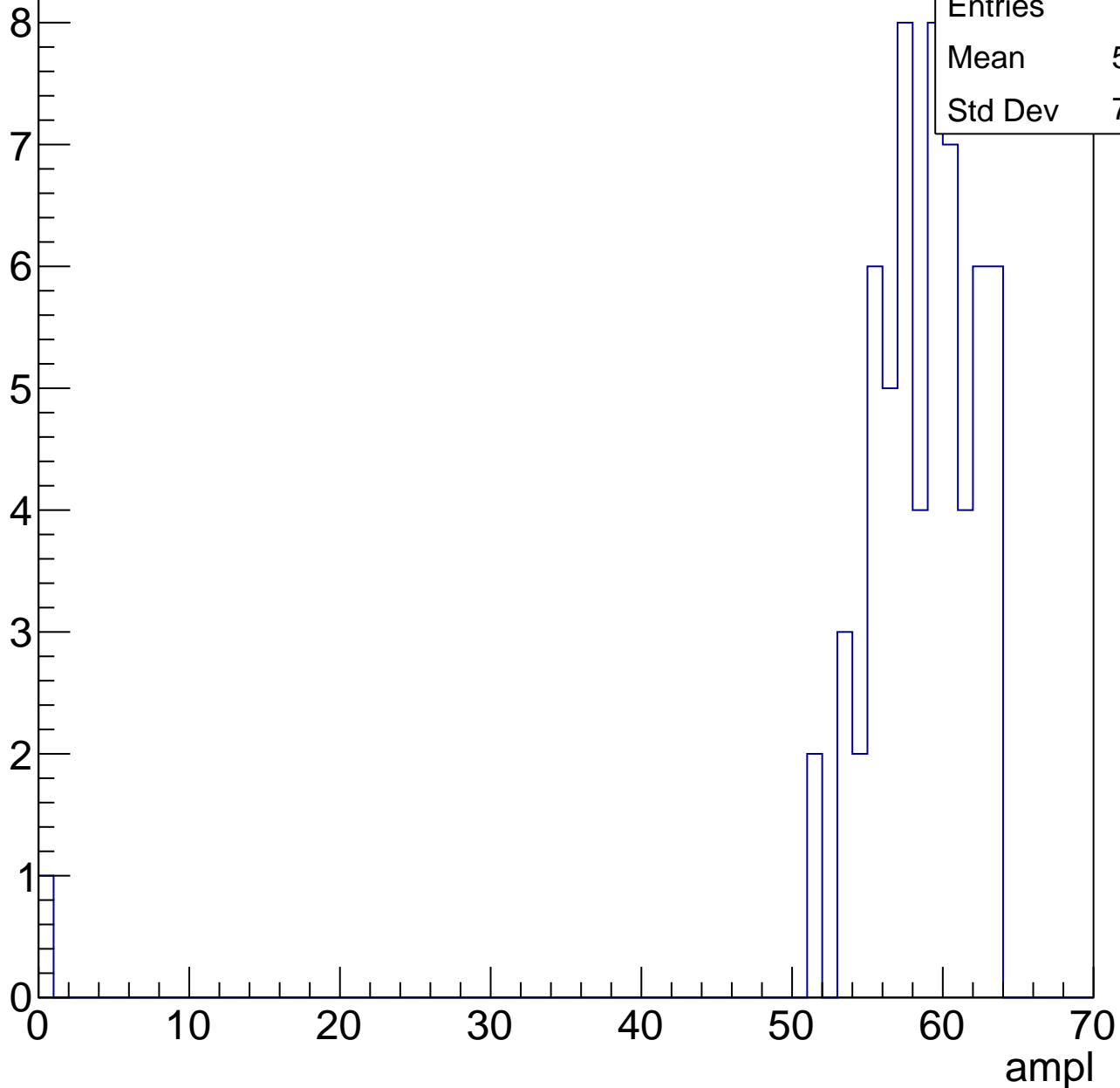


B1L102S, U15-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.31 |
| Std Dev | 7.971 |



B1L102S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 17 |
| Mean | 60.94 |
| Std Dev | 1.662 |

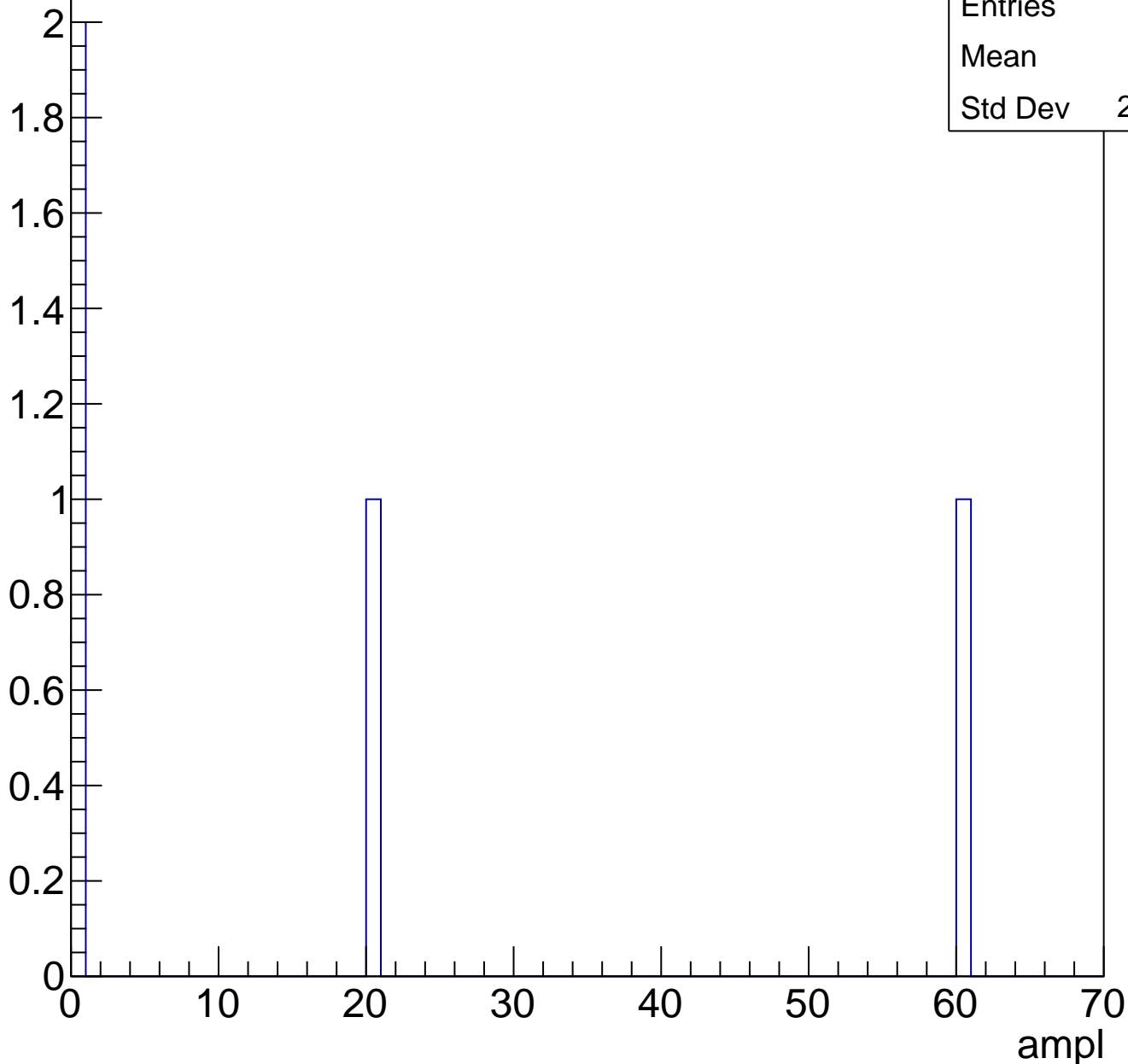
ampl

0 10 20 30 40 50 60 70

B1L102S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 20 |
| Std Dev | 24.49 |

B1L102S, U15-ch61, adc0

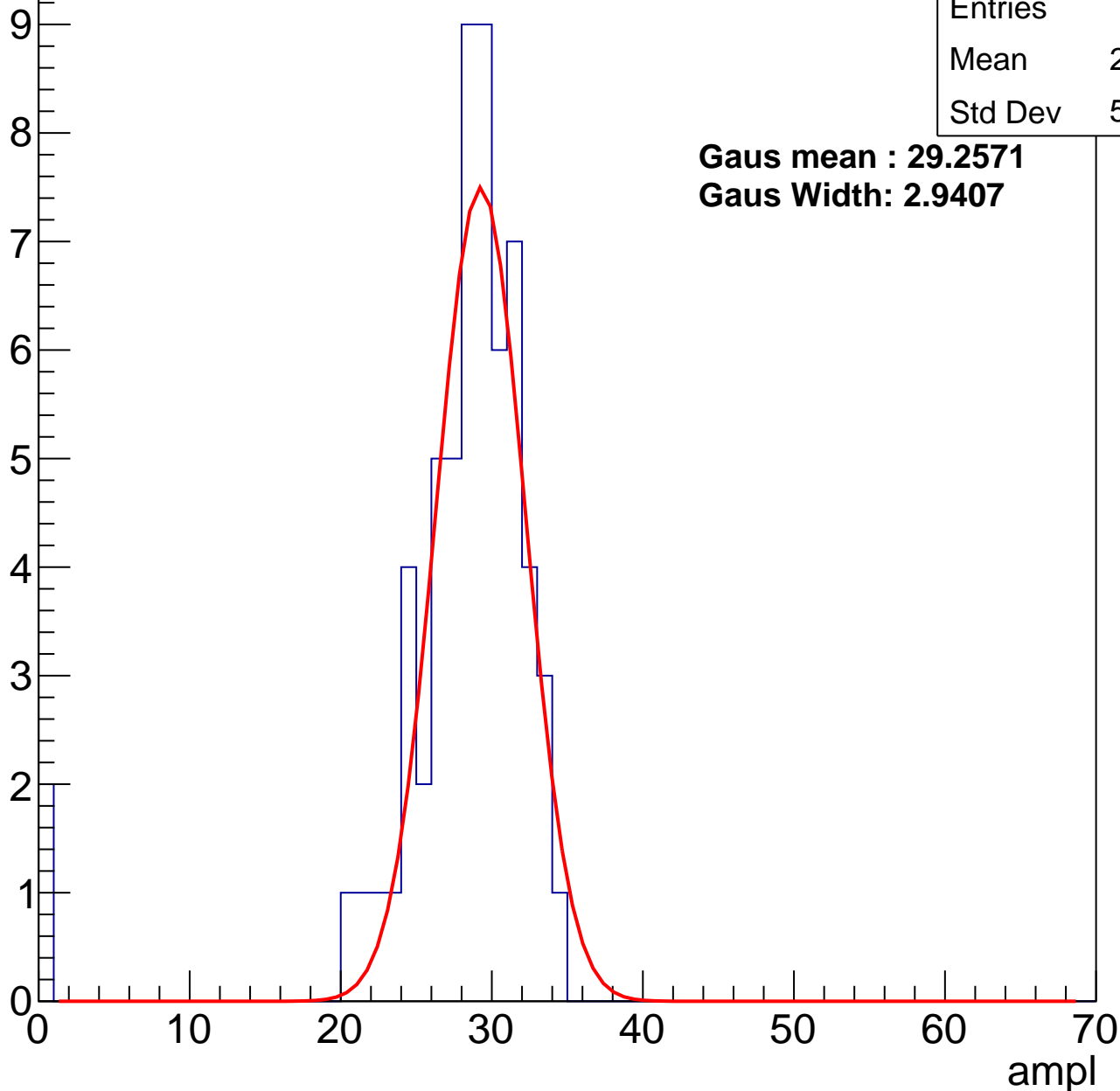
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 27.34 |
| Std Dev | 5.859 |

Gaus mean : 29.2571

Gaus Width: 2.9407



B1L102S, U15-ch61, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 91 |
| Mean | 35.51 |
| Std Dev | 3.827 |

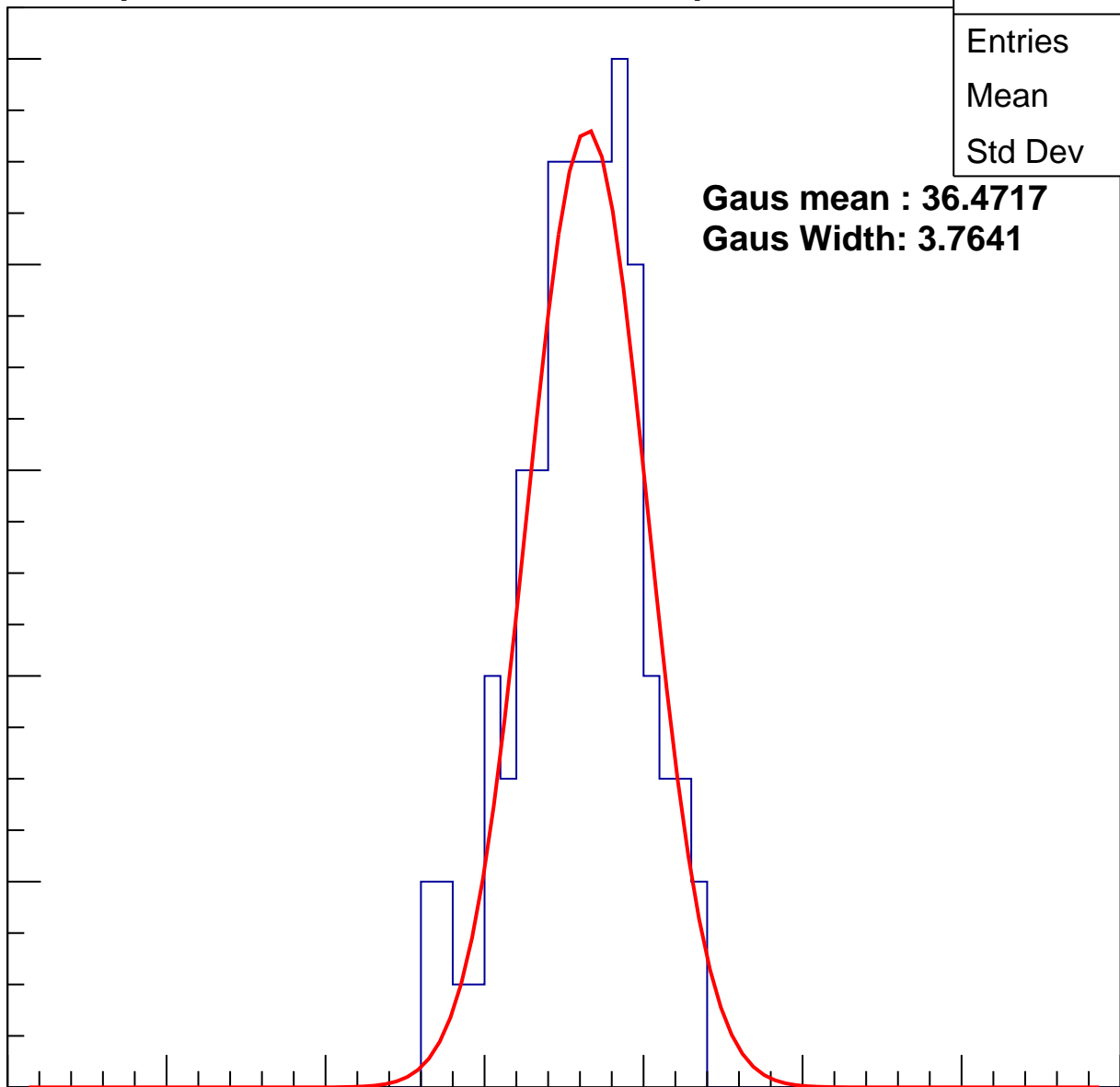
Gaus mean : 36.4717
Gaus Width: 3.7641

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

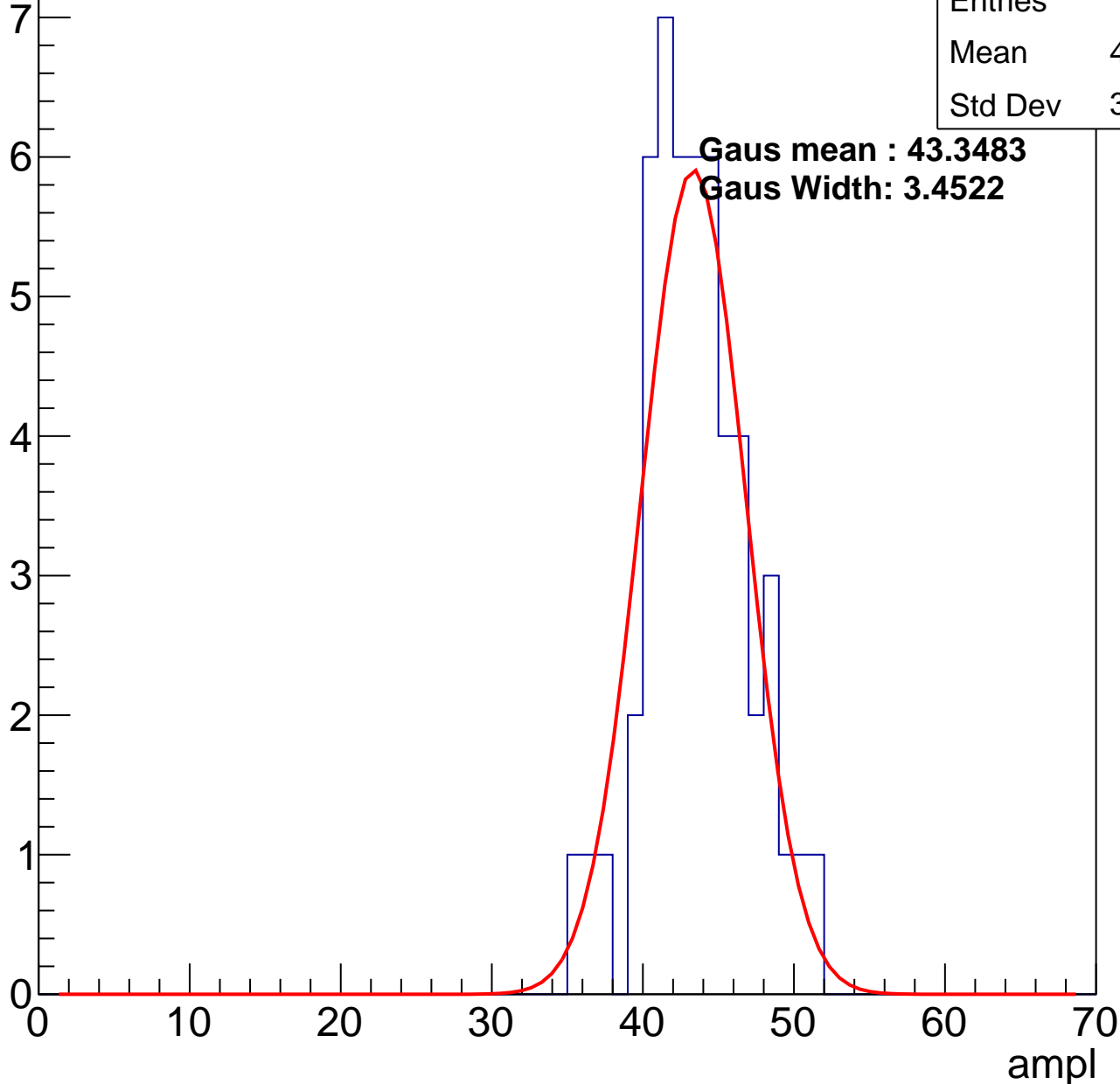


B1L102S, U15-ch61, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 43.06 |
| Std Dev | 3.348 |

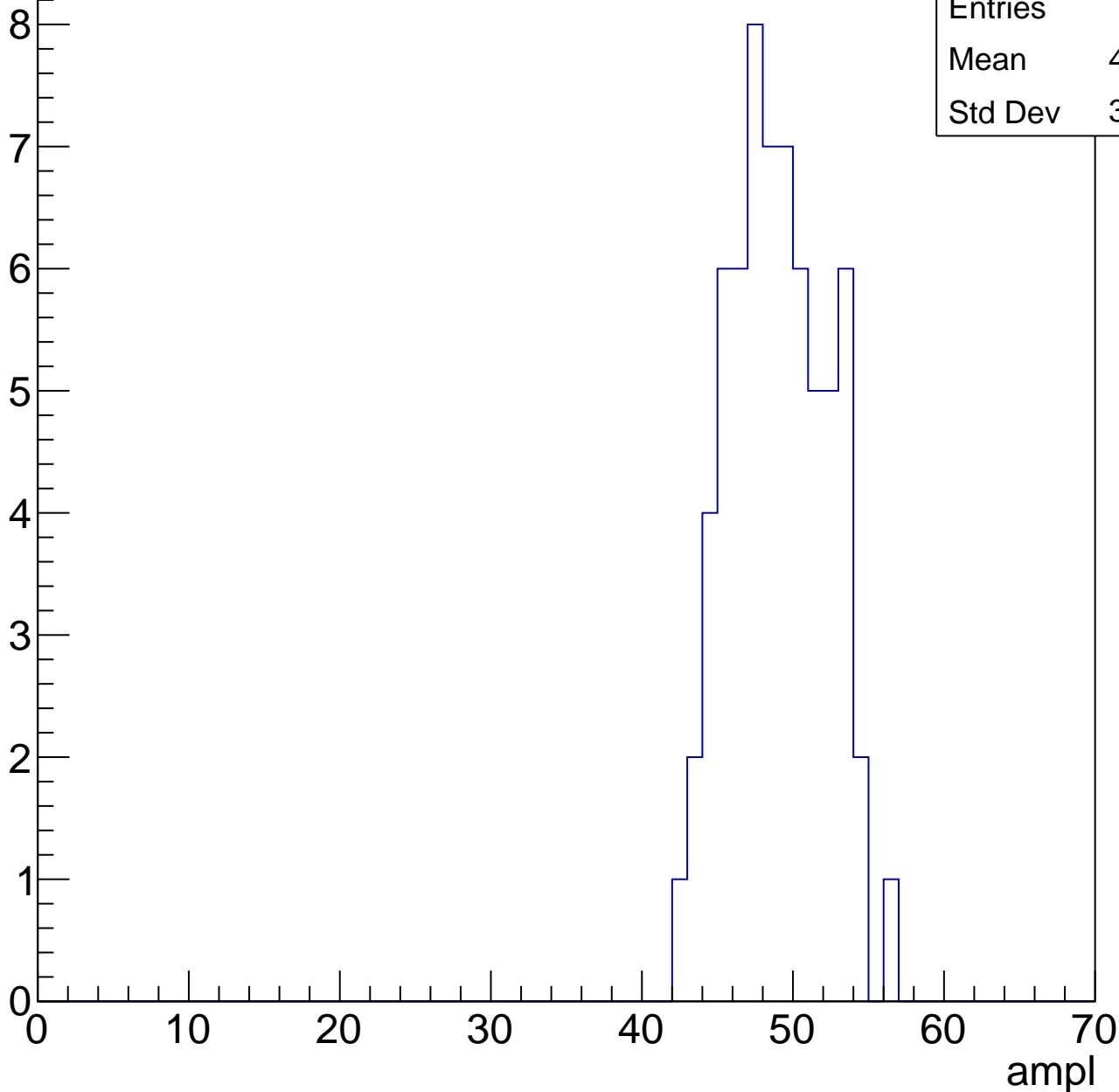


B1L102S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 48.52 |
| Std Dev | 3.163 |

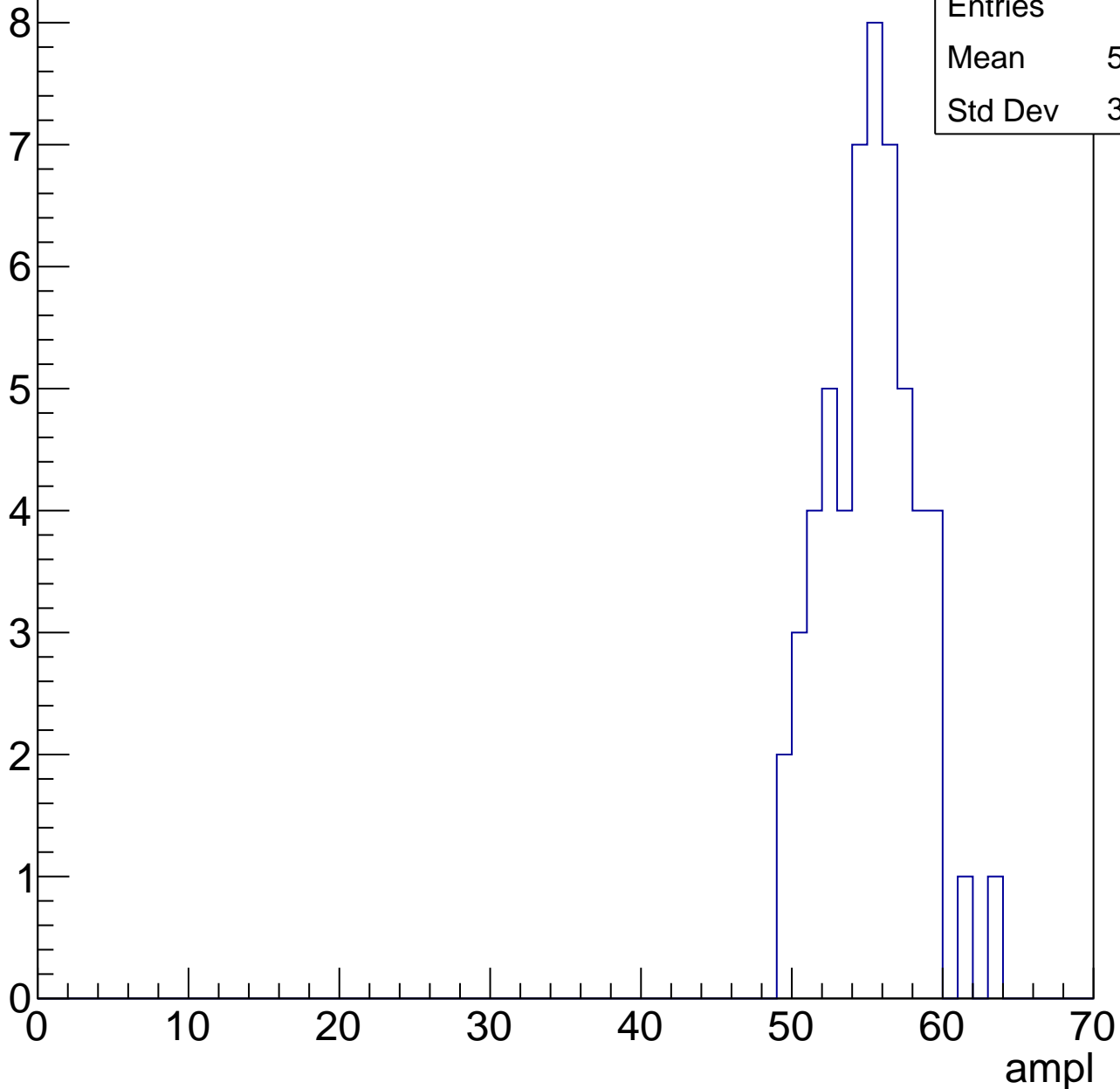


B1L102S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.75 |
| Std Dev | 3.022 |

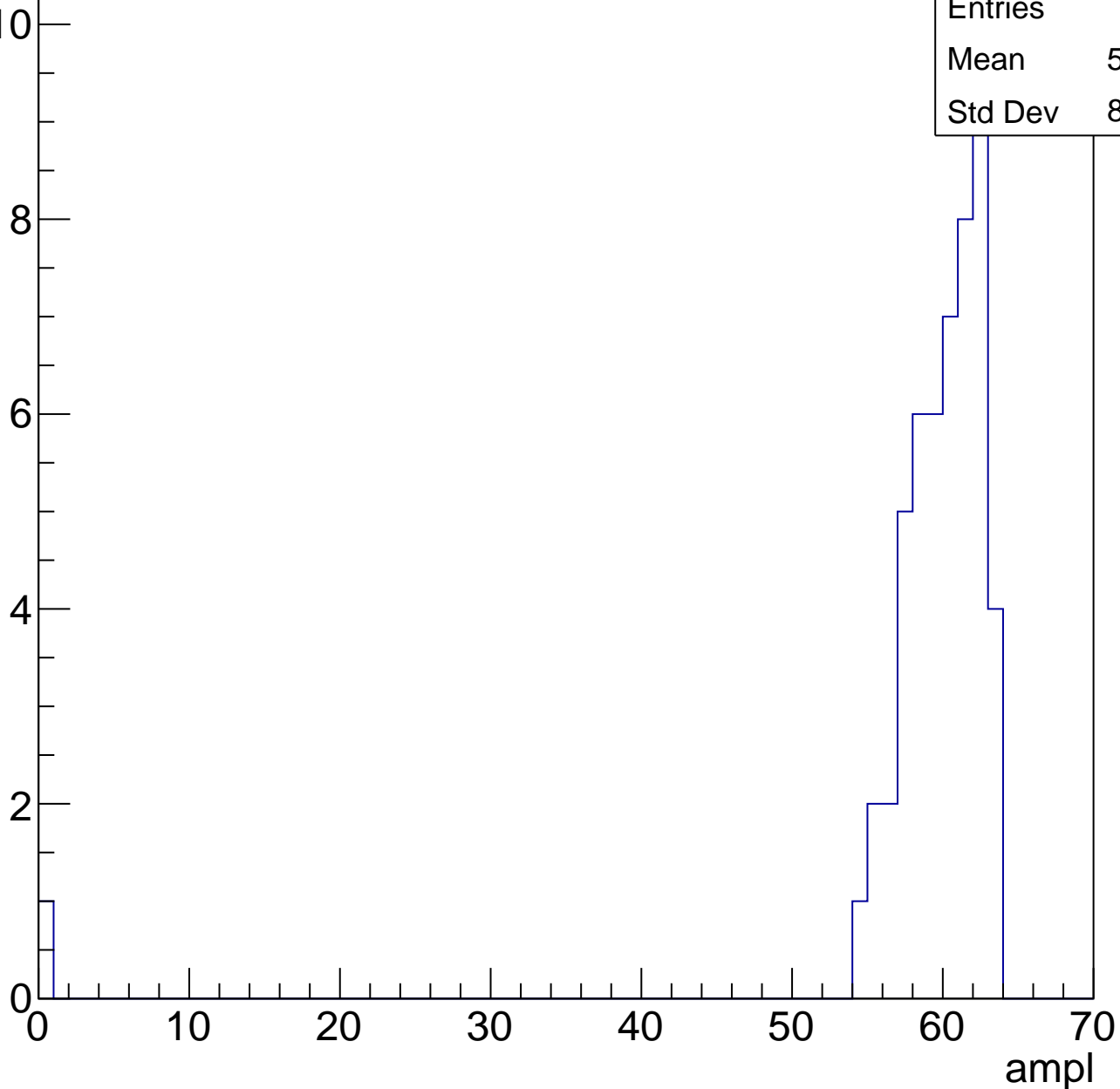


B1L102S, U15-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.52 |
| Std Dev | 8.507 |

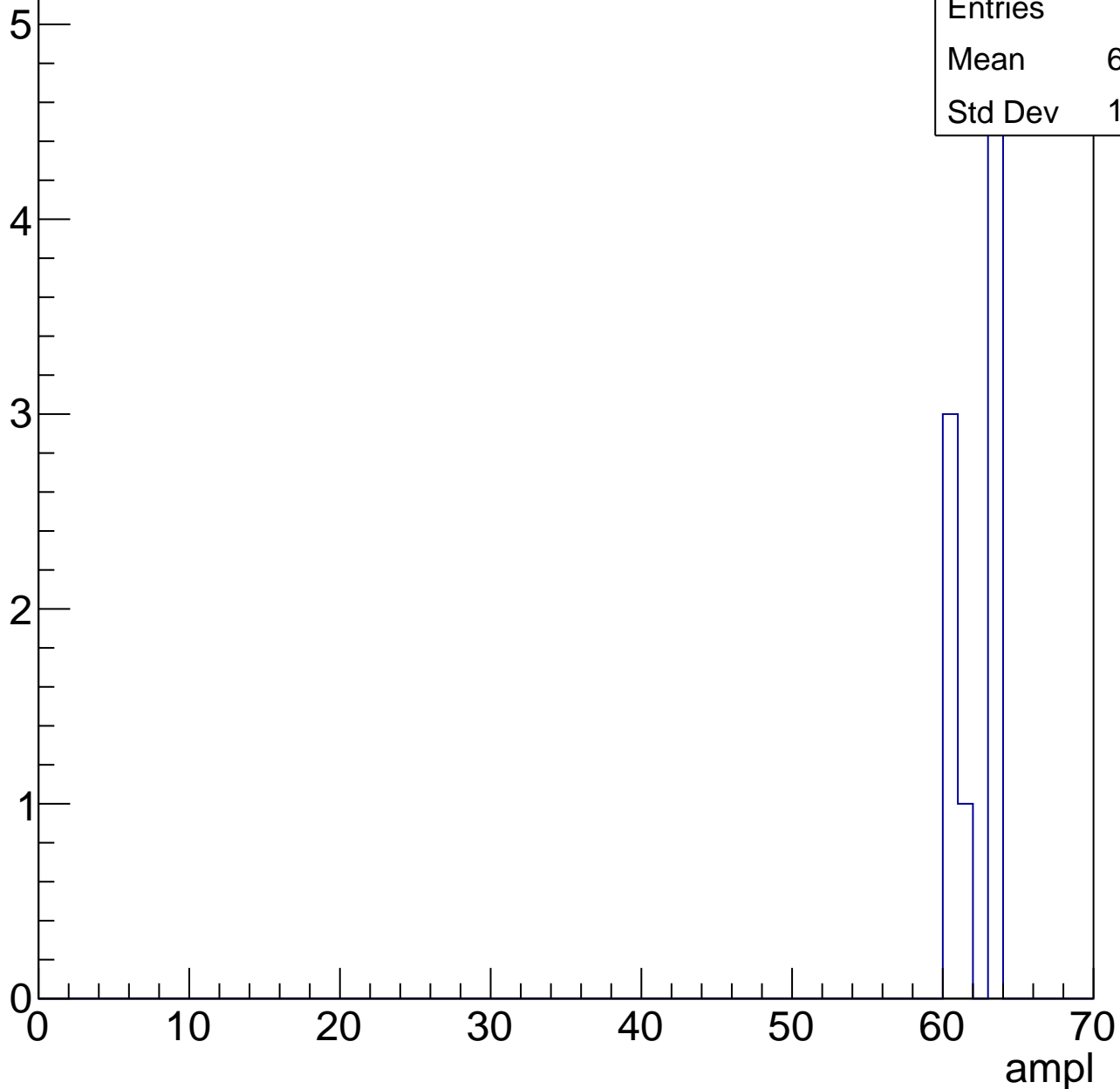


B1L102S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 9 |
| Mean | 61.78 |
| Std Dev | 1.397 |



B1L102S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch62, adc0

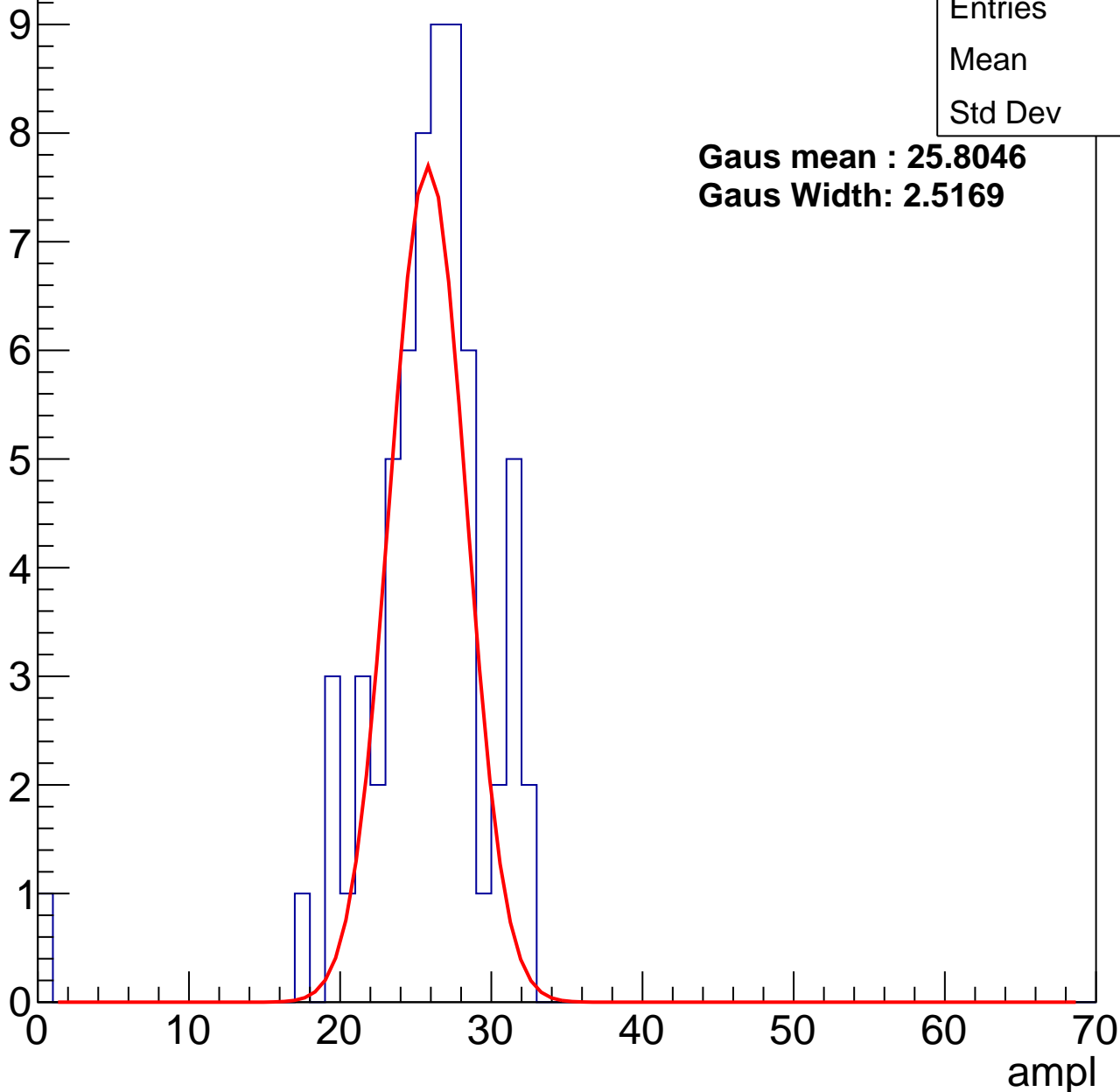
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 64 |
| Mean | 25.2 |
| Std Dev | 4.6 |

Gaus mean : 25.8046

Gaus Width: 2.5169



B1L102S, U15-ch62, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 83 |
| Mean | 33.33 |
| Std Dev | 3.567 |

Gaus mean : 33.7897

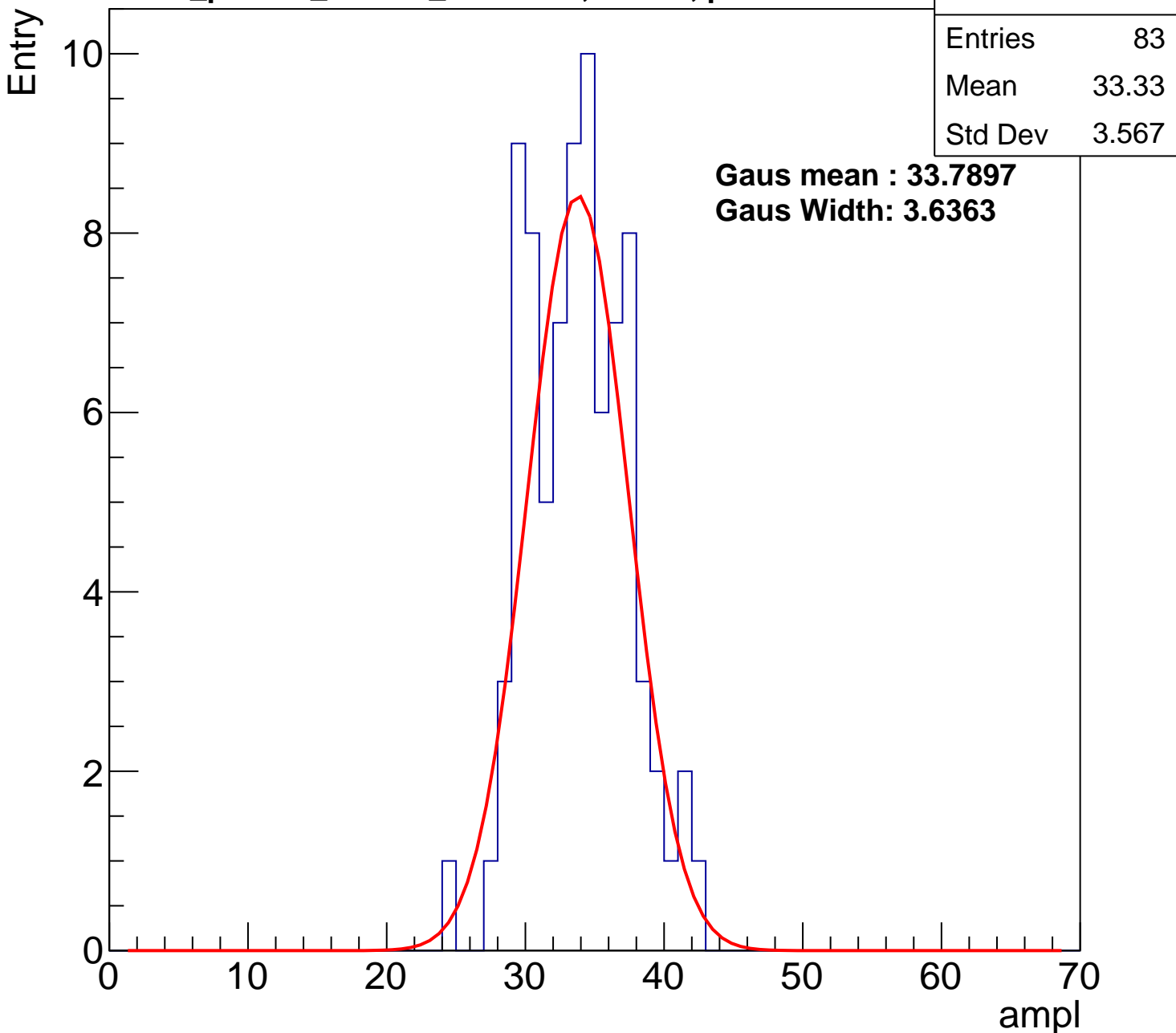
Gaus Width: 3.6363

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch62, adc2

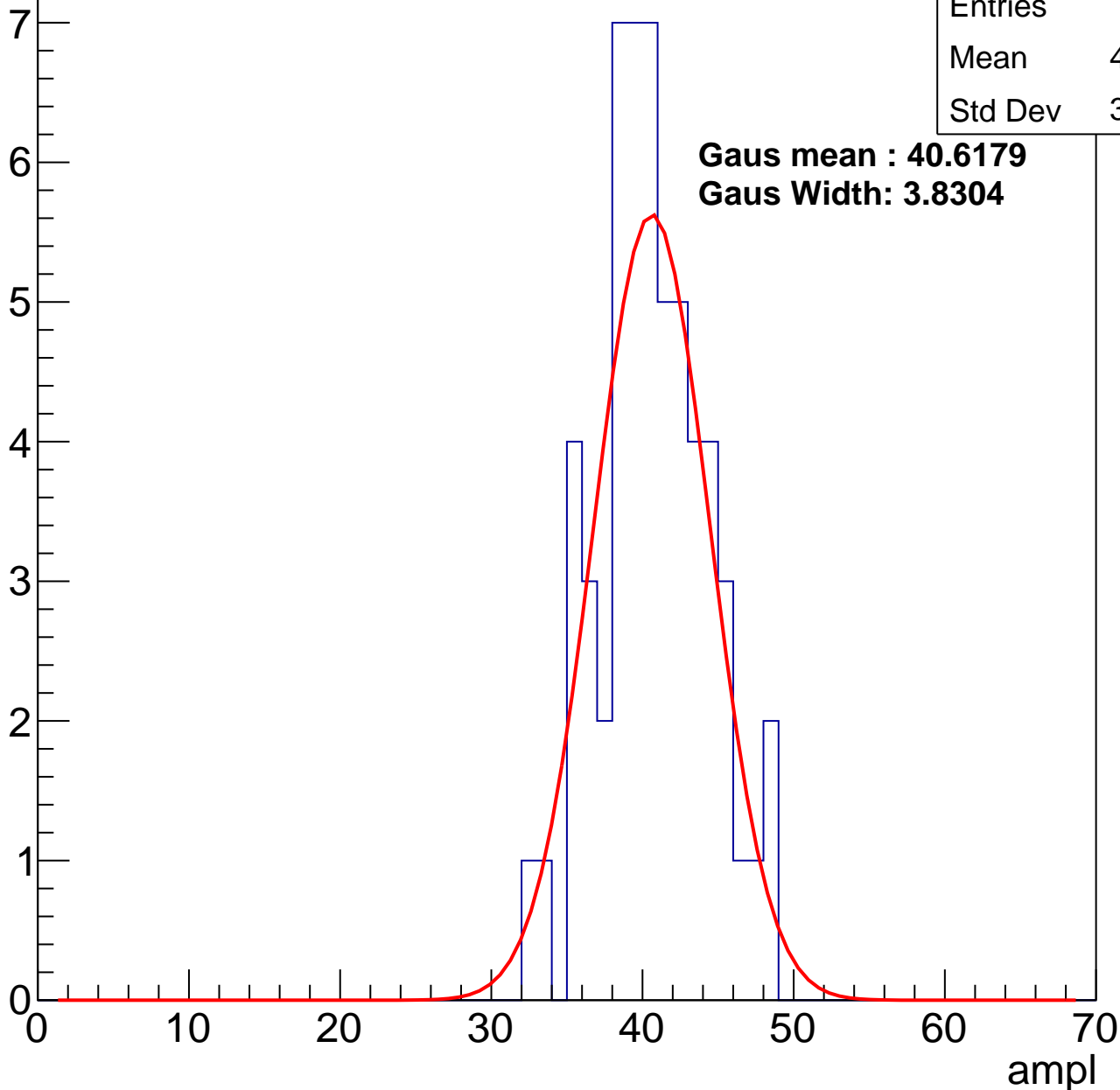
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 40.23 |
| Std Dev | 3.564 |

Gaus mean : 40.6179

Gaus Width: 3.8304

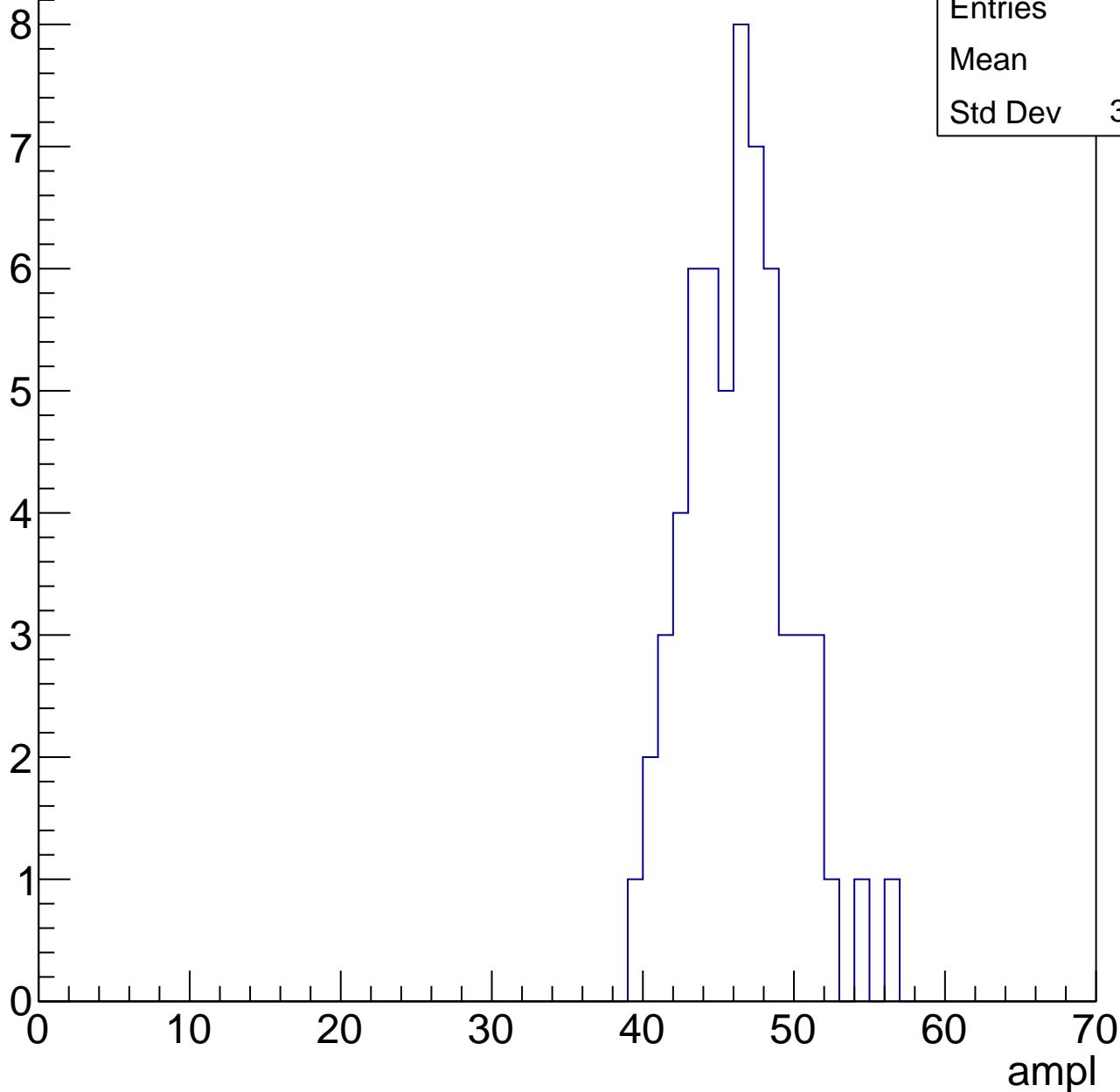


B1L102S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

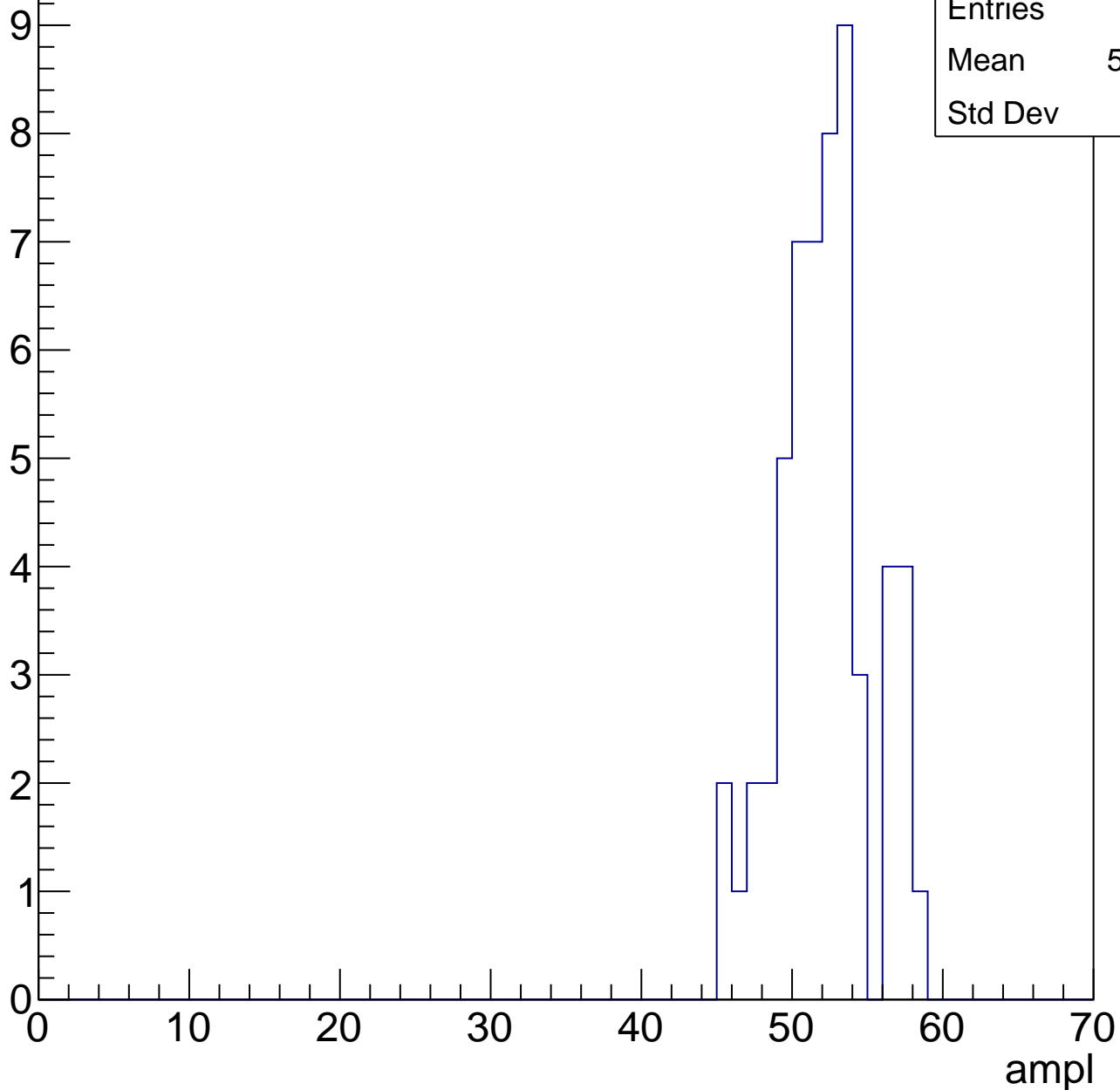
| | |
|---------|-------|
| Entries | 60 |
| Mean | 45.9 |
| Std Dev | 3.472 |



B1L102S, U15-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

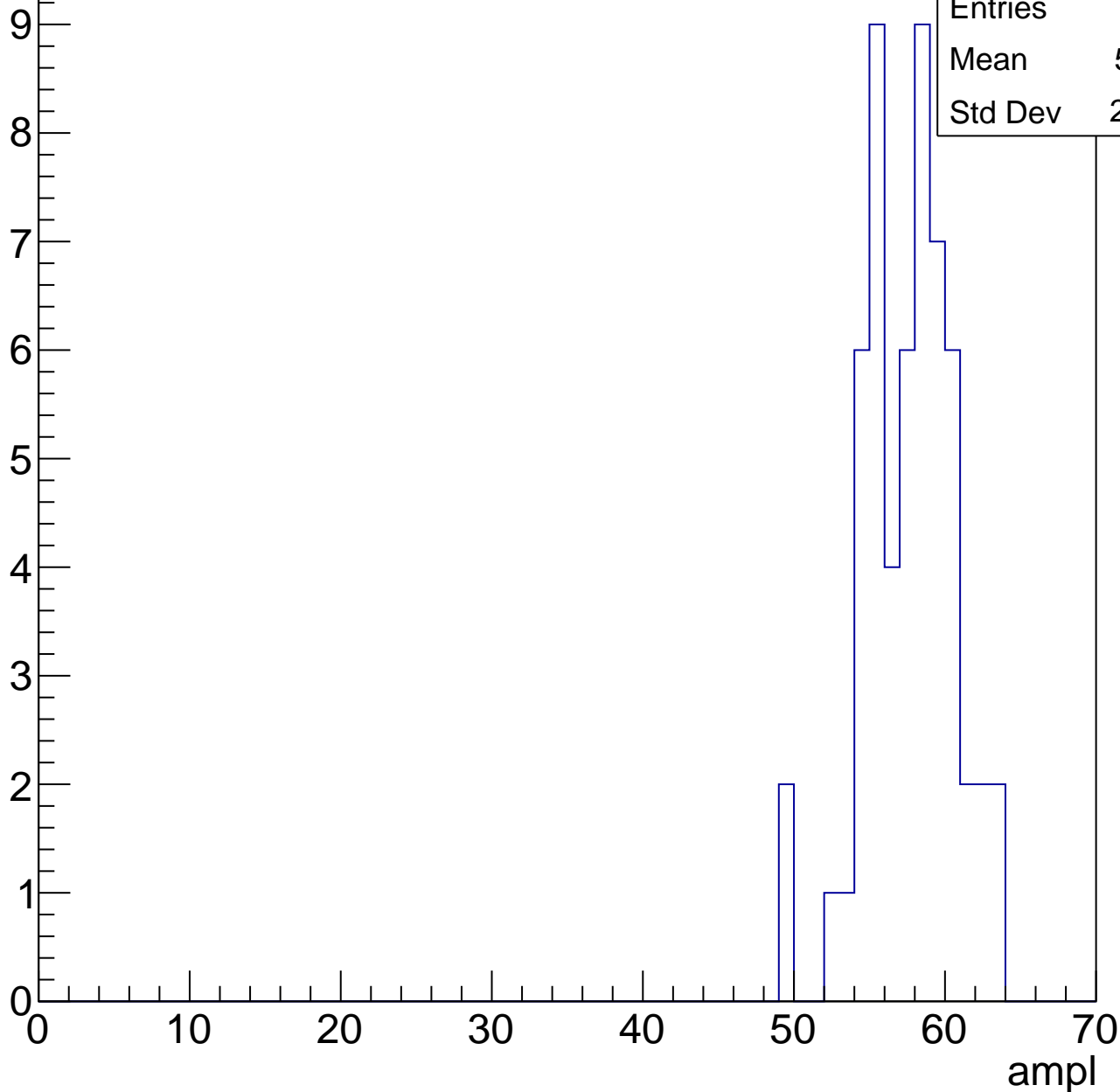


B1L102S, U15-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 57.11 |
| Std Dev | 2.983 |

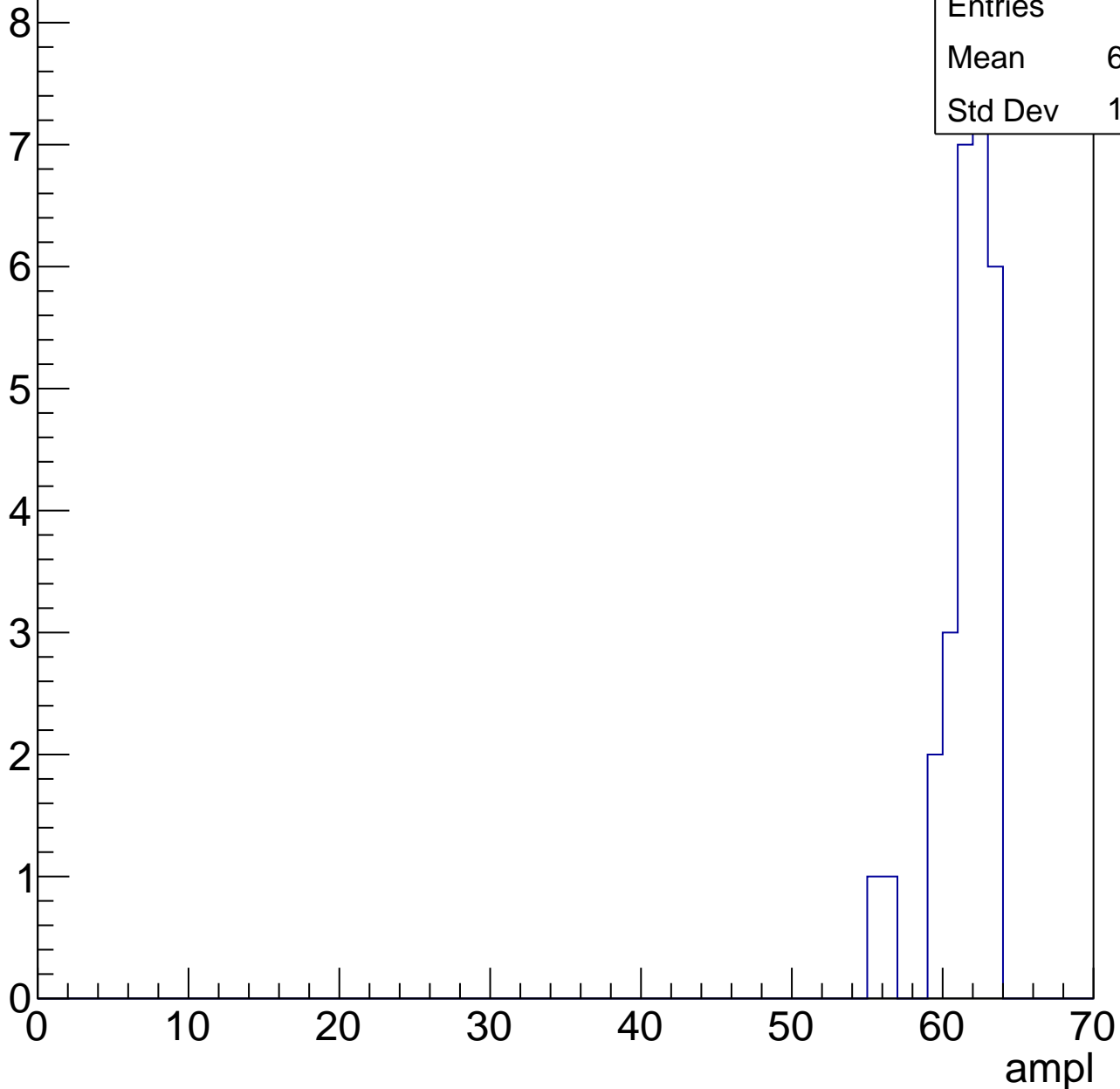


B1L102S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

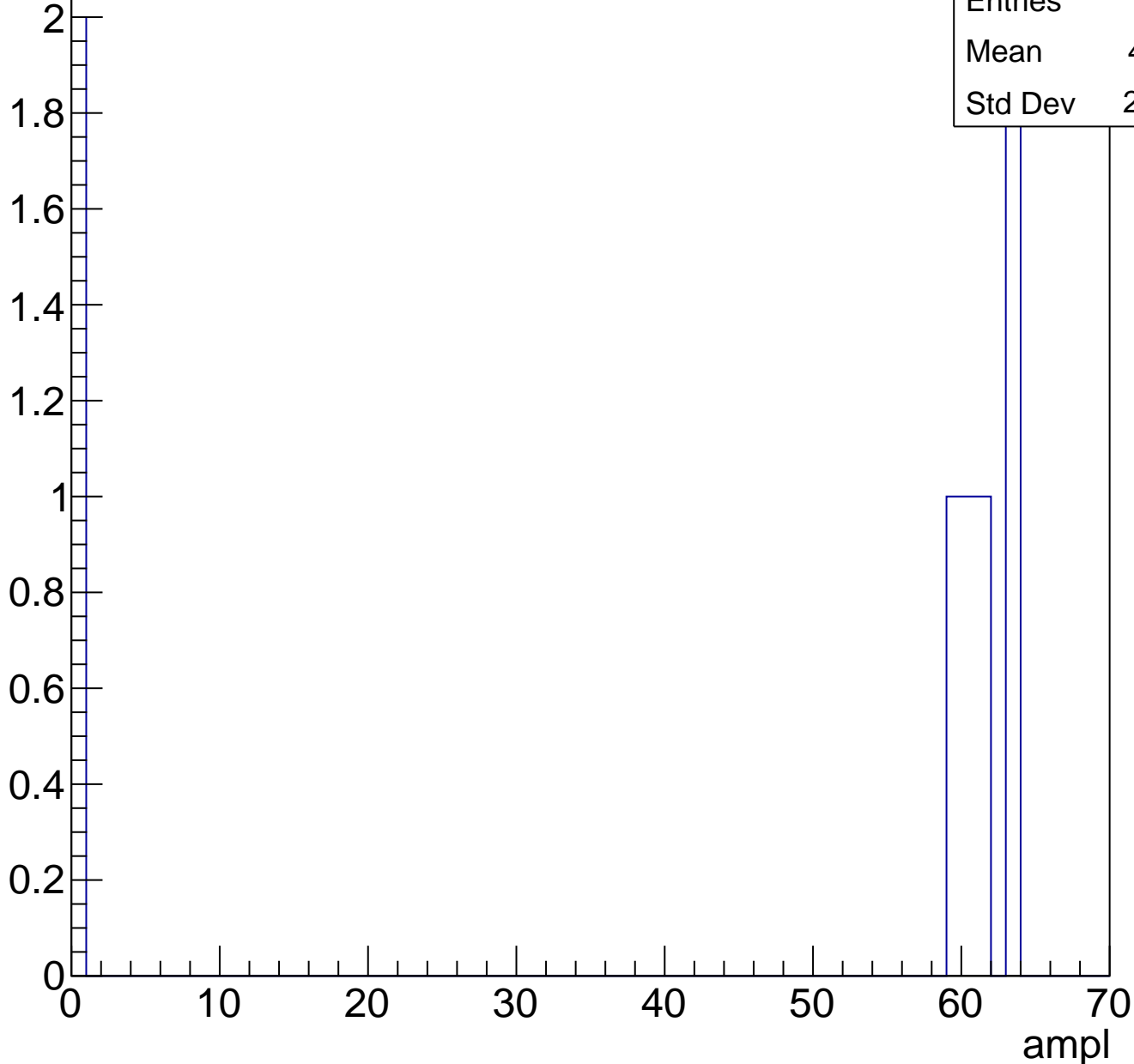
| | |
|---------|-------|
| Entries | 28 |
| Mean | 61.07 |
| Std Dev | 1.926 |



B1L102S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch63, adc0

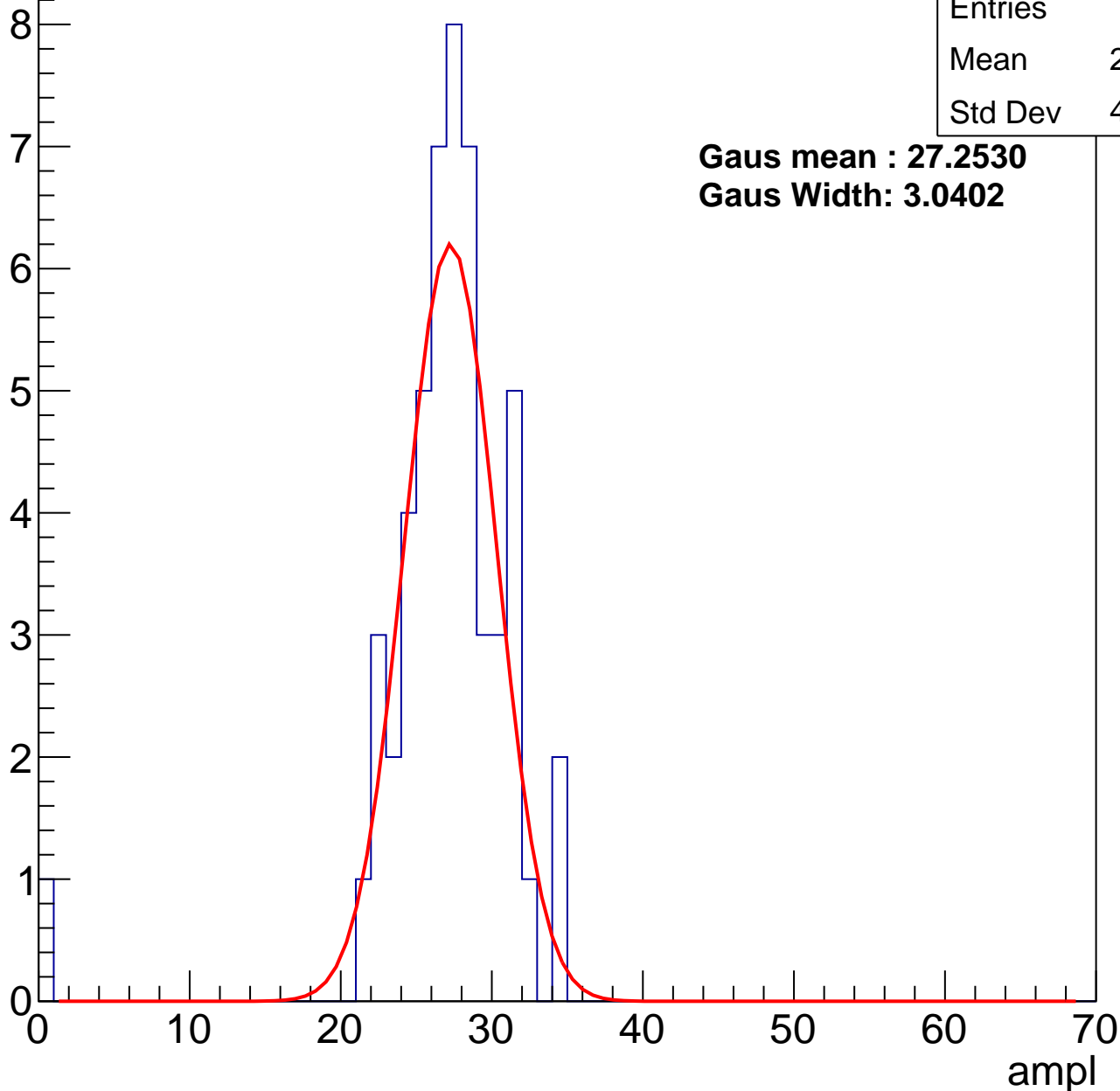
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 26.54 |
| Std Dev | 4.749 |

Gaus mean : 27.2530

Gaus Width: 3.0402



B1L102S, U15-ch63, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 90 |
| Mean | 34.34 |
| Std Dev | 3.39 |

Gaus mean : 34.6841

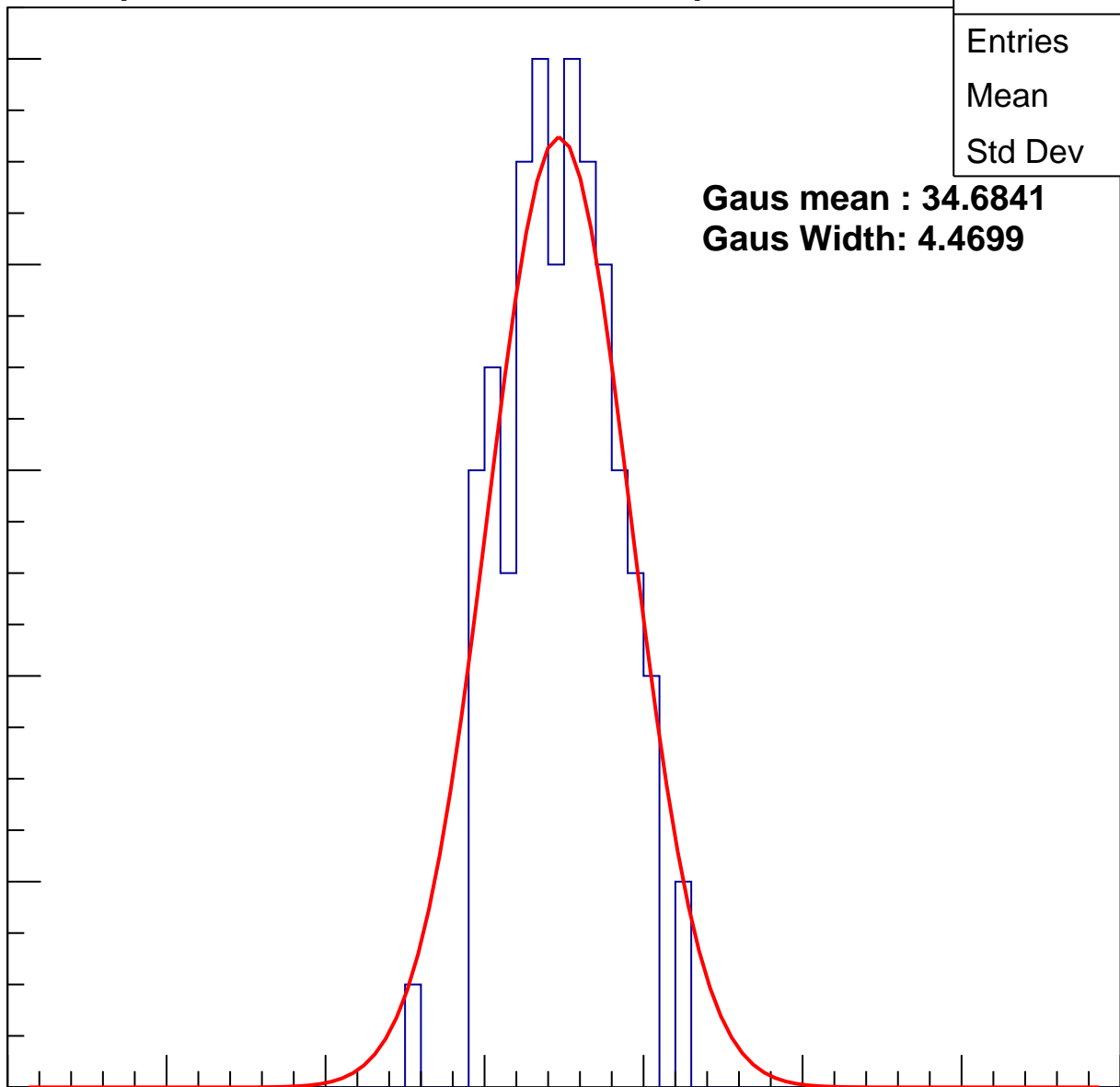
Gaus Width: 4.4699

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch63, adc2

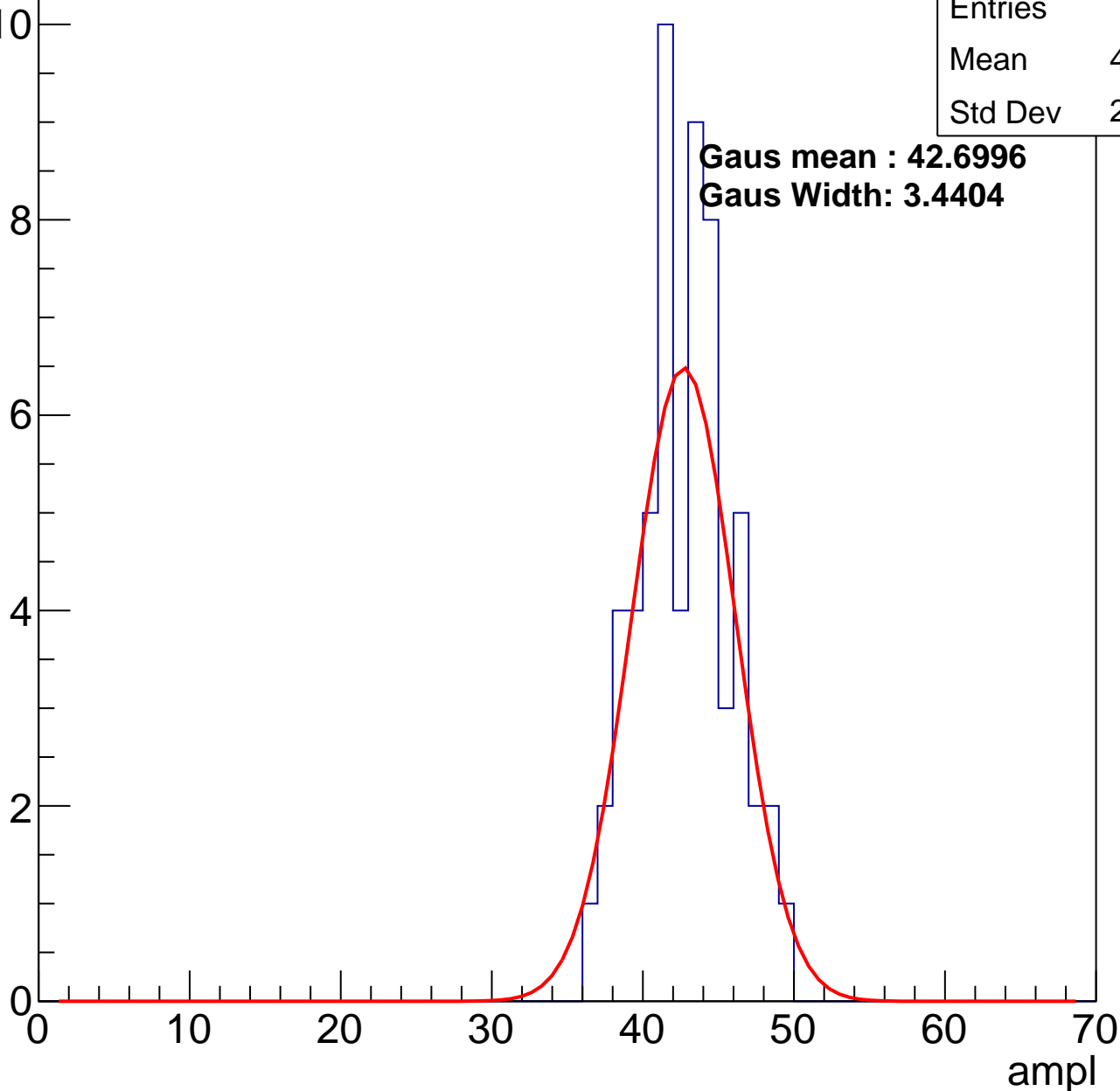
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 42.32 |
| Std Dev | 2.969 |

Gaus mean : 42.6996

Gaus Width: 3.4404

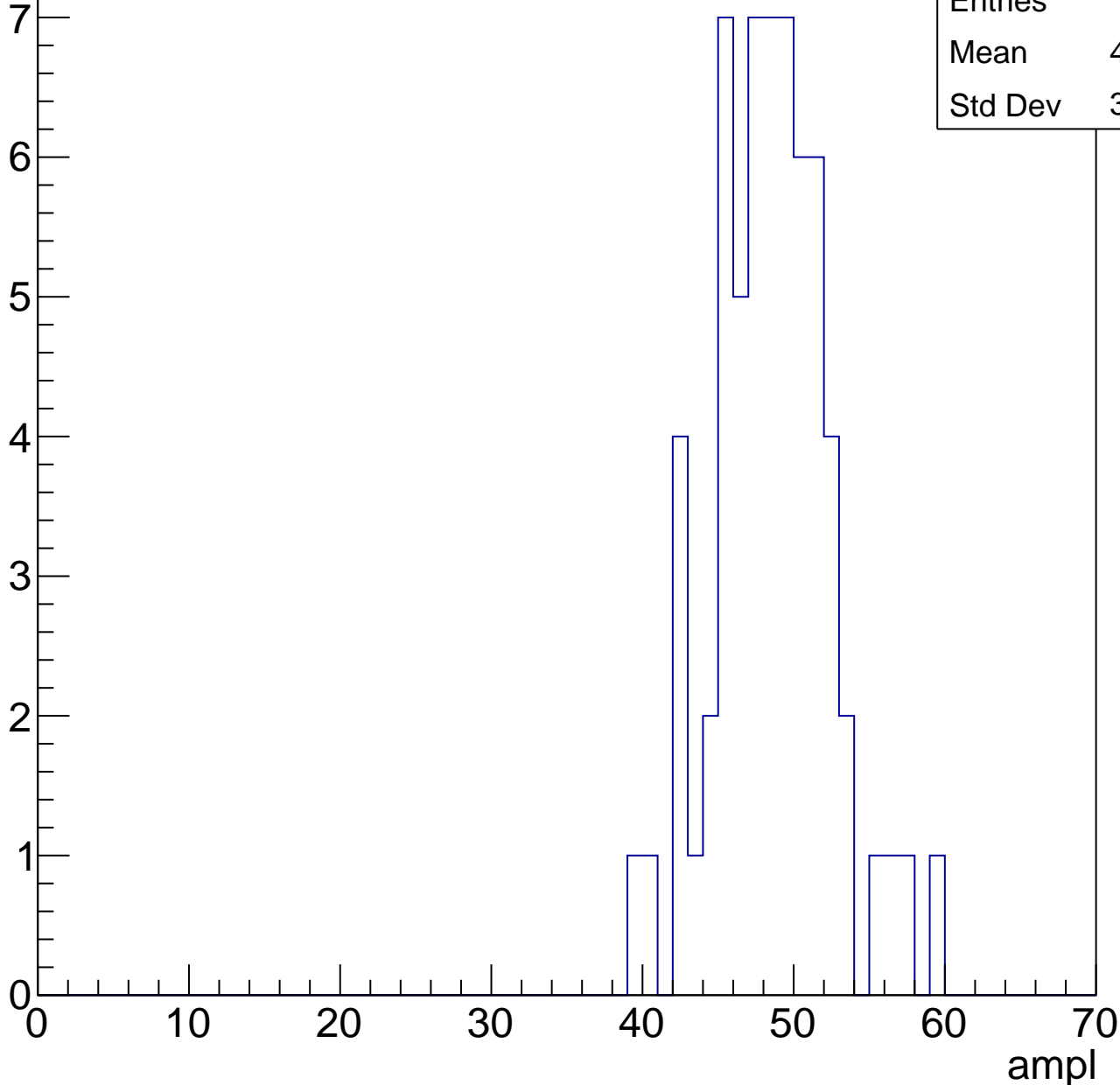


B1L102S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.09 |
| Std Dev | 3.852 |

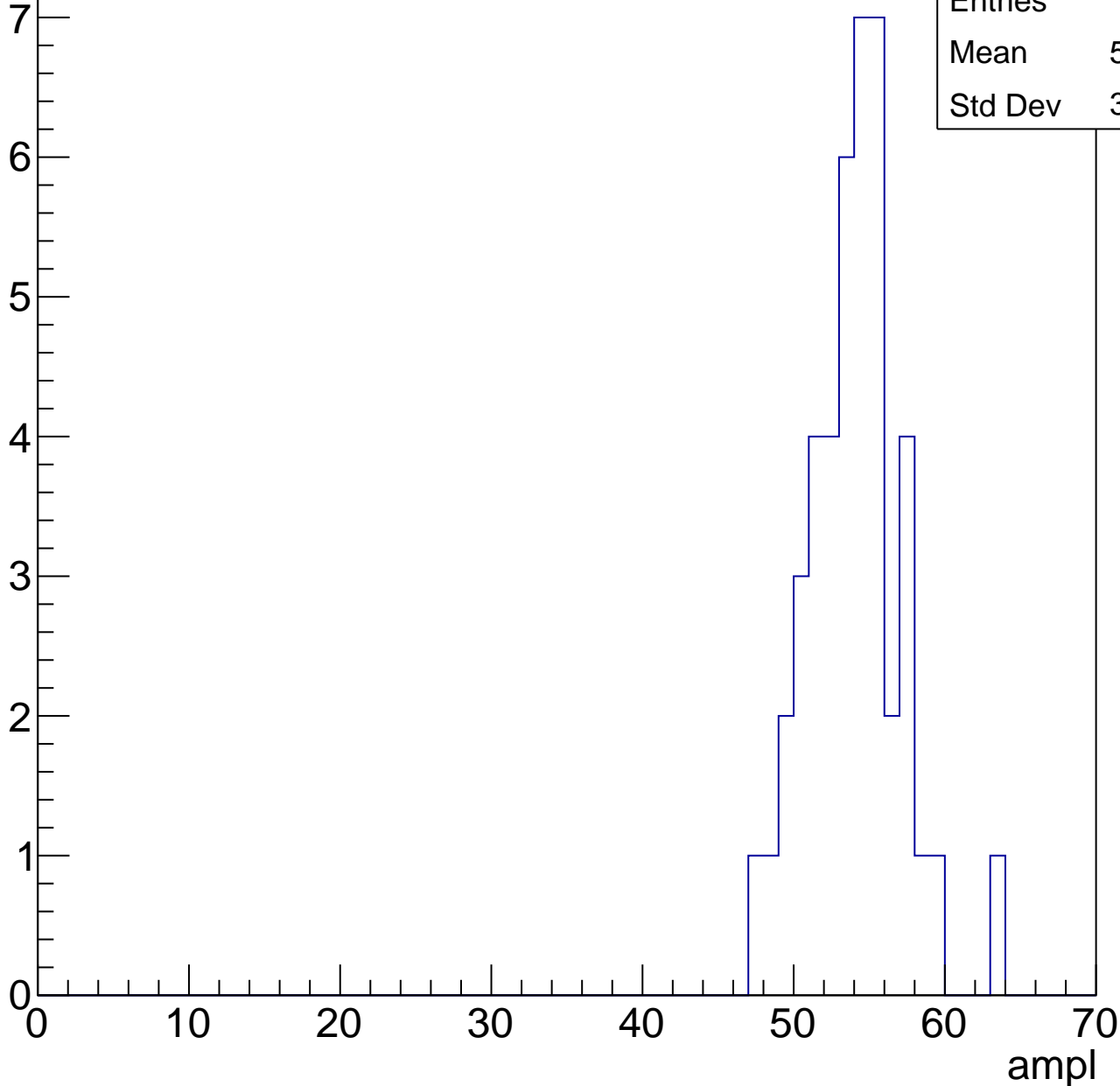


B1L102S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 53.55 |
| Std Dev | 3.034 |

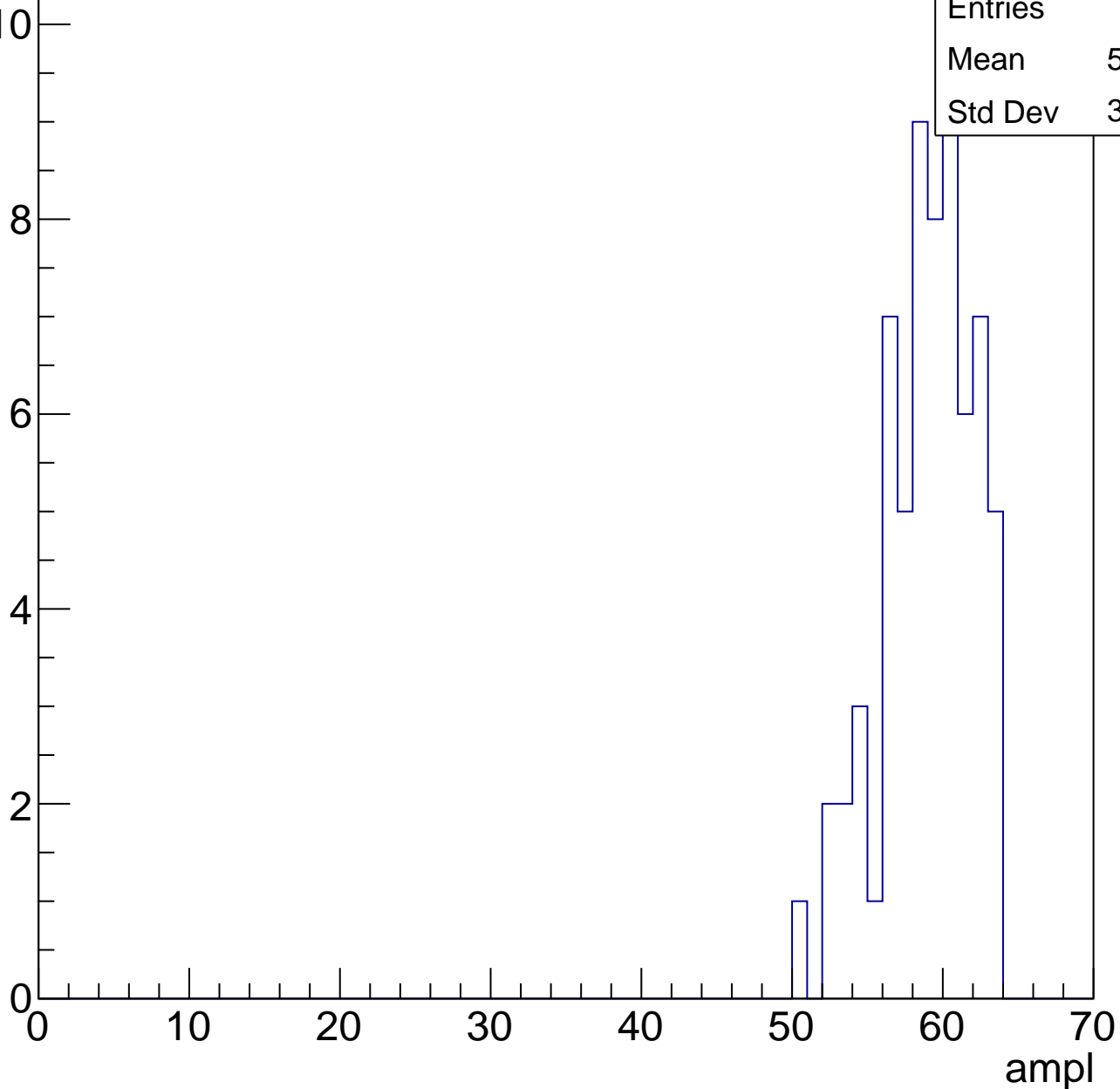


B1L102S, U15-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

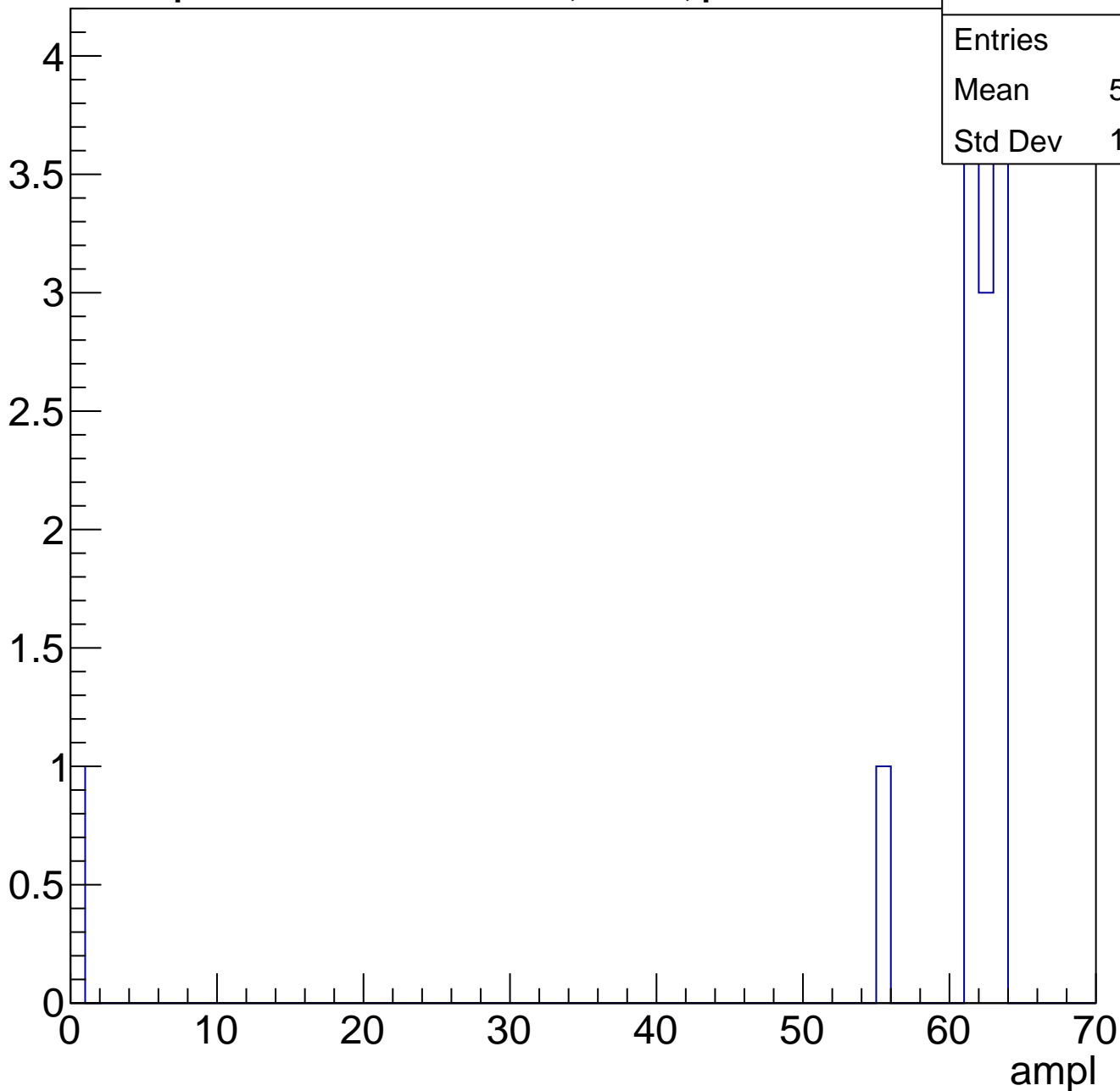
| | |
|---------|-------|
| Entries | 66 |
| Mean | 58.53 |
| Std Dev | 3.006 |



B1L102S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch64, adc0

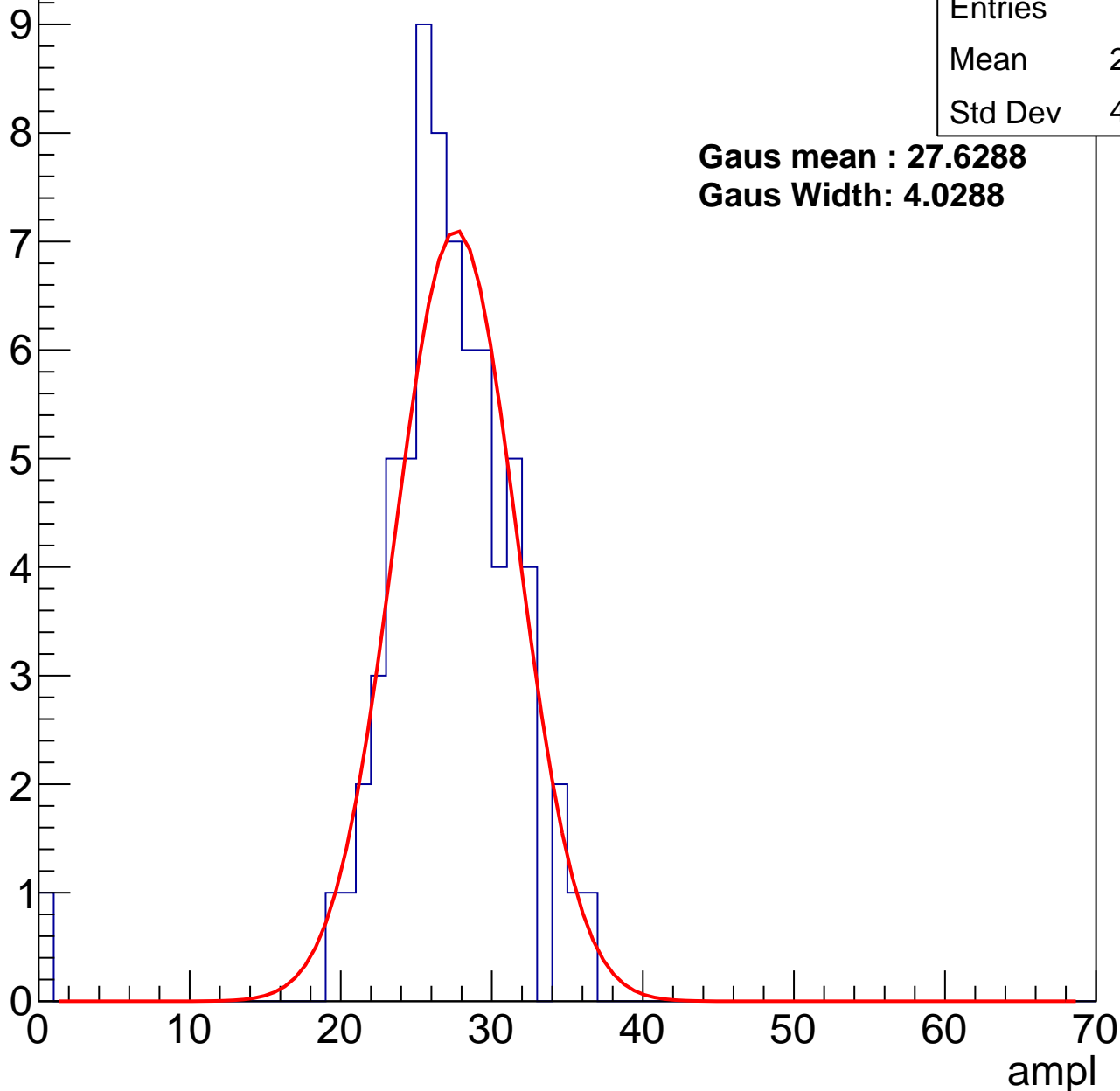
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 26.59 |
| Std Dev | 4.799 |

Gaus mean : 27.6288

Gaus Width: 4.0288



B1L102S, U15-ch64, adc1

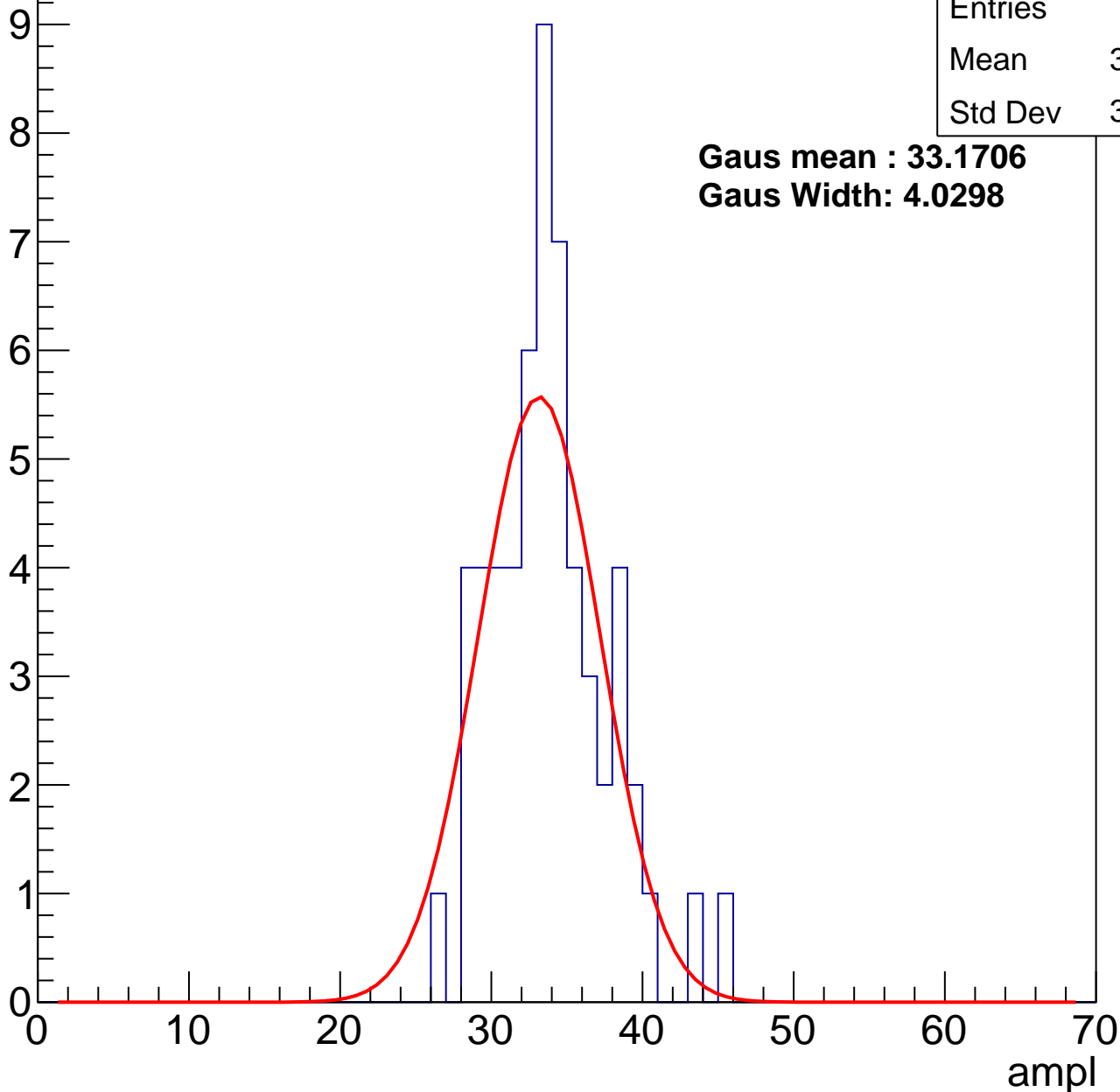
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 33.42 |
| Std Dev | 3.765 |

Gaus mean : 33.1706

Gaus Width: 4.0298



B1L102S, U15-ch64, adc2

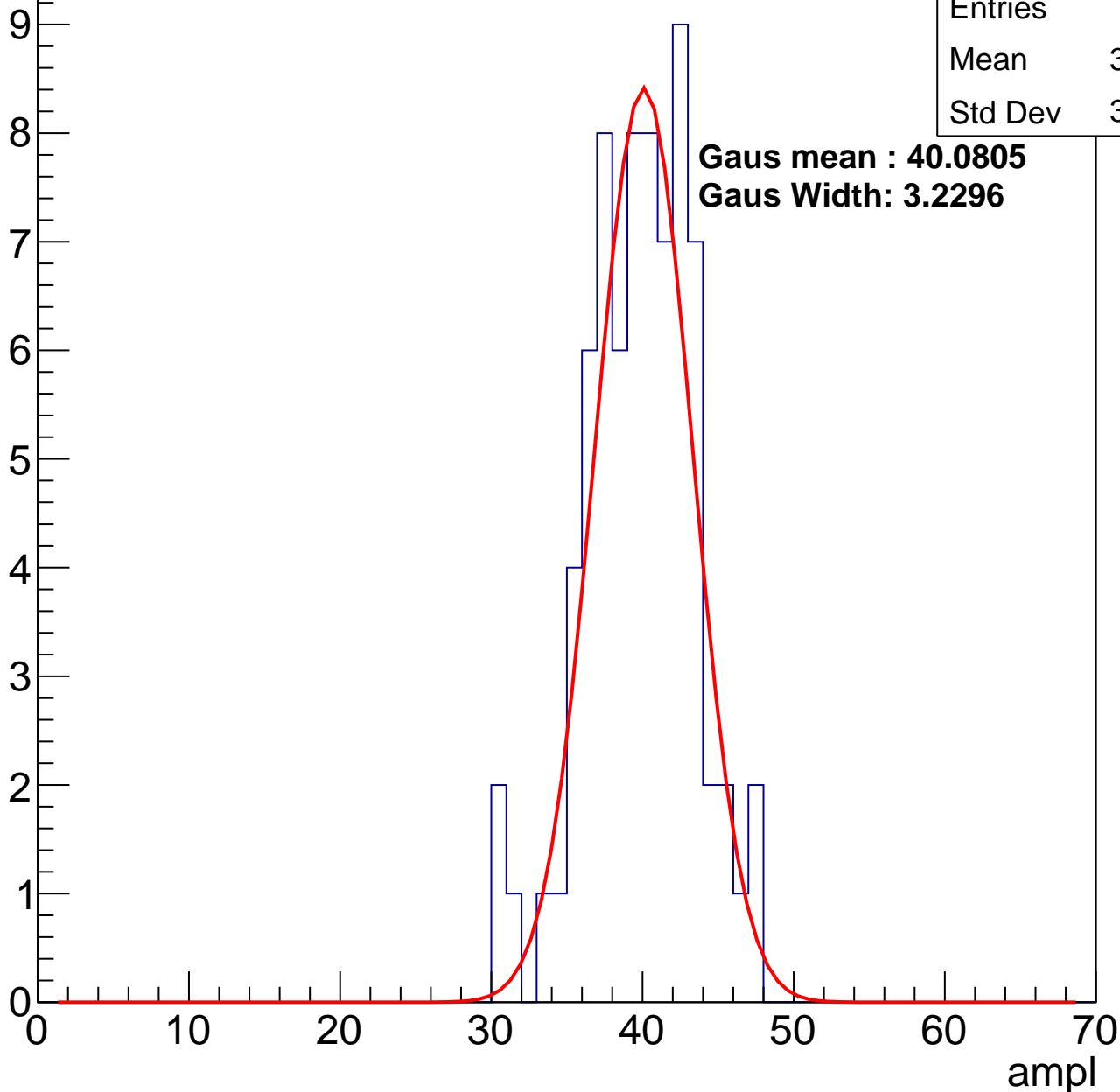
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 39.39 |
| Std Dev | 3.577 |

Gaus mean : 40.0805

Gaus Width: 3.2296

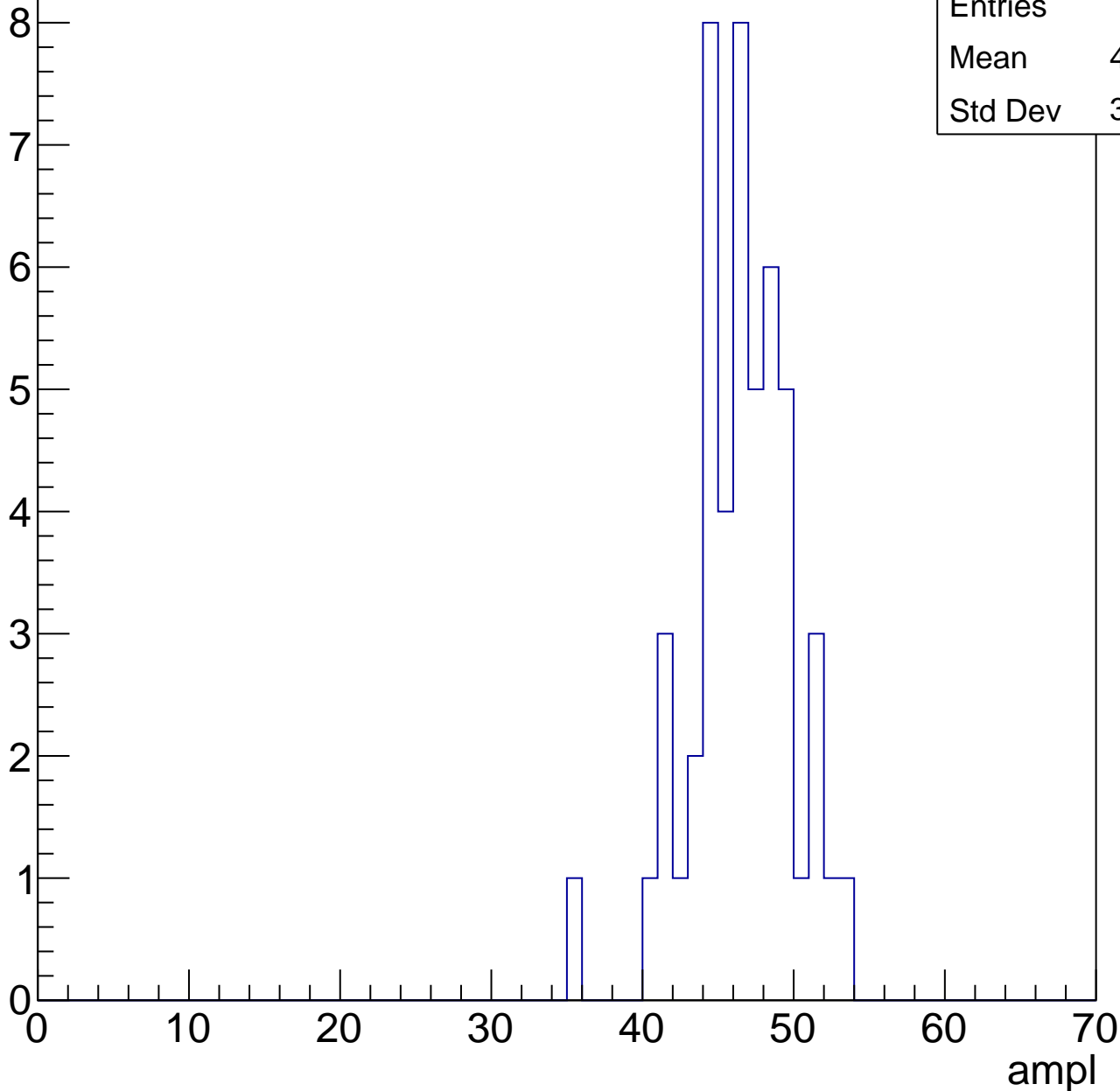


B1L102S, U15-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

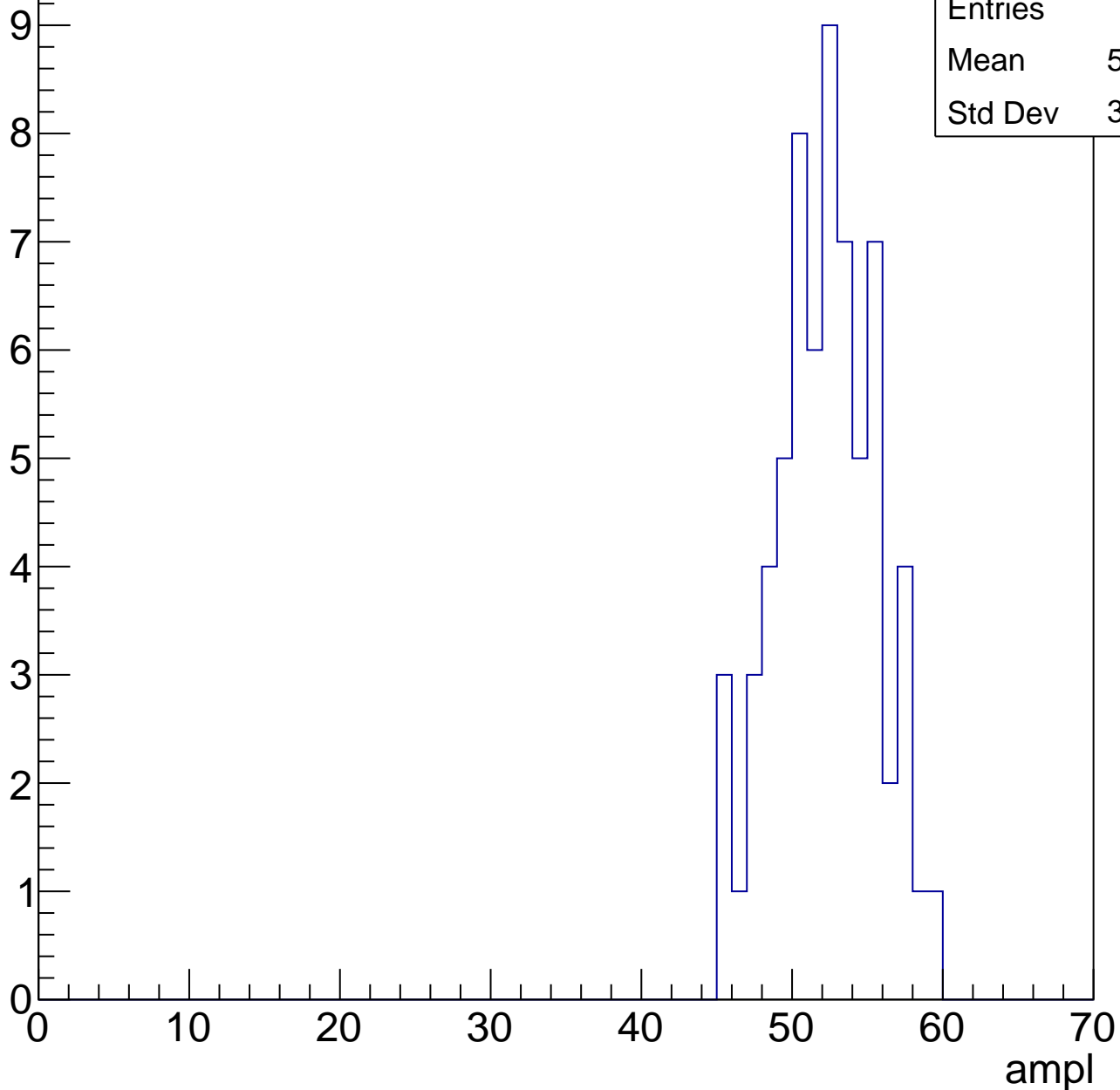
| | |
|---------|-------|
| Entries | 50 |
| Mean | 46.04 |
| Std Dev | 3.322 |



B1L102S, U15-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

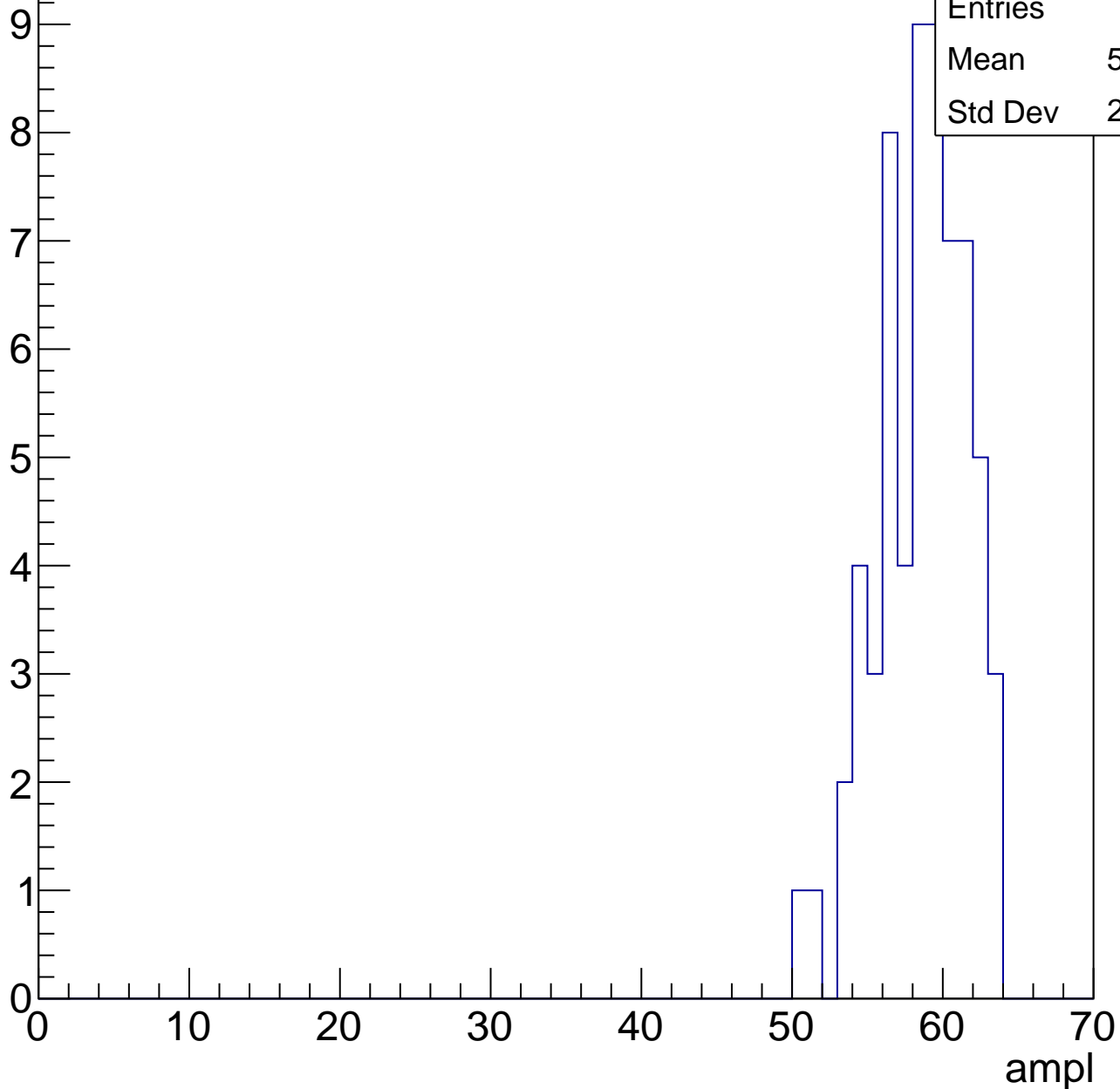
Entry



B1L102S, U15-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

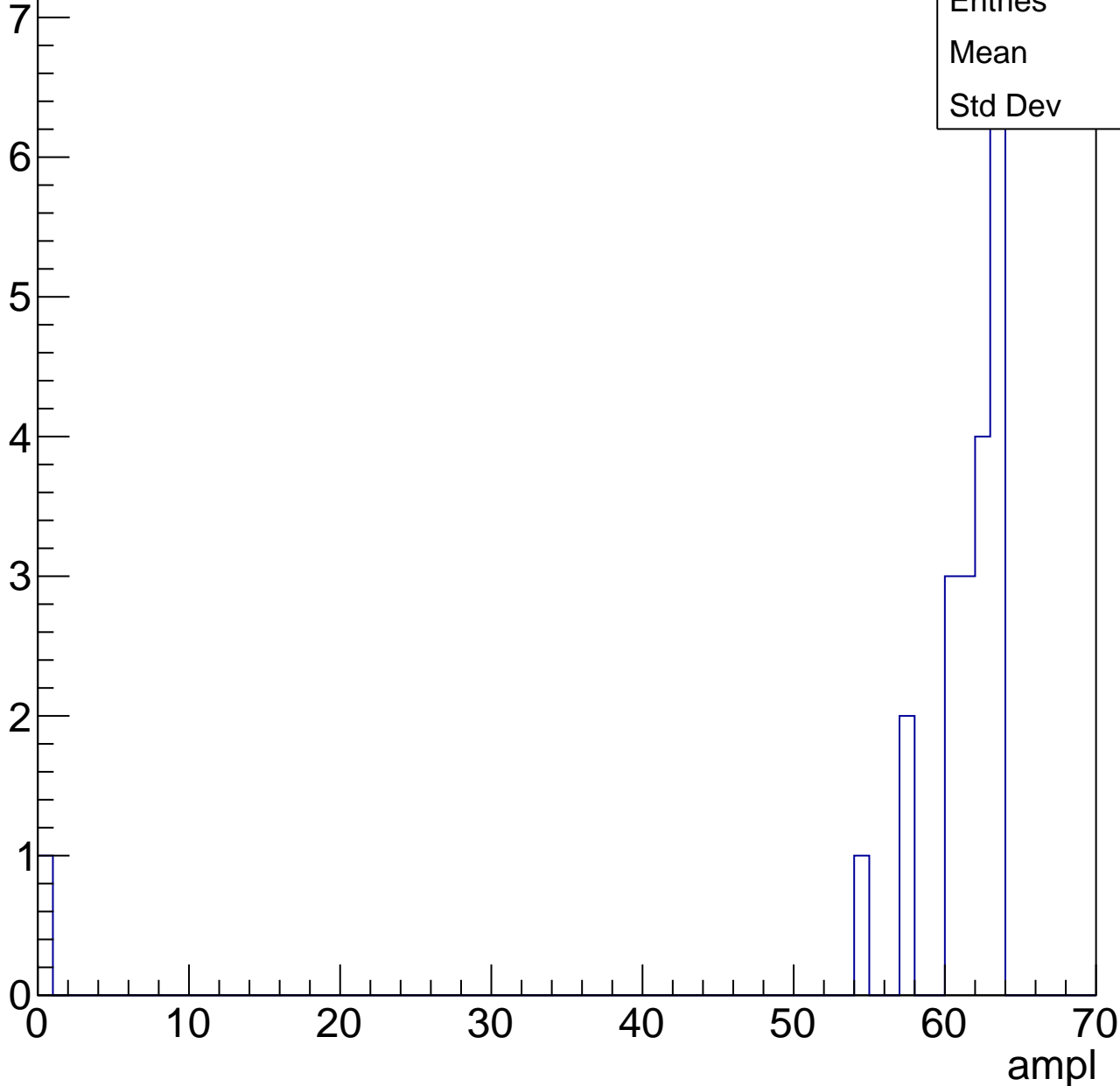


B1L102S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 21 |
| Mean | 58.1 |
| Std Dev | 13.2 |



B1L102S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch65, adc0

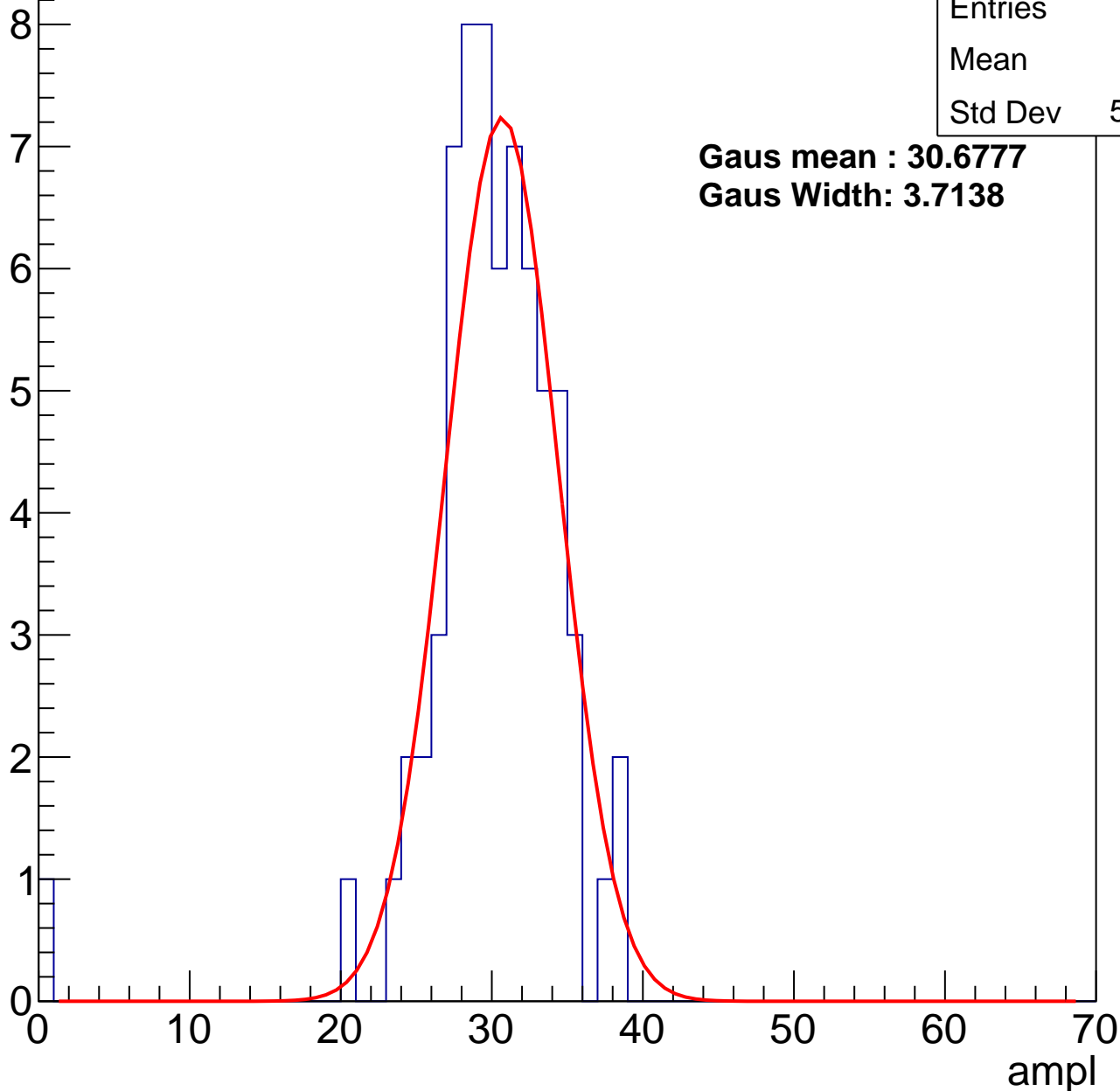
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 29.5 |
| Std Dev | 5.019 |

Gaus mean : 30.6777

Gaus Width: 3.7138



B1L102S, U15-ch65, adc1

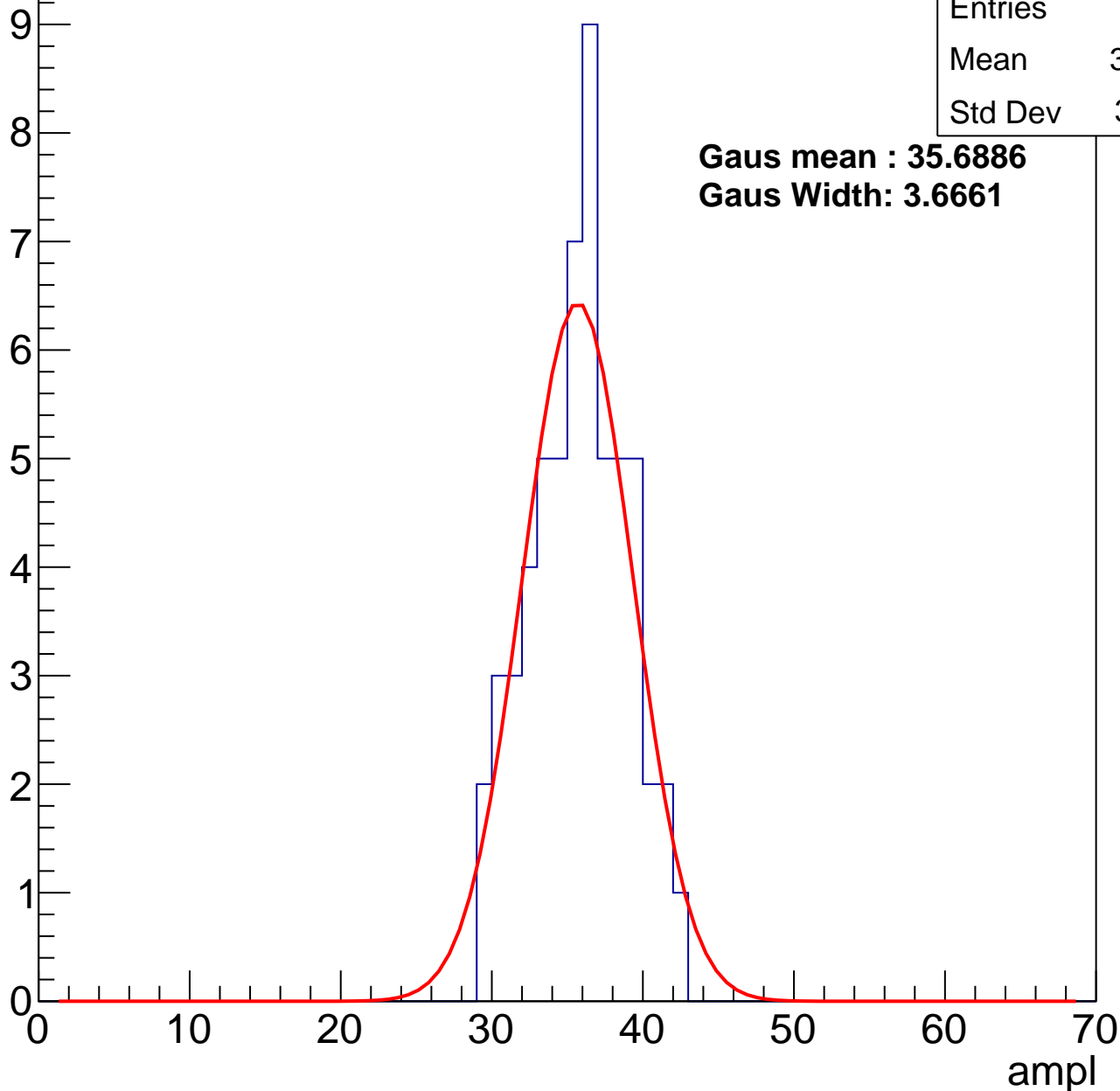
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 35.29 |
| Std Dev | 3.151 |

Gaus mean : 35.6886

Gaus Width: 3.6661



B1L102S, U15-ch65, adc2

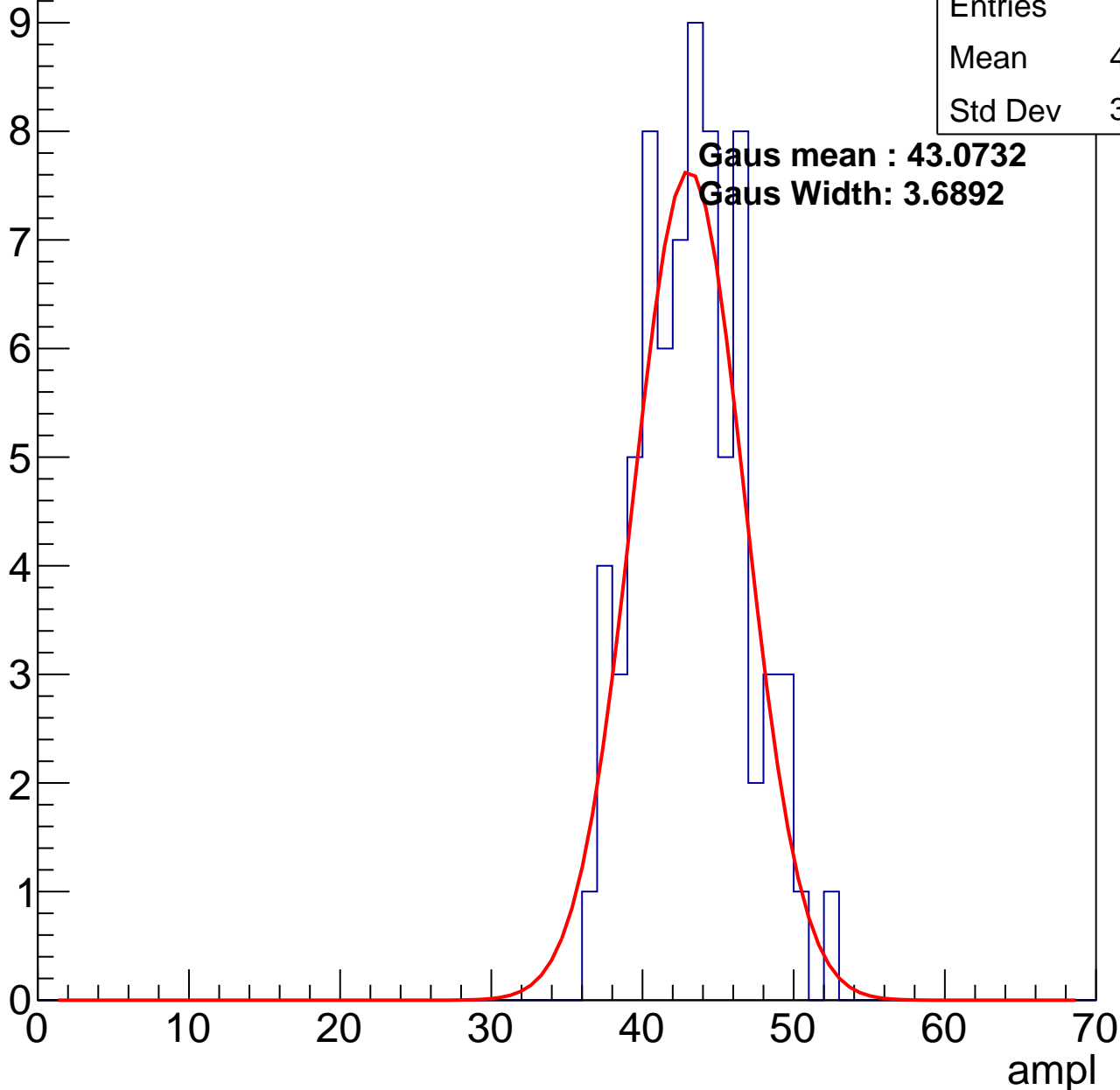
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 42.86 |
| Std Dev | 3.477 |

Gaus mean : 43.0732

Gaus Width: 3.6892

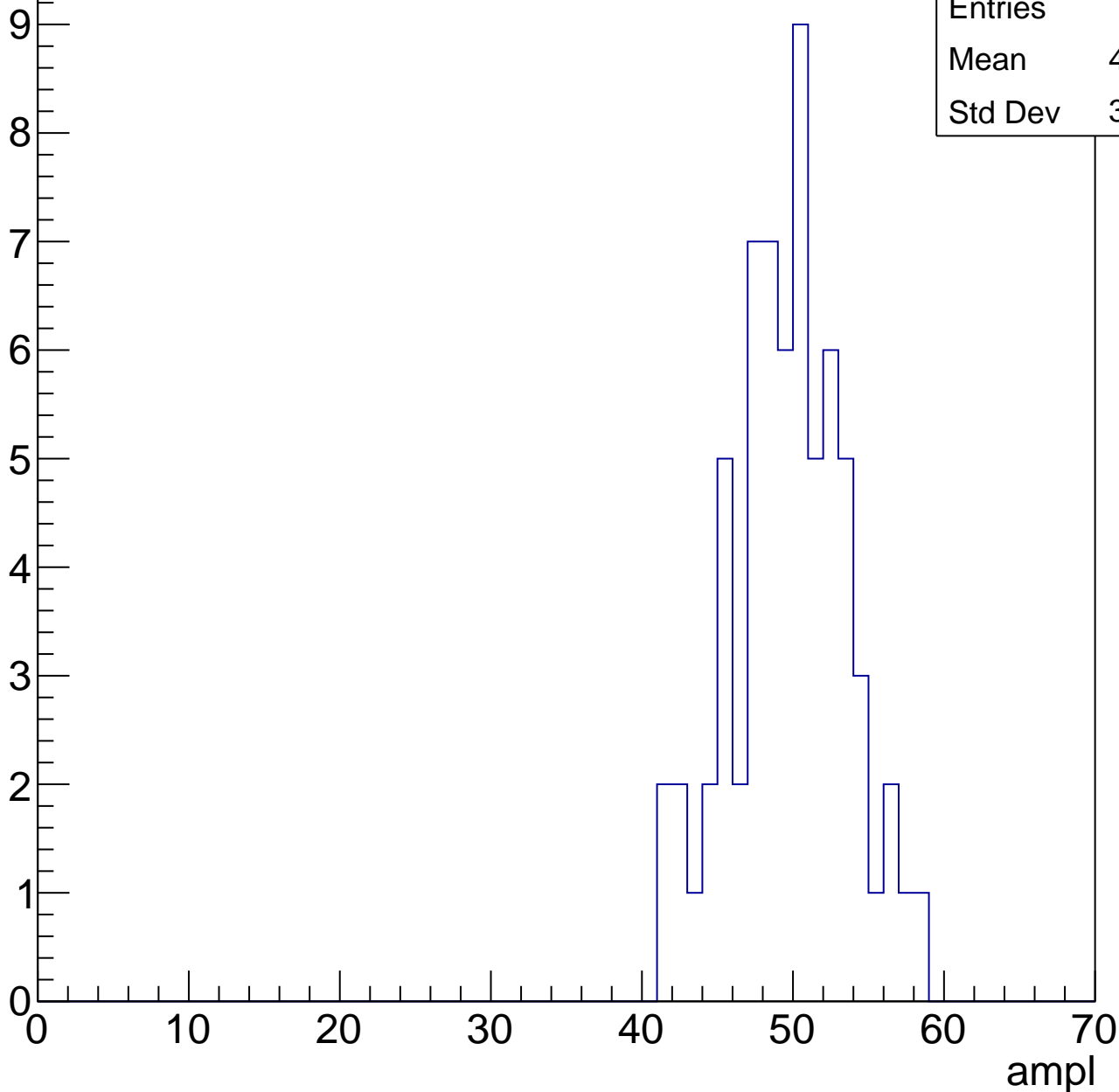


B1L102S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 49.24 |
| Std Dev | 3.782 |

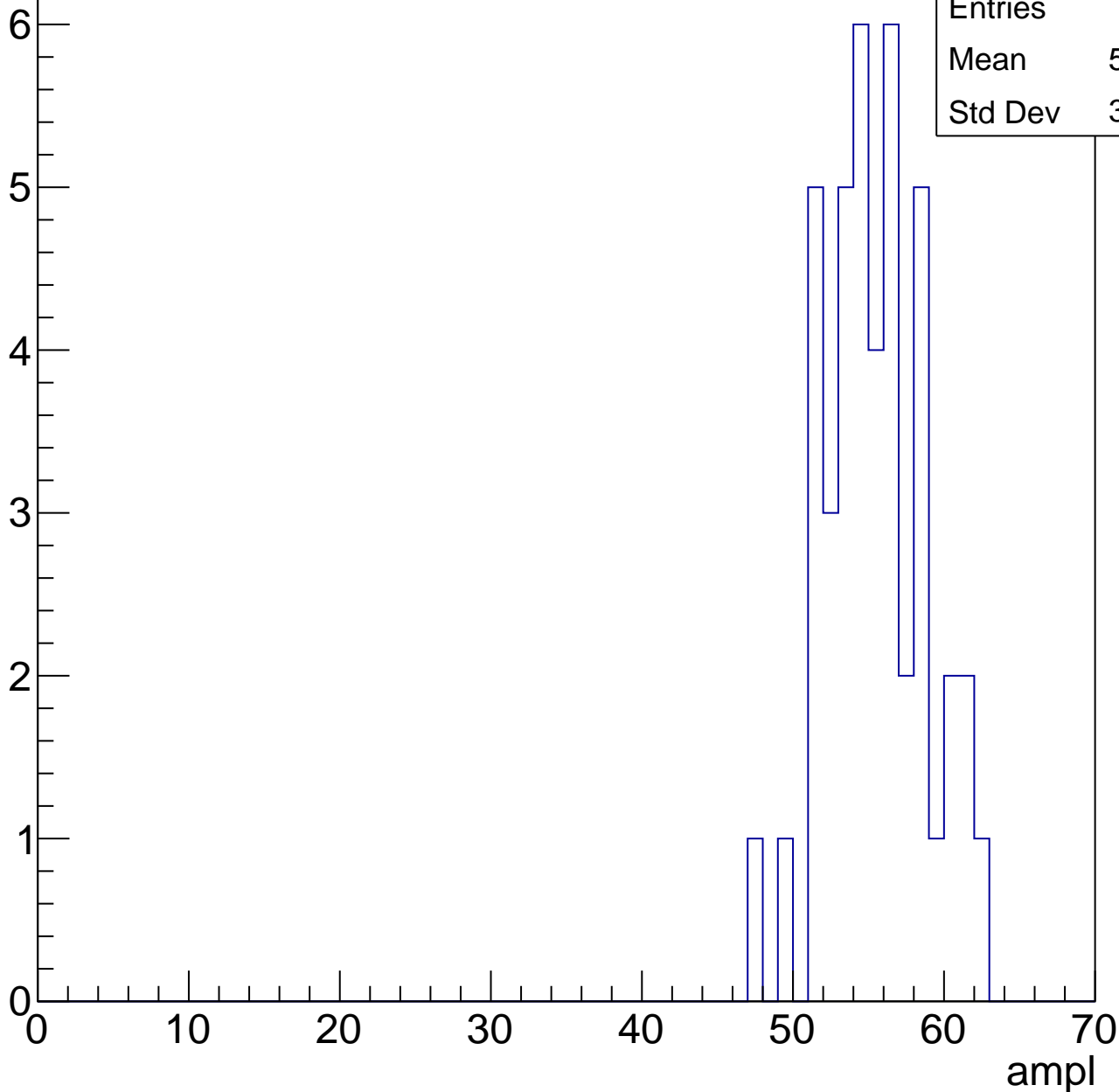


B1L102S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 54.98 |
| Std Dev | 3.299 |

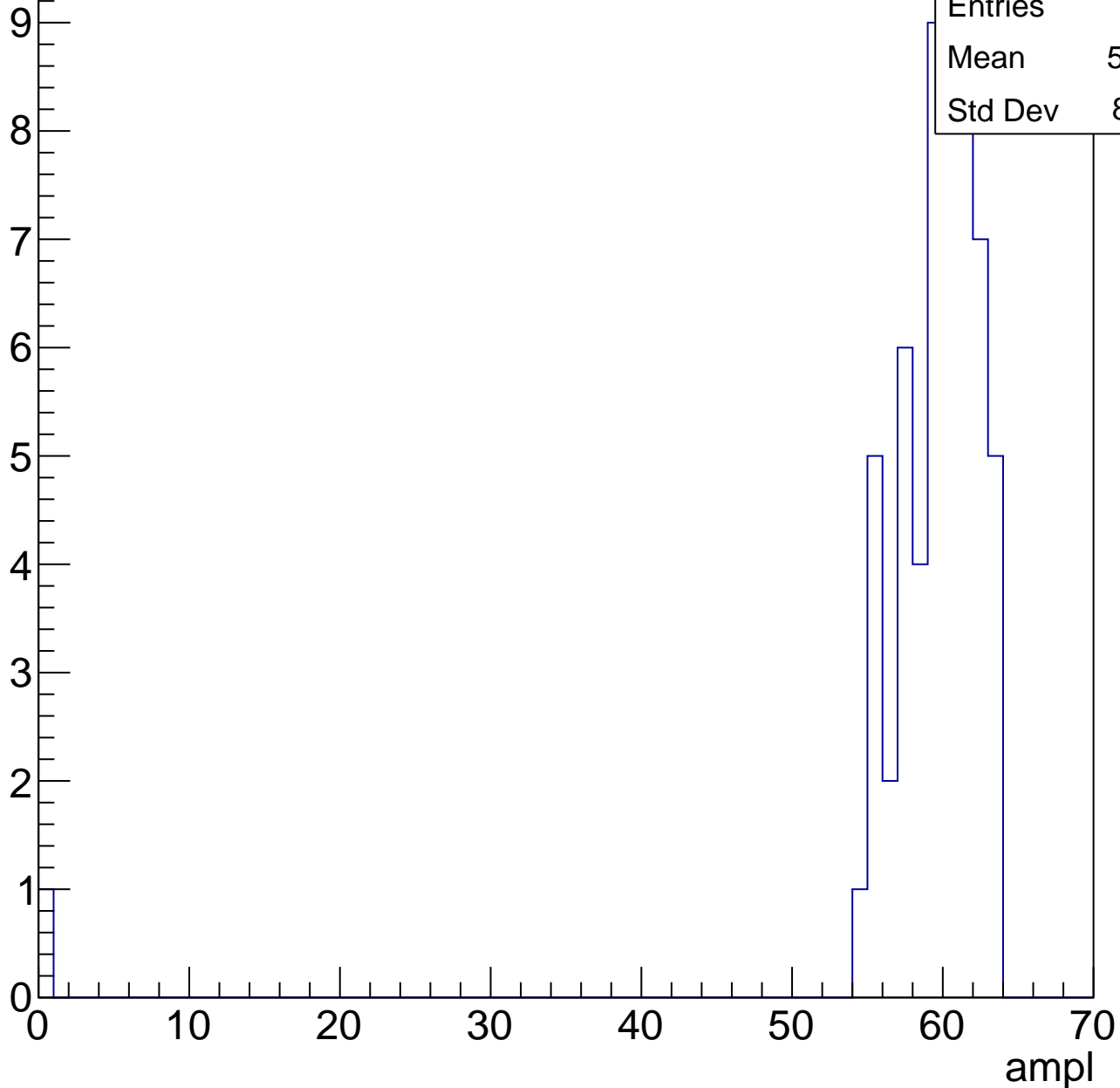


B1L102S, U15-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.27 |
| Std Dev | 8.221 |

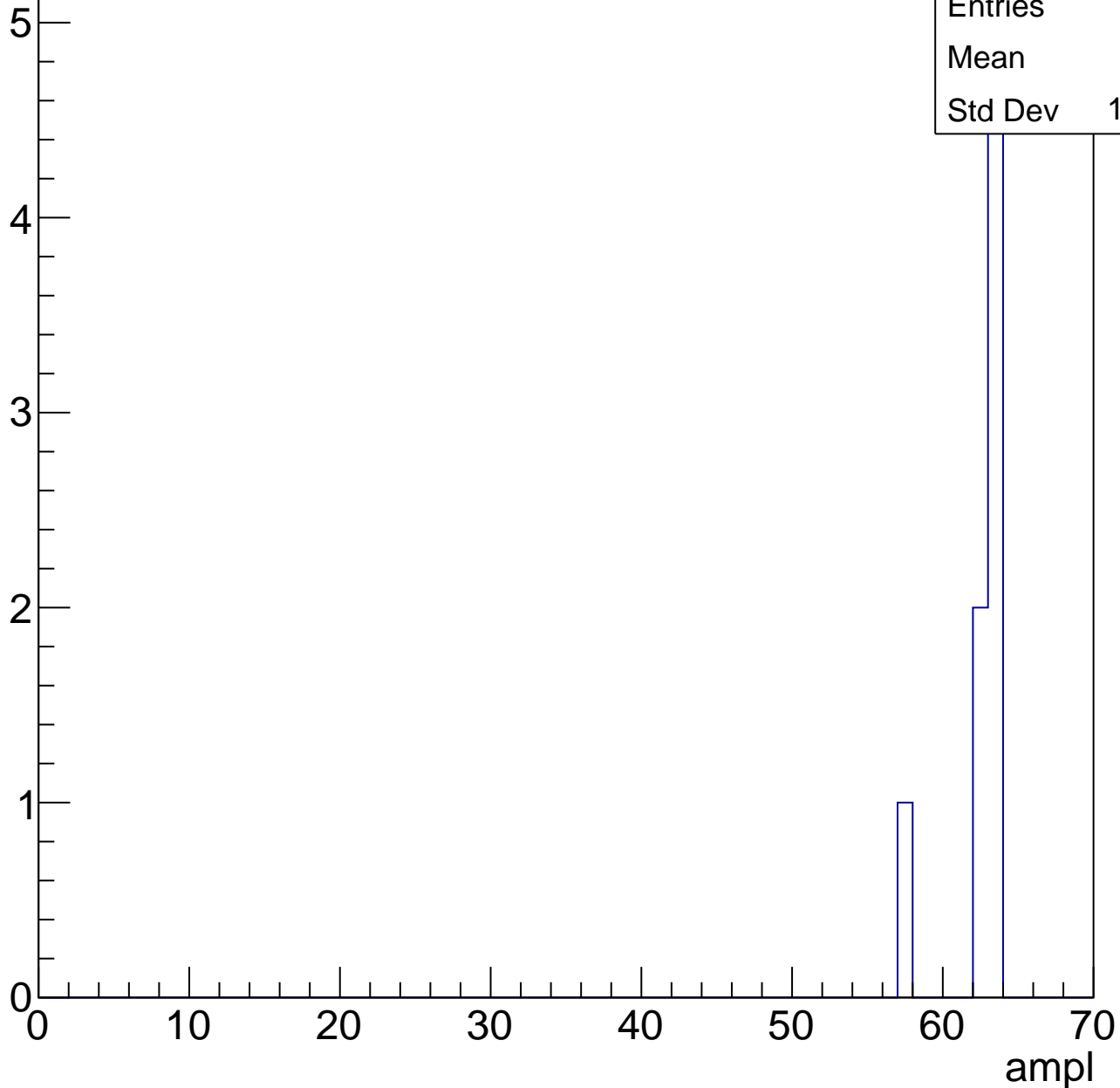


B1L102S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 62 |
| Std Dev | 1.936 |



B1L102S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch66, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 26.37 |
| Std Dev | 5.589 |

Gaus mean : 26.9514

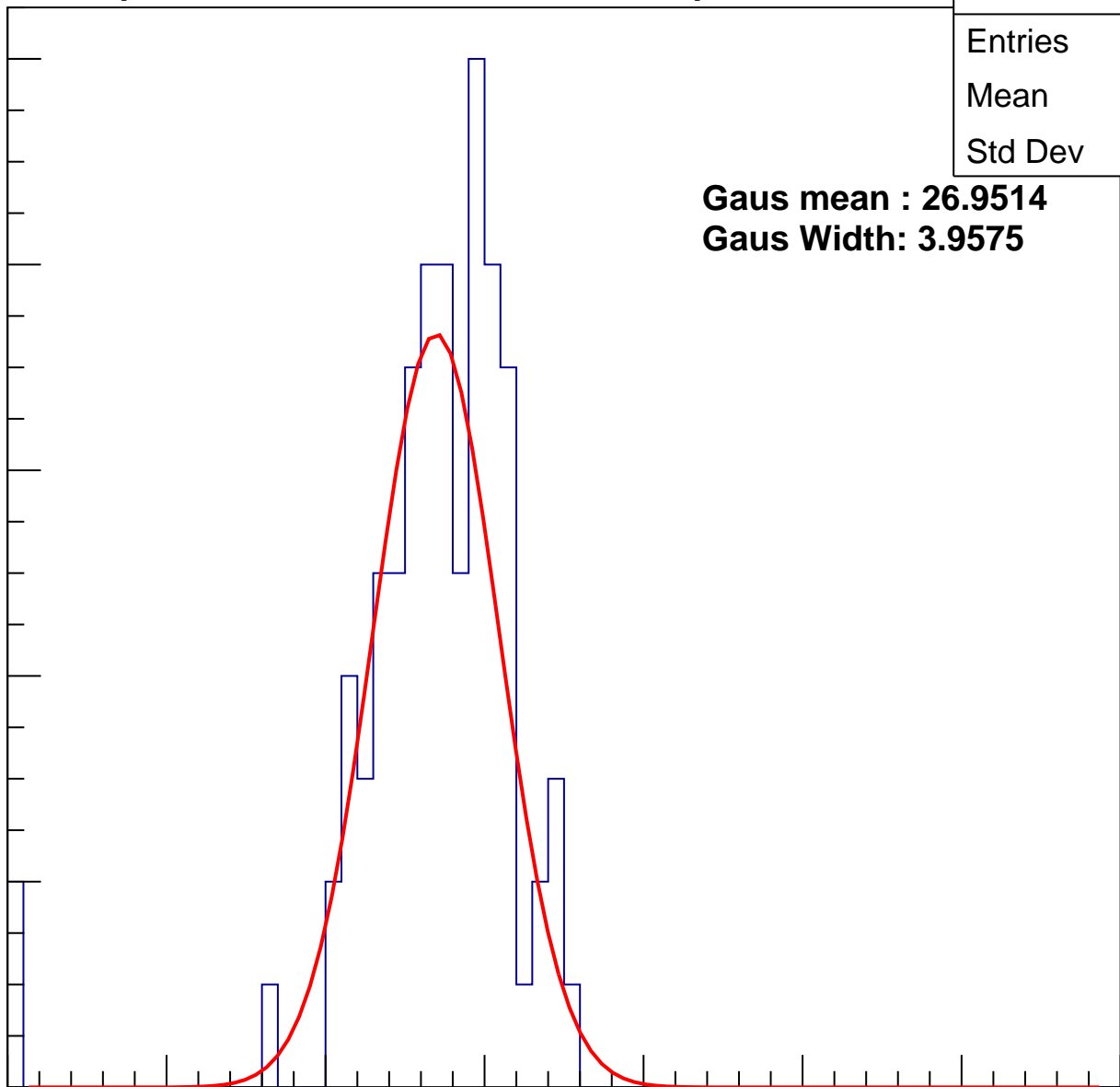
Gaus Width: 3.9575

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch66, adc1

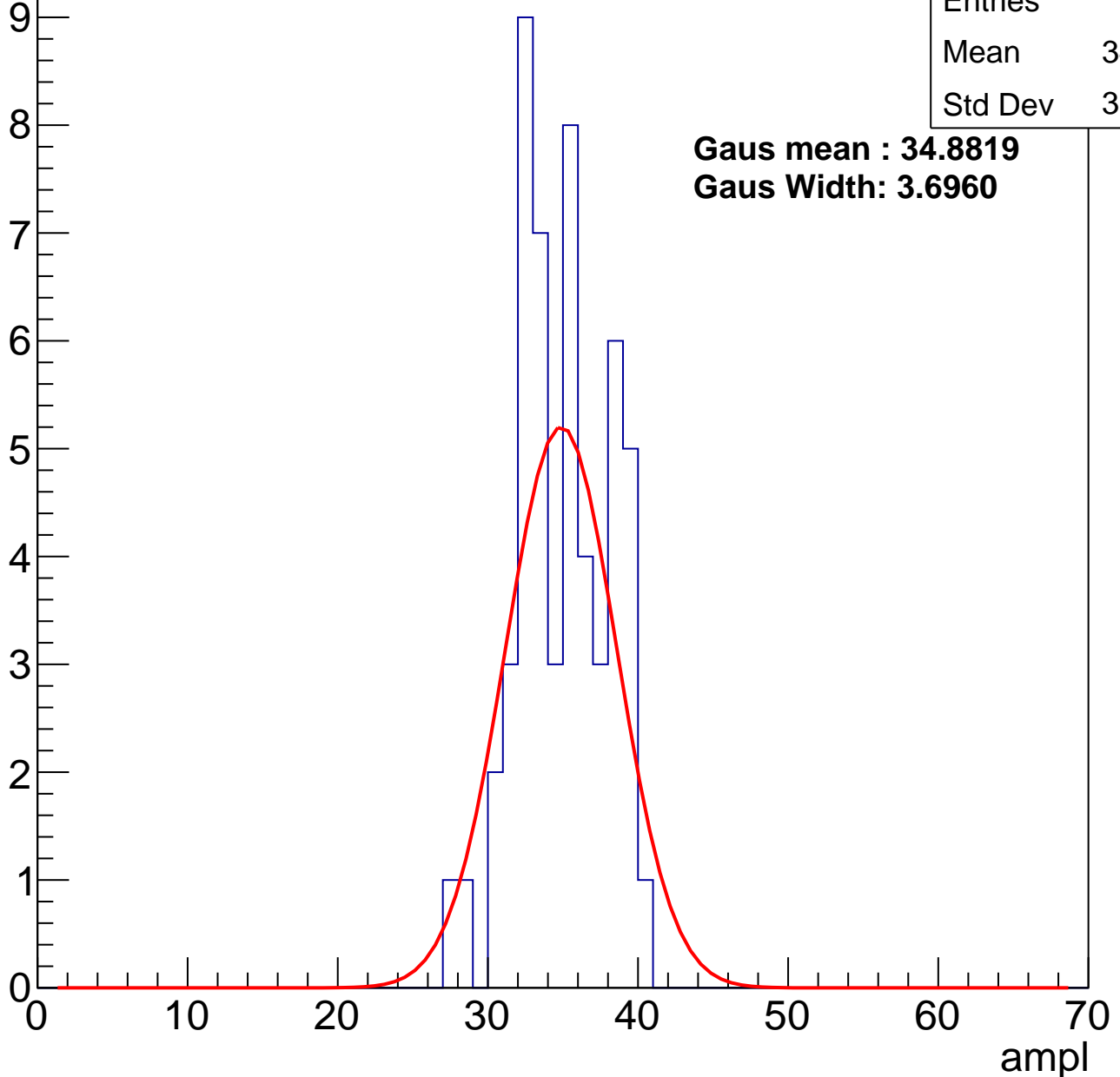
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 34.47 |
| Std Dev | 3.019 |

Gaus mean : 34.8819

Gaus Width: 3.6960



B1L102S, U15-ch66, adc2

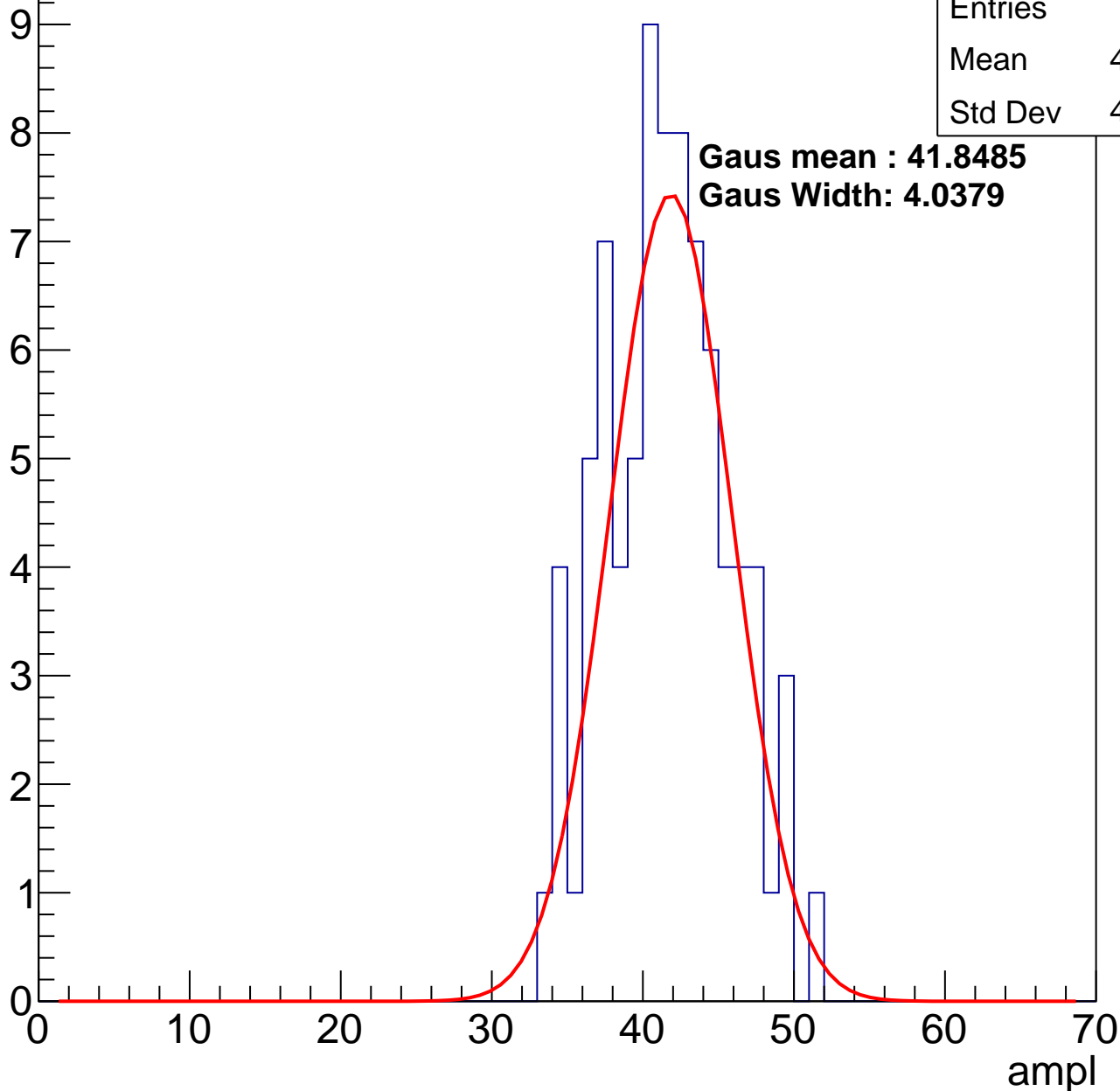
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 41.18 |
| Std Dev | 4.034 |

Gaus mean : 41.8485

Gaus Width: 4.0379

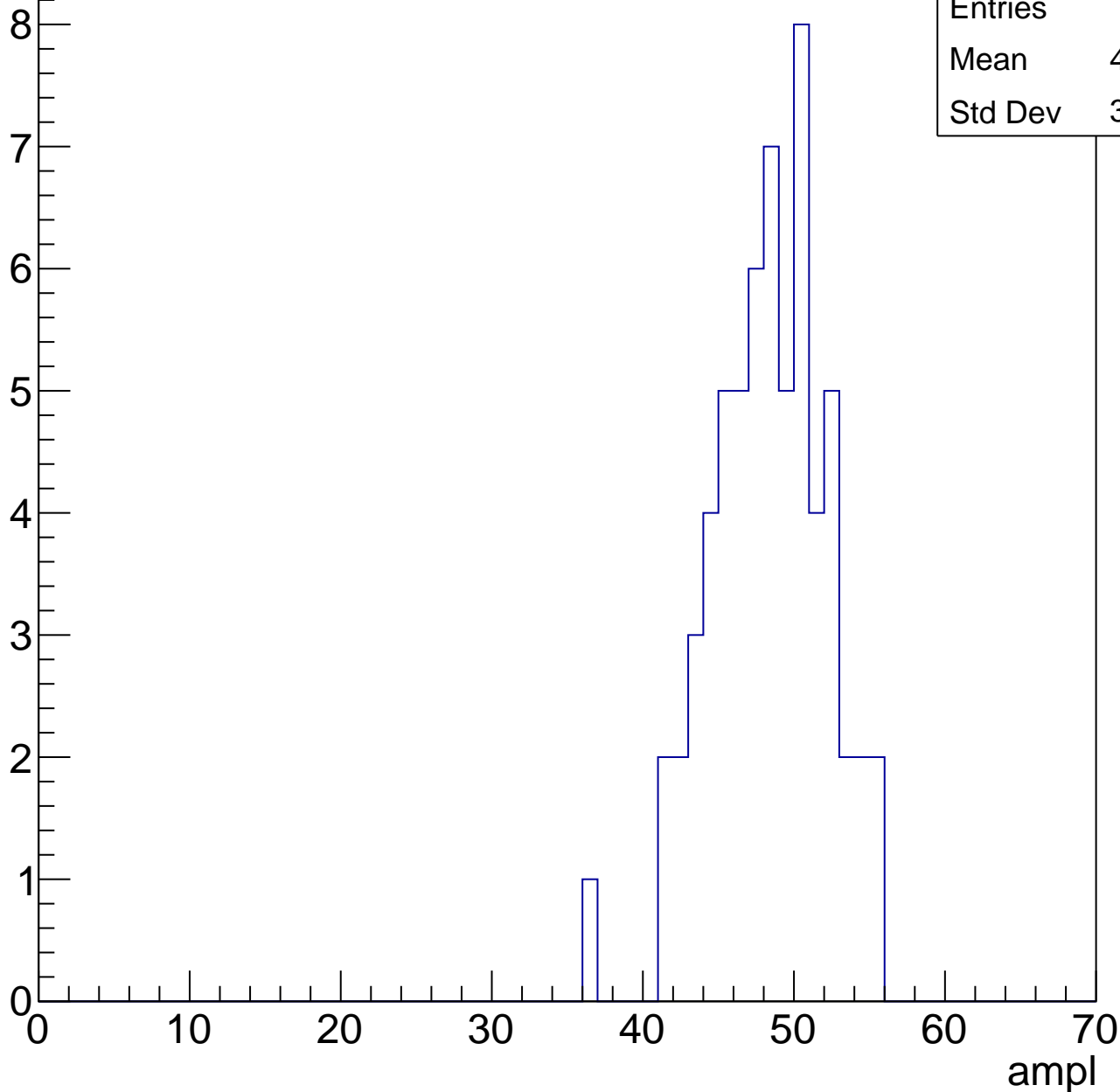


B1L102S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

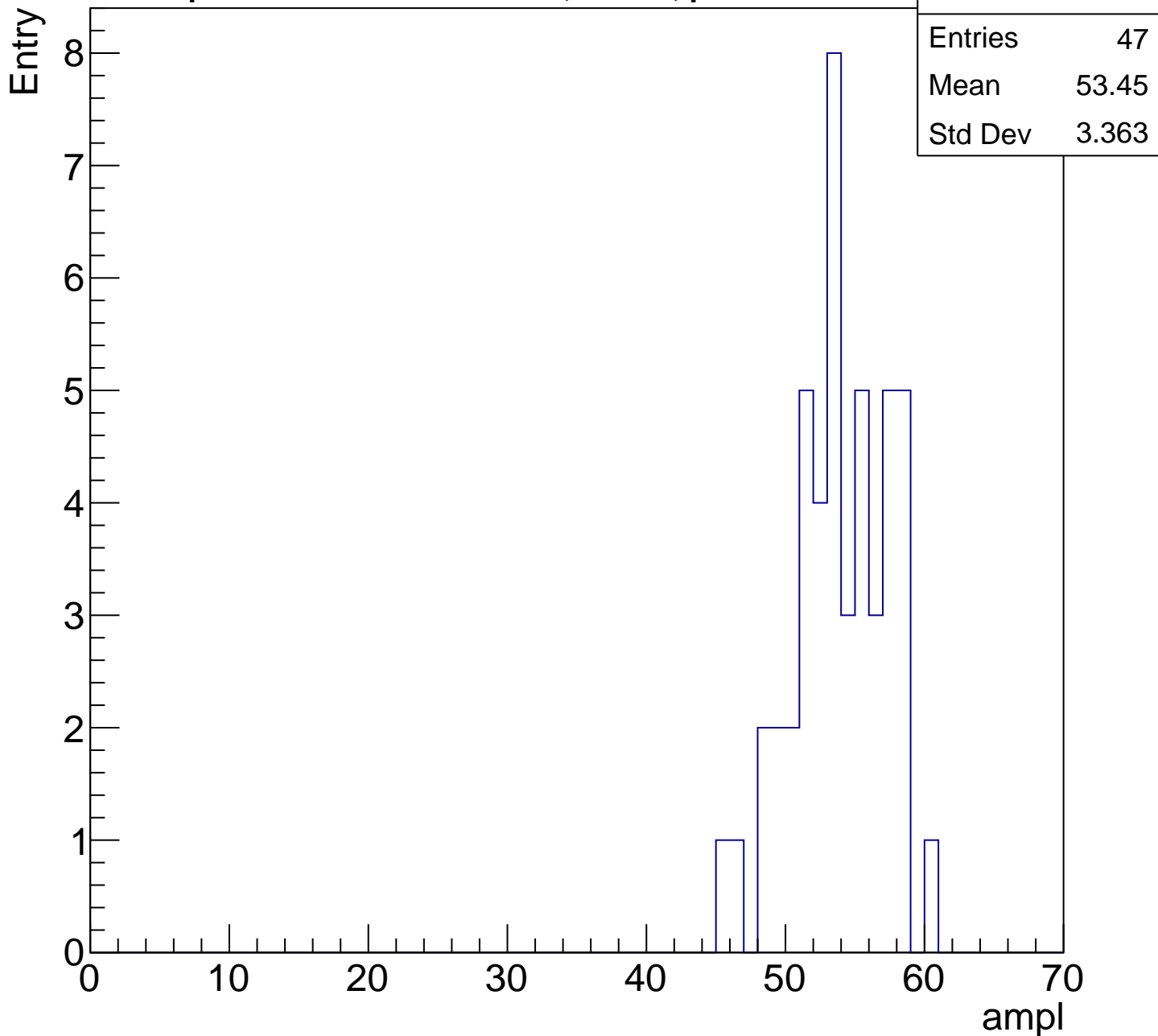
Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 47.83 |
| Std Dev | 3.769 |



B1L102S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

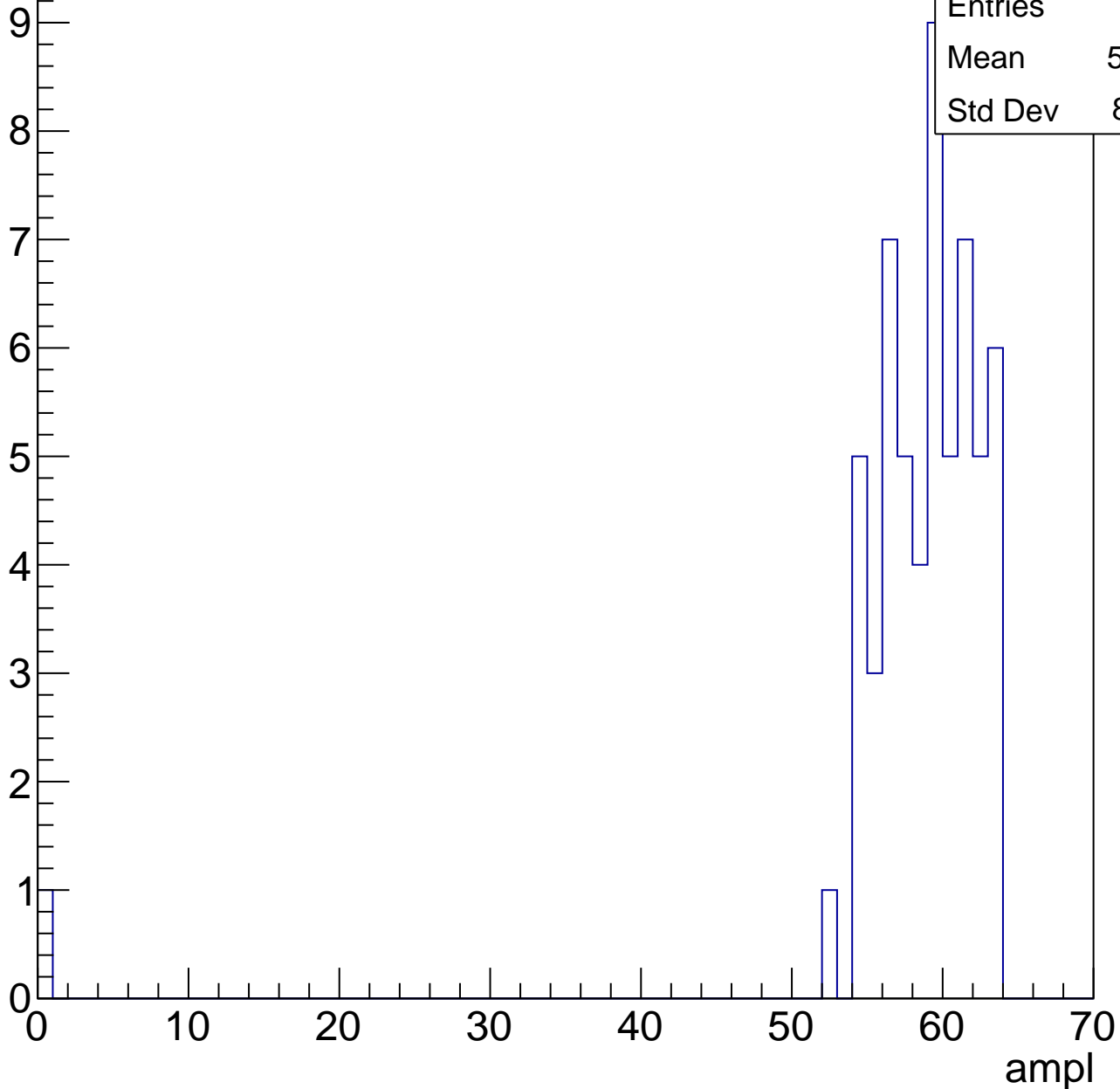


B1L102S, U15-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 57.62 |
| Std Dev | 8.151 |

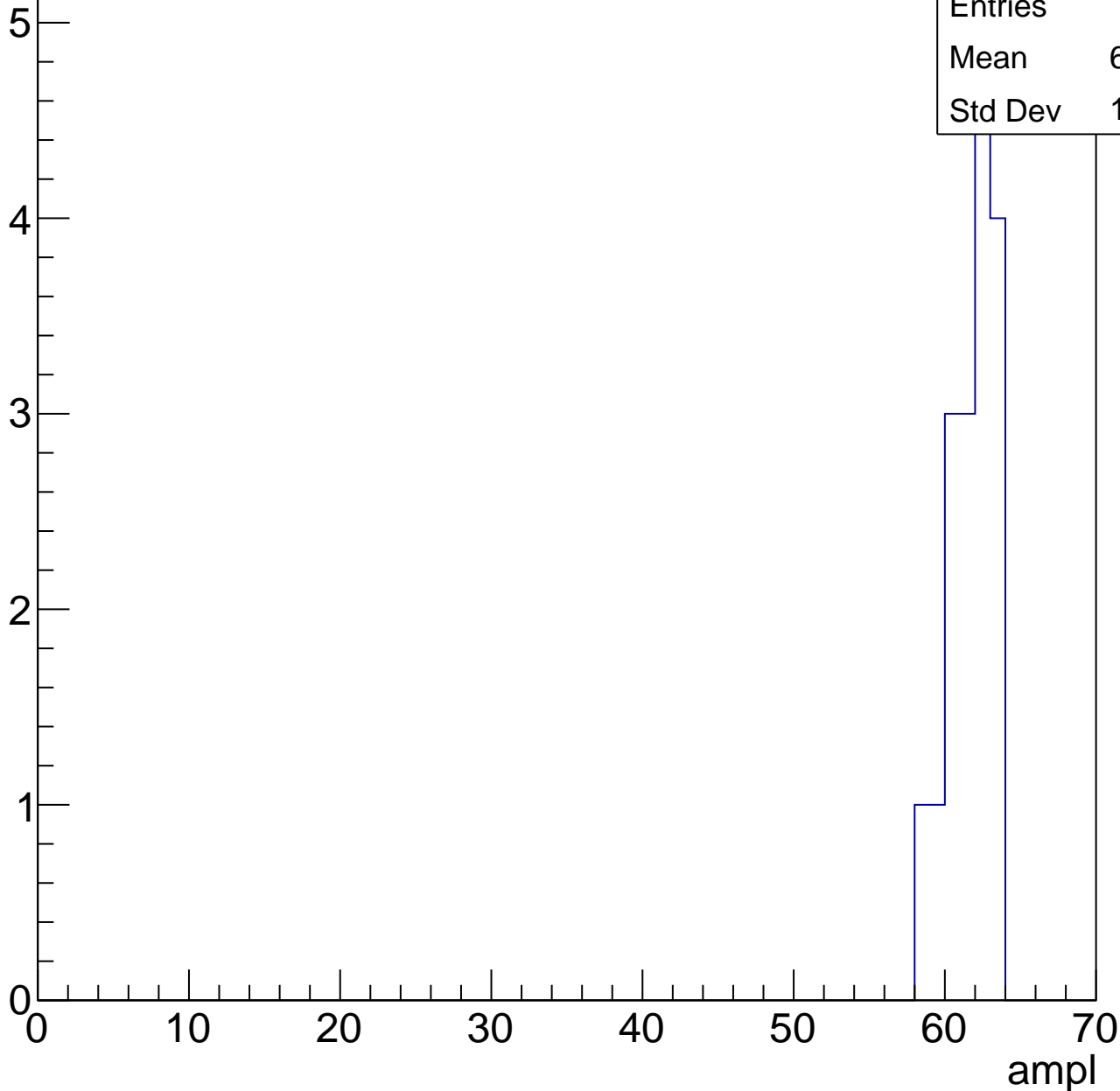


B1L102S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 61.29 |
| Std Dev | 1.446 |



B1L102S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch67, adc0

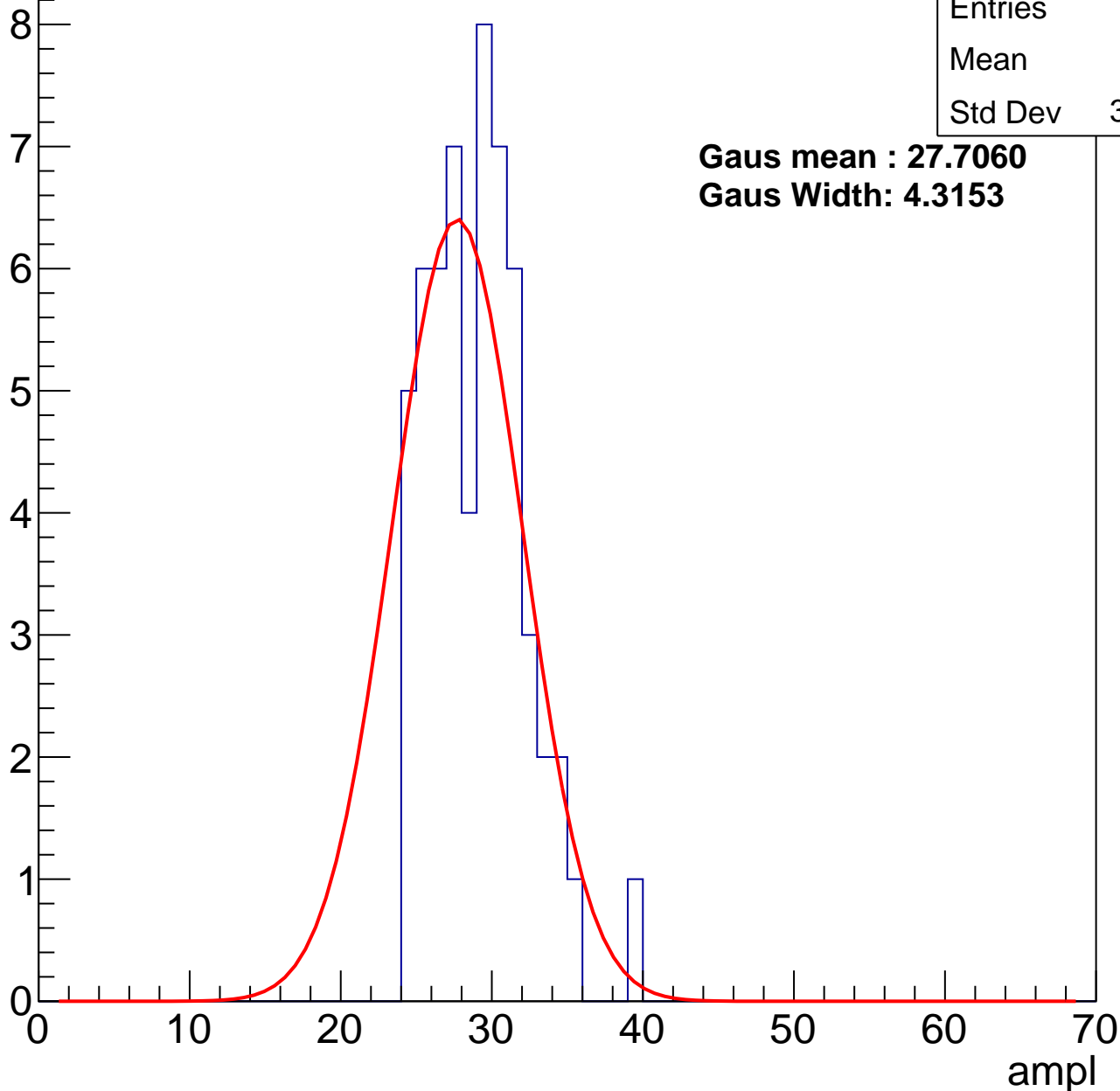
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 28.6 |
| Std Dev | 3.146 |

Gaus mean : 27.7060

Gaus Width: 4.3153



B1L102S, U15-ch67, adc1

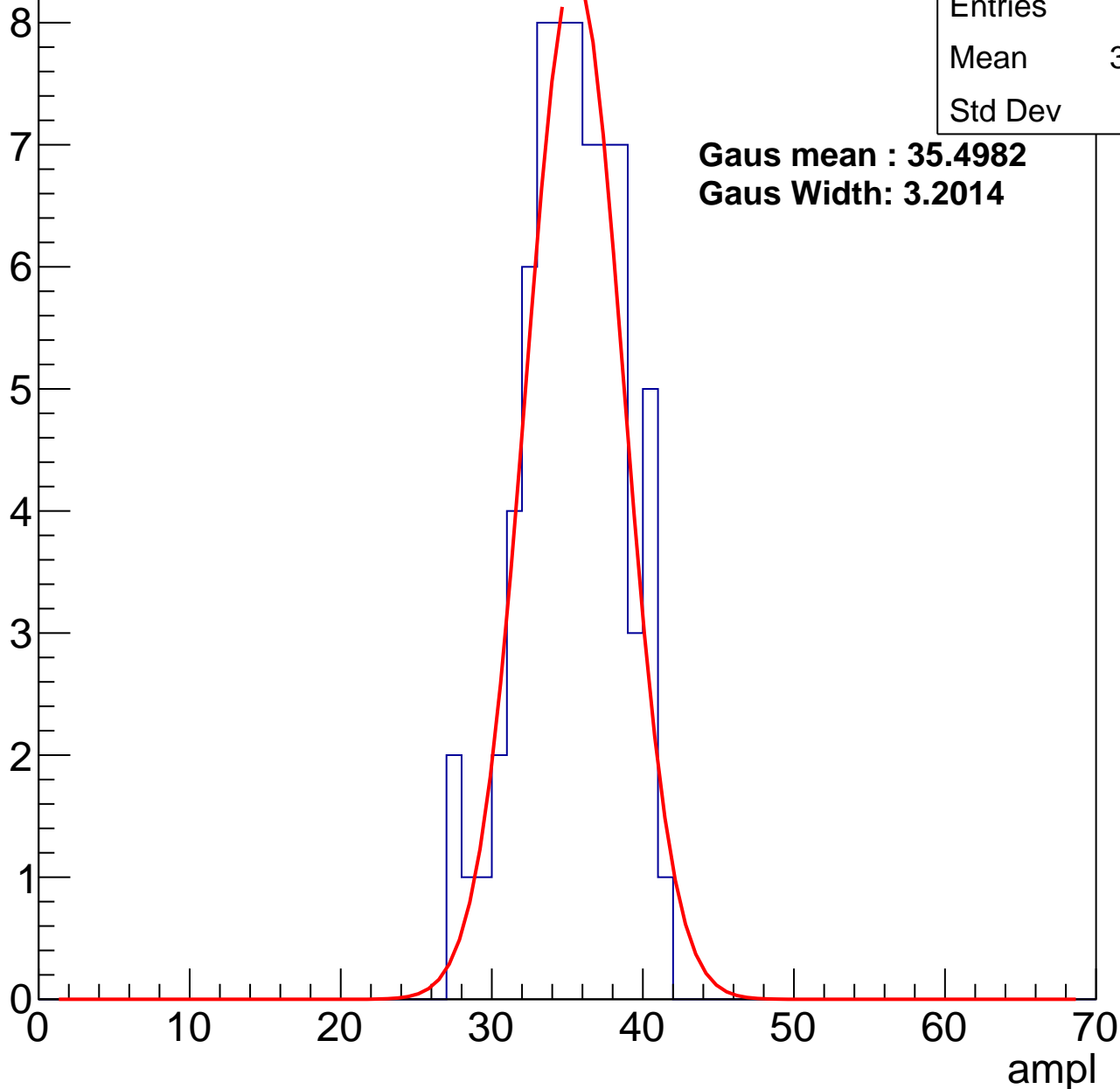
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 34.83 |
| Std Dev | 3.22 |

Gaus mean : 35.4982

Gaus Width: 3.2014



B1L102S, U15-ch67, adc2

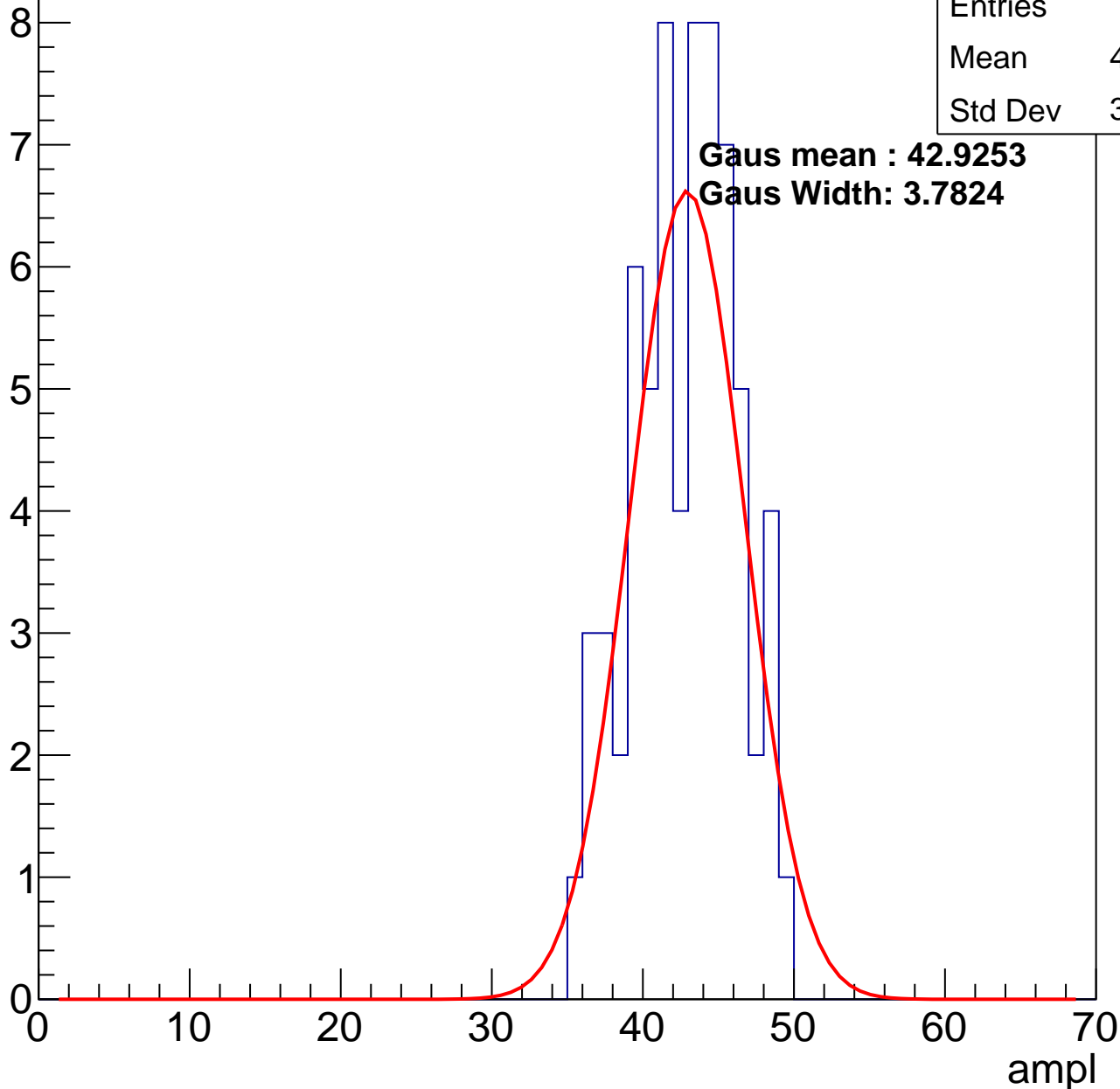
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 42.33 |
| Std Dev | 3.379 |

Gaus mean : 42.9253

Gaus Width: 3.7824



B1L102S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

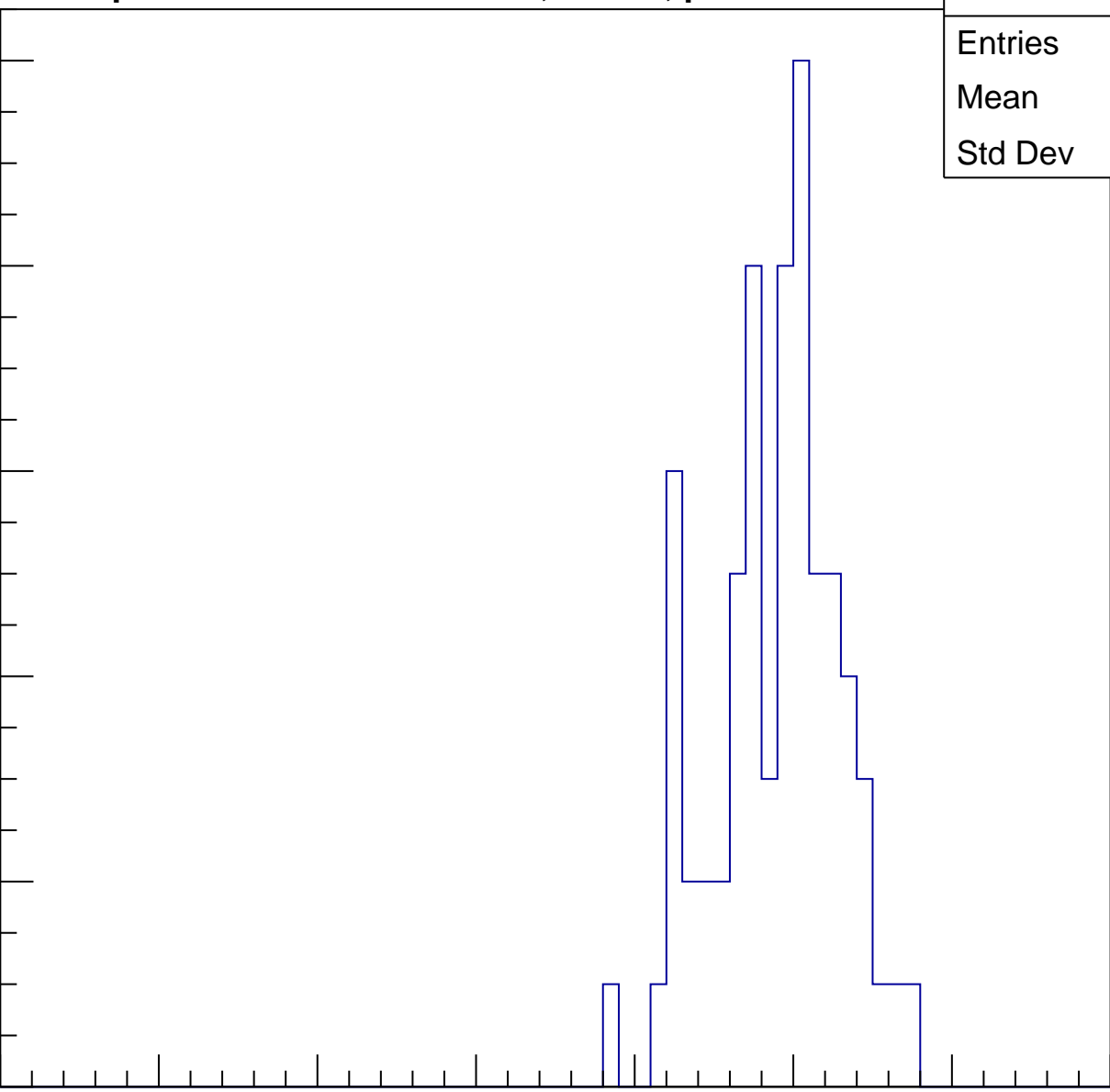
| | |
|---------|-------|
| Entries | 68 |
| Mean | 48.44 |
| Std Dev | 3.938 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

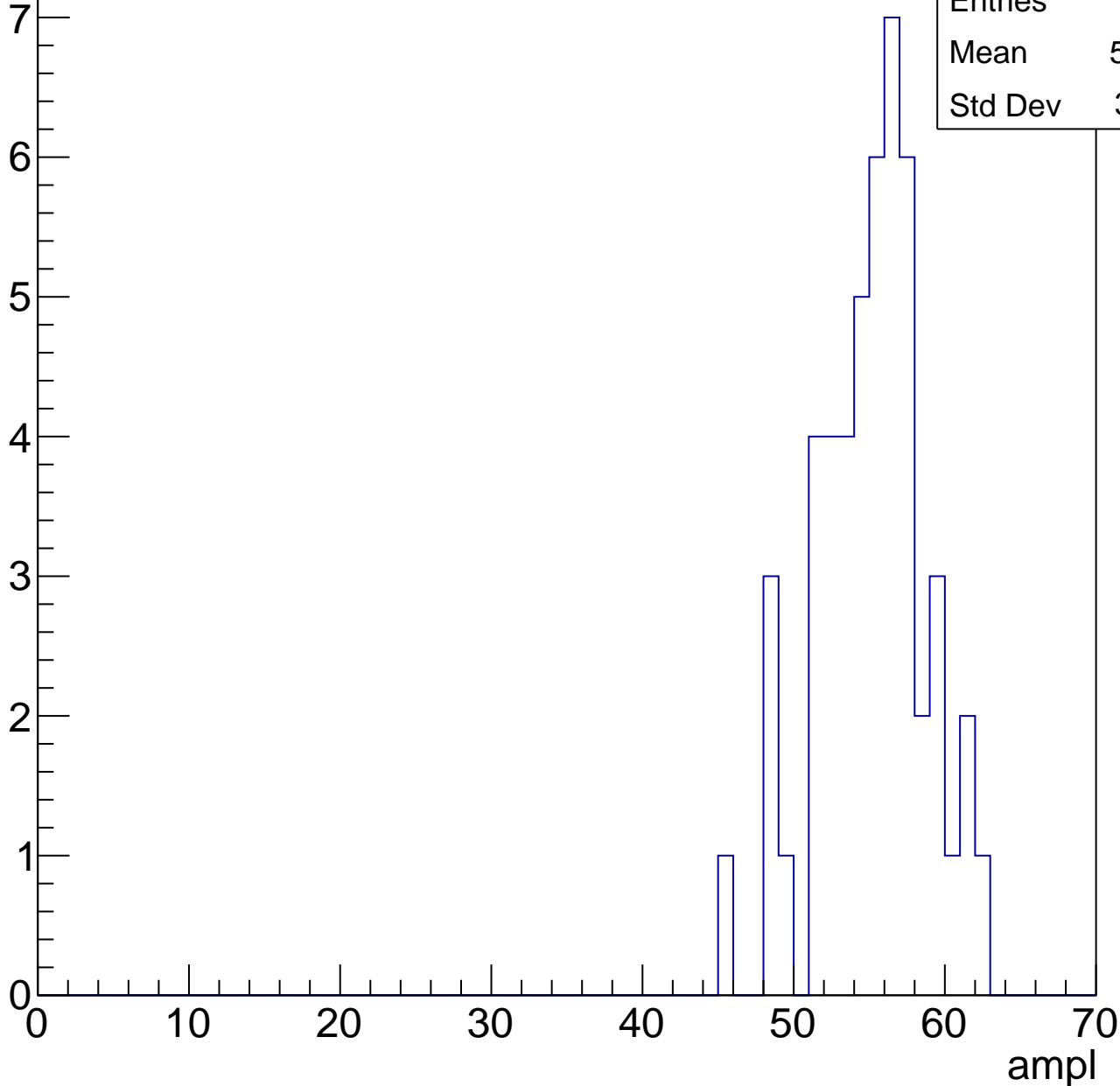


B1L102S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 54.66 |
| Std Dev | 3.581 |



B1L102S, U15-ch67, adc5

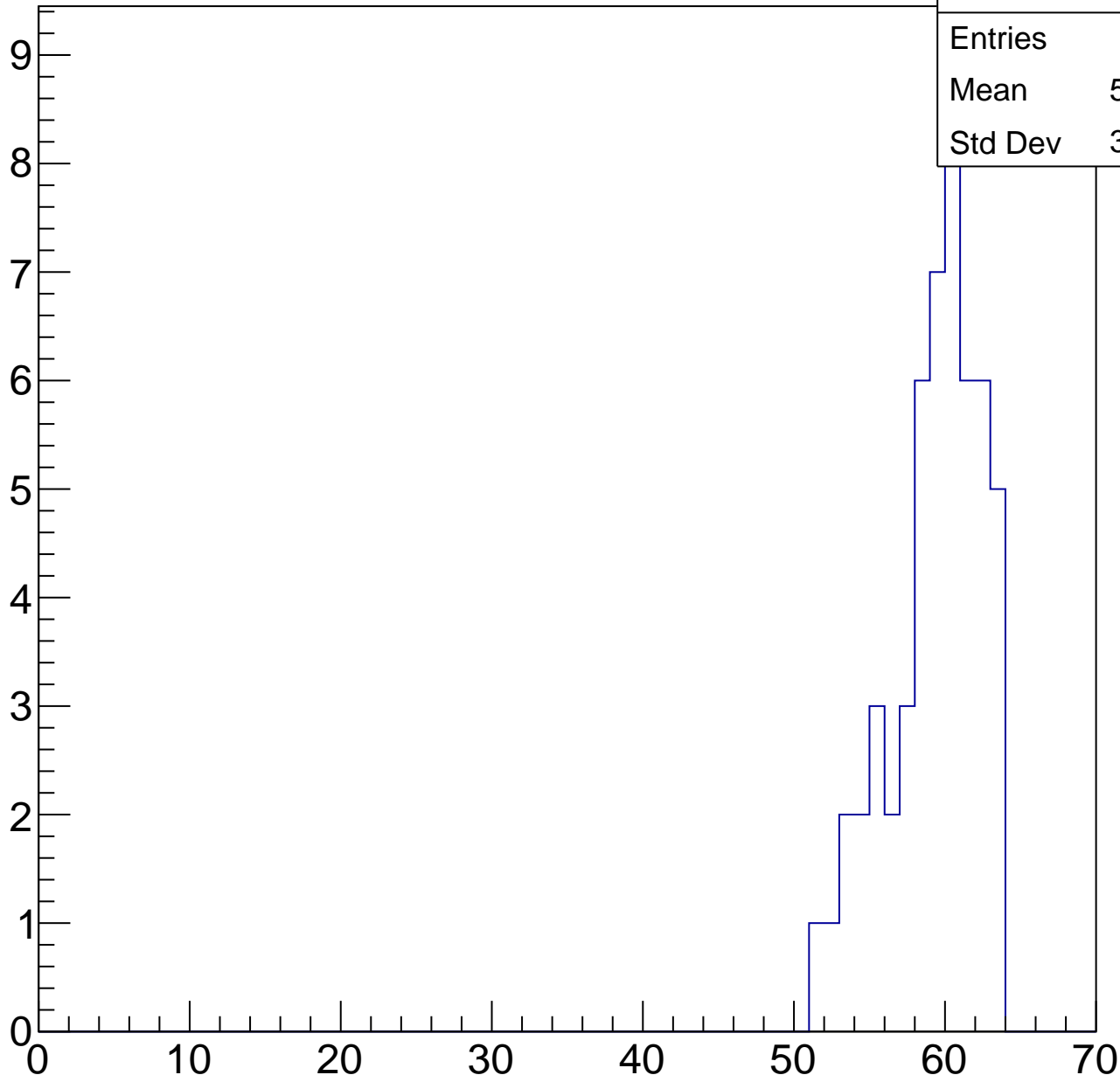
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.85 |
| Std Dev | 3.024 |

ampl

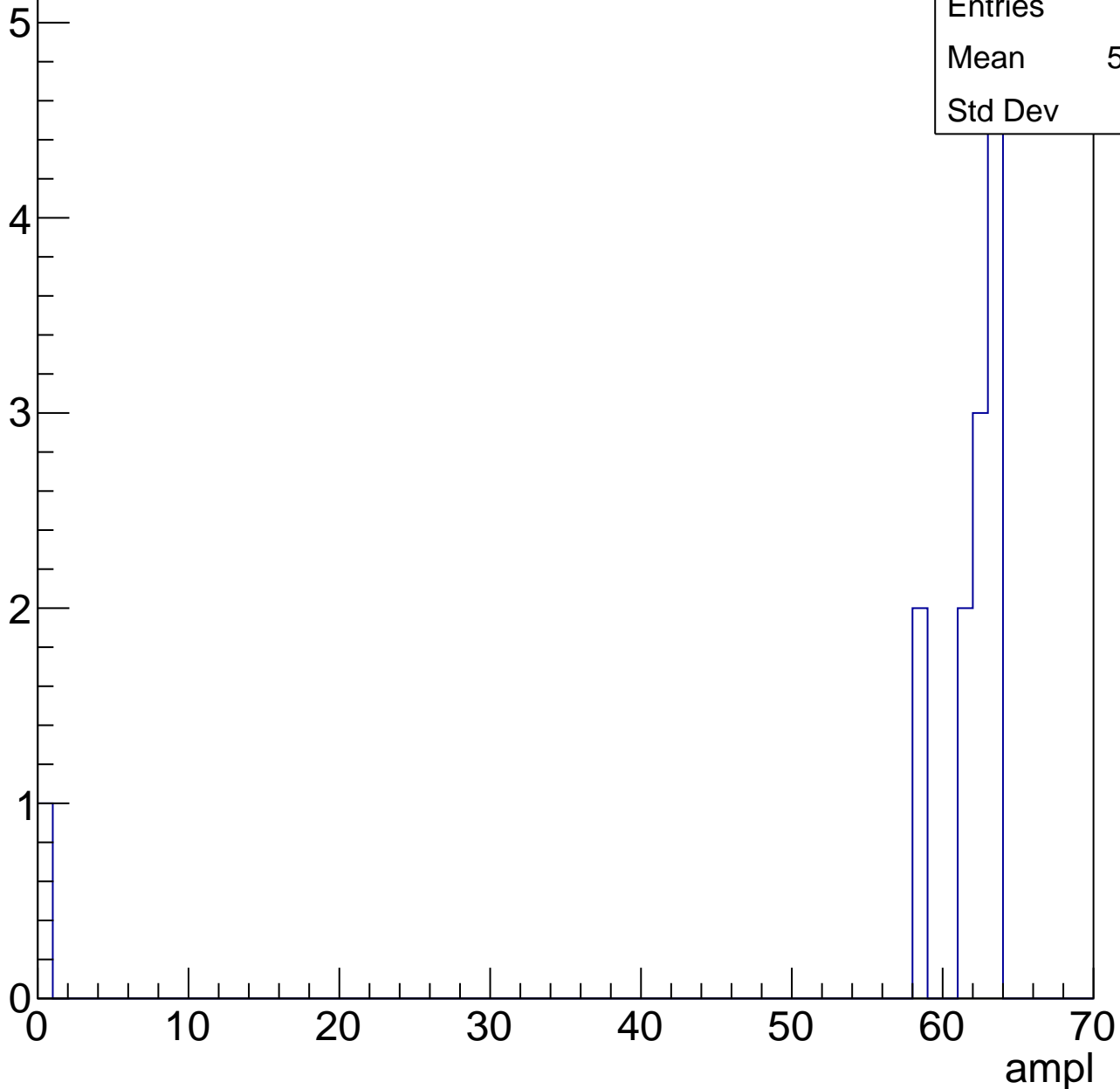


B1L102S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 56.85 |
| Std Dev | 16.5 |



B1L102S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch68, adc0

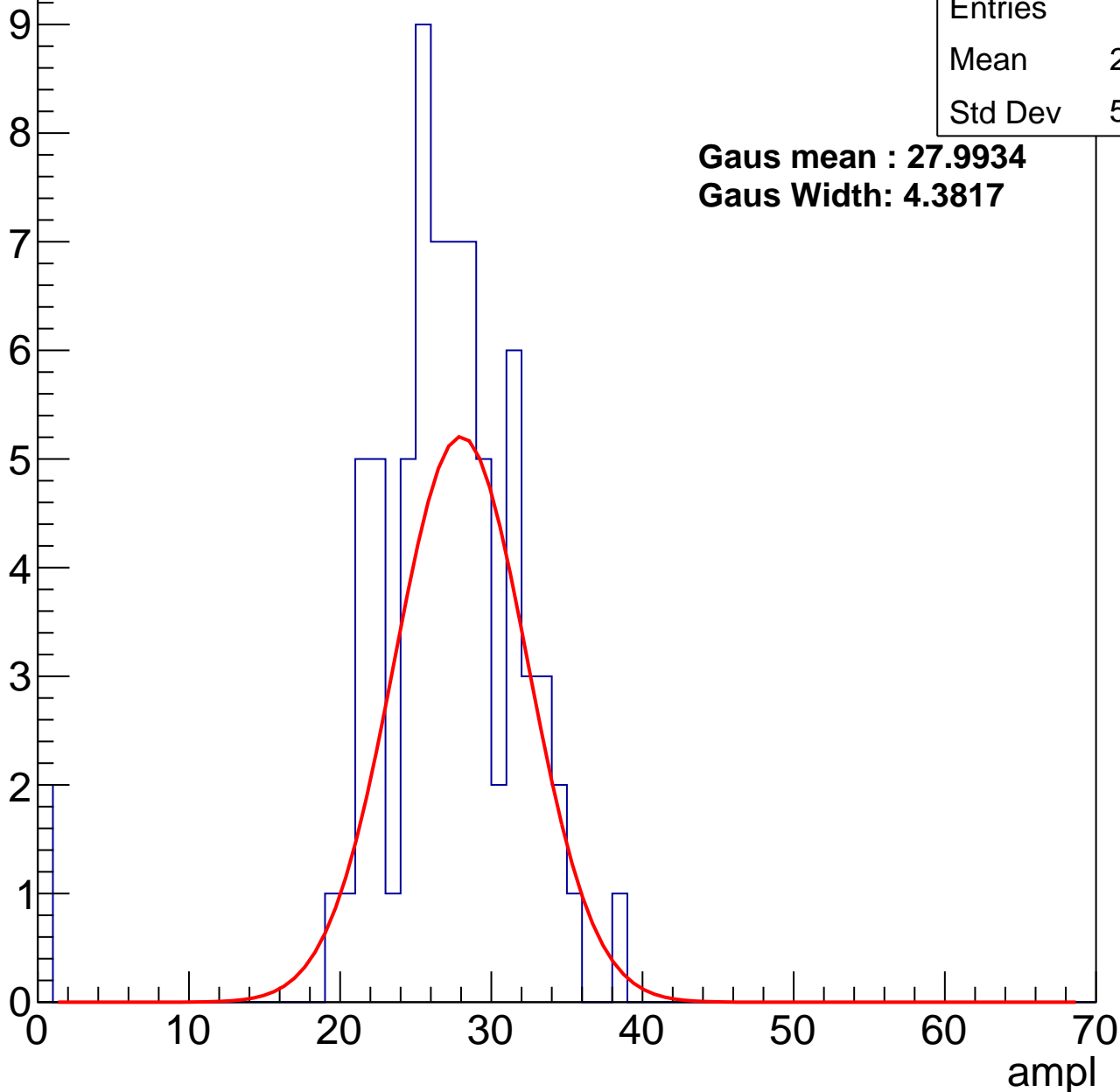
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 26.25 |
| Std Dev | 5.905 |

Gaus mean : 27.9934

Gaus Width: 4.3817



B1L102S, U15-ch68, adc1

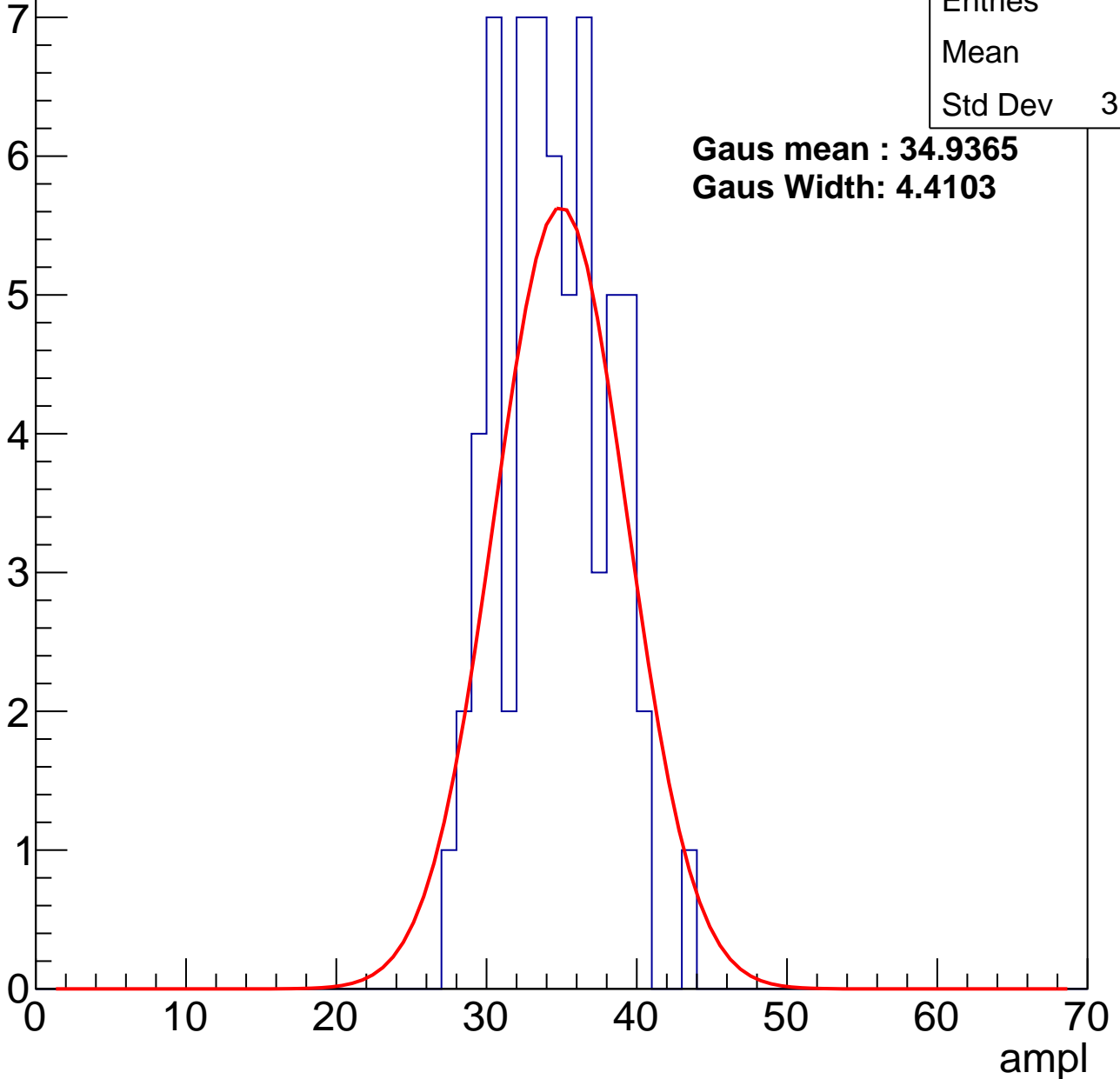
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 34 |
| Std Dev | 3.544 |

Gaus mean : 34.9365

Gaus Width: 4.4103



B1L102S, U15-ch68, adc2

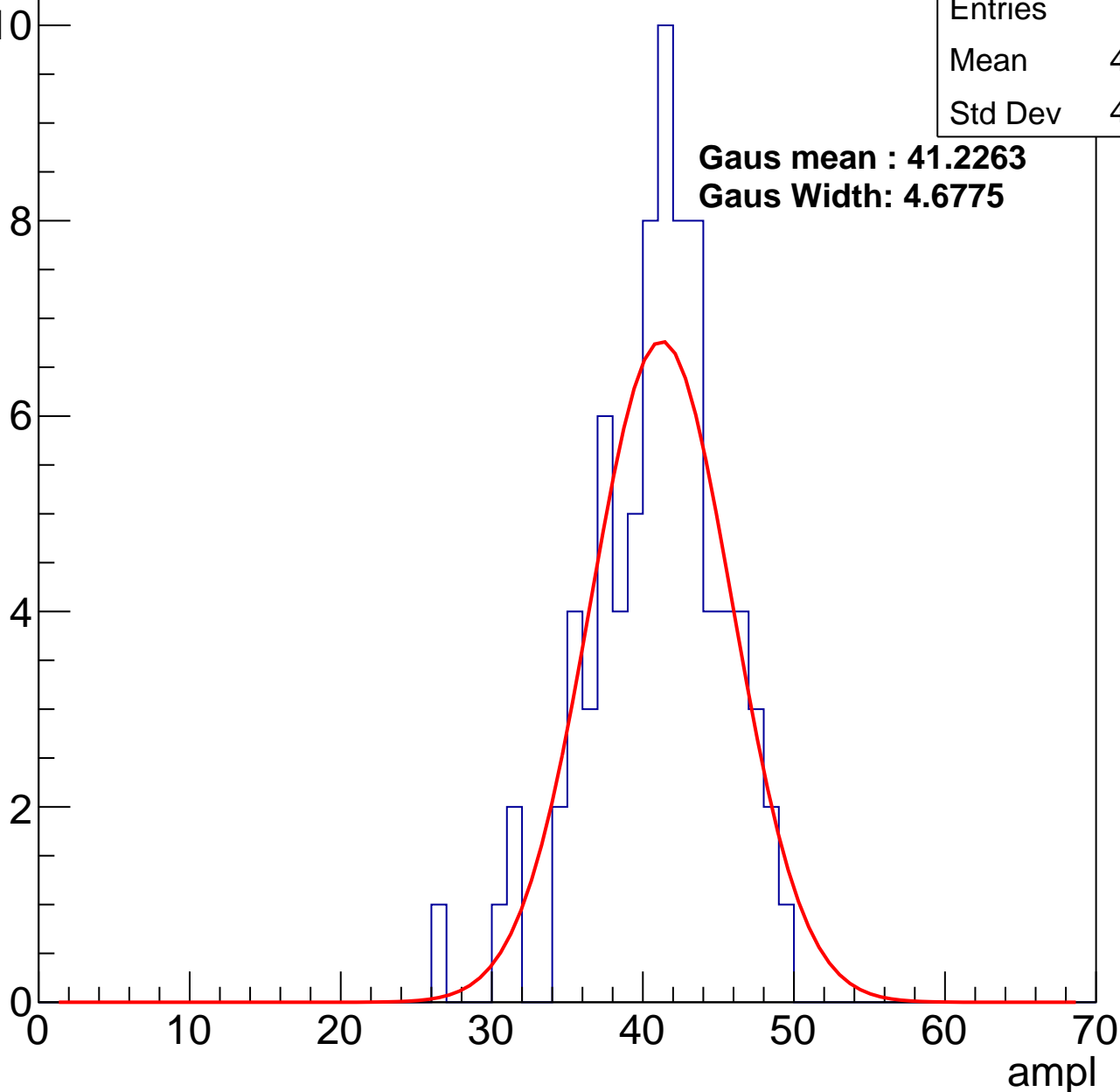
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 40.49 |
| Std Dev | 4.347 |

Gaus mean : 41.2263

Gaus Width: 4.6775

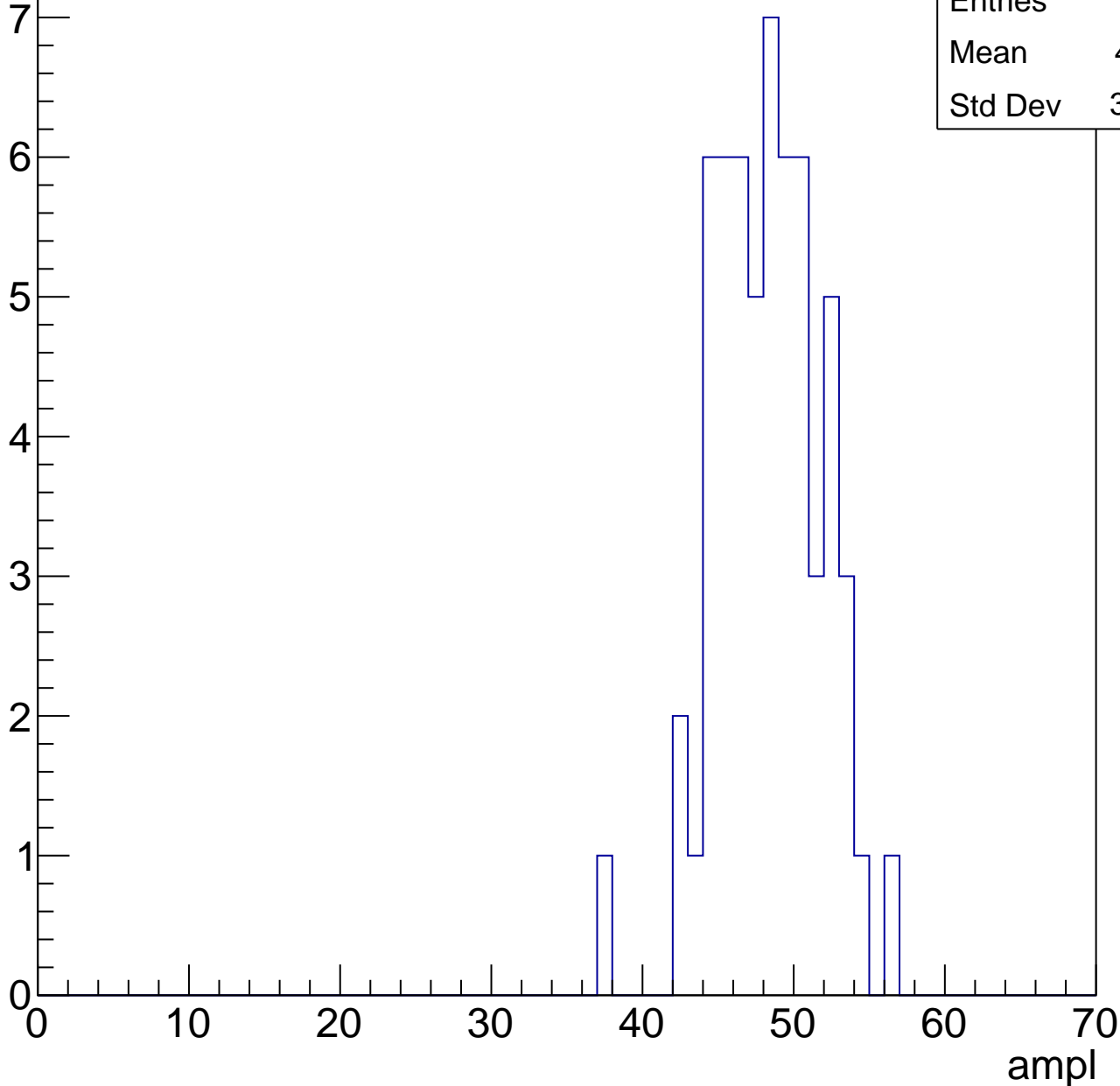


B1L102S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 47.81 |
| Std Dev | 3.462 |

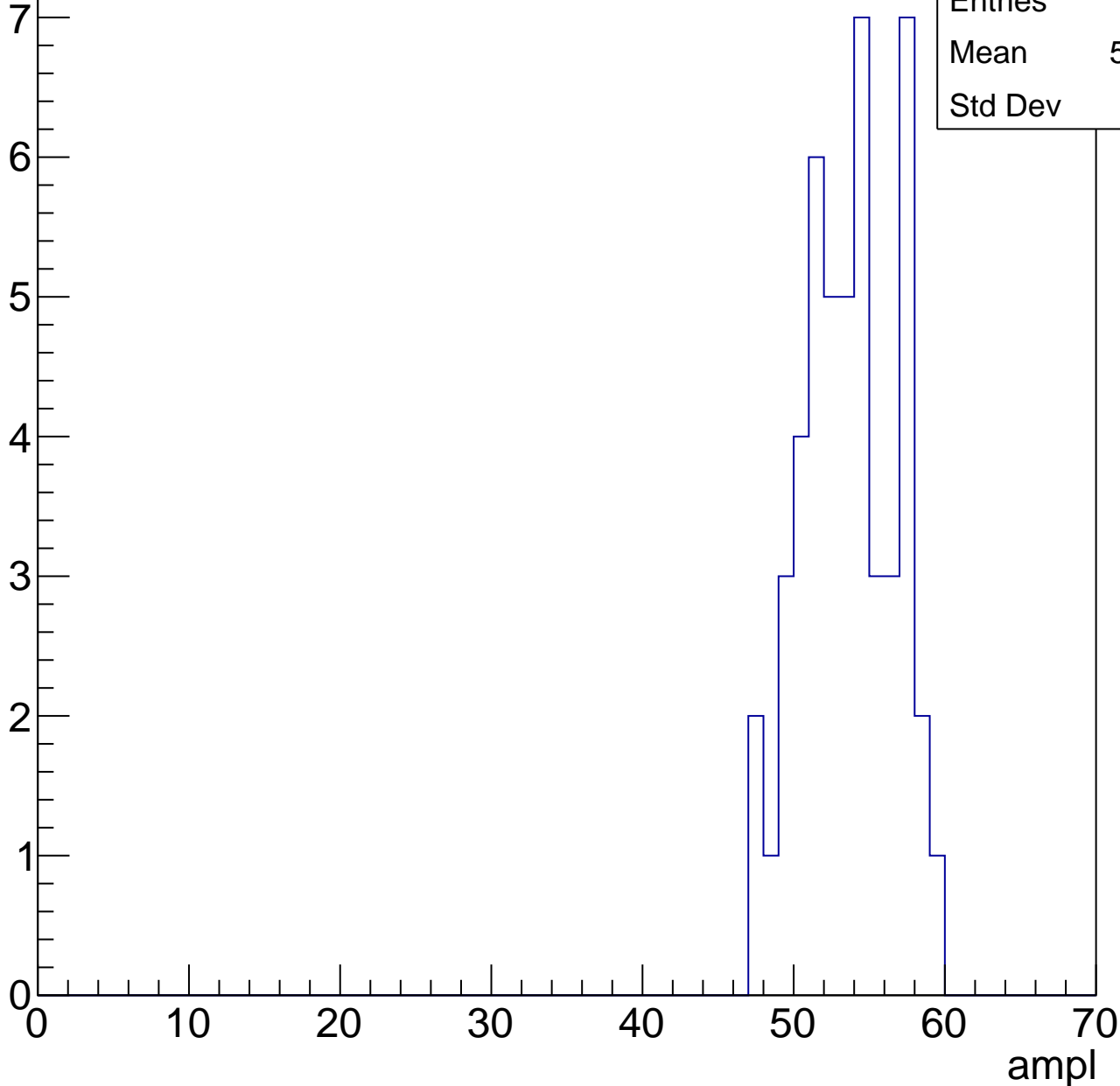


B1L102S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

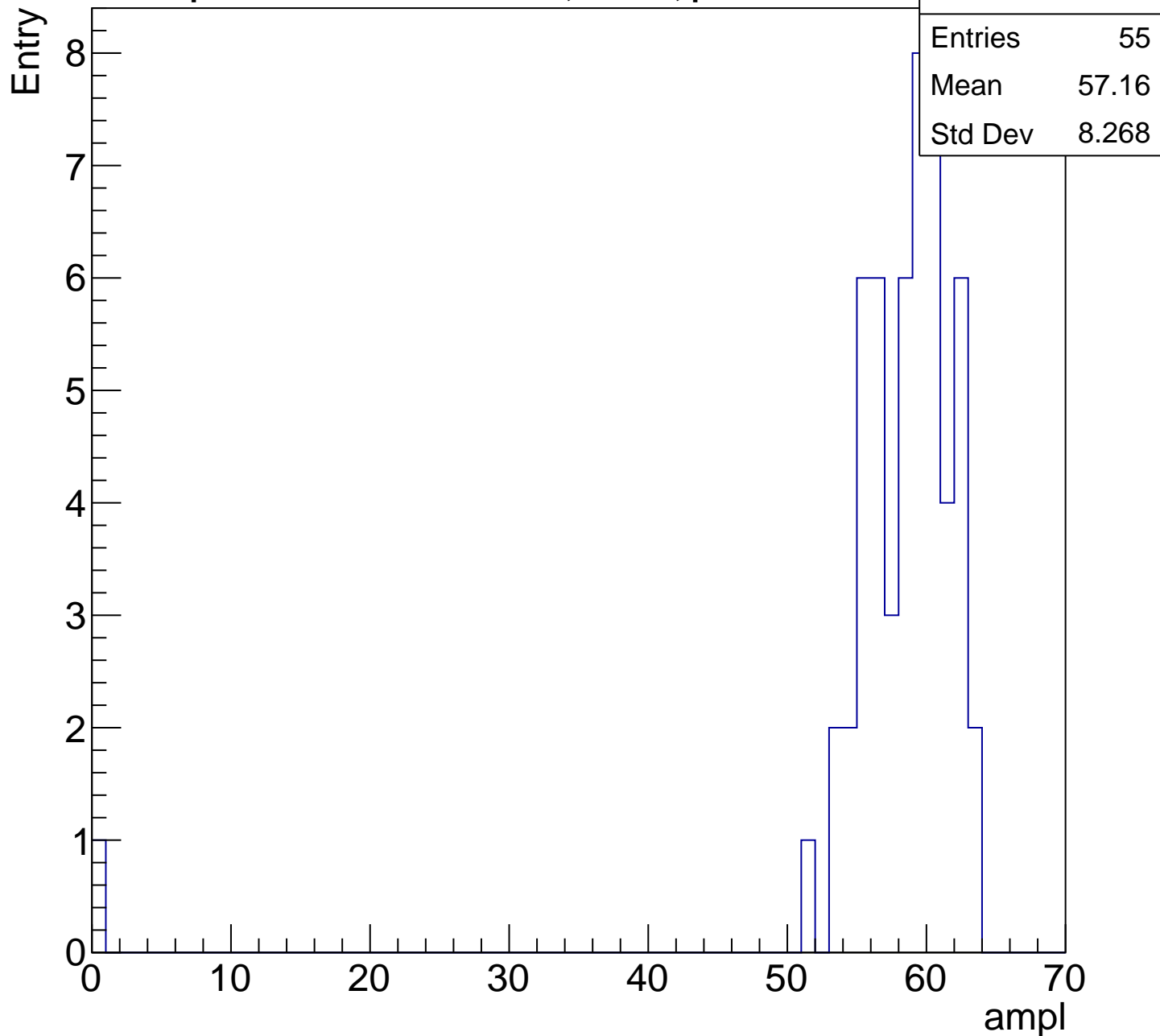
Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 53.16 |
| Std Dev | 3.04 |



B1L102S, U15-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

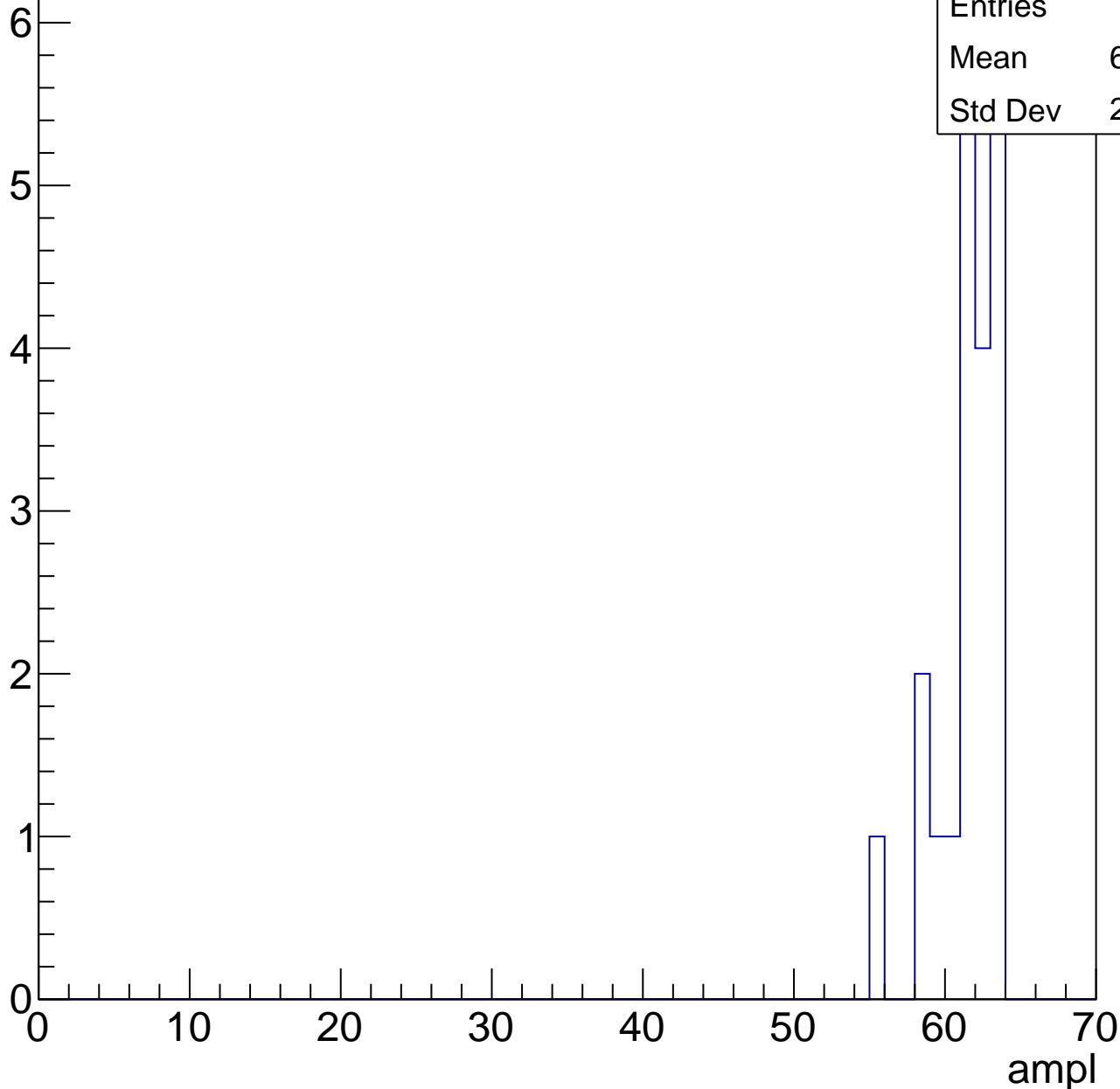


B1L102S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

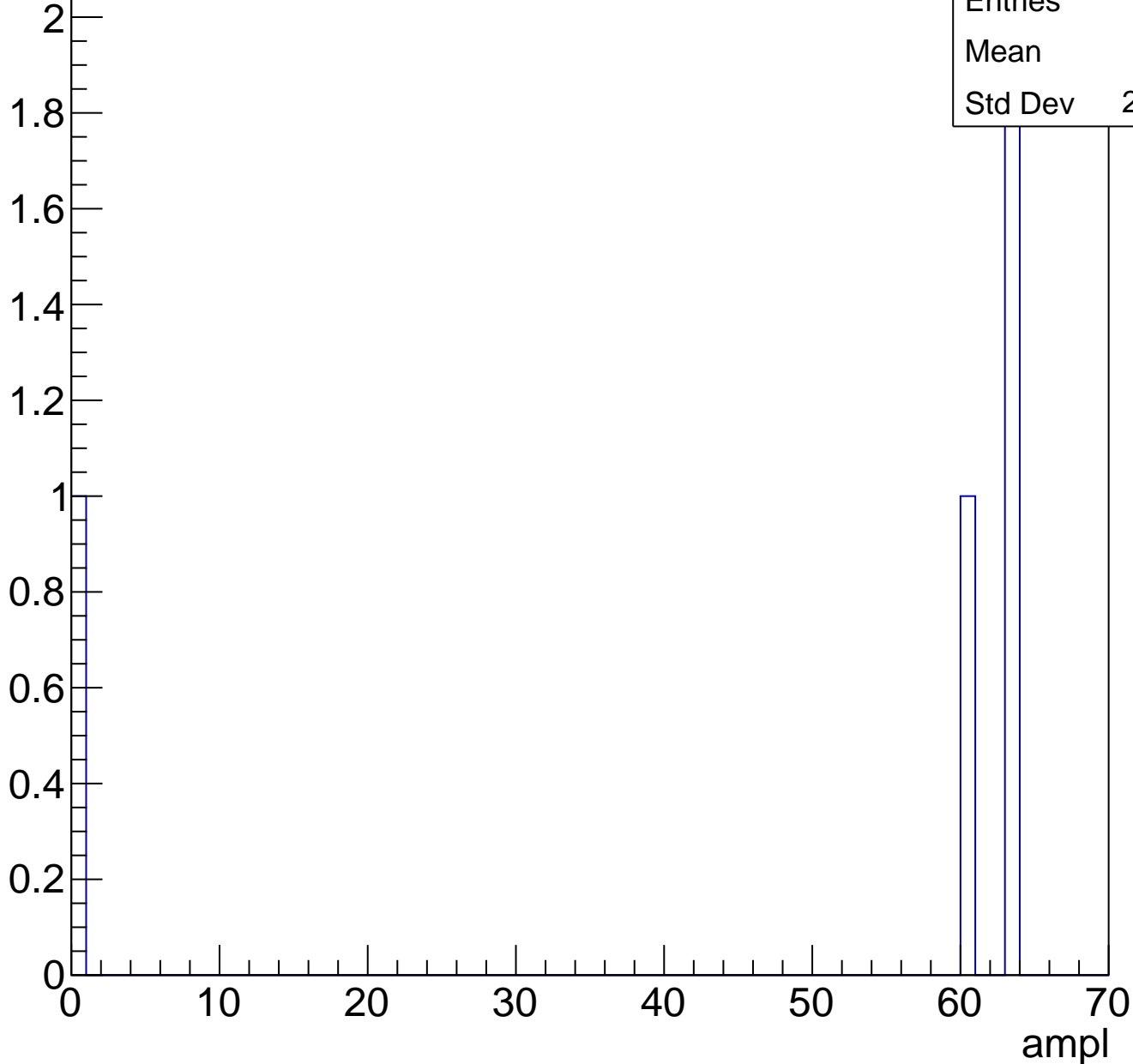
| | |
|---------|-------|
| Entries | 21 |
| Mean | 61.05 |
| Std Dev | 2.035 |



B1L102S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch69, adc0

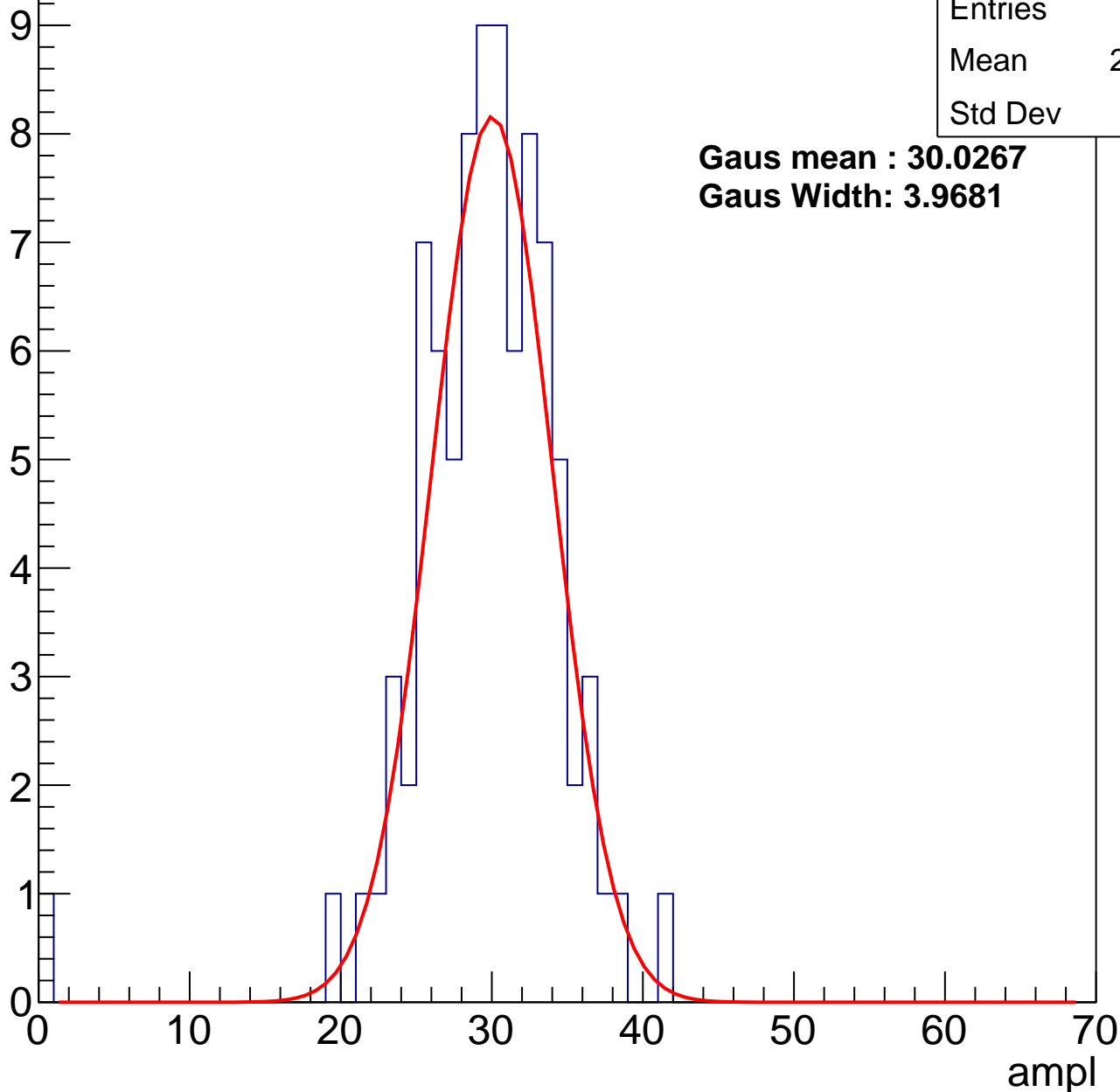
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 87 |
| Mean | 29.16 |
| Std Dev | 5.09 |

Gaus mean : 30.0267

Gaus Width: 3.9681



B1L102S, U15-ch69, adc1

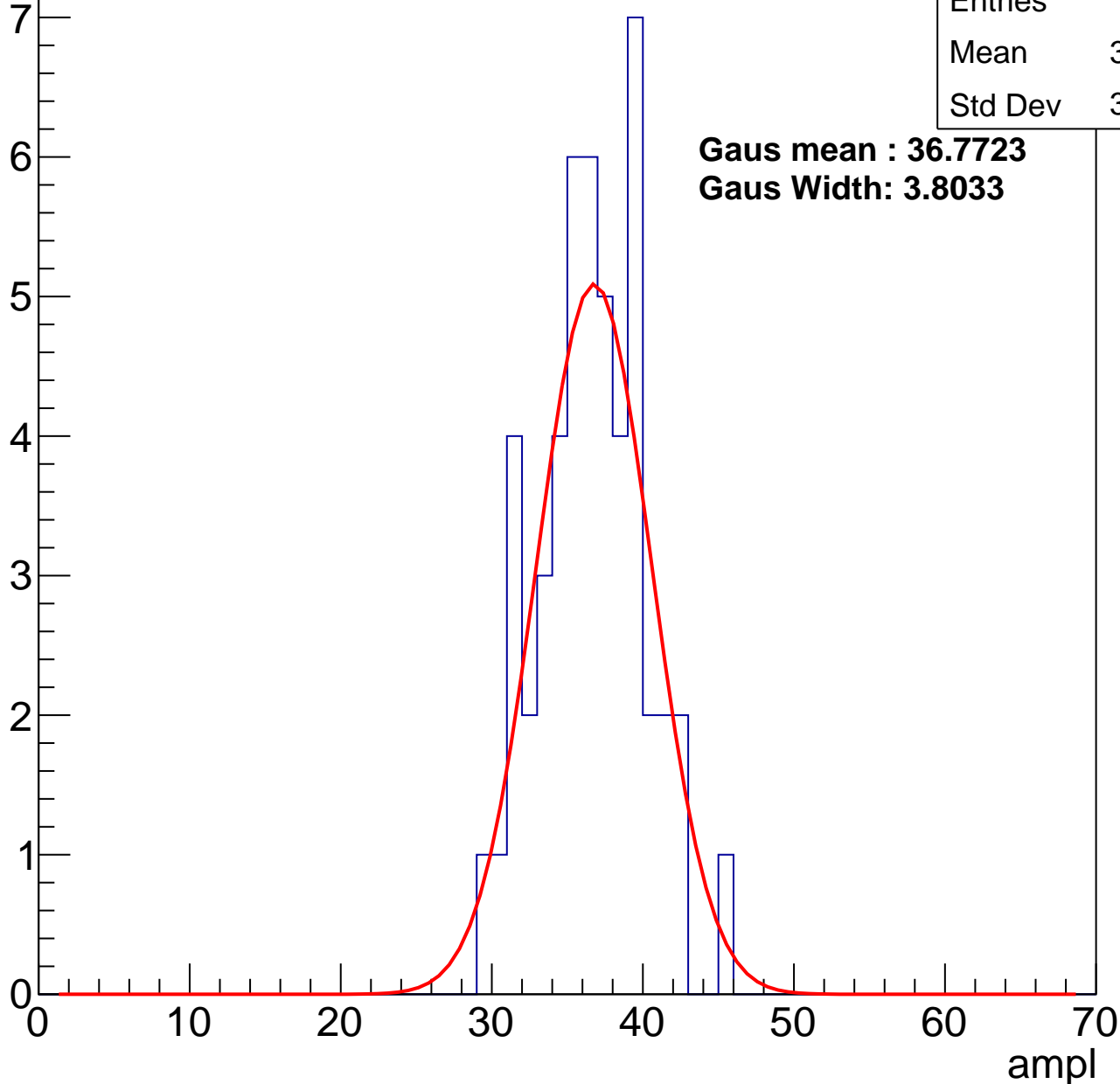
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 36.18 |
| Std Dev | 3.416 |

Gaus mean : 36.7723

Gaus Width: 3.8033



B1L102S, U15-ch69, adc2

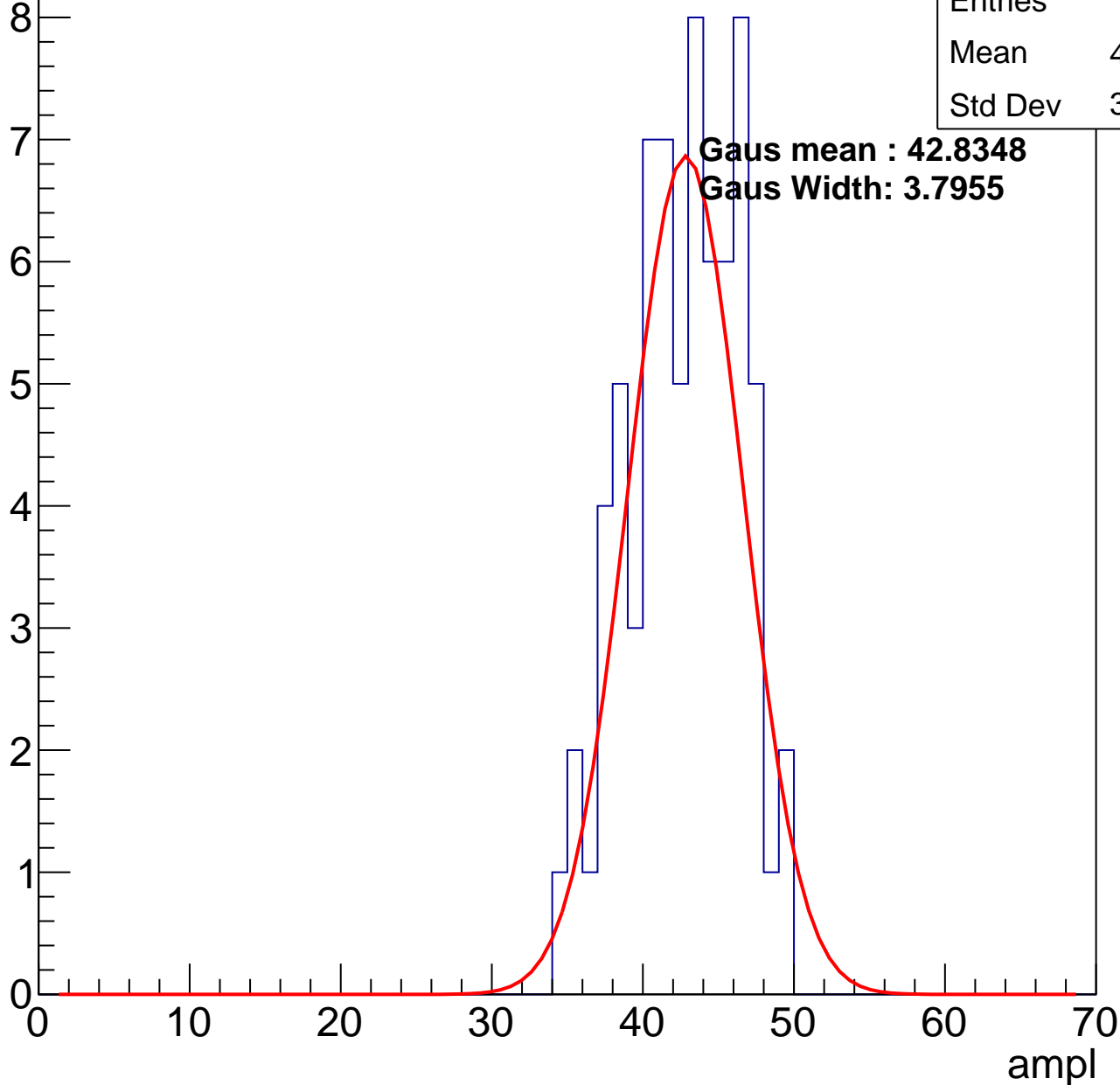
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.24 |
| Std Dev | 3.578 |

Gaus mean : 42.8348

Gaus Width: 3.7955

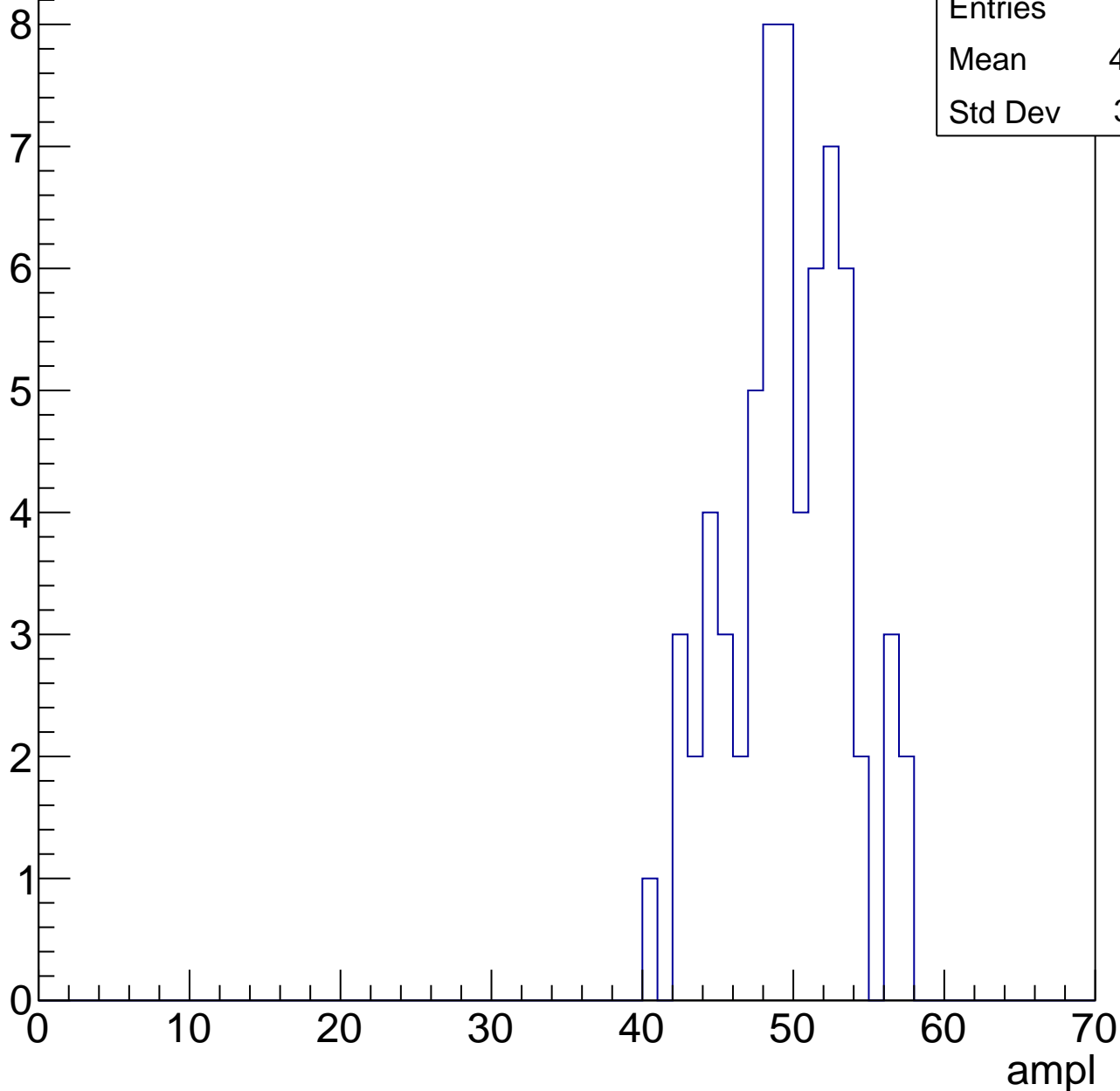


B1L102S, U15-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 49.15 |
| Std Dev | 3.901 |

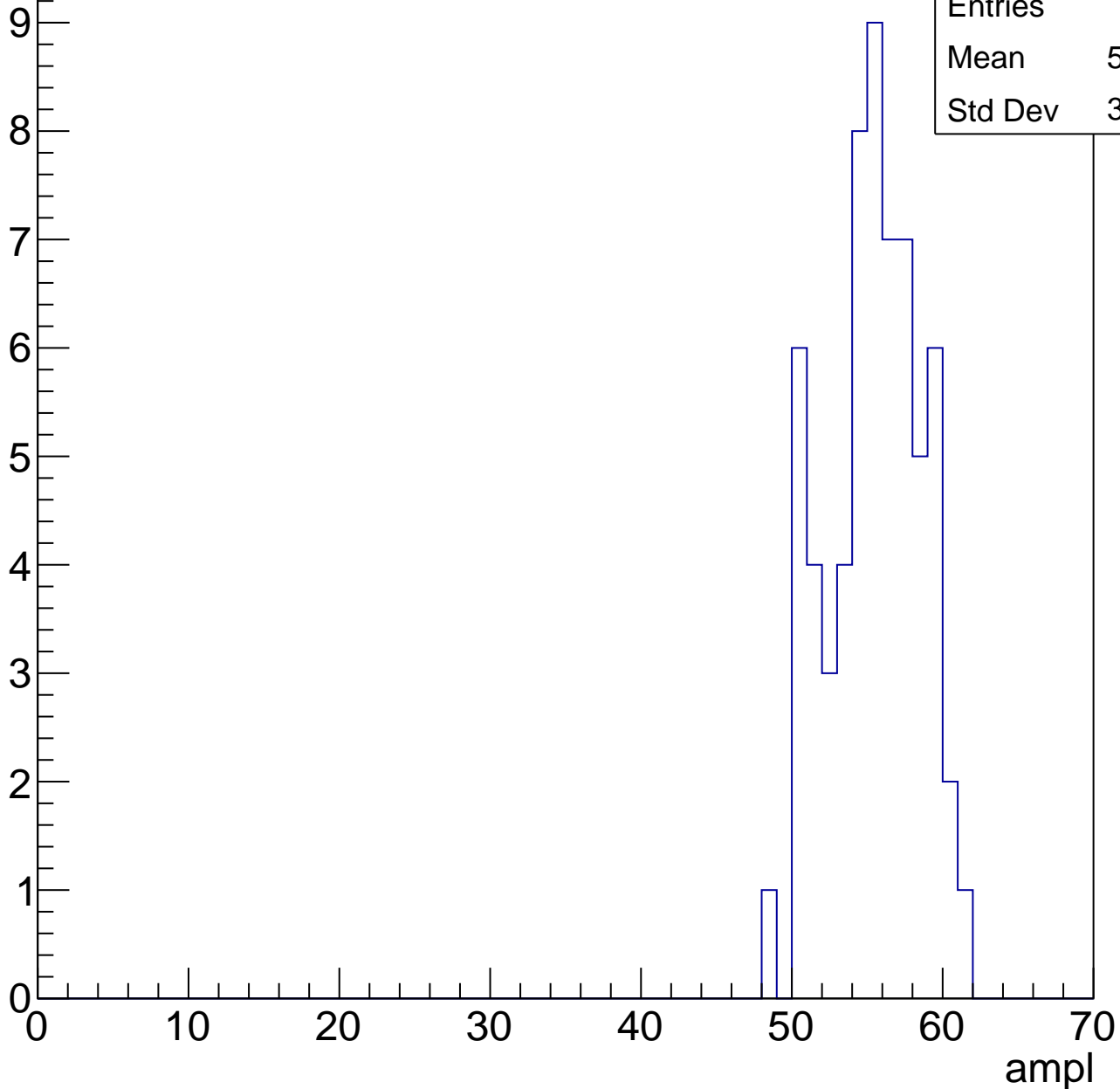


B1L102S, U15-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 54.97 |
| Std Dev | 3.024 |



B1L102S, U15-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

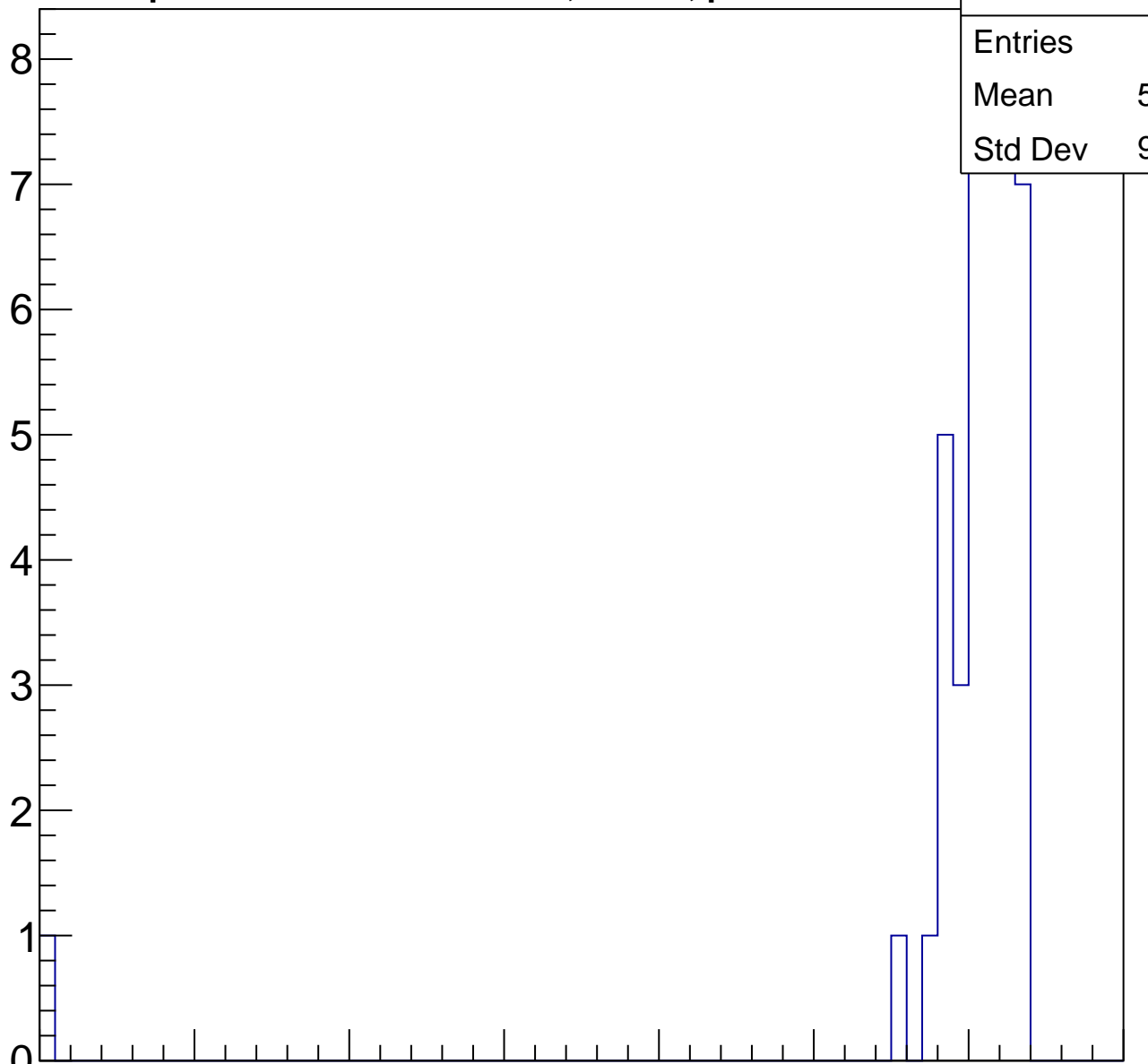
Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.14 |
| Std Dev | 9.423 |

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch70, adc0

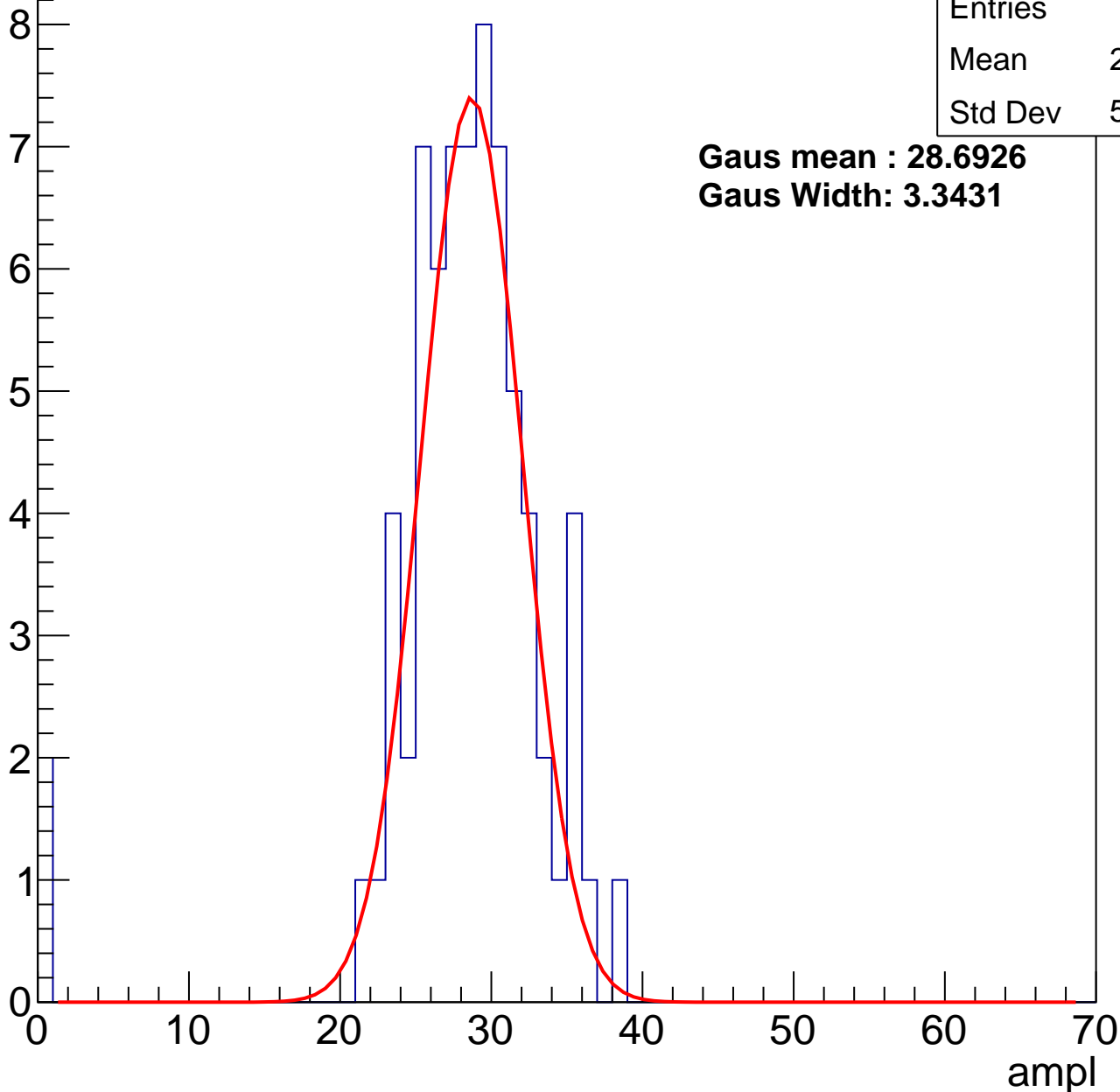
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 27.69 |
| Std Dev | 5.932 |

Gaus mean : 28.6926

Gaus Width: 3.3431



B1L102S, U15-ch70, adc1

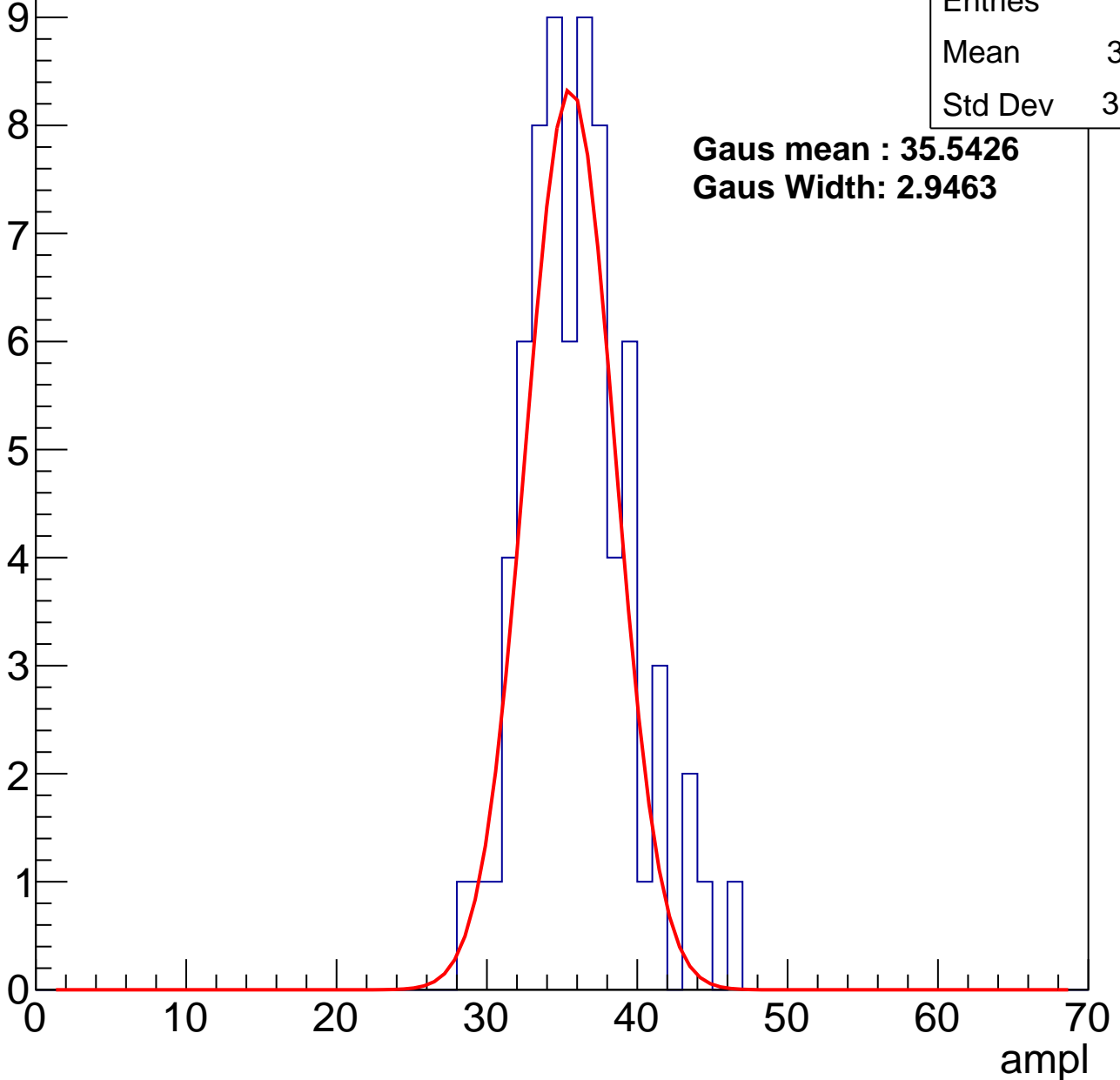
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.61 |
| Std Dev | 3.518 |

Gaus mean : 35.5426

Gaus Width: 2.9463



B1L102S, U15-ch70, adc2

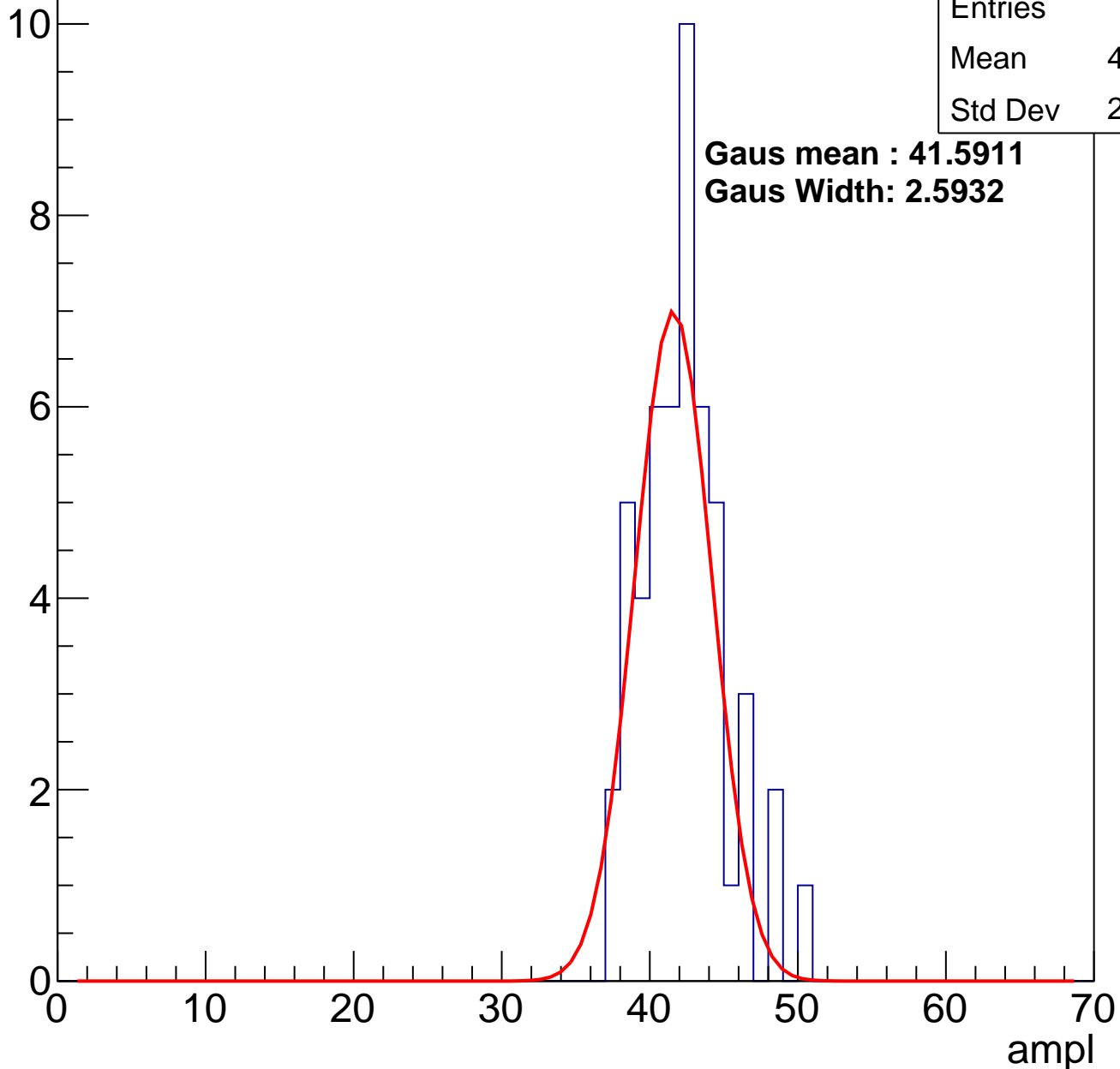
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 51 |
| Mean | 41.82 |
| Std Dev | 2.847 |

Gaus mean : 41.5911

Gaus Width: 2.5932

Entry



B1L102S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

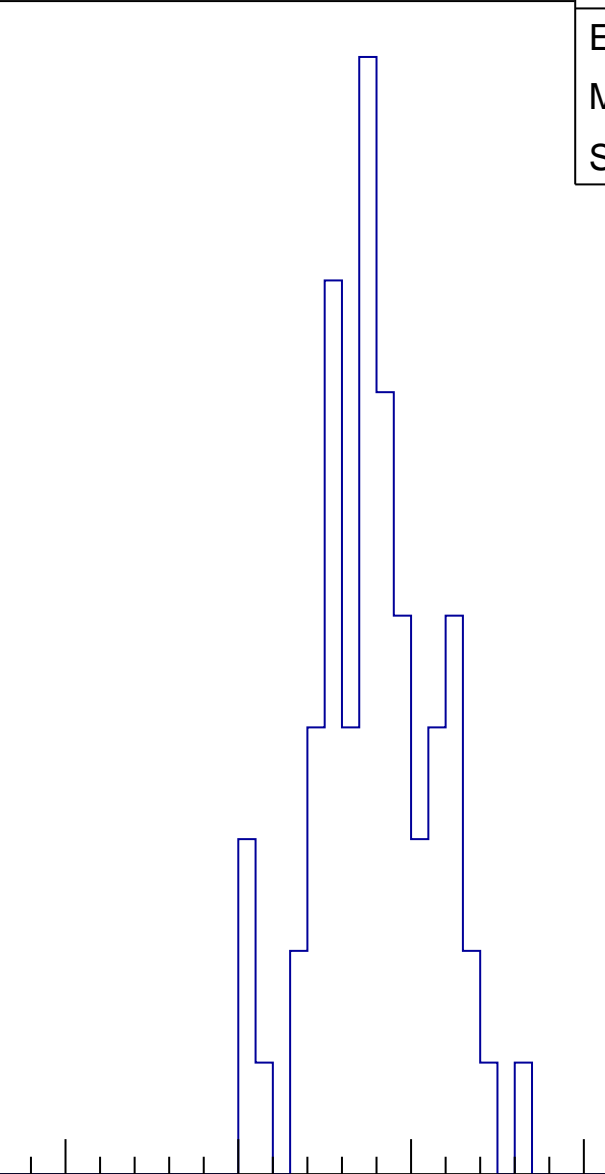
| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.47 |
| Std Dev | 3.462 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

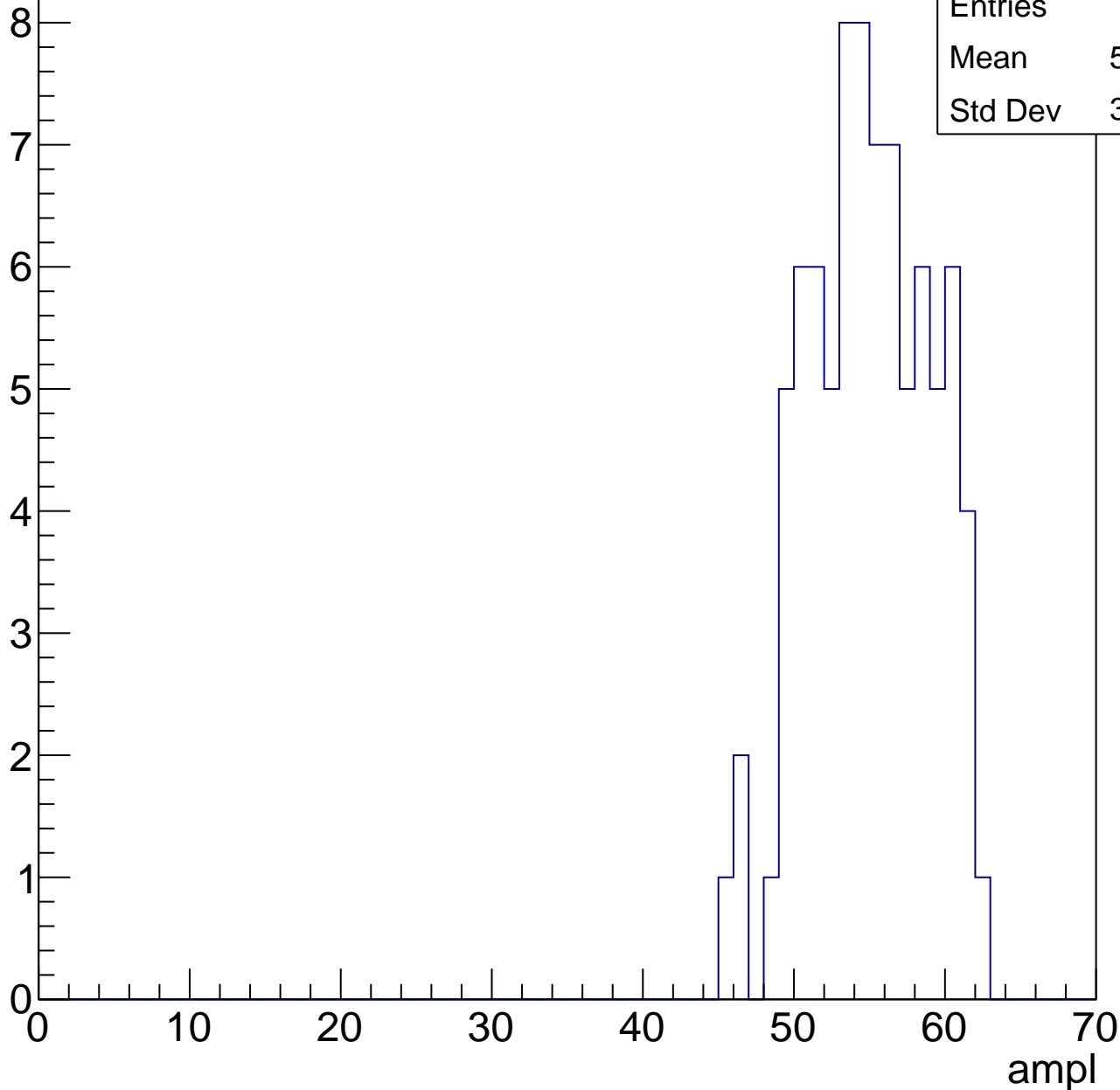


B1L102S, U15-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 54.49 |
| Std Dev | 3.965 |

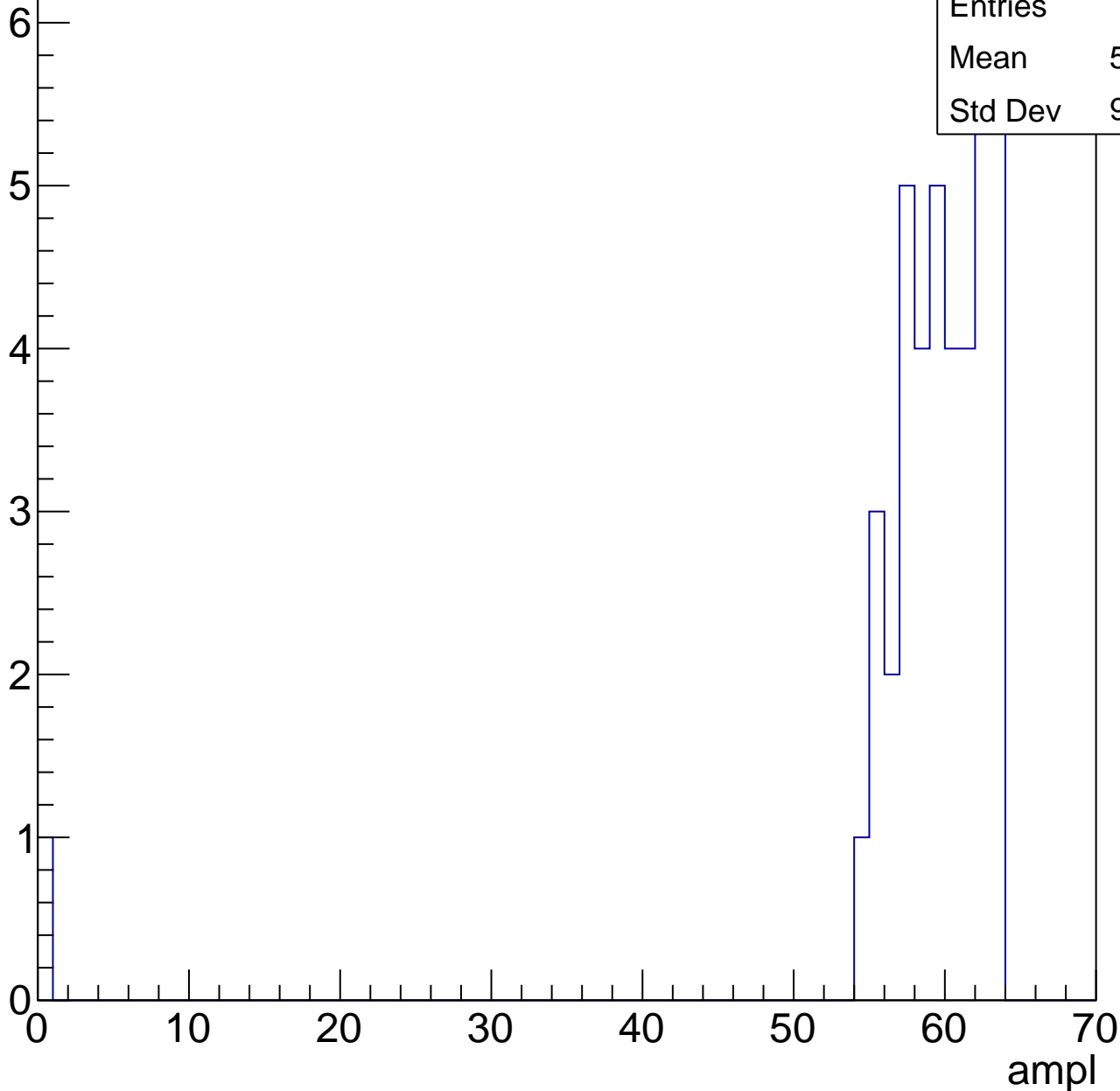


B1L102S, U15-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 57.98 |
| Std Dev | 9.529 |



B1L102S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch71, adc0

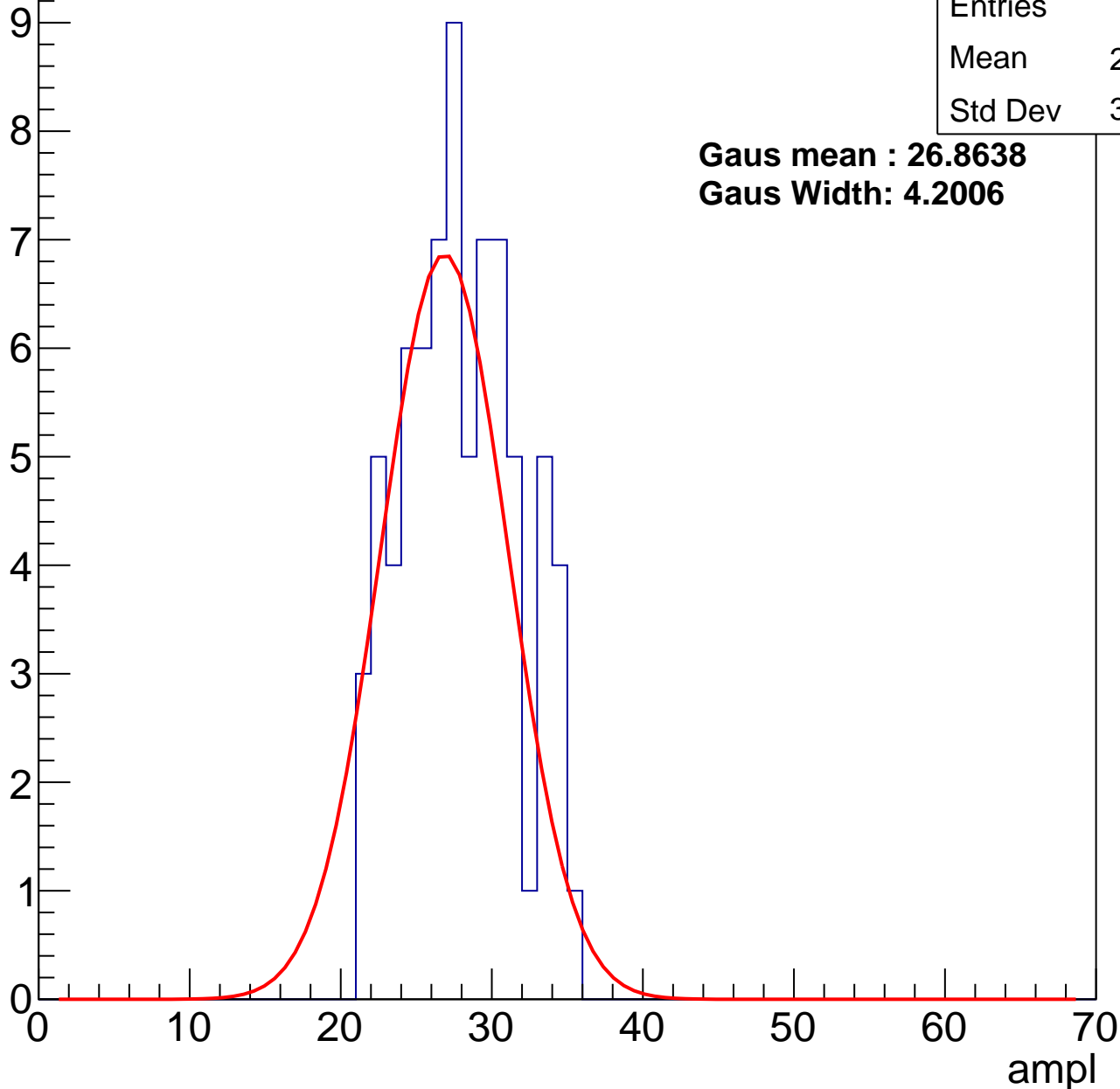
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 27.47 |
| Std Dev | 3.664 |

Gaus mean : 26.8638

Gaus Width: 4.2006



B1L102S, U15-ch71, adc1

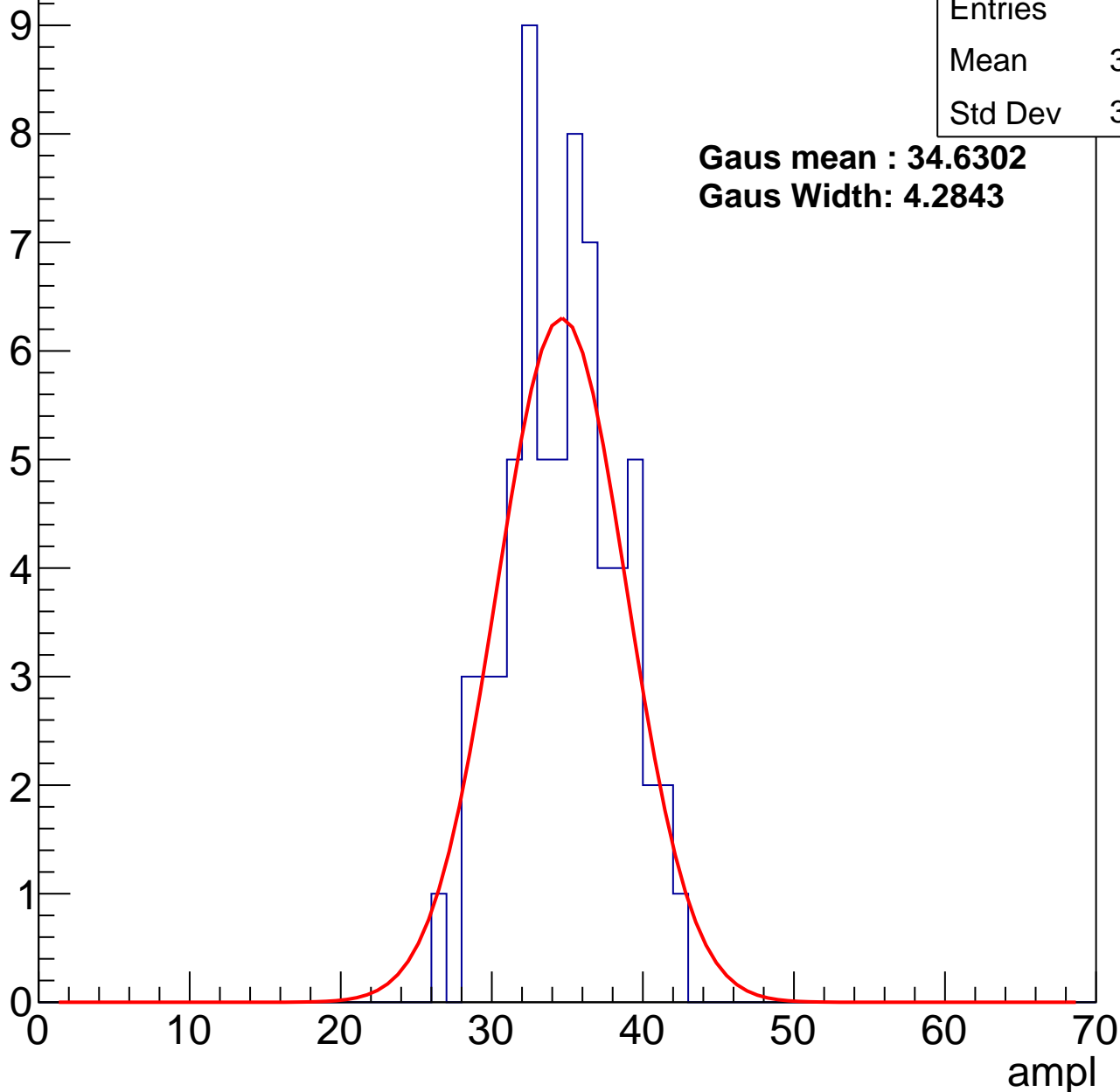
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 34.27 |
| Std Dev | 3.606 |

Gaus mean : 34.6302

Gaus Width: 4.2843



B1L102S, U15-ch71, adc2

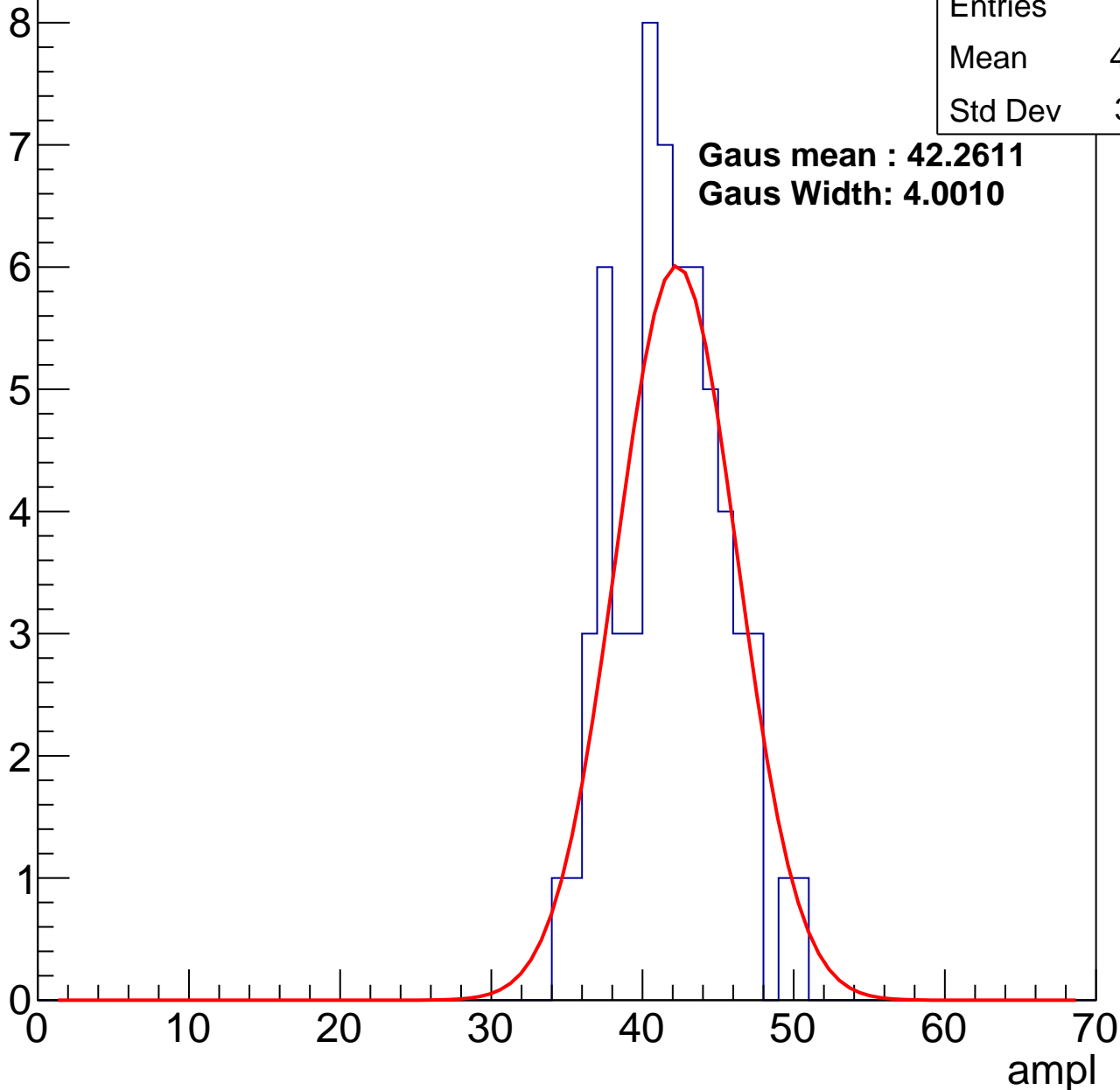
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.39 |
| Std Dev | 3.531 |

Gaus mean : 42.2611

Gaus Width: 4.0010

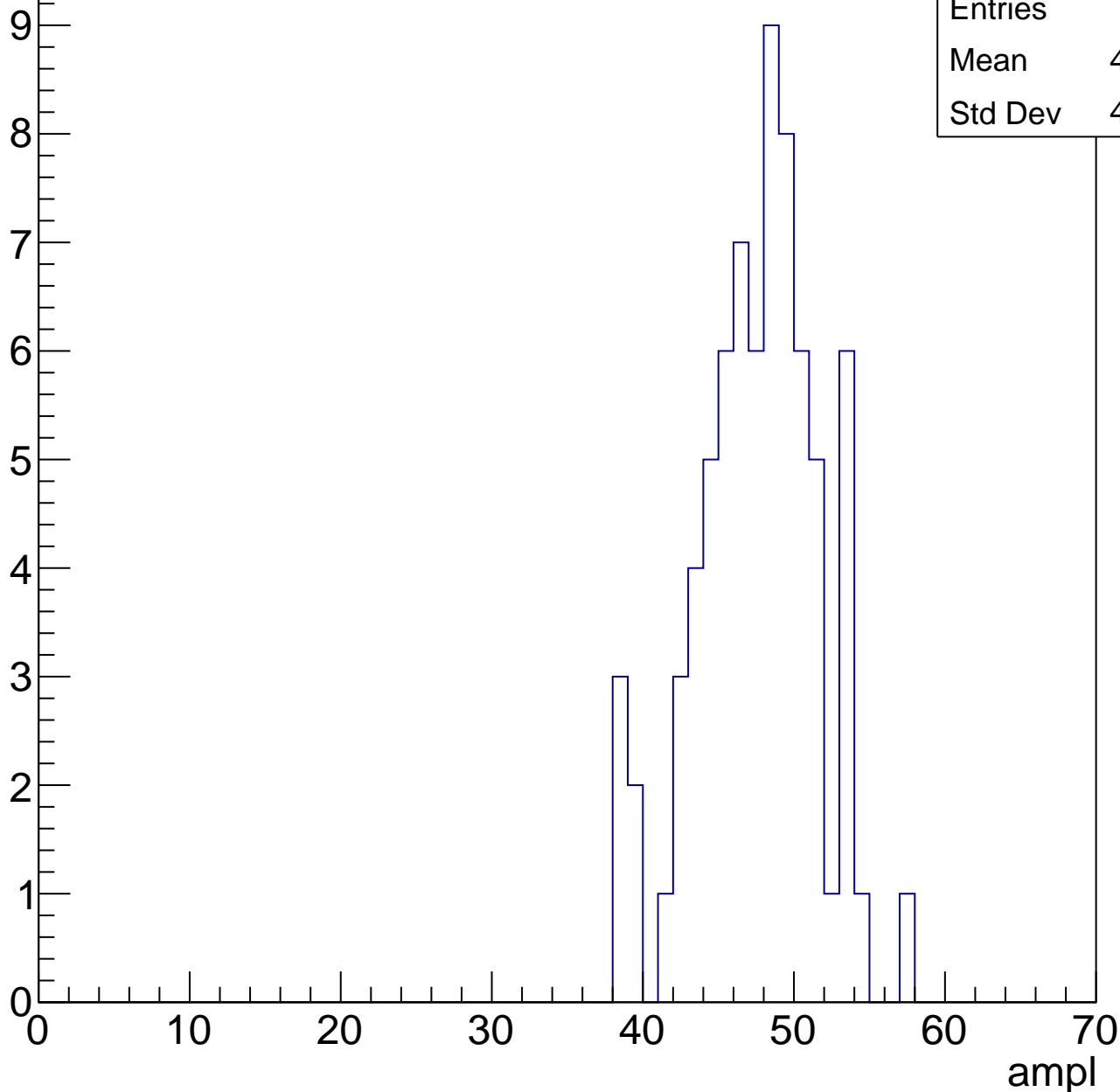


B1L102S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 47.09 |
| Std Dev | 4.004 |

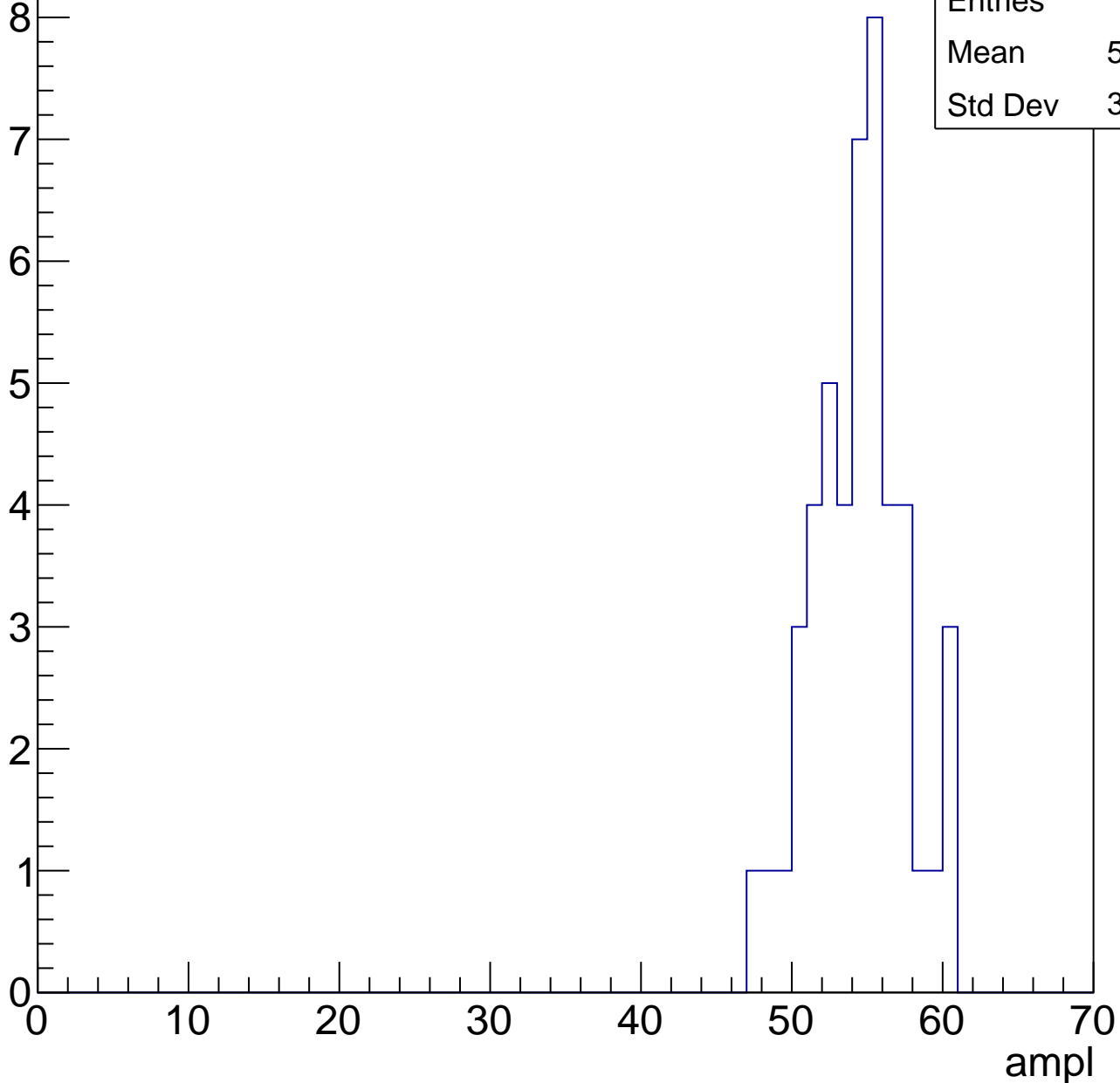


B1L102S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 53.98 |
| Std Dev | 3.014 |

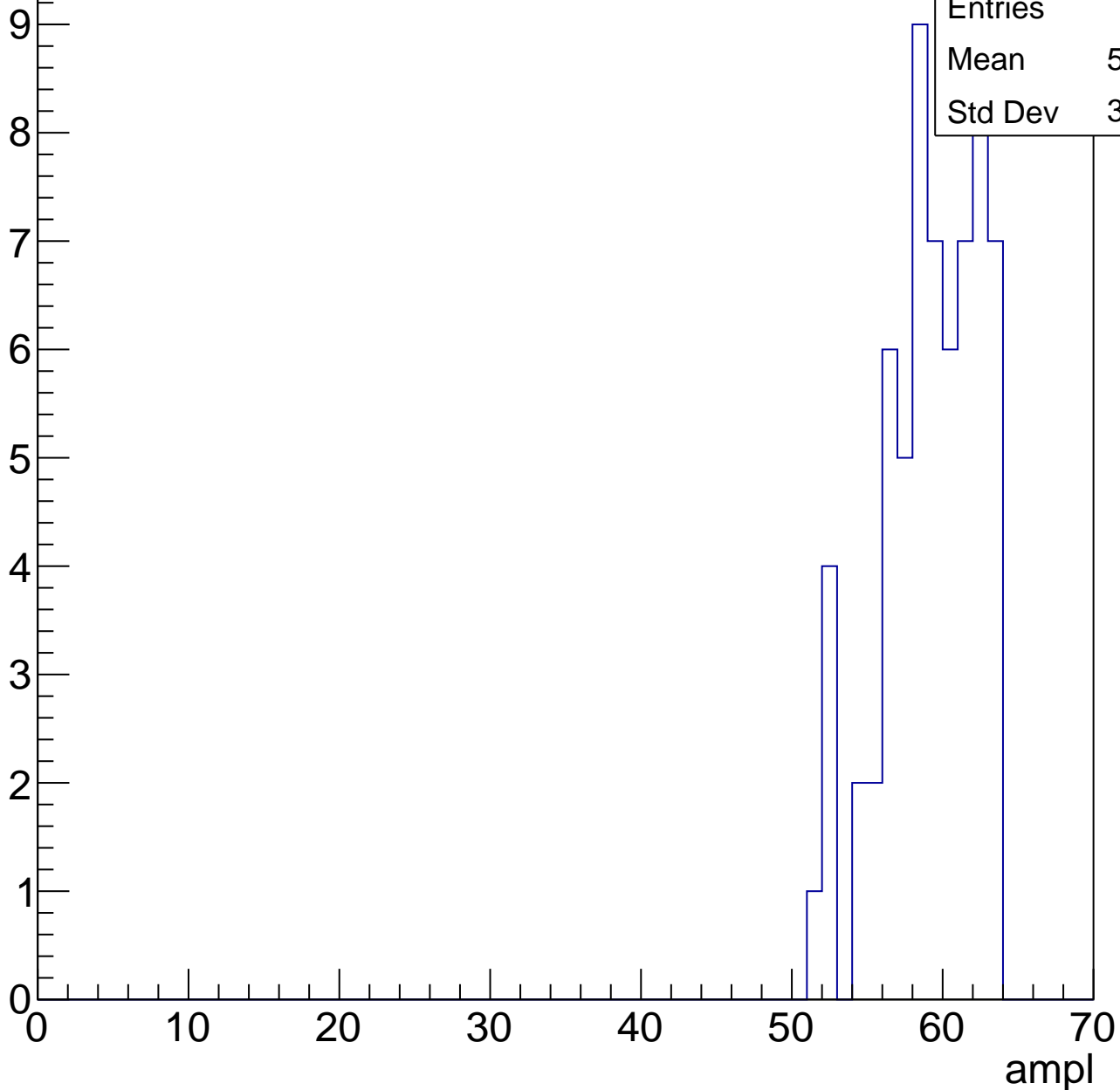


B1L102S, U15-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 58.75 |
| Std Dev | 3.158 |



B1L102S, U15-ch71, adc6

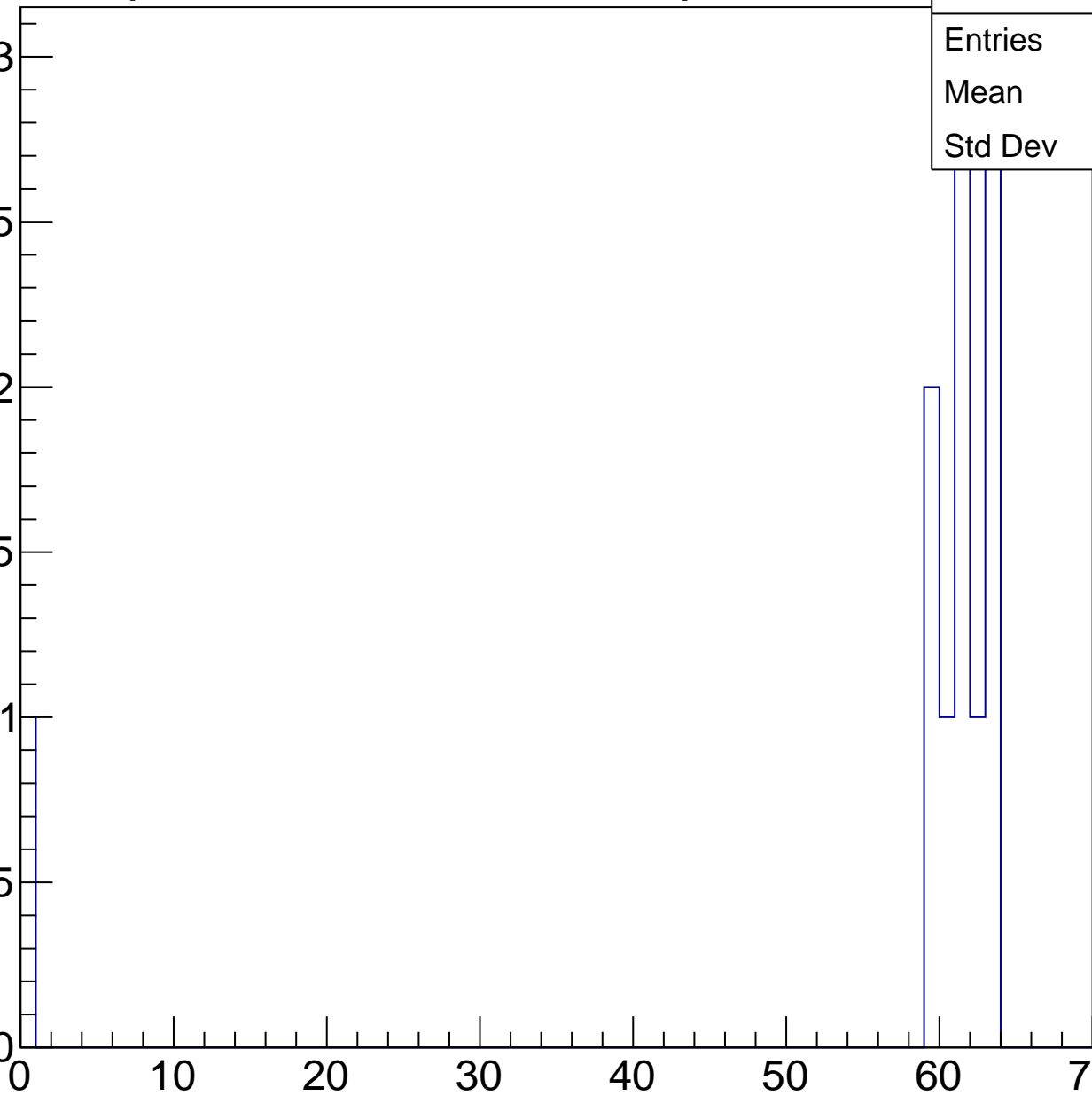
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 11 |
| Mean | 55.64 |
| Std Dev | 17.65 |

ampl



B1L102S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch72, adc0

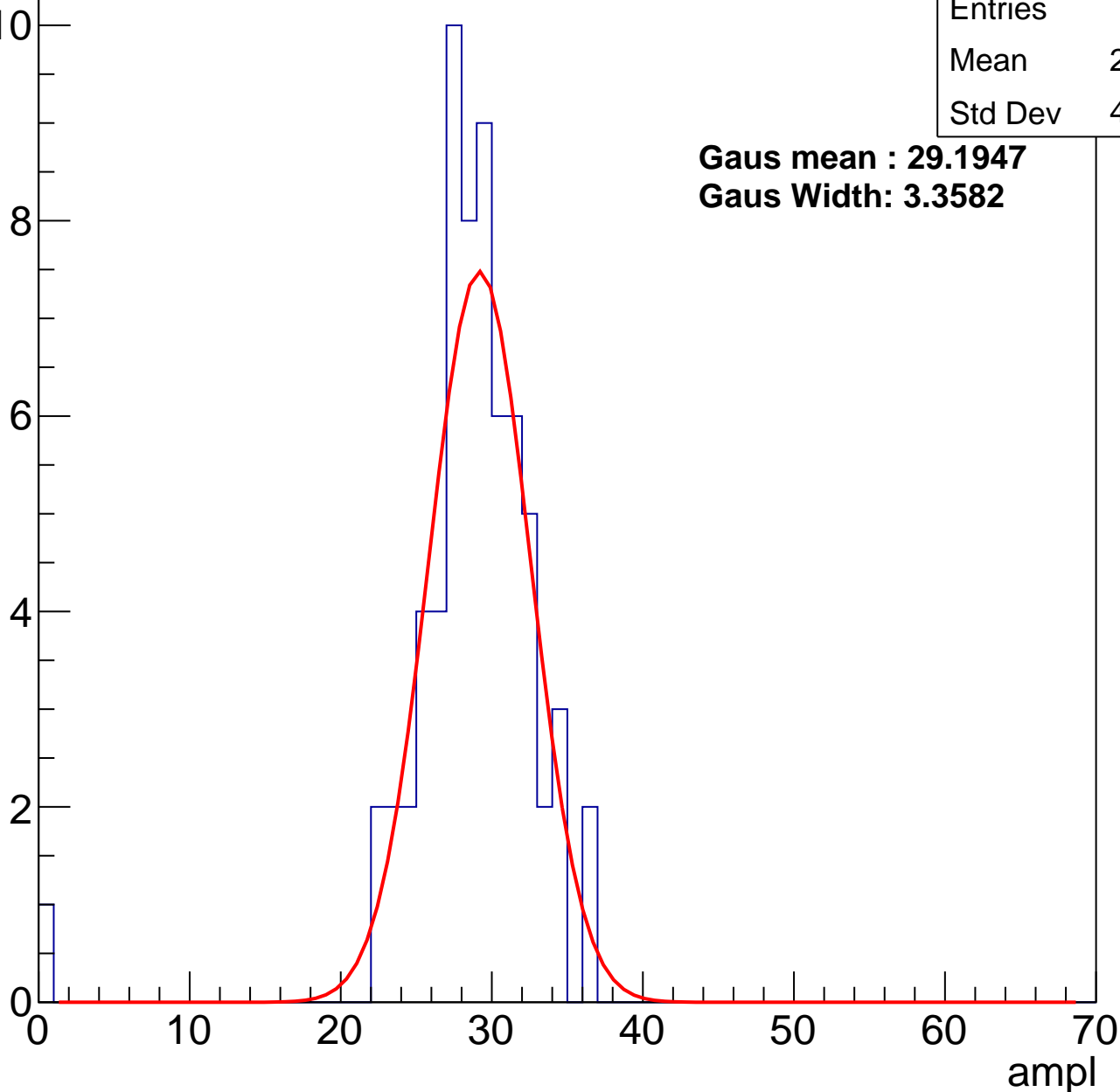
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 28.23 |
| Std Dev | 4.687 |

Gaus mean : 29.1947

Gaus Width: 3.3582



B1L102S, U15-ch72, adc1

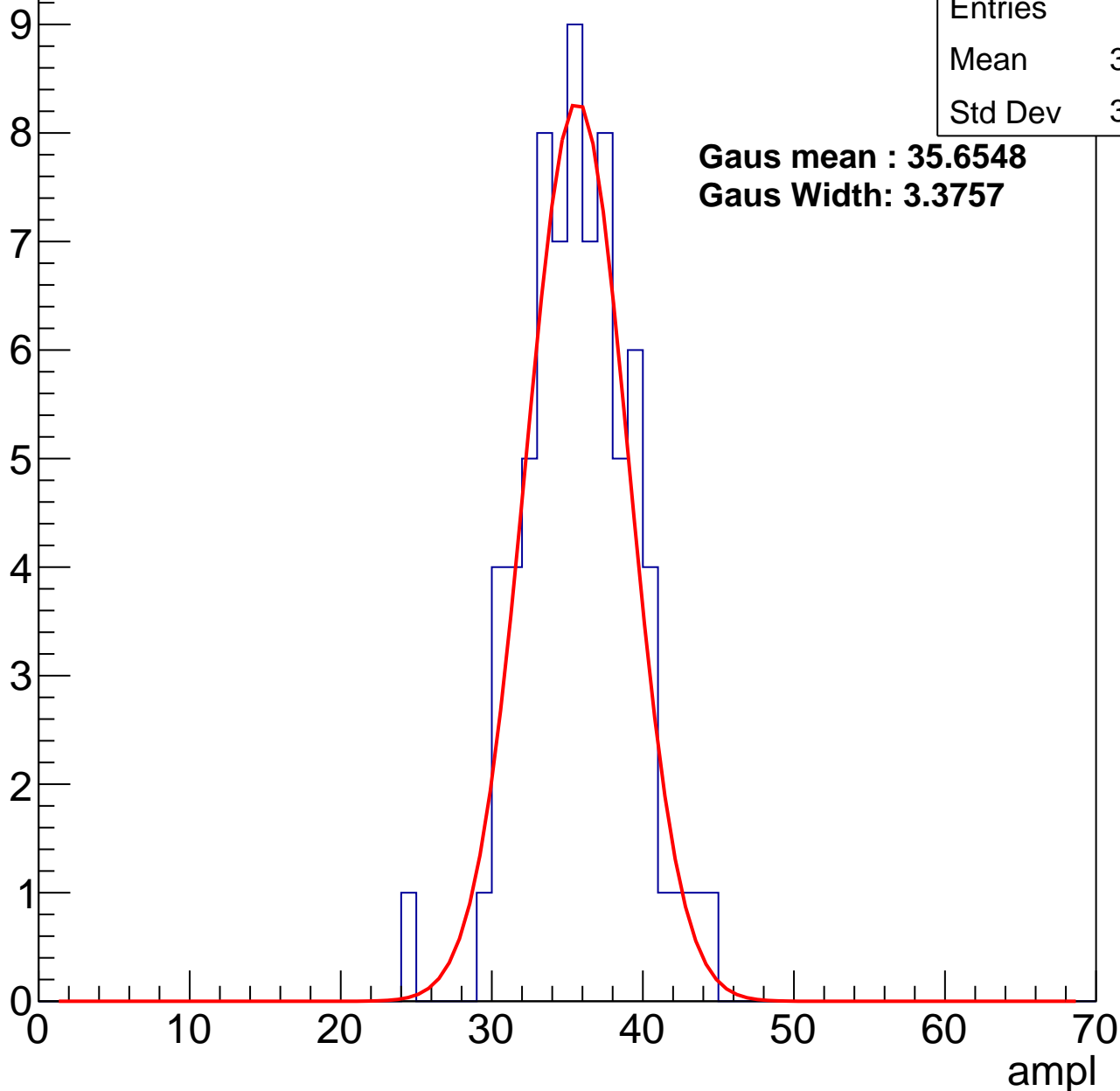
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 35.29 |
| Std Dev | 3.529 |

Gaus mean : 35.6548

Gaus Width: 3.3757



B1L102S, U15-ch72, adc2

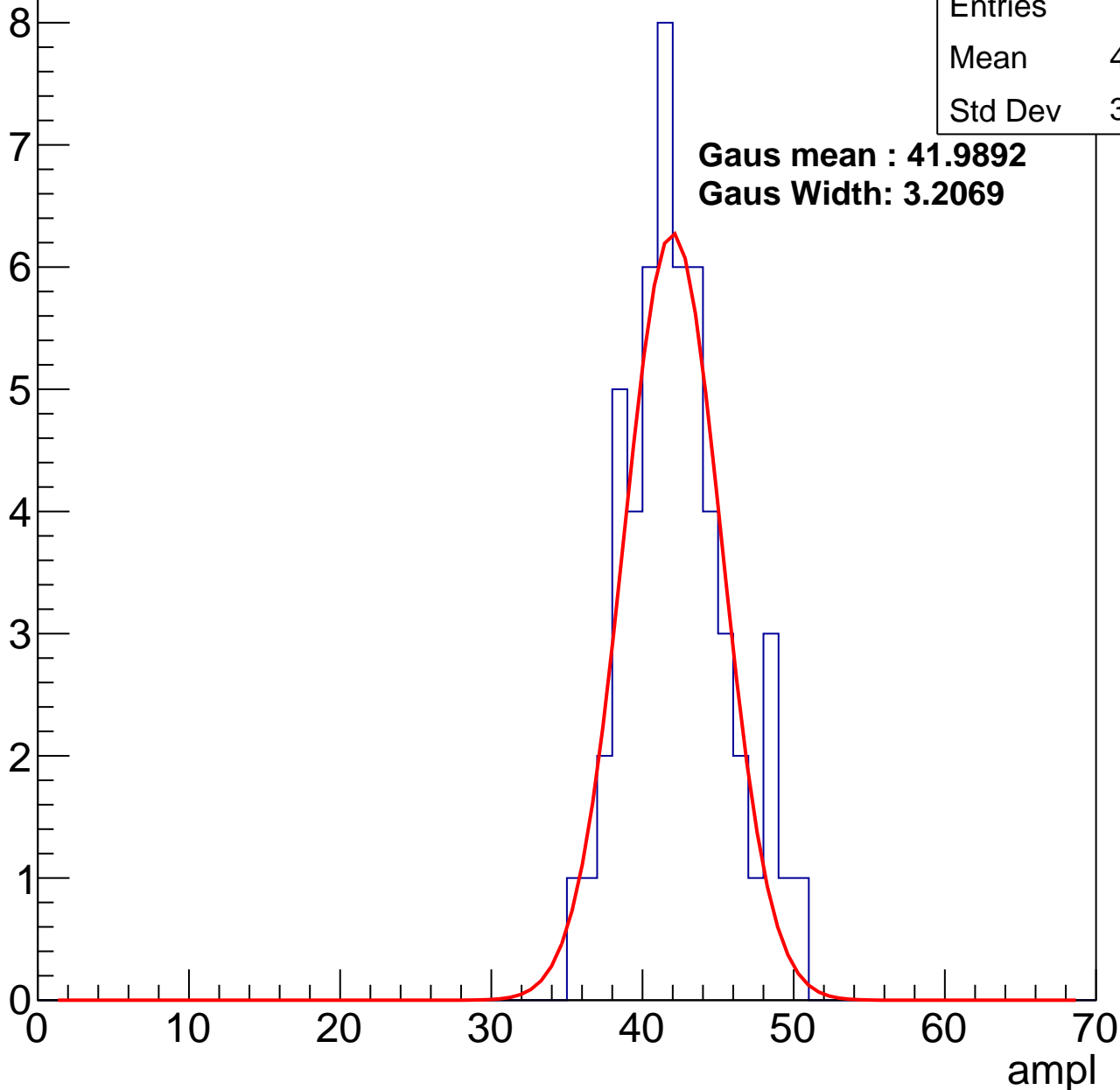
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 41.89 |
| Std Dev | 3.359 |

Gaus mean : 41.9892

Gaus Width: 3.2069

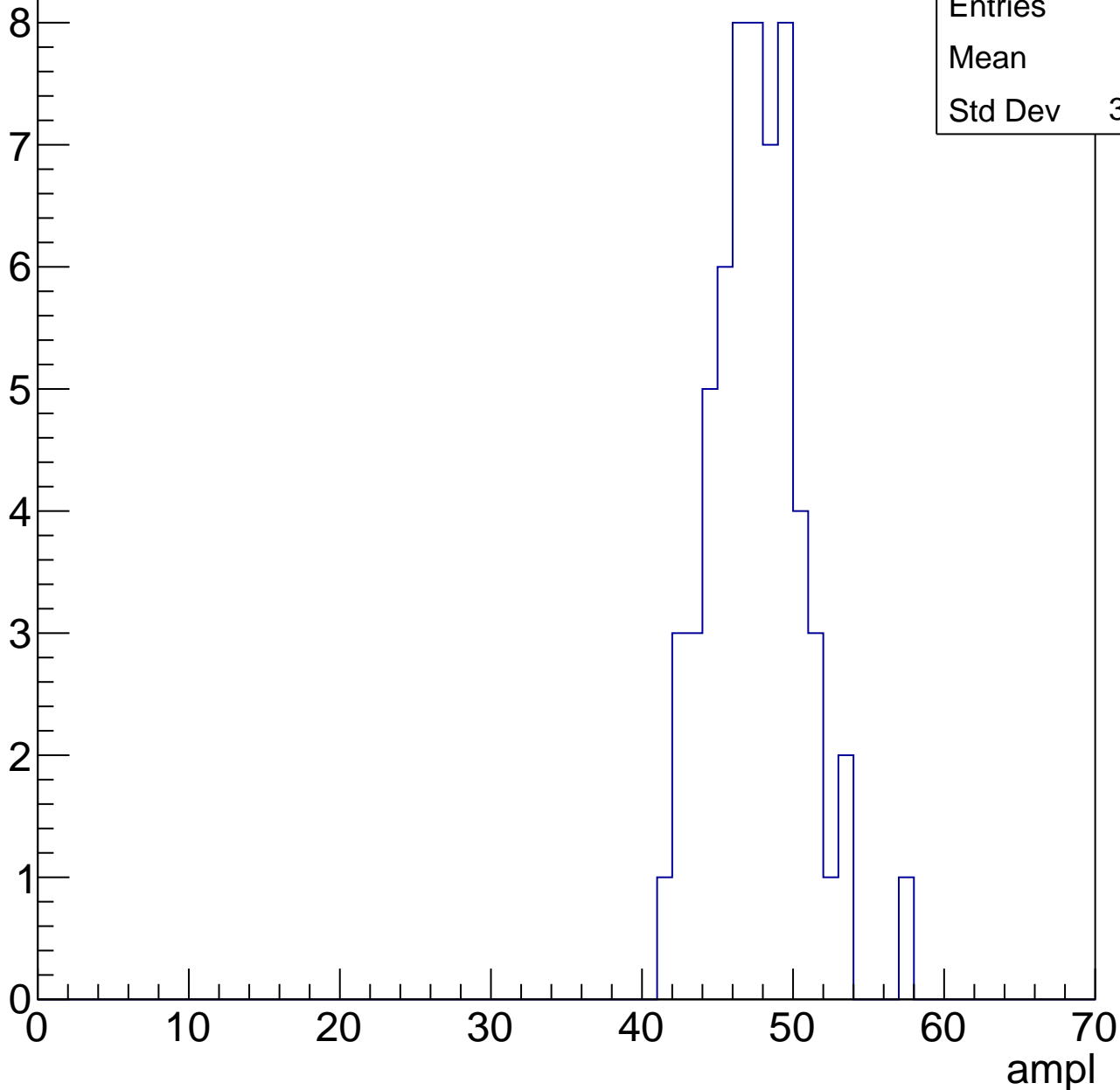


B1L102S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

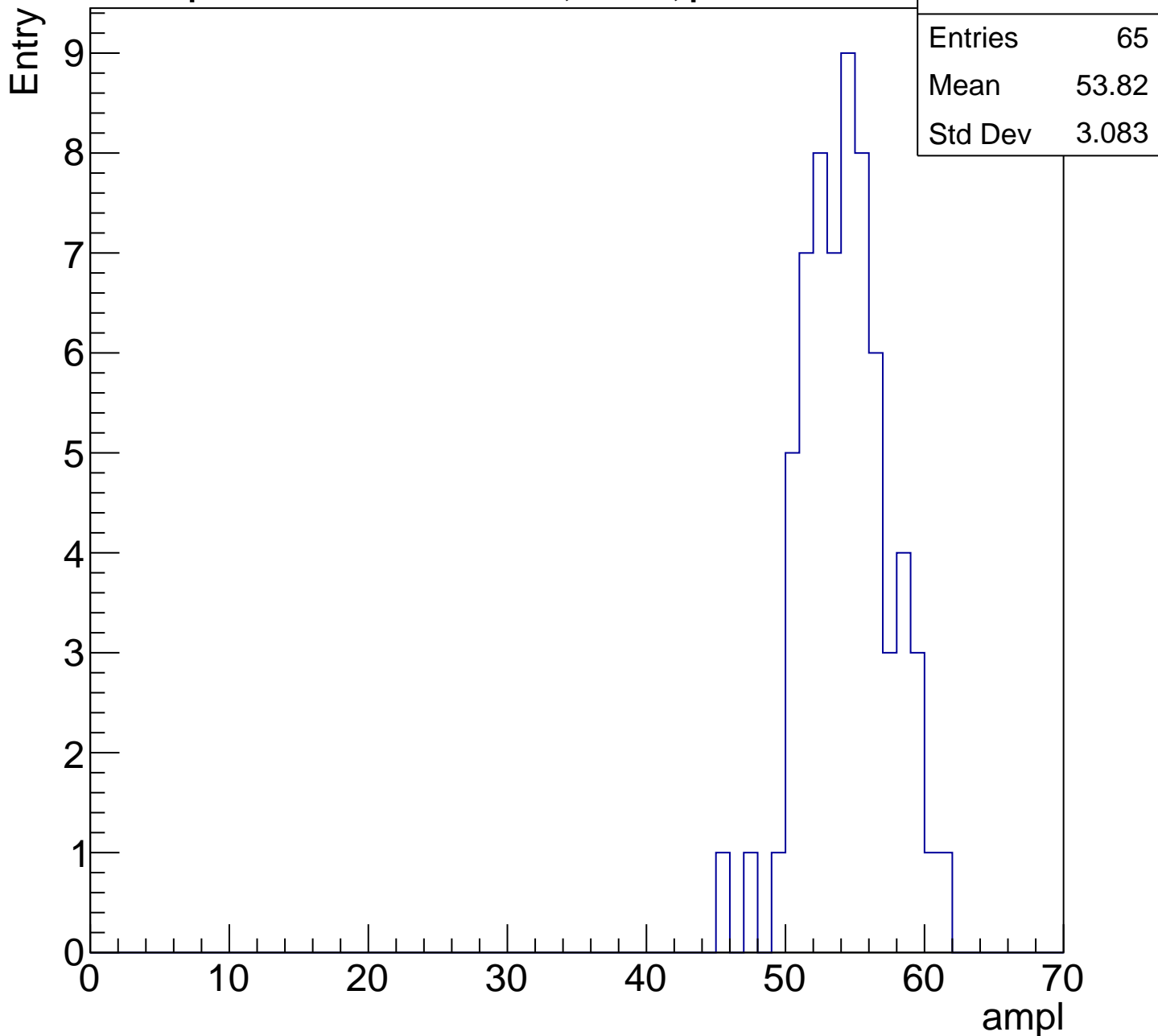
Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 47.1 |
| Std Dev | 3.042 |



B1L102S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

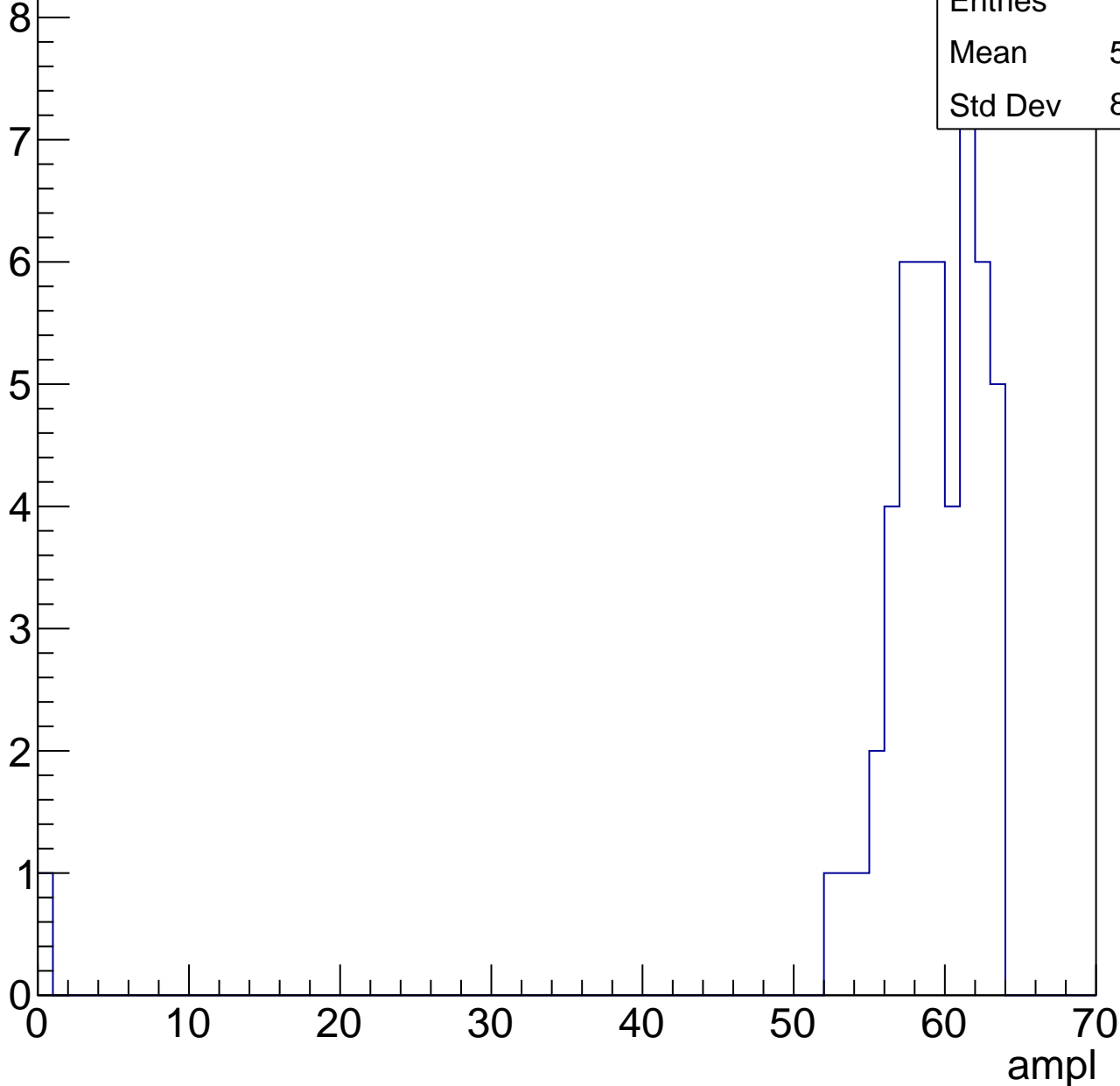


B1L102S, U15-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 57.88 |
| Std Dev | 8.627 |

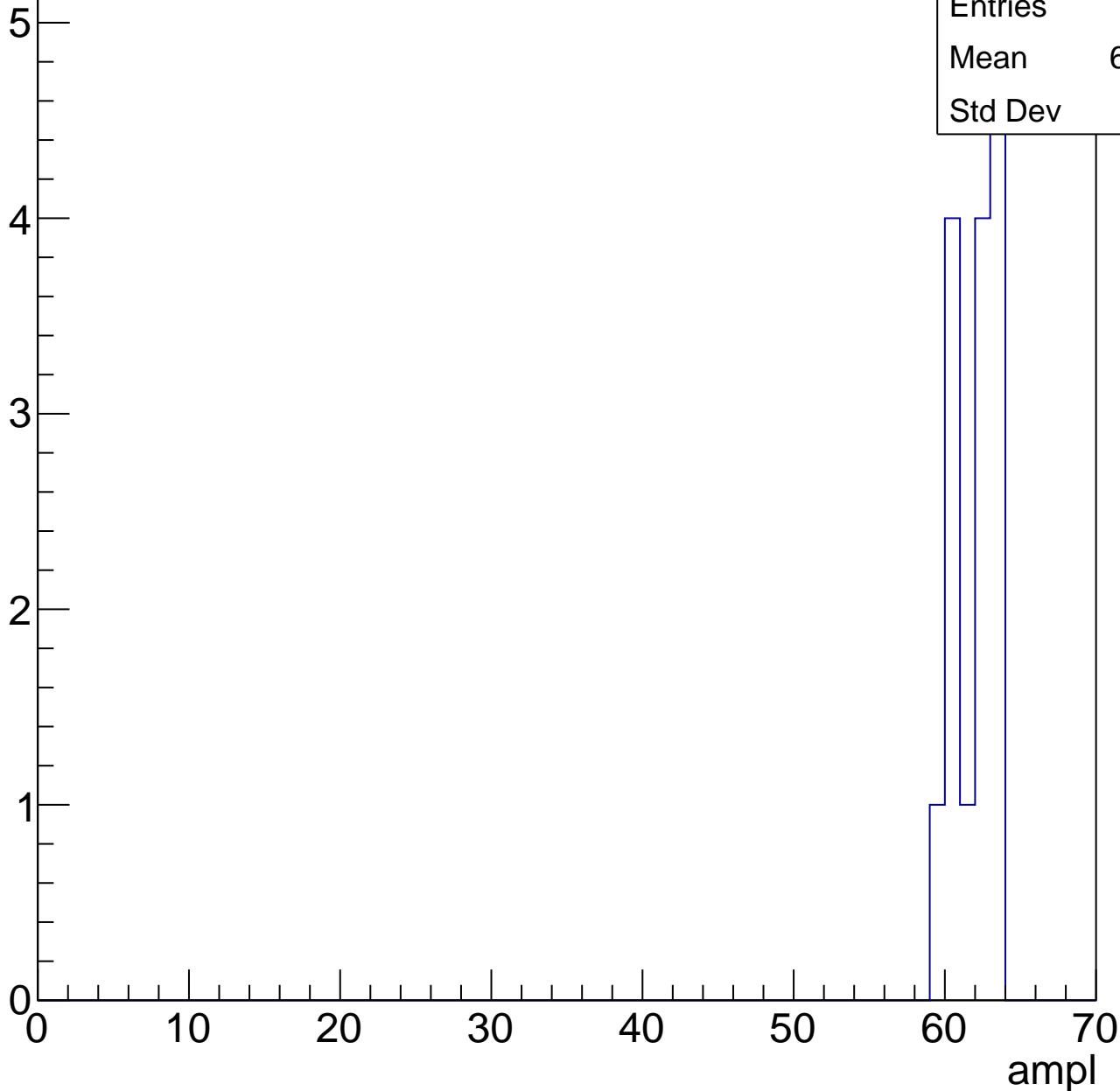


B1L102S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

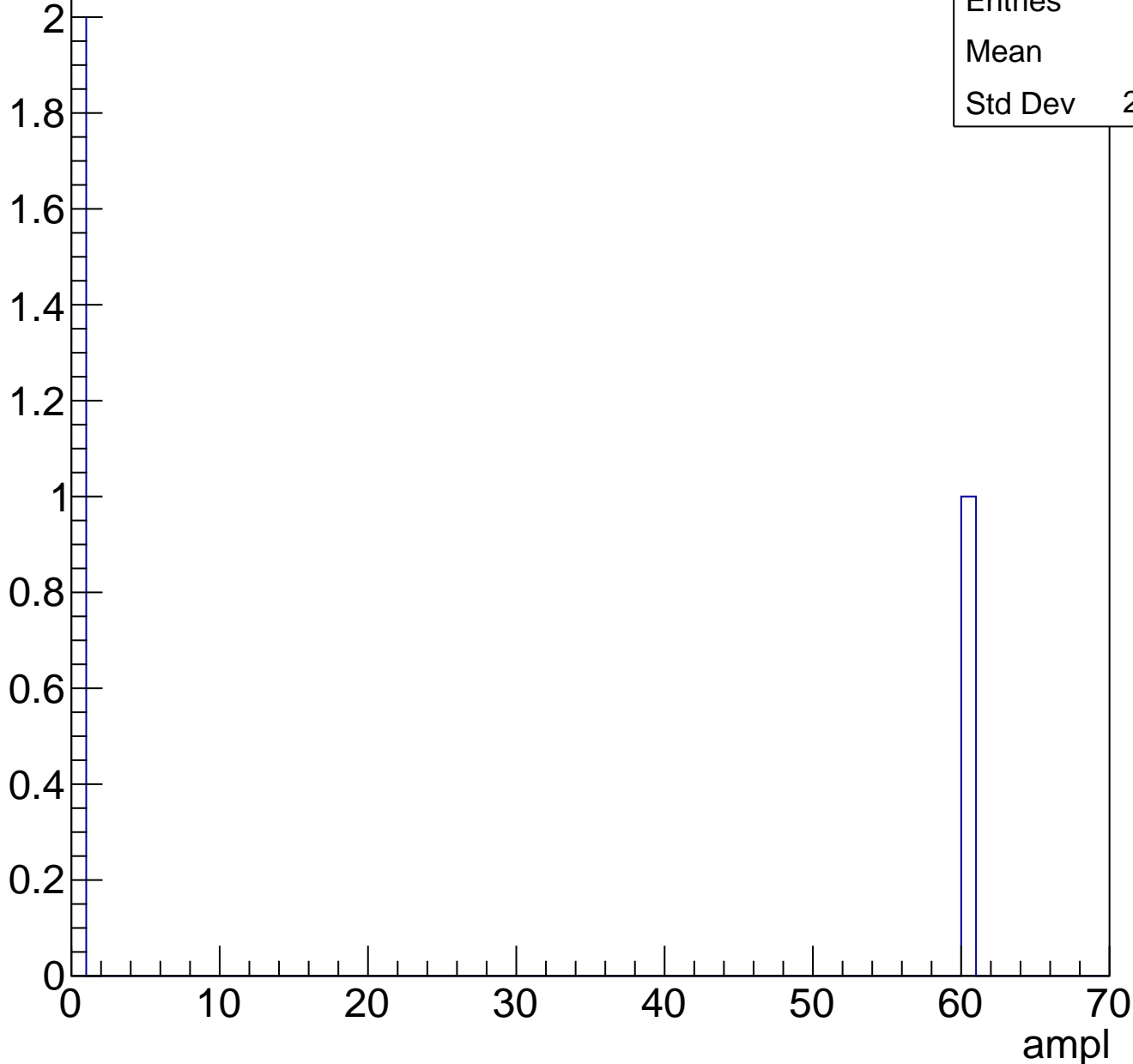
| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.53 |
| Std Dev | 1.36 |



B1L102S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 20 |
| Std Dev | 28.28 |

B1L102S, U15-ch73, adc0

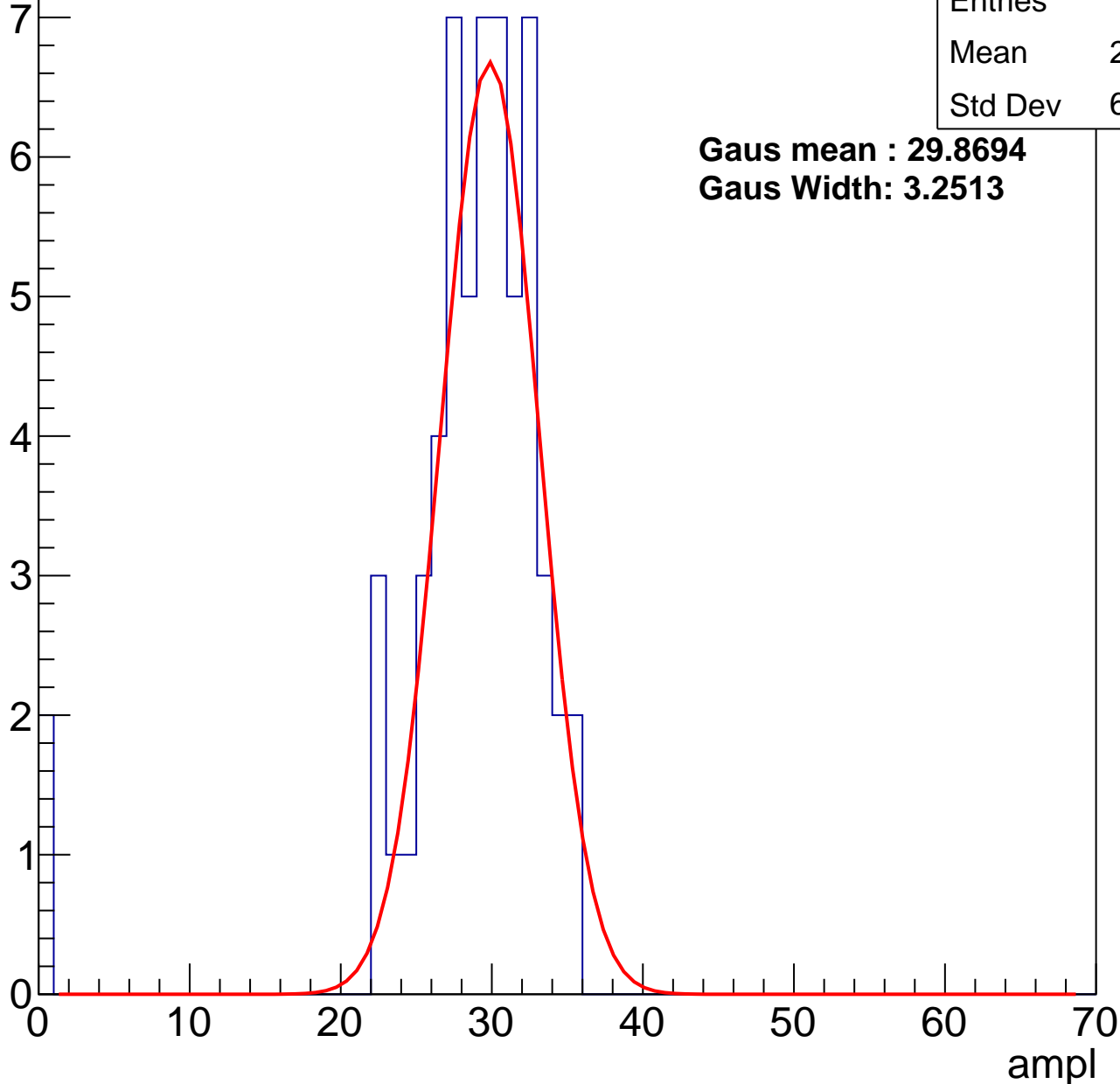
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 27.97 |
| Std Dev | 6.112 |

Gaus mean : 29.8694

Gaus Width: 3.2513



B1L102S, U15-ch73, adc1

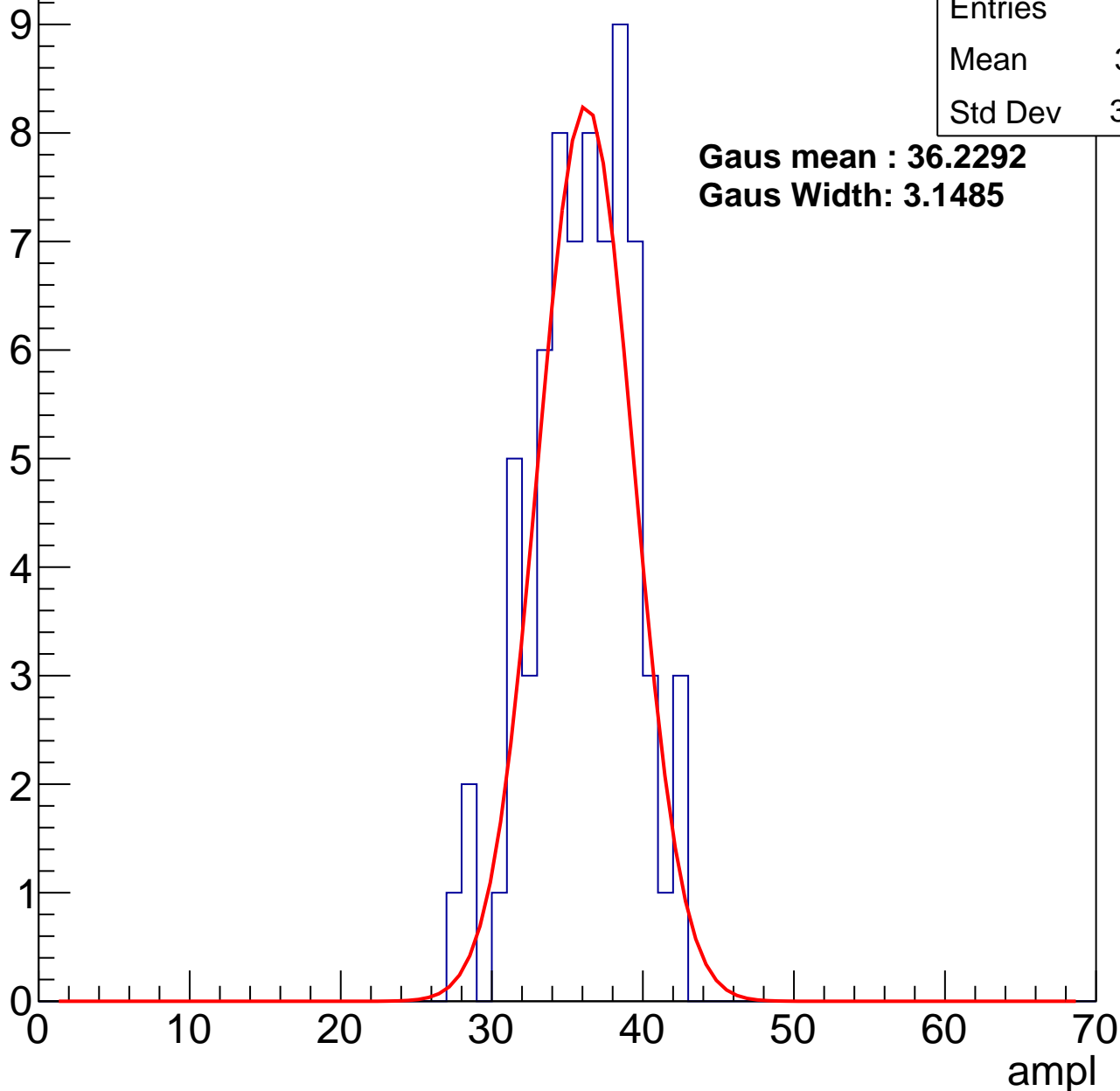
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 35.61 |
| Std Dev | 3.329 |

Gaus mean : 36.2292

Gaus Width: 3.1485



B1L102S, U15-ch73, adc2

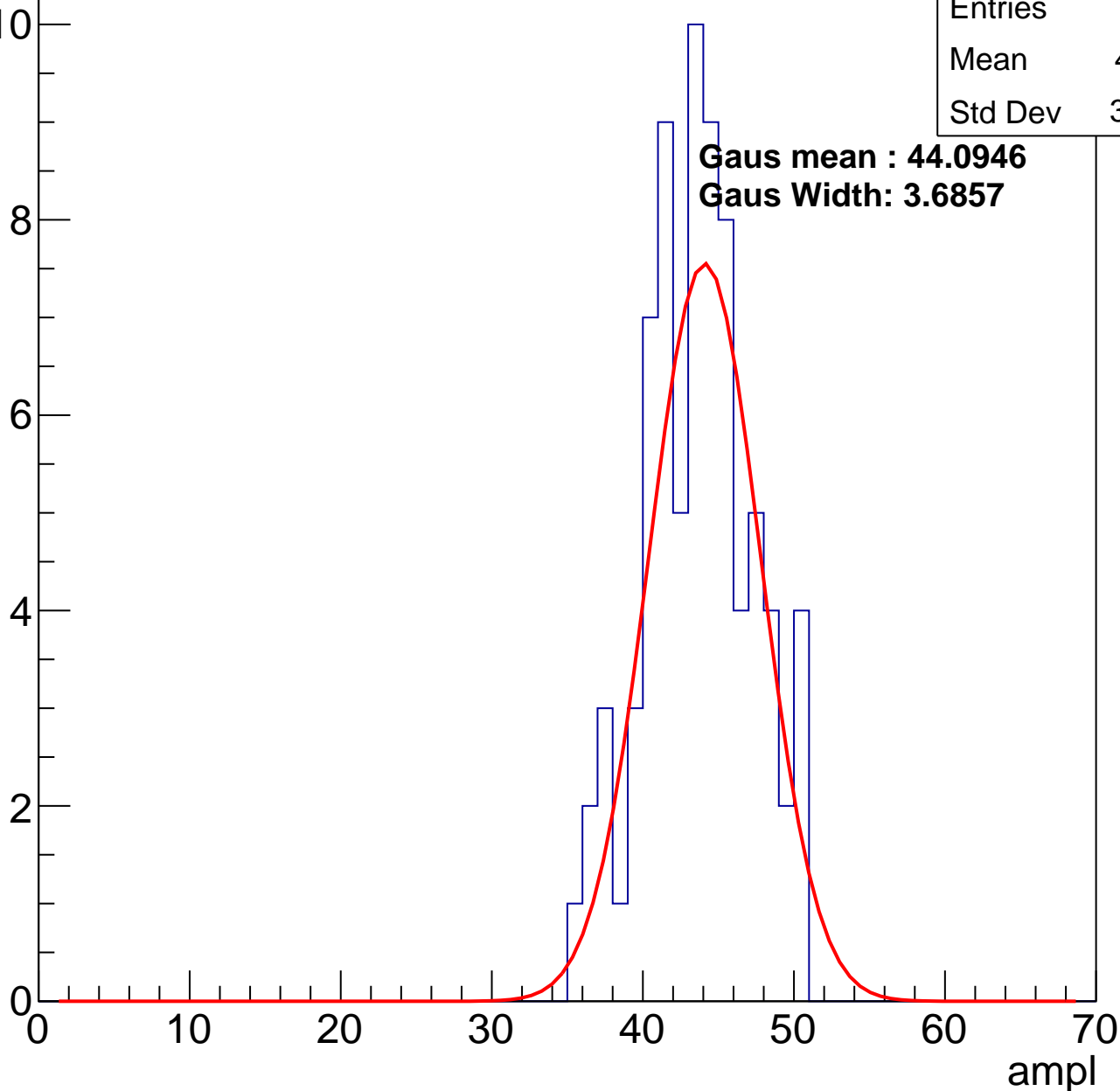
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 43.21 |
| Std Dev | 3.547 |

Gaus mean : 44.0946

Gaus Width: 3.6857

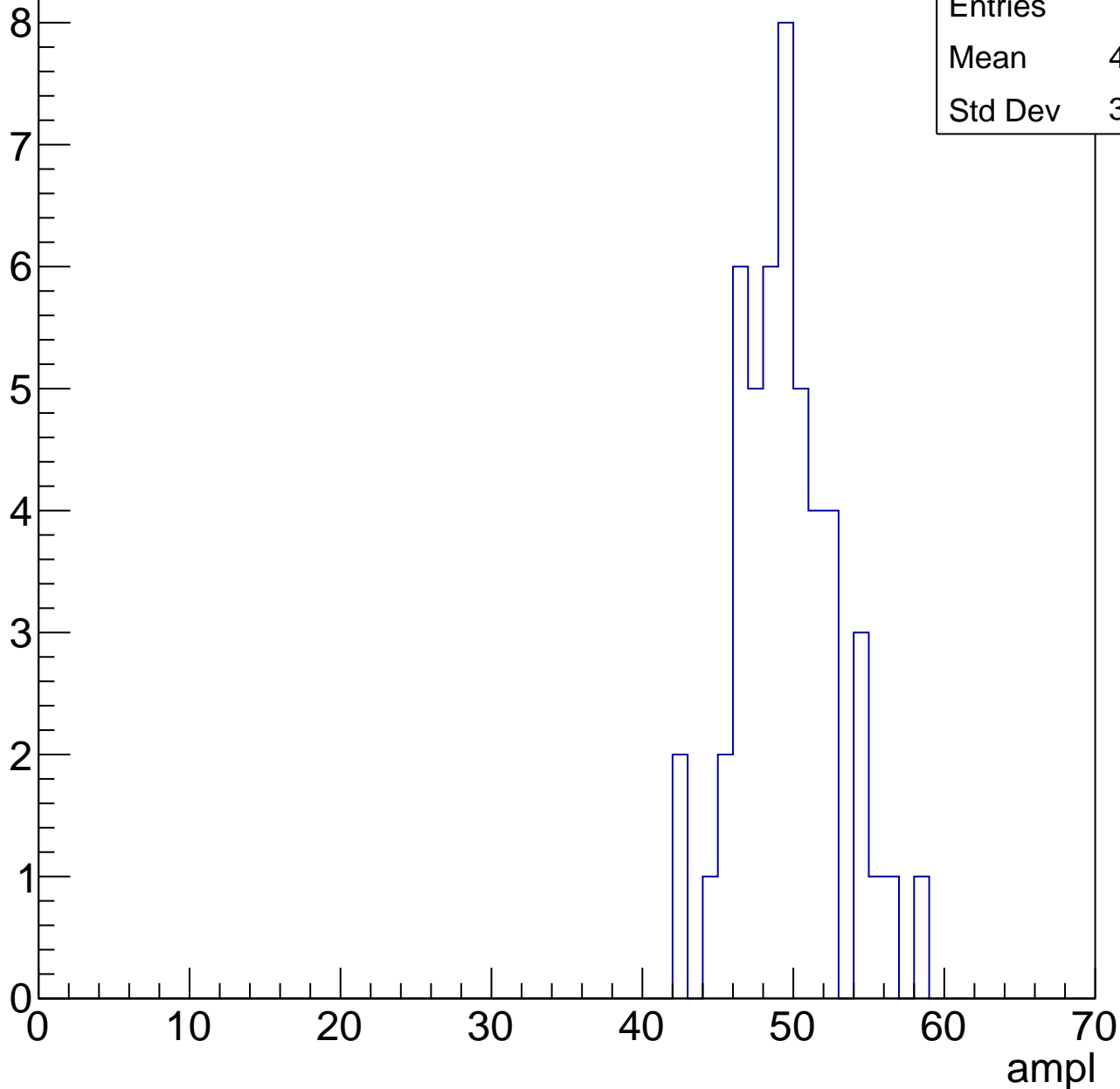


B1L102S, U15-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 49.02 |
| Std Dev | 3.298 |

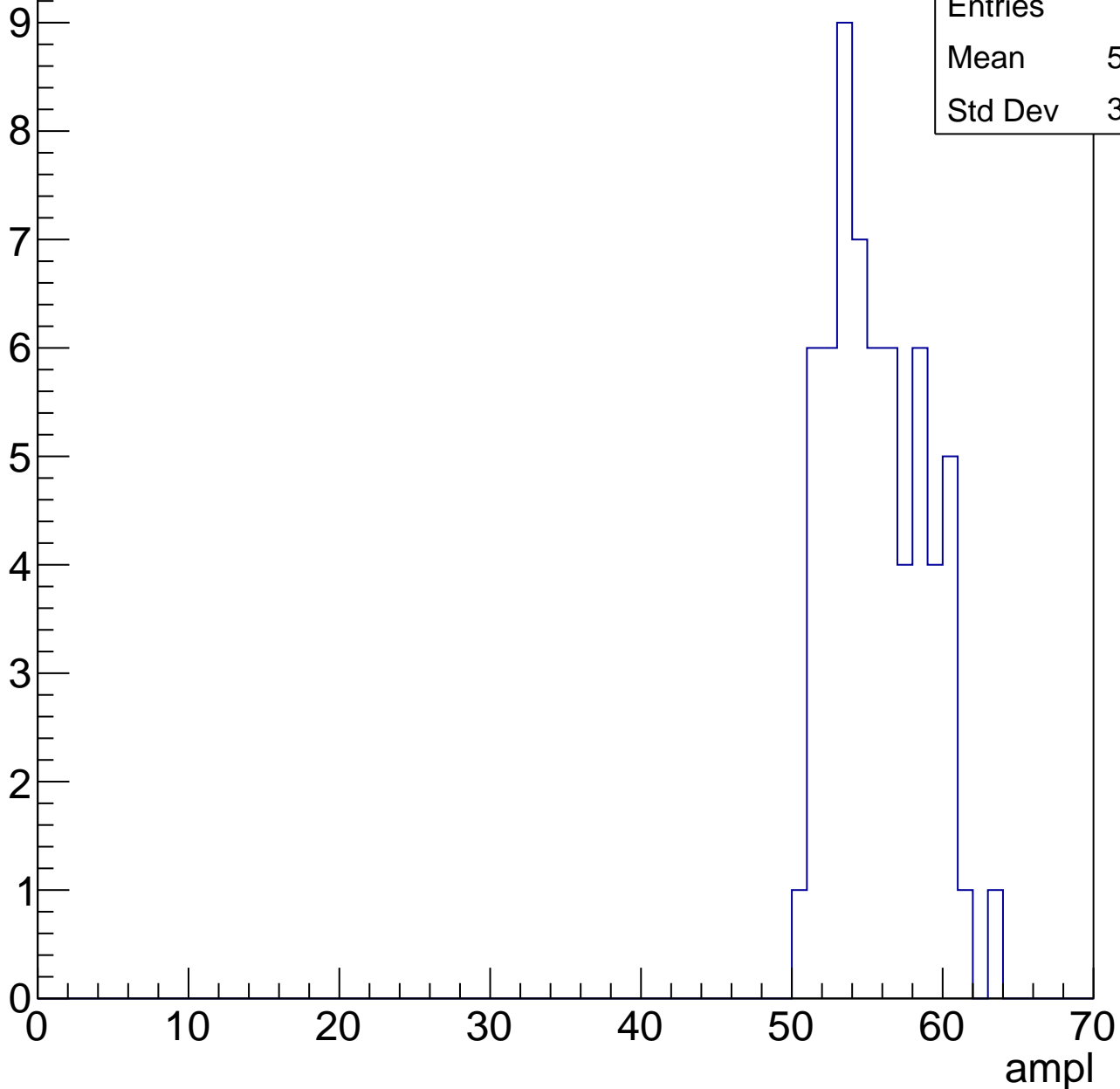


B1L102S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 55.24 |
| Std Dev | 3.057 |

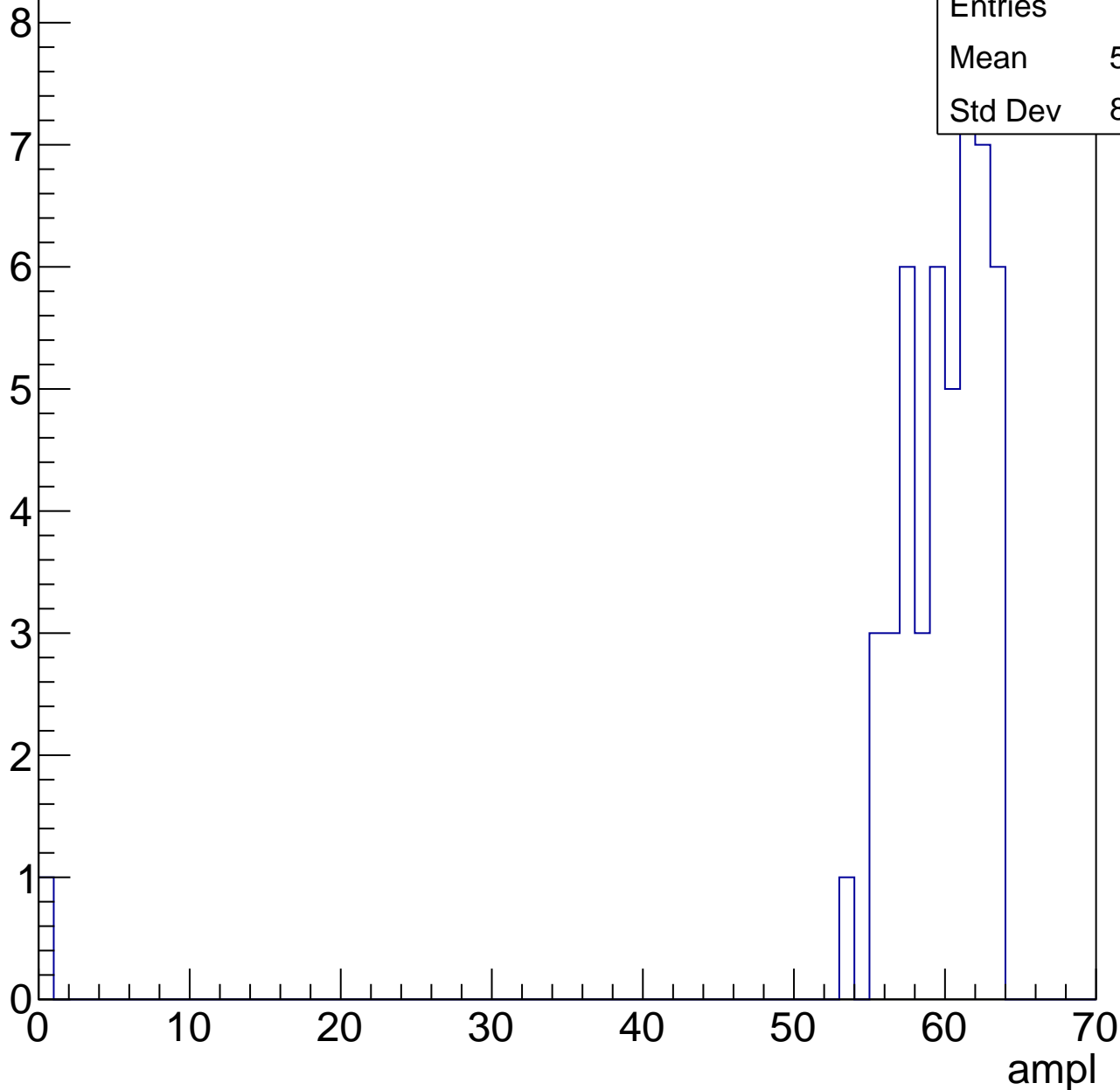


B1L102S, U15-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.29 |
| Std Dev | 8.795 |



B1L102S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch74, adc0

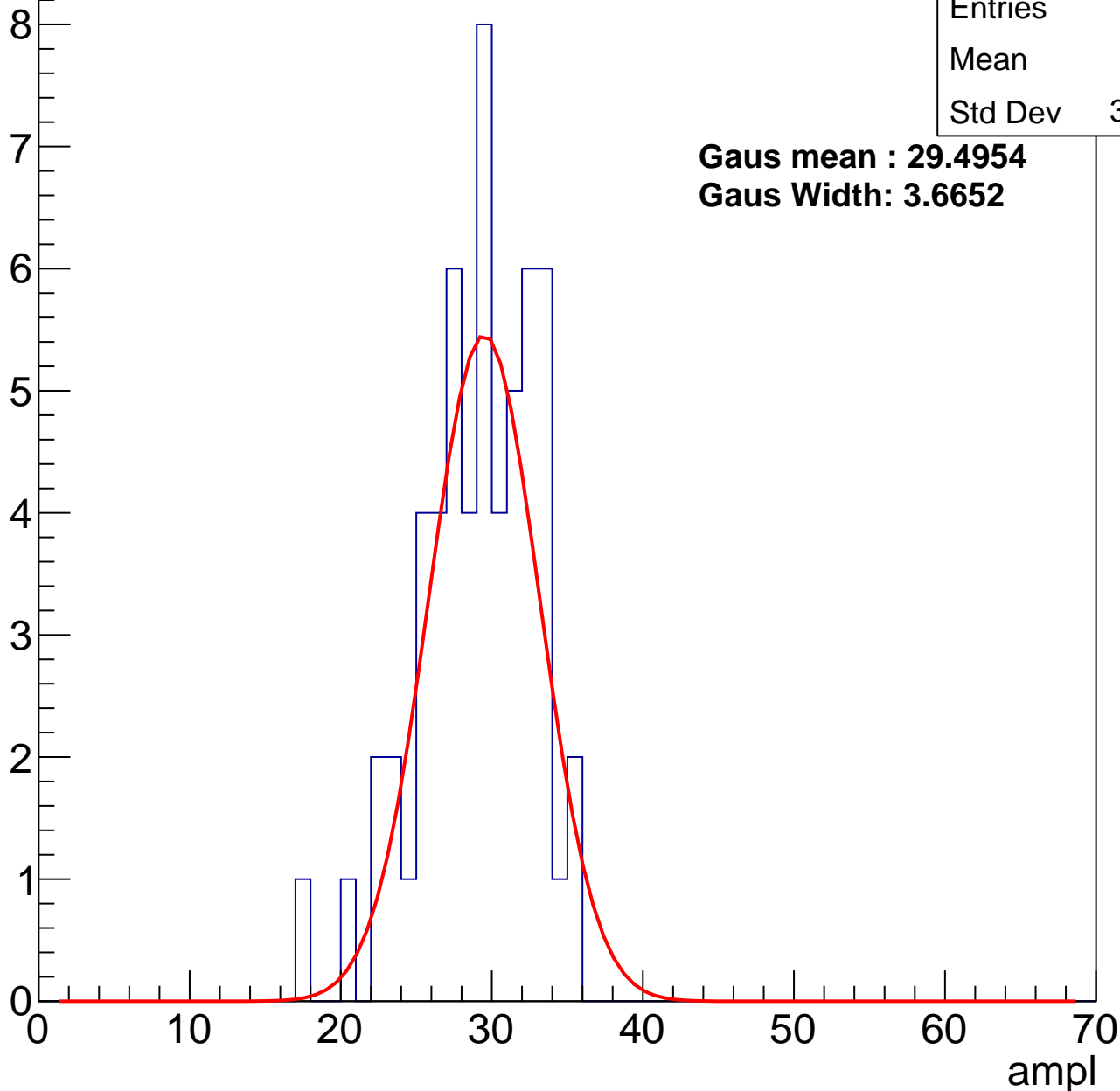
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 28.6 |
| Std Dev | 3.769 |

Gaus mean : 29.4954

Gaus Width: 3.6652



B1L102S, U15-ch74, adc1

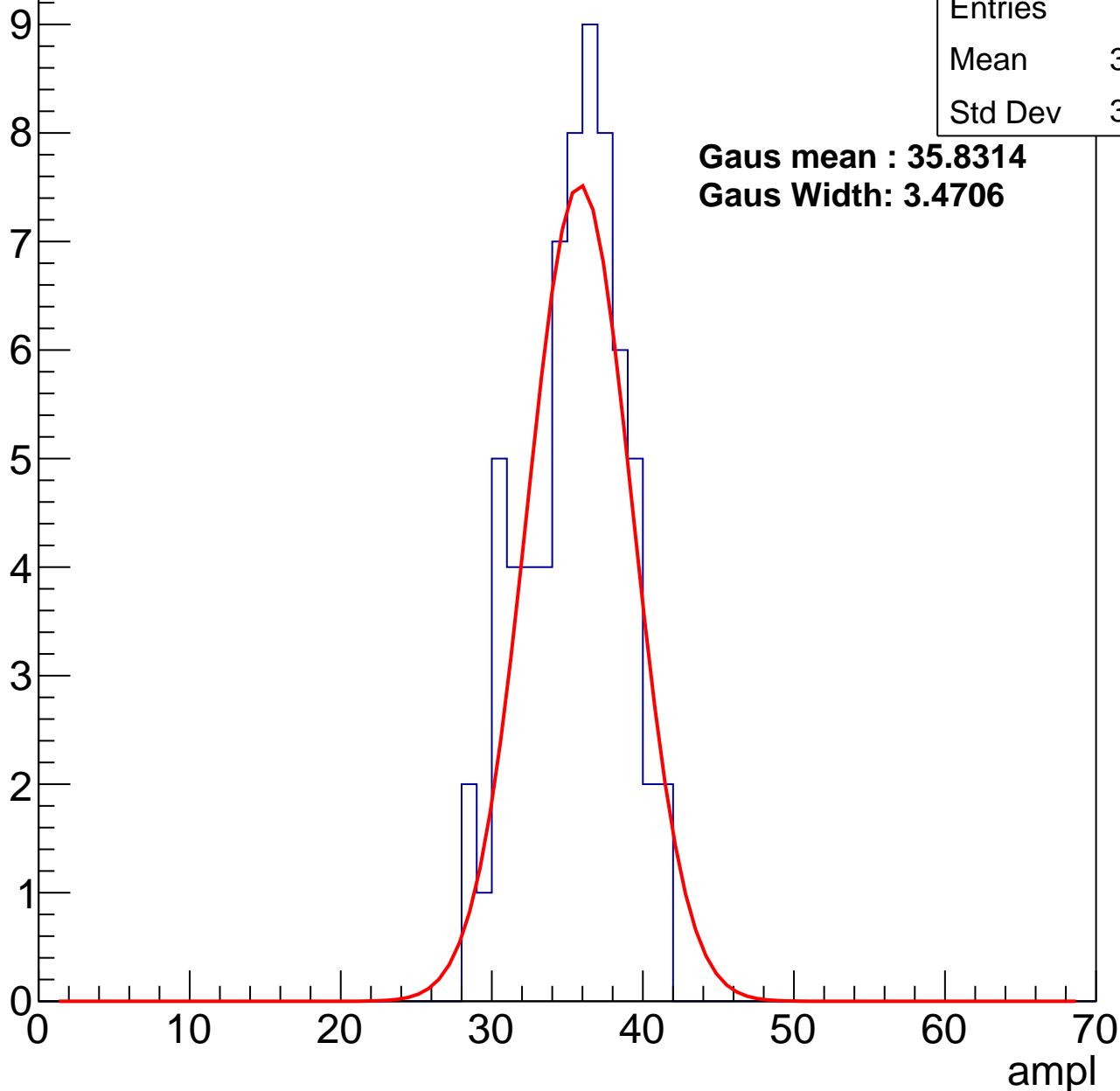
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 34.96 |
| Std Dev | 3.183 |

Gaus mean : 35.8314

Gaus Width: 3.4706



B1L102S, U15-ch74, adc2

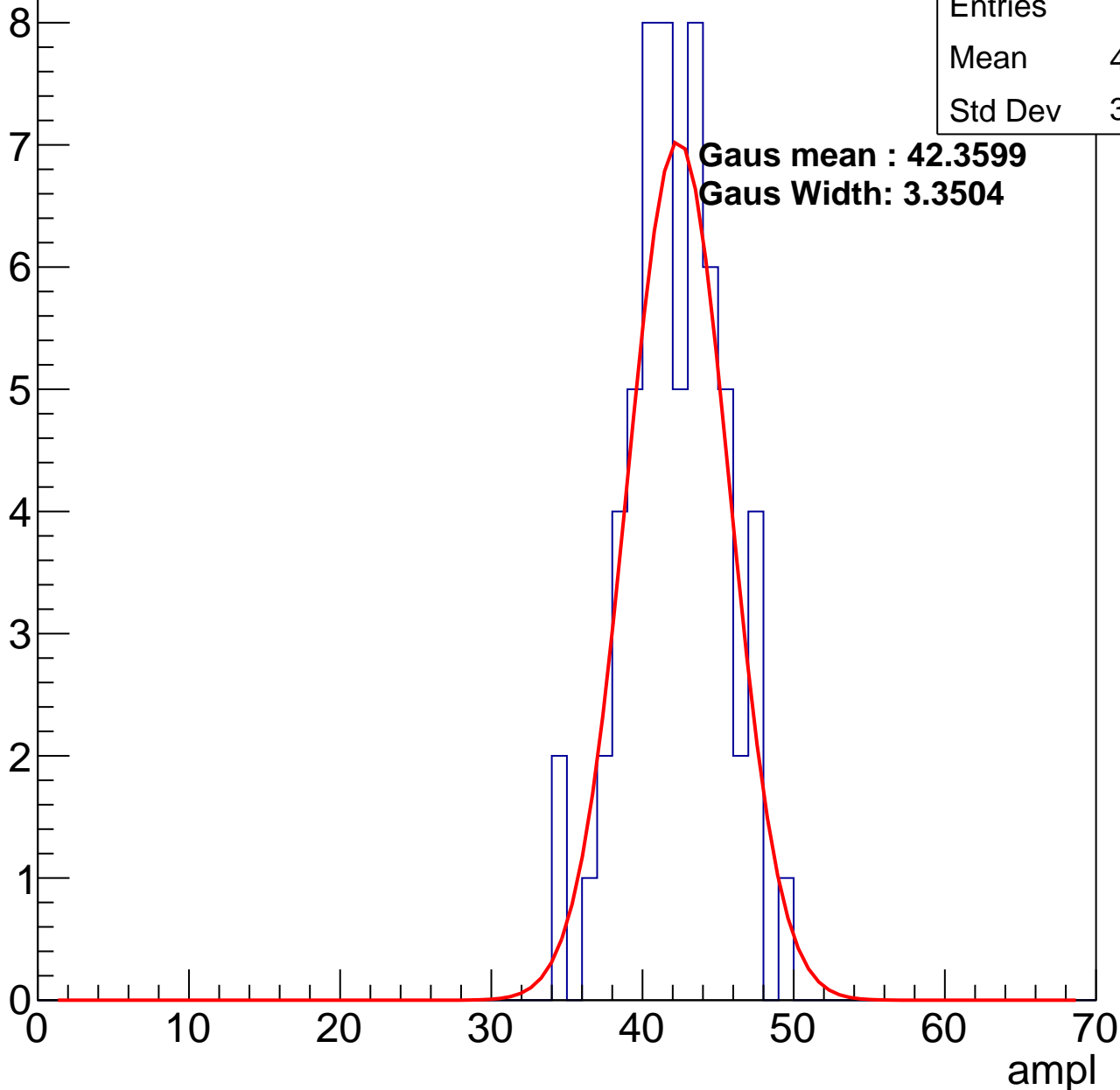
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.72 |
| Std Dev | 3.178 |

Gaus mean : 42.3599

Gaus Width: 3.3504

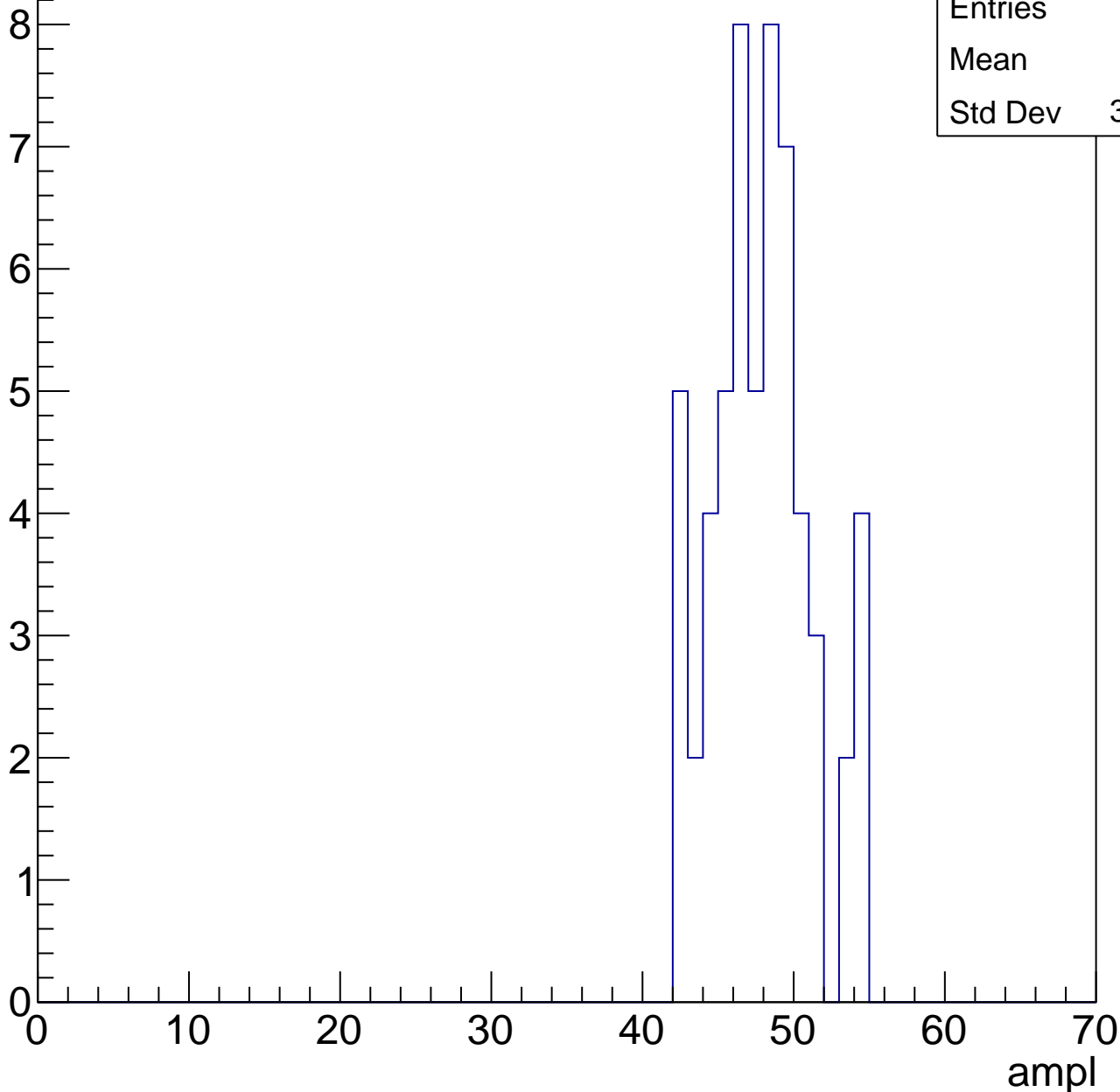


B1L102S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 47.4 |
| Std Dev | 3.244 |

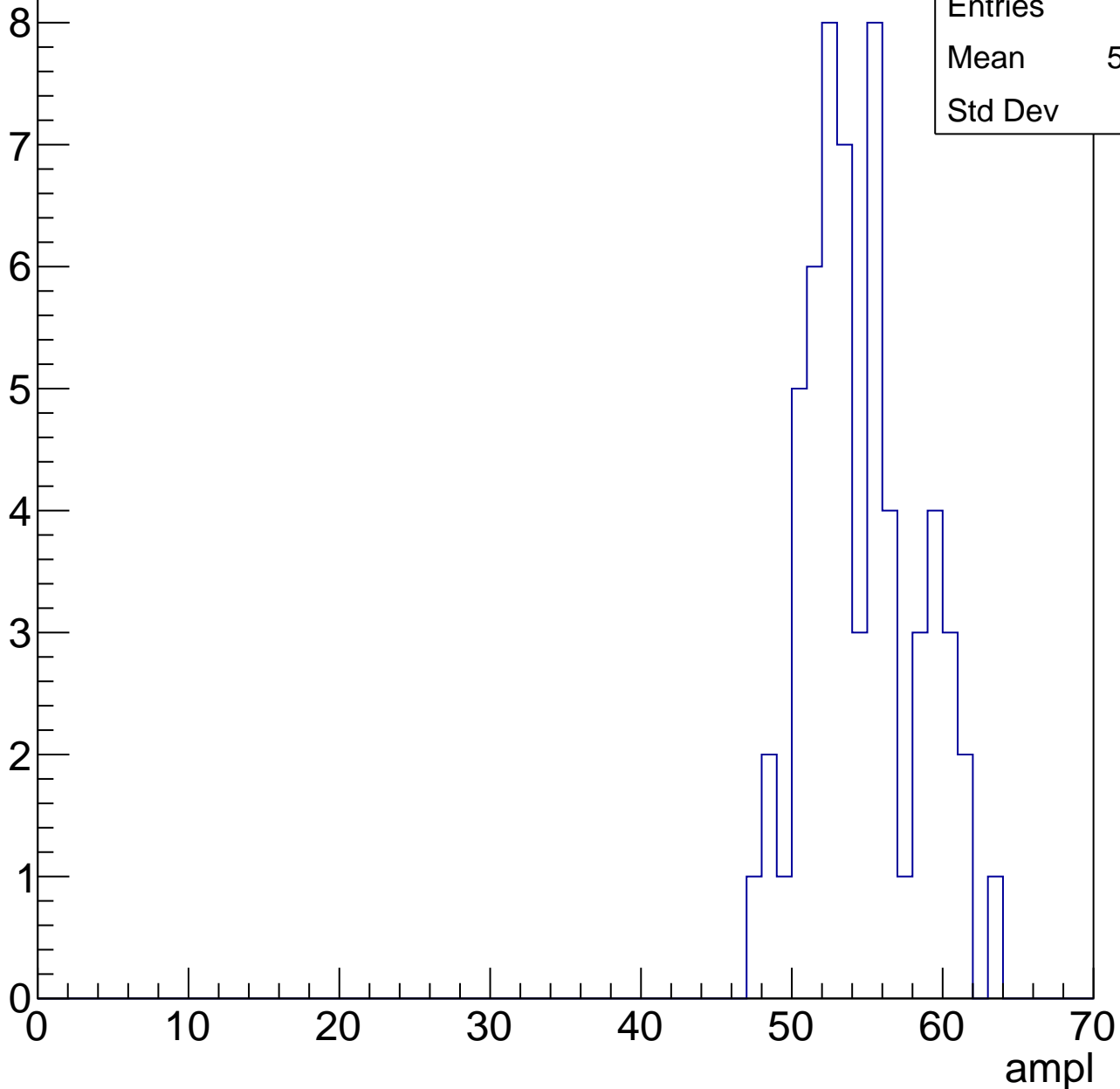


B1L102S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.12 |
| Std Dev | 3.65 |

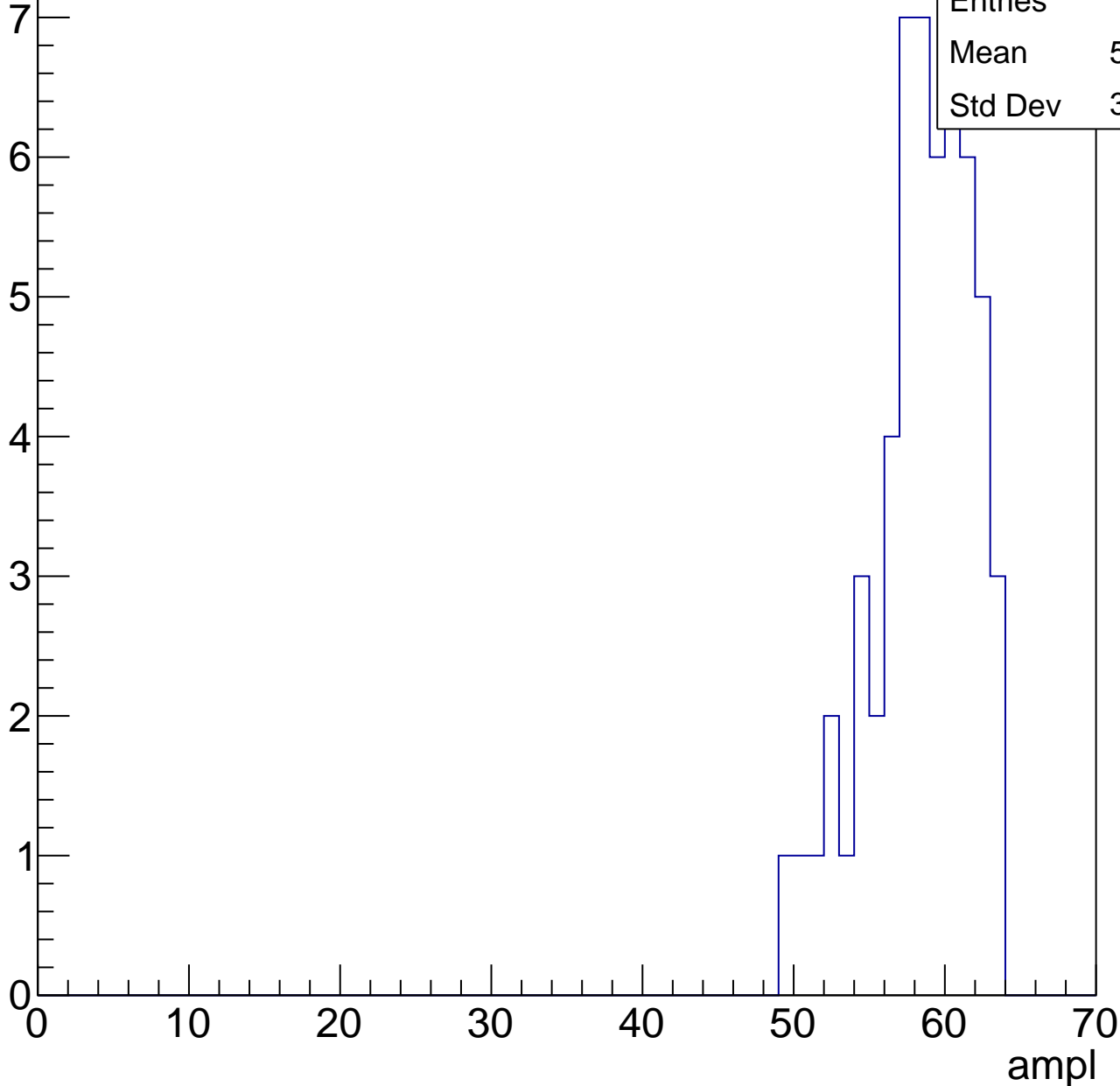


B1L102S, U15-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 57.98 |
| Std Dev | 3.335 |

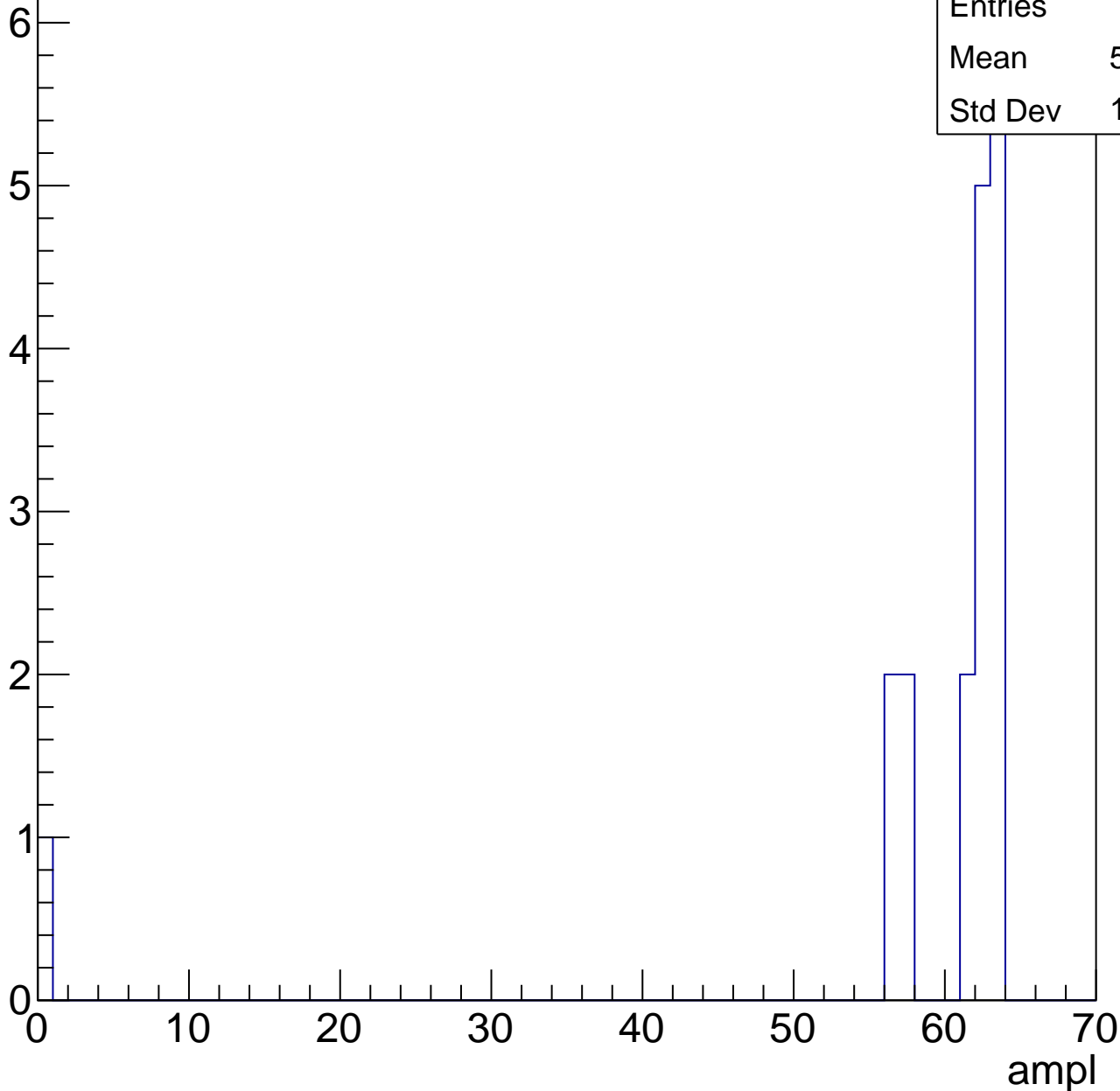


B1L102S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.56 |
| Std Dev | 14.18 |



B1L102S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

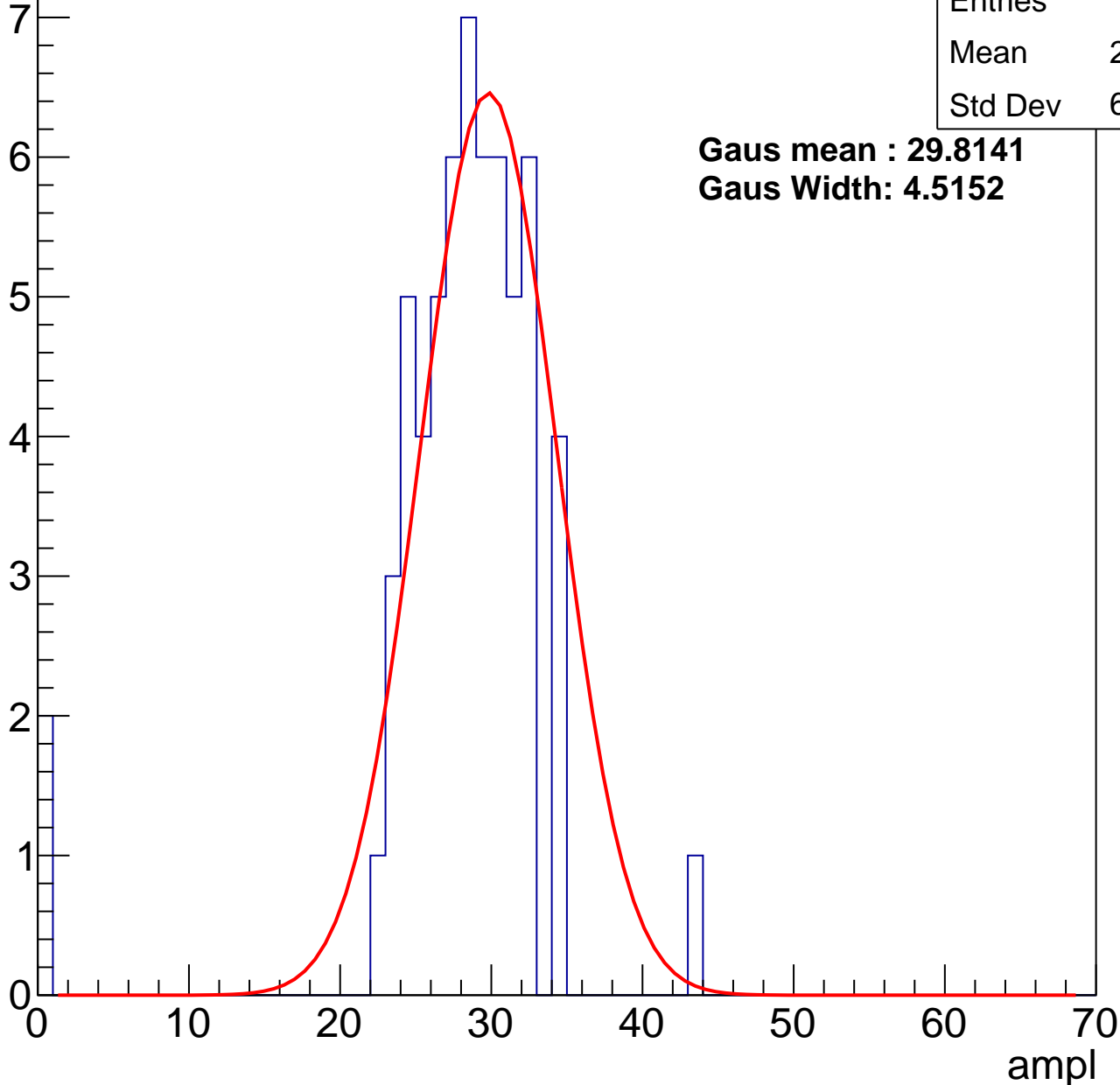
B1L102S, U15-ch75, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 27.52 |
| Std Dev | 6.203 |

Gaus mean : 29.8141
Gaus Width: 4.5152



B1L102S, U15-ch75, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.1 |
| Std Dev | 3.796 |

Gaus mean : 35.2771

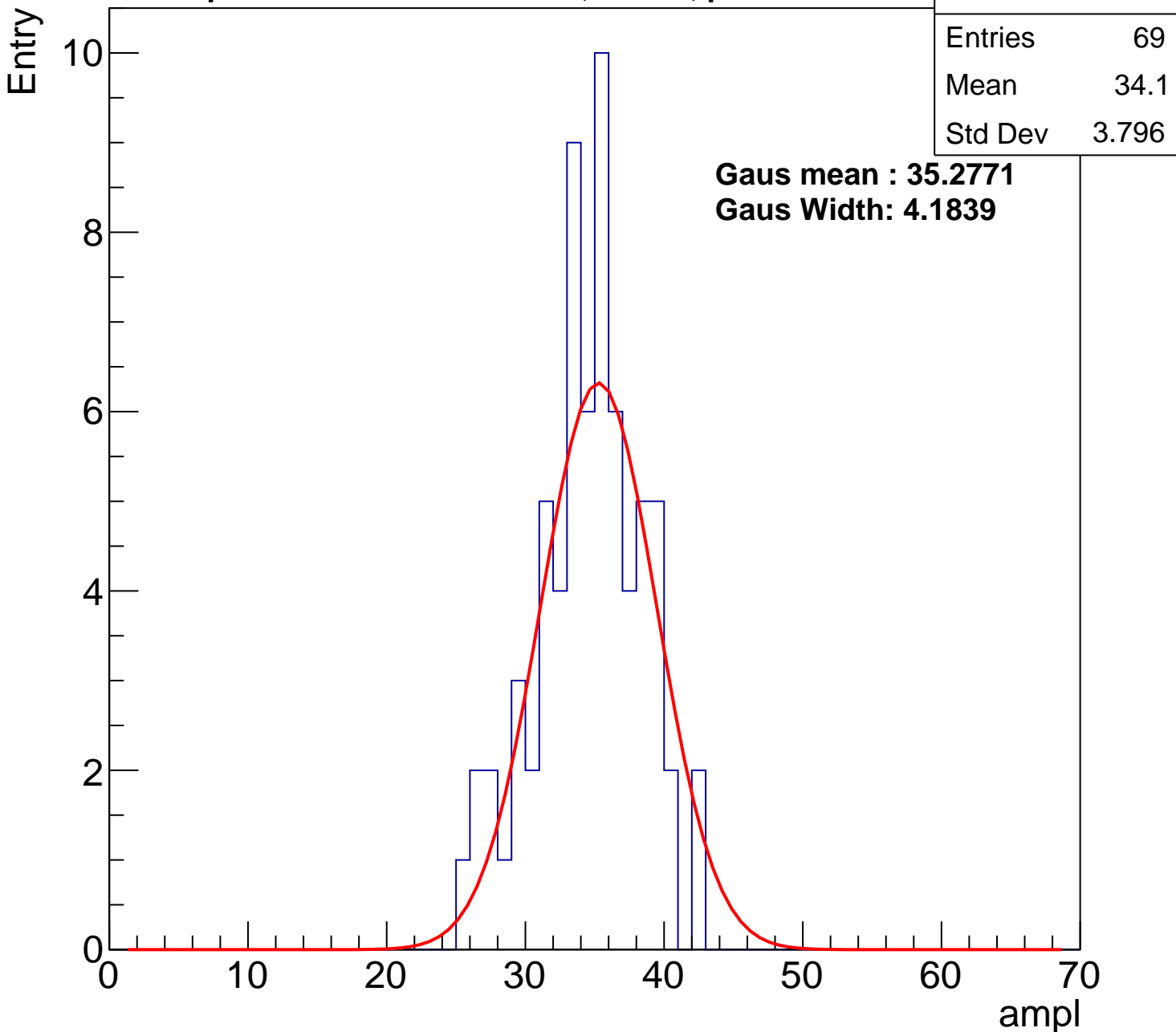
Gaus Width: 4.1839

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch75, adc2

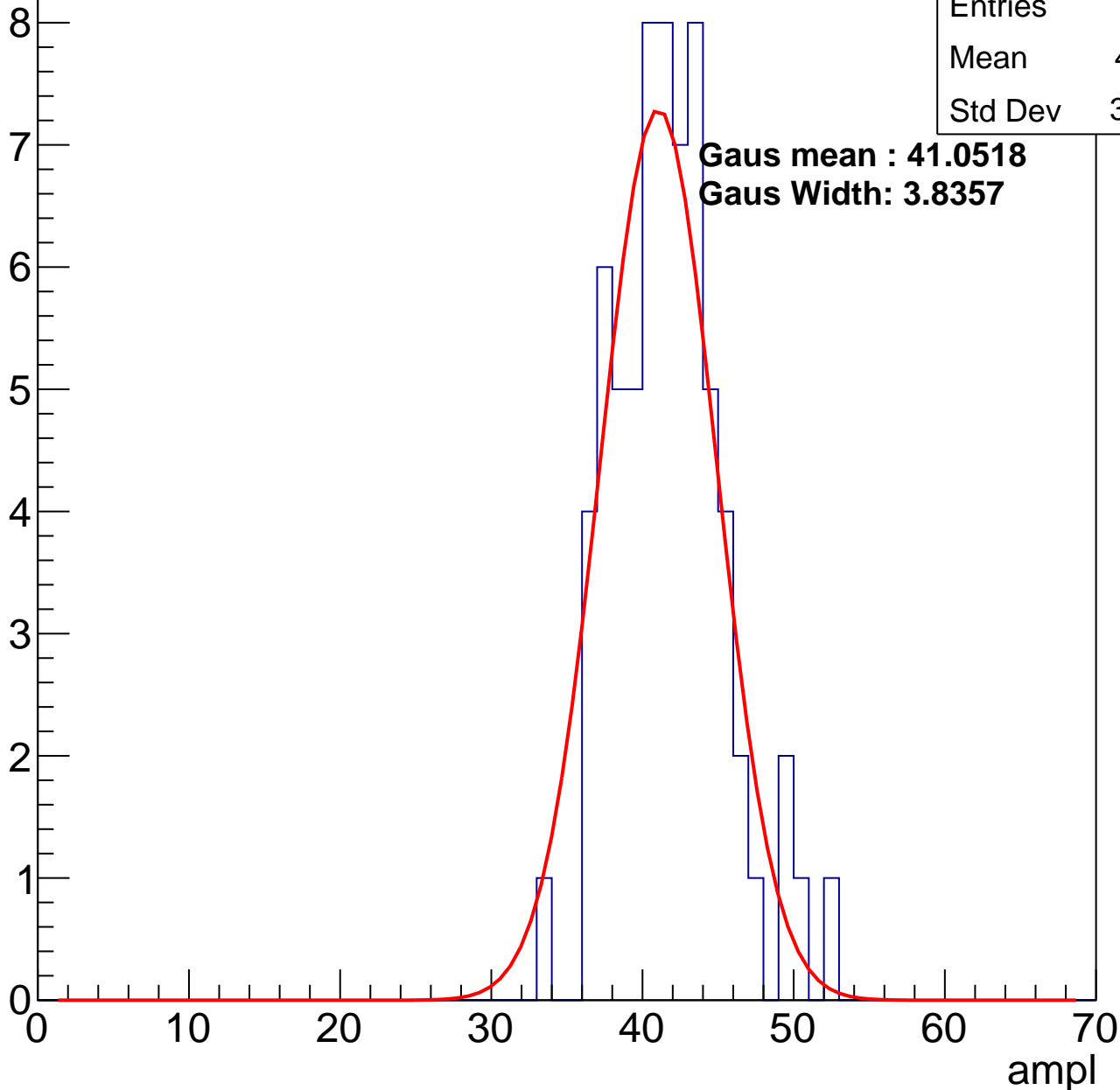
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 41.31 |
| Std Dev | 3.619 |

Gaus mean : 41.0518

Gaus Width: 3.8357

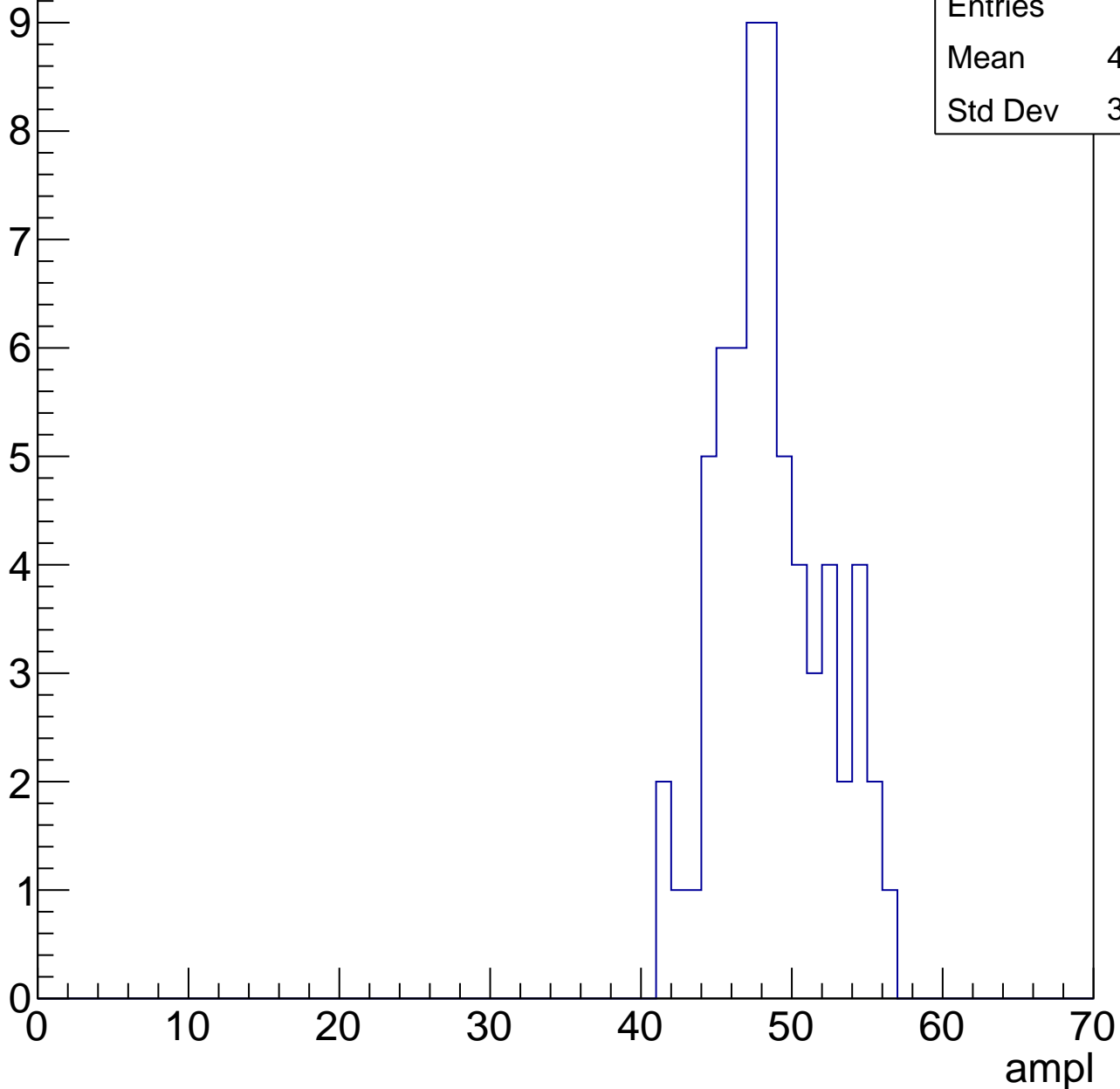


B1L102S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 48.16 |
| Std Dev | 3.519 |

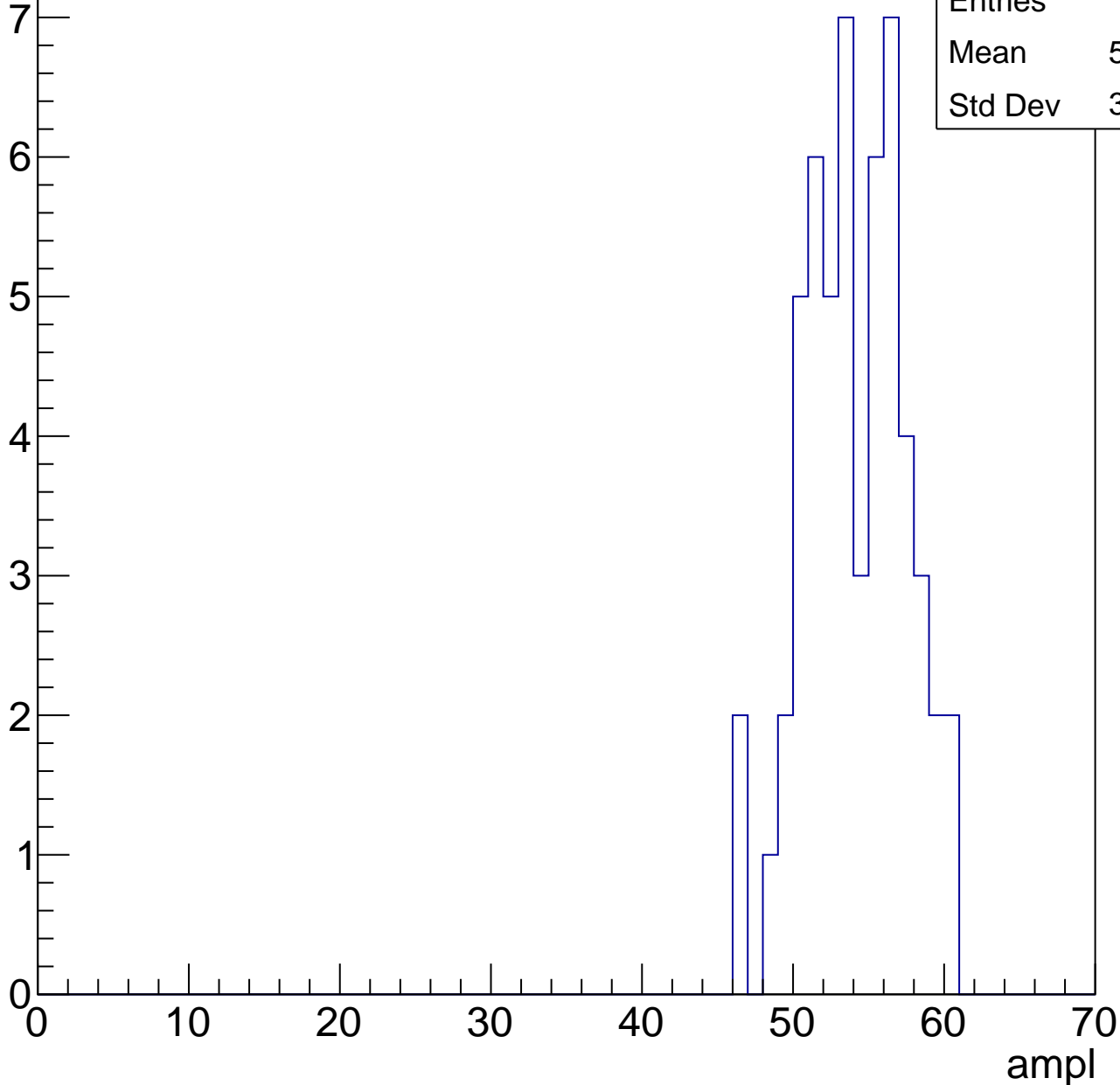


B1L102S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 53.62 |
| Std Dev | 3.322 |

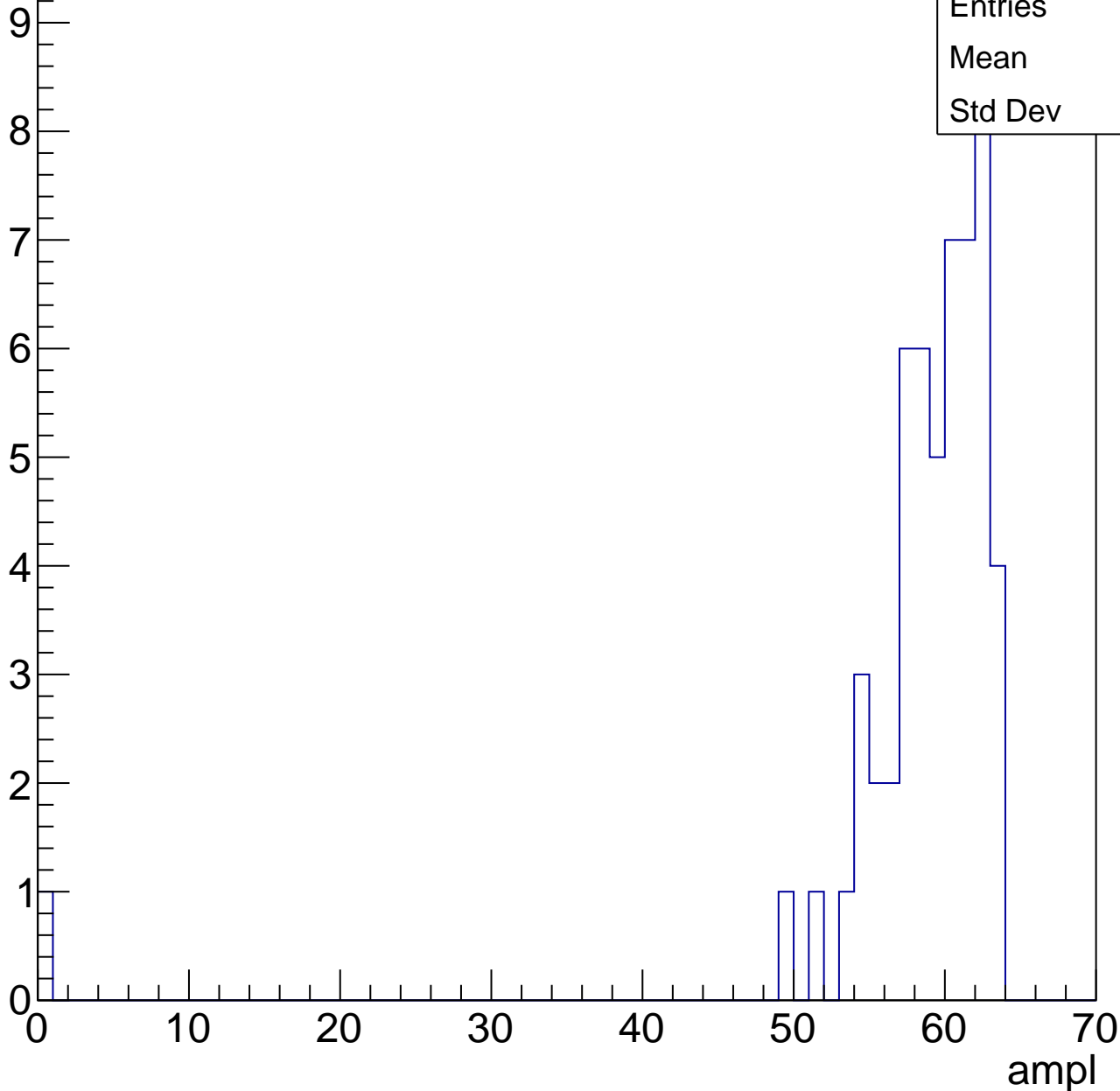


B1L102S, U15-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

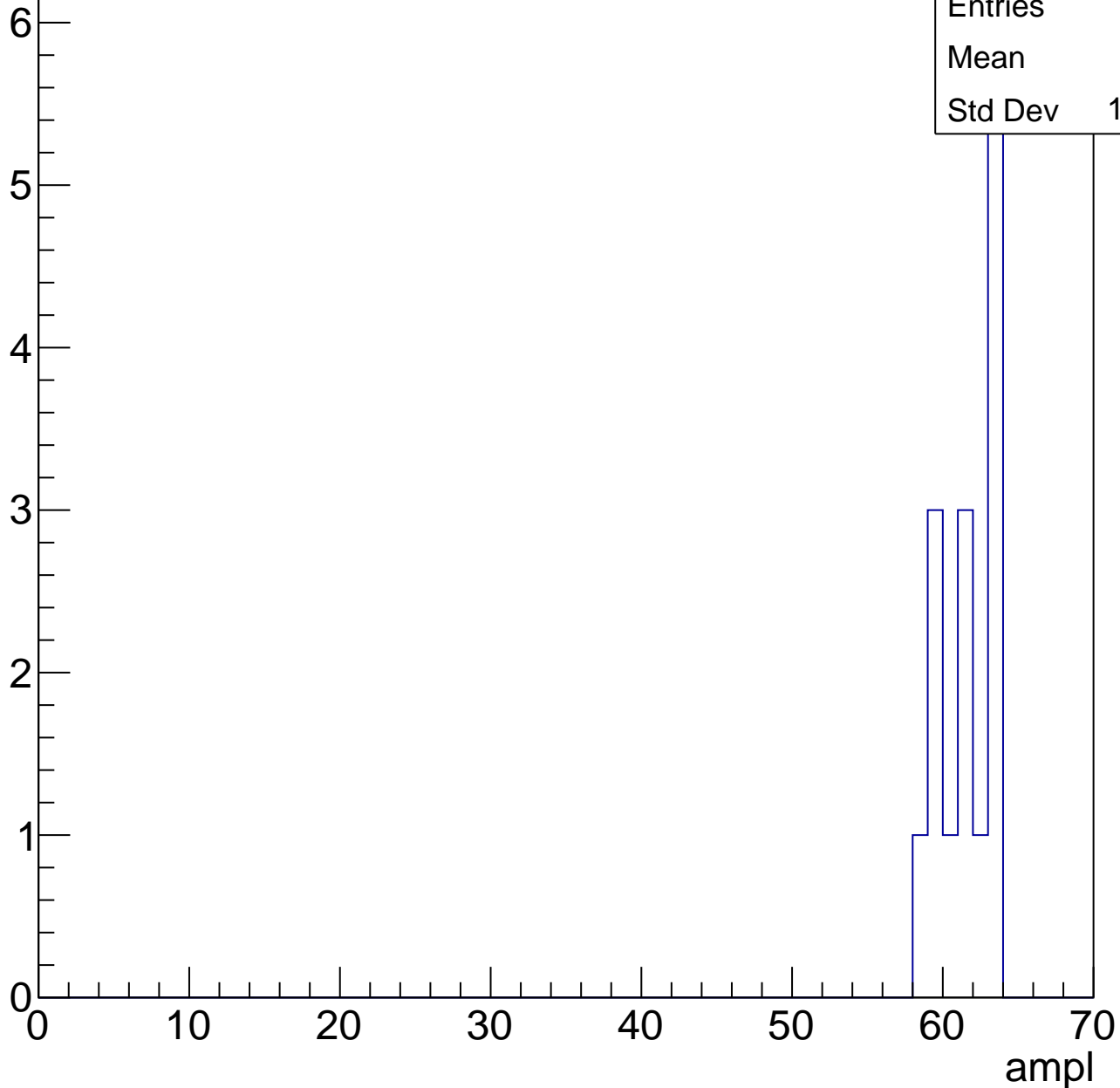
| | |
|---------|------|
| Entries | 55 |
| Mean | 57.8 |
| Std Dev | 8.46 |



B1L102S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

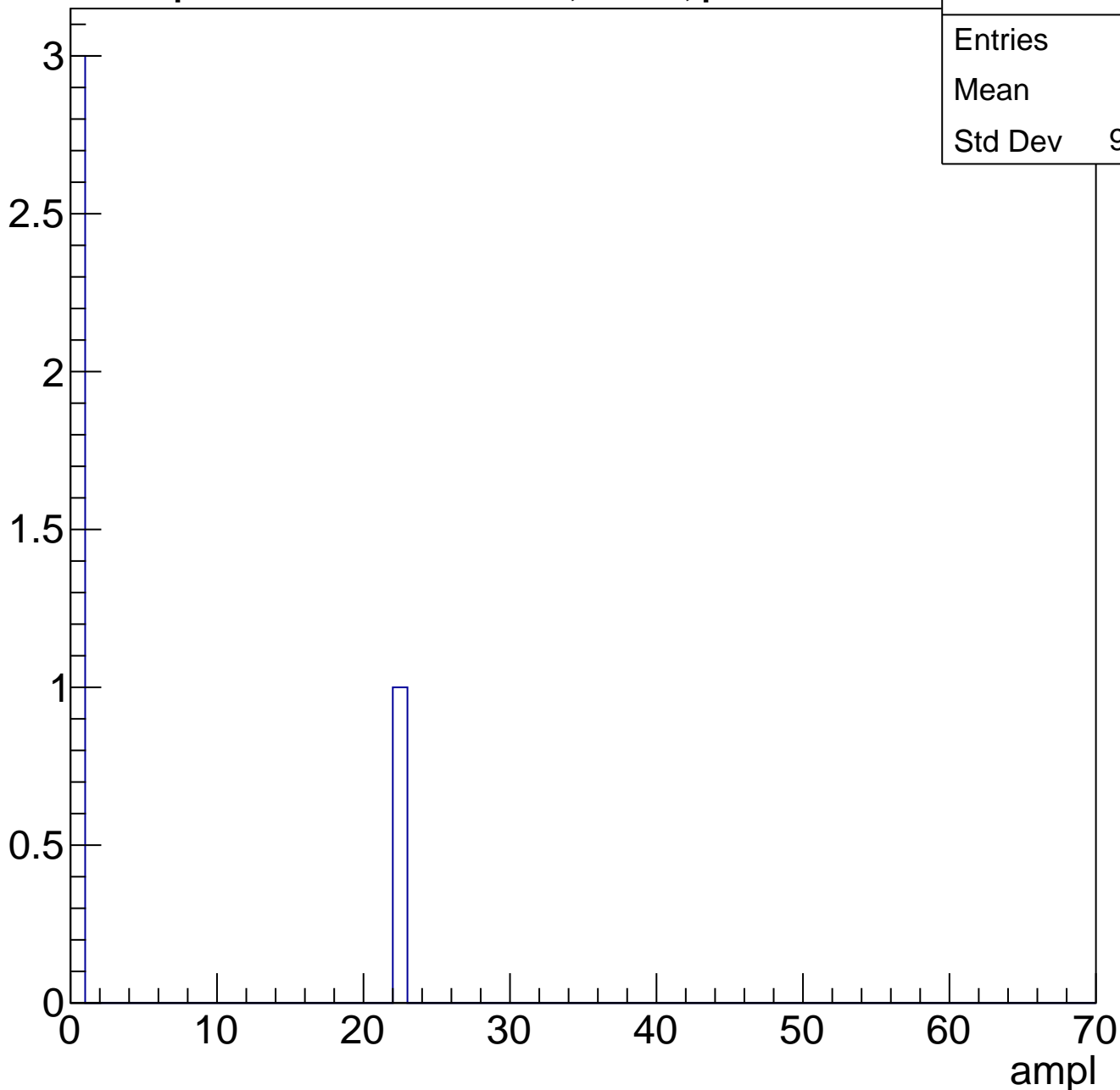
Entry



B1L102S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 5.5 |
| Std Dev | 9.526 |

B1L102S, U15-ch76, adc0

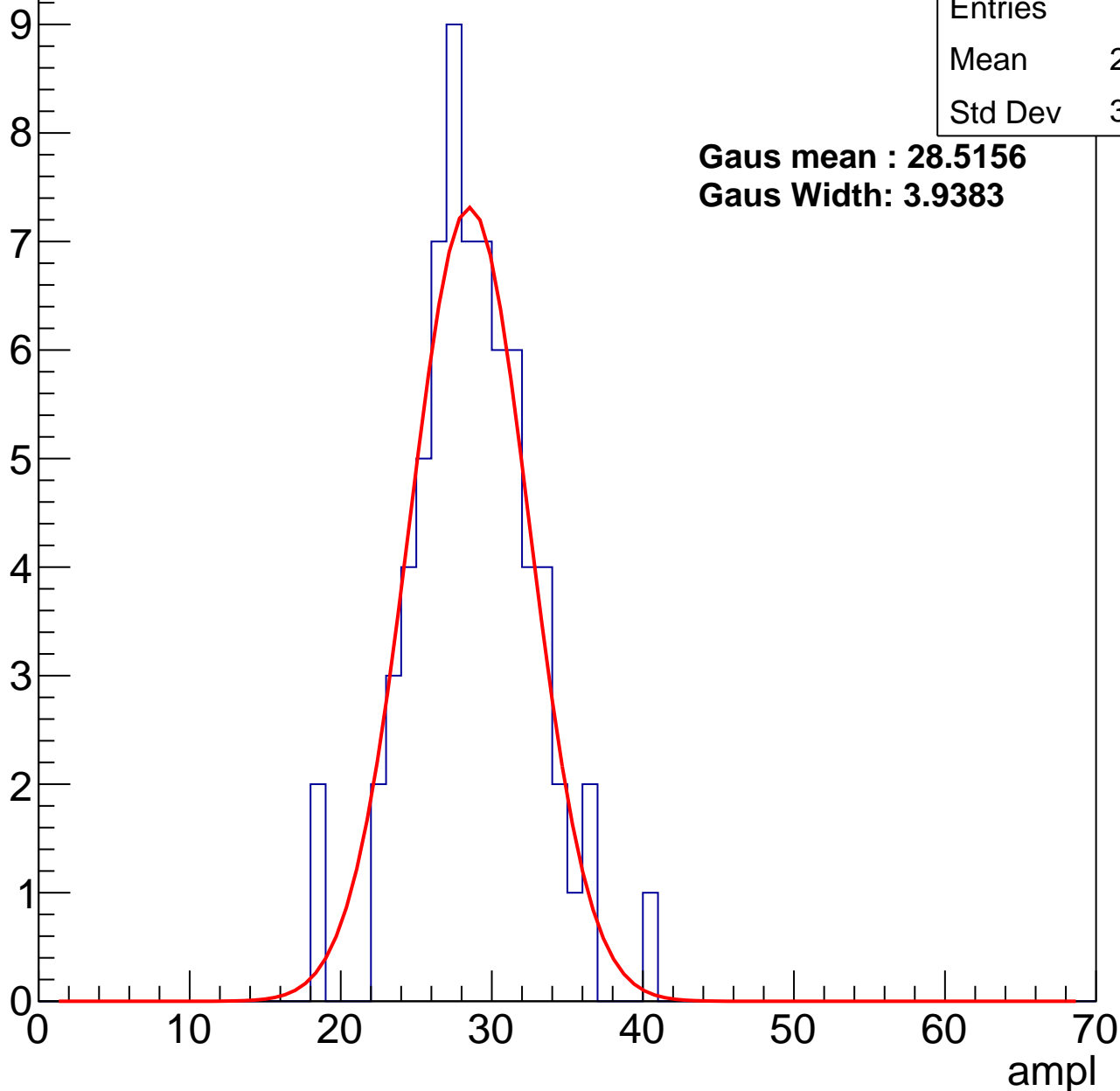
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 28.26 |
| Std Dev | 3.962 |

Gaus mean : 28.5156

Gaus Width: 3.9383



B1L102S, U15-ch76, adc1

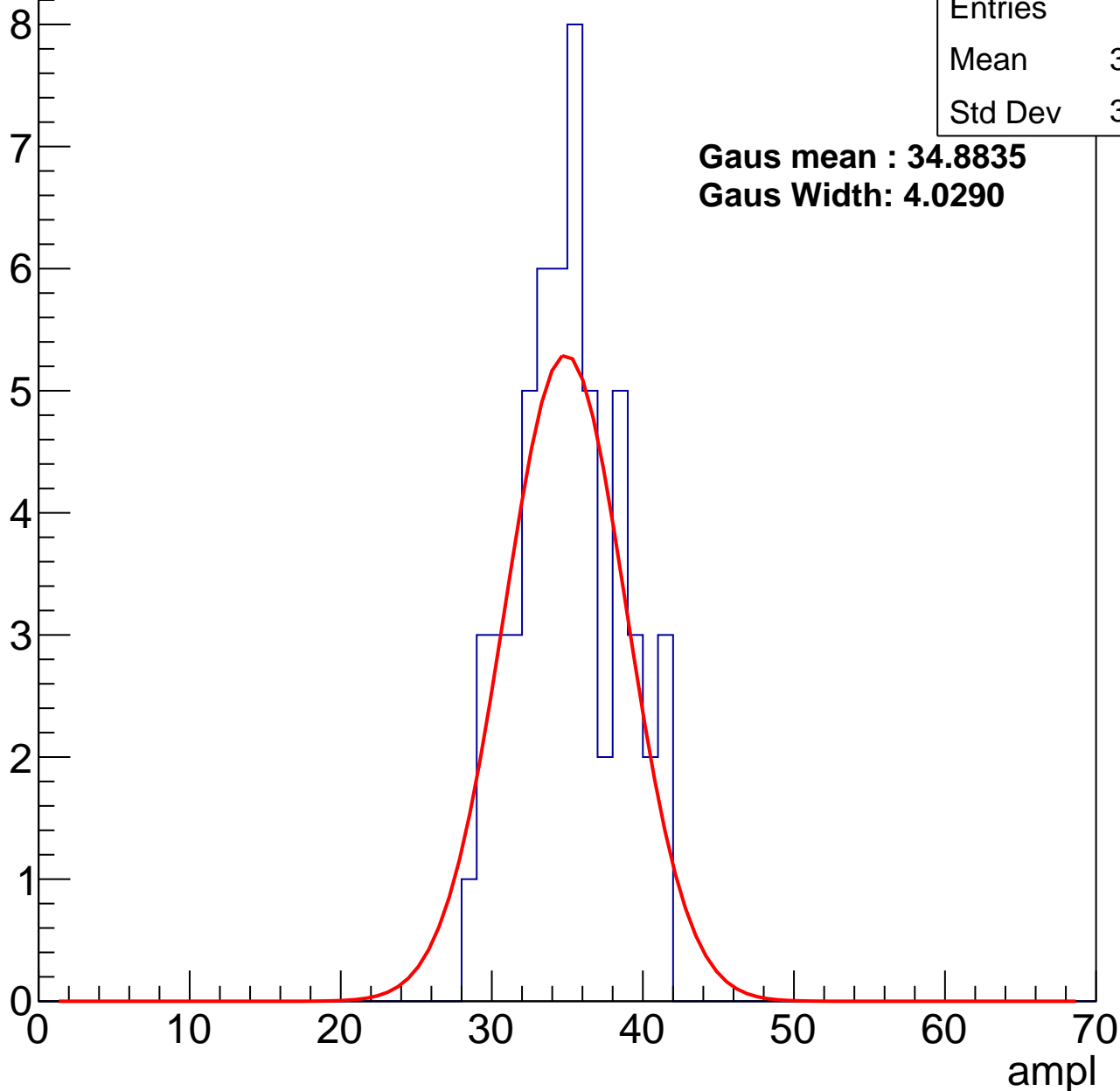
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 34.62 |
| Std Dev | 3.333 |

Gaus mean : 34.8835

Gaus Width: 4.0290



B1L102S, U15-ch76, adc2

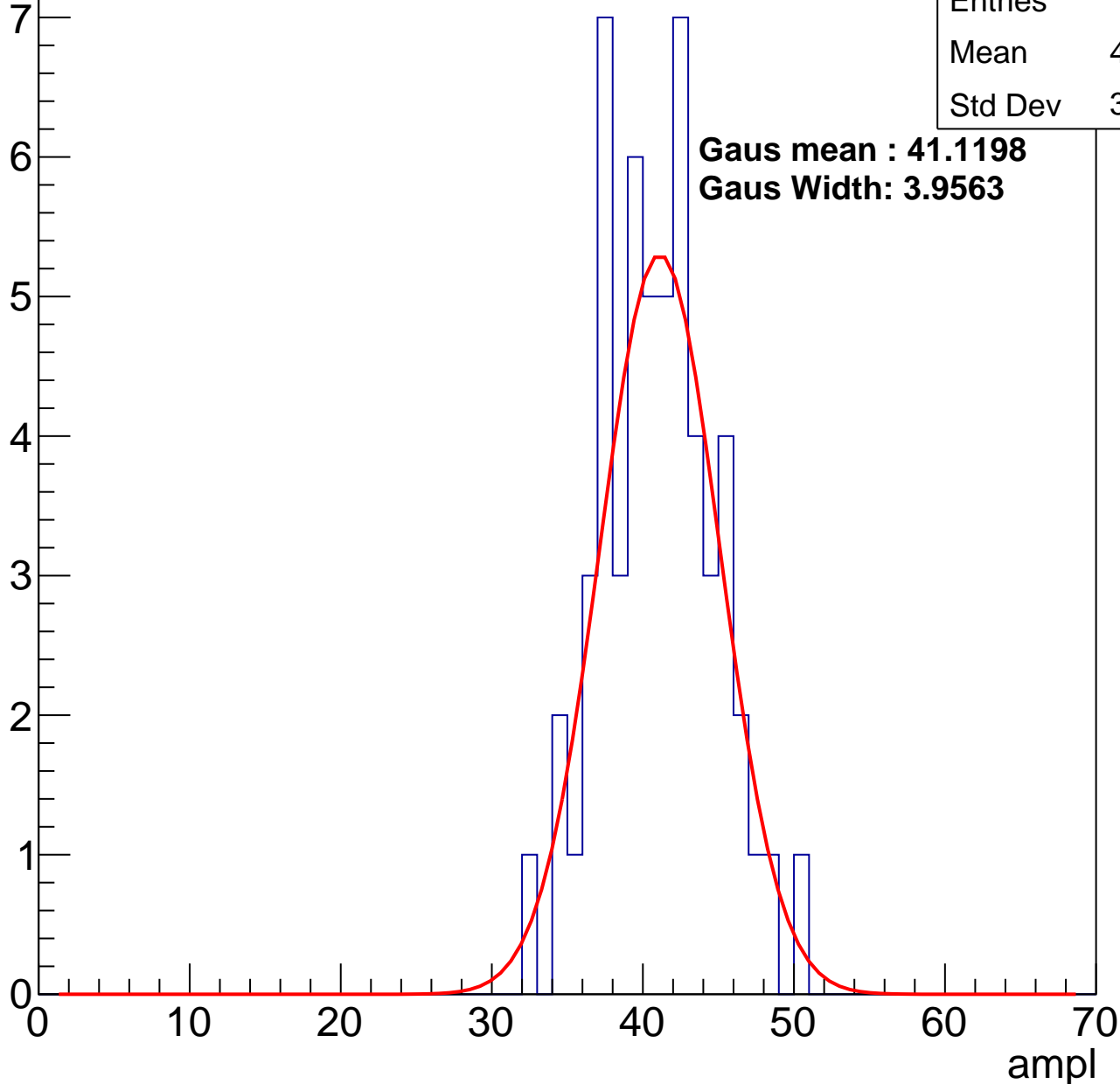
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 40.54 |
| Std Dev | 3.722 |

Gaus mean : 41.1198

Gaus Width: 3.9563

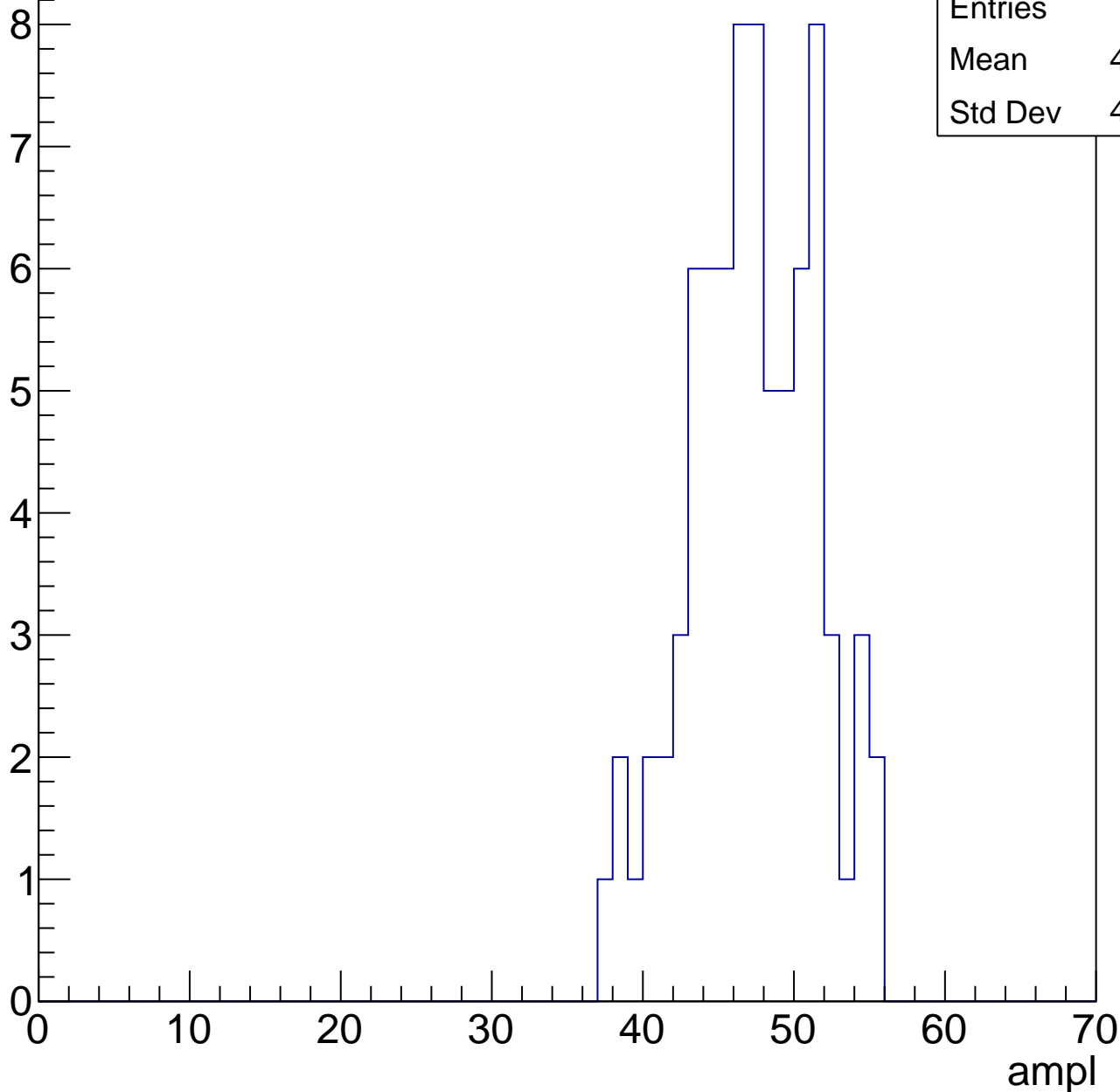


B1L102S, U15-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 46.79 |
| Std Dev | 4.149 |

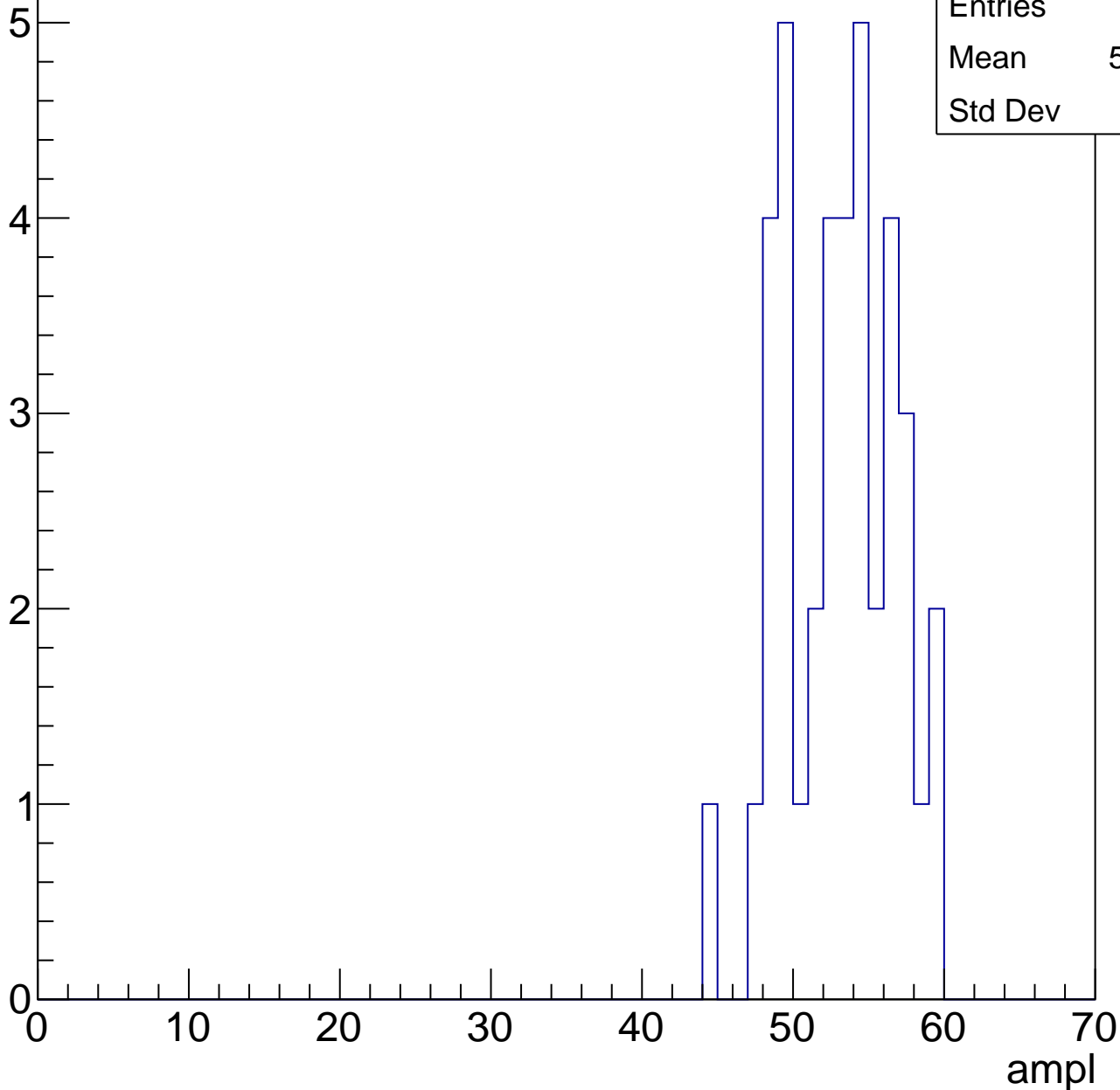


B1L102S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 52.59 |
| Std Dev | 3.6 |

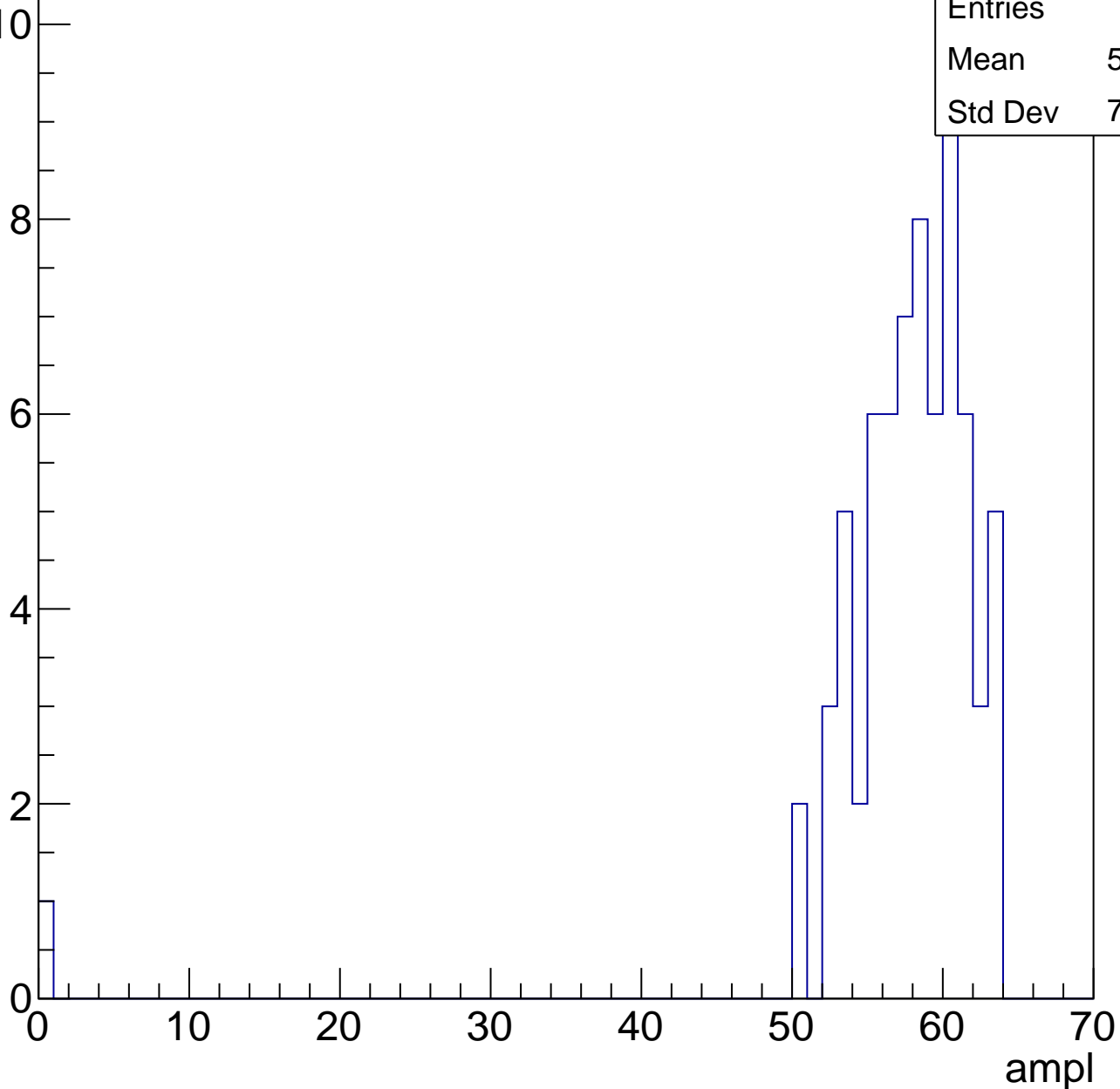


B1L102S, U15-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 56.84 |
| Std Dev | 7.579 |

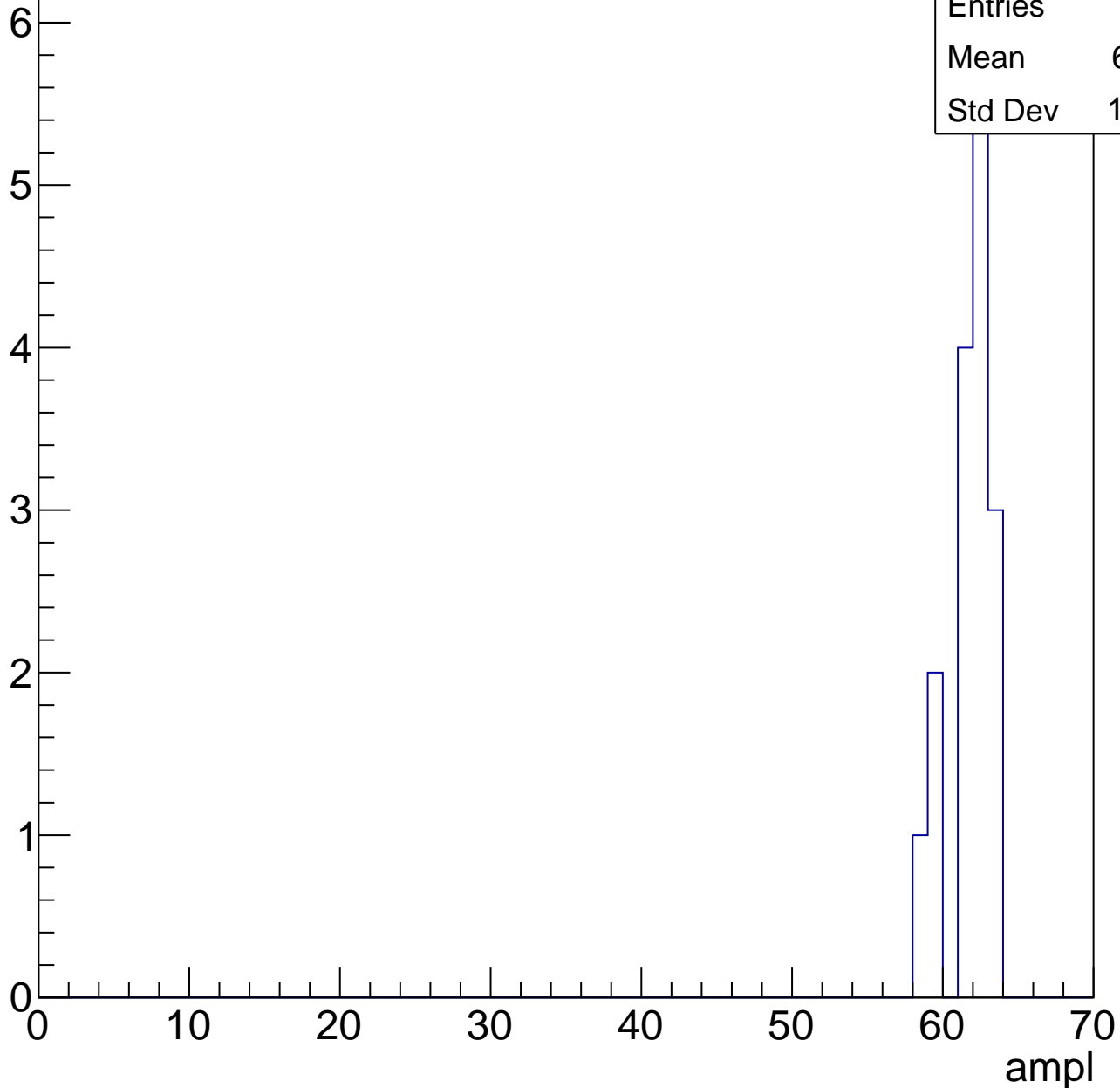


B1L102S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

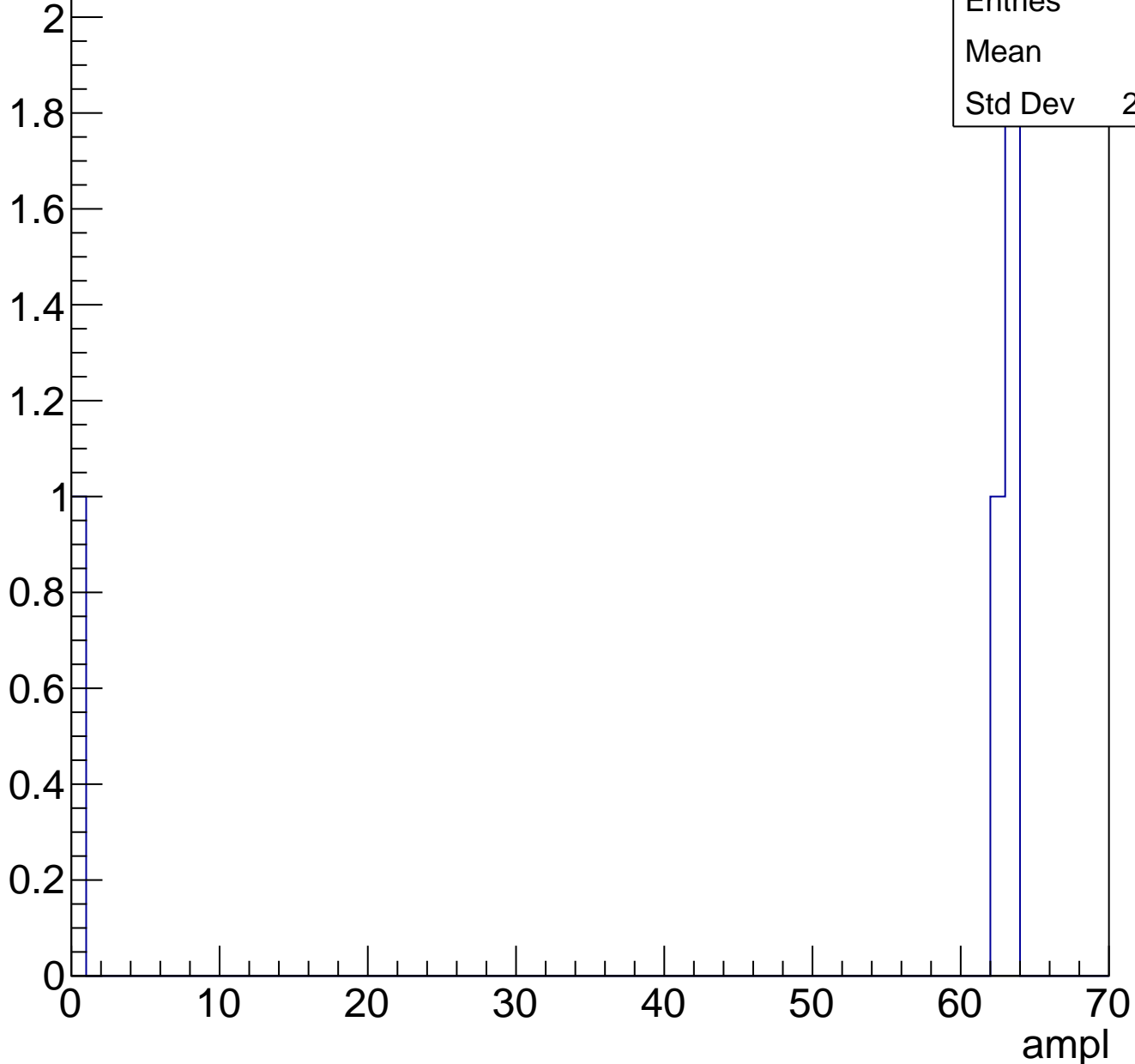
| | |
|---------|-------|
| Entries | 16 |
| Mean | 61.31 |
| Std Dev | 1.446 |



B1L102S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch77, adc0

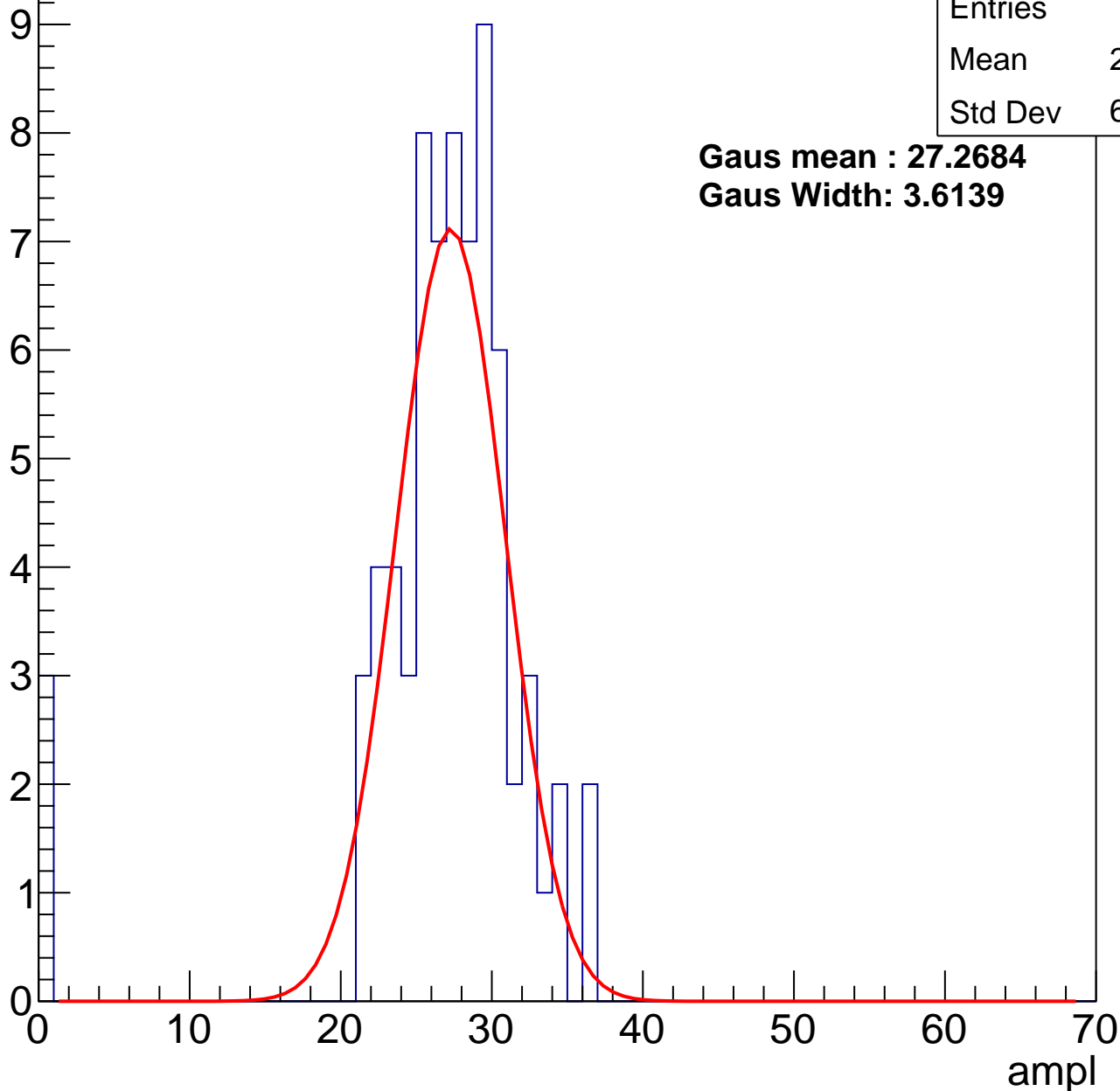
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 26.12 |
| Std Dev | 6.422 |

Gaus mean : 27.2684

Gaus Width: 3.6139



B1L102S, U15-ch77, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 85 |
| Mean | 34.56 |
| Std Dev | 3.858 |

Gaus mean : 35.5285

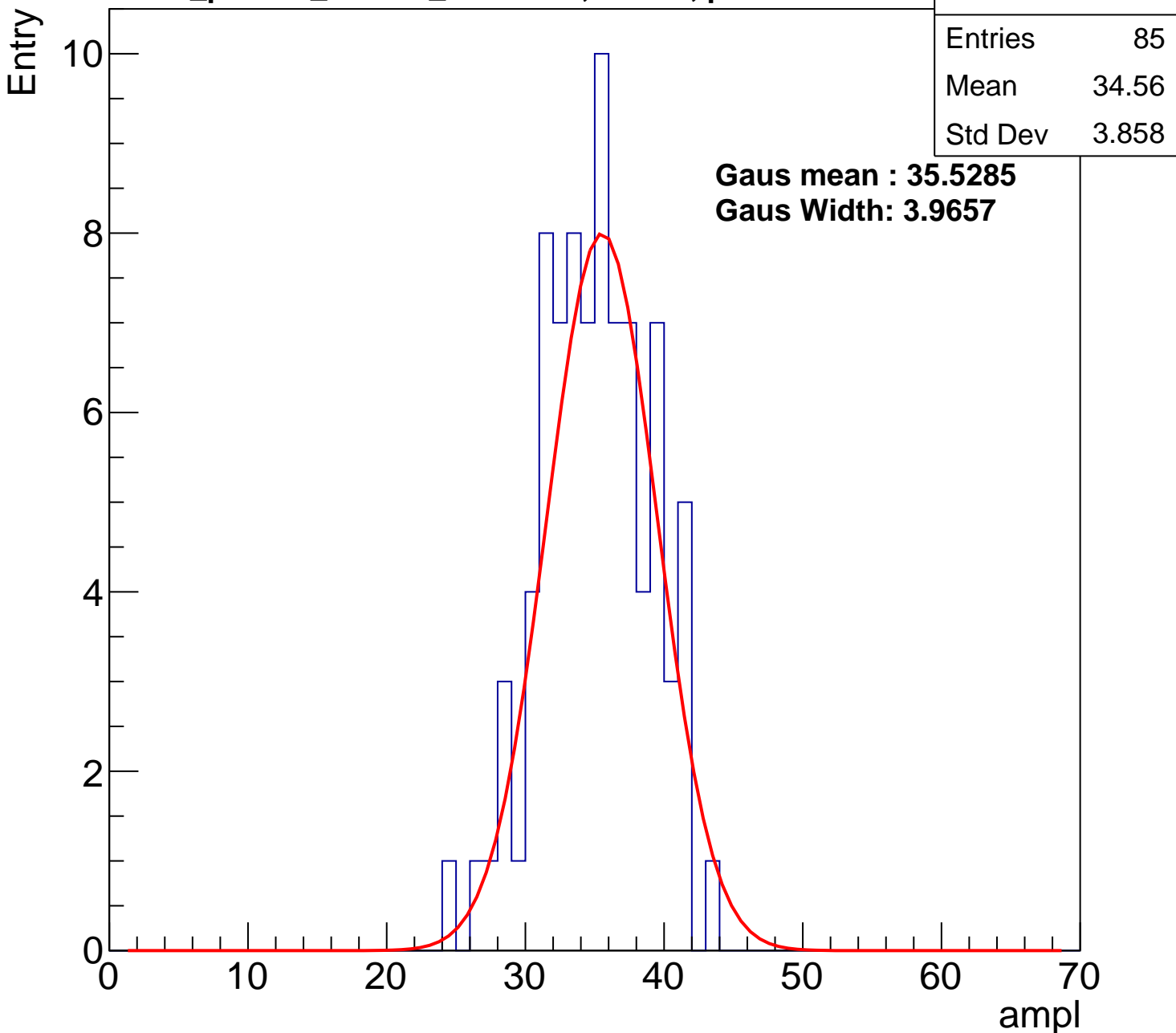
Gaus Width: 3.9657

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch77, adc2

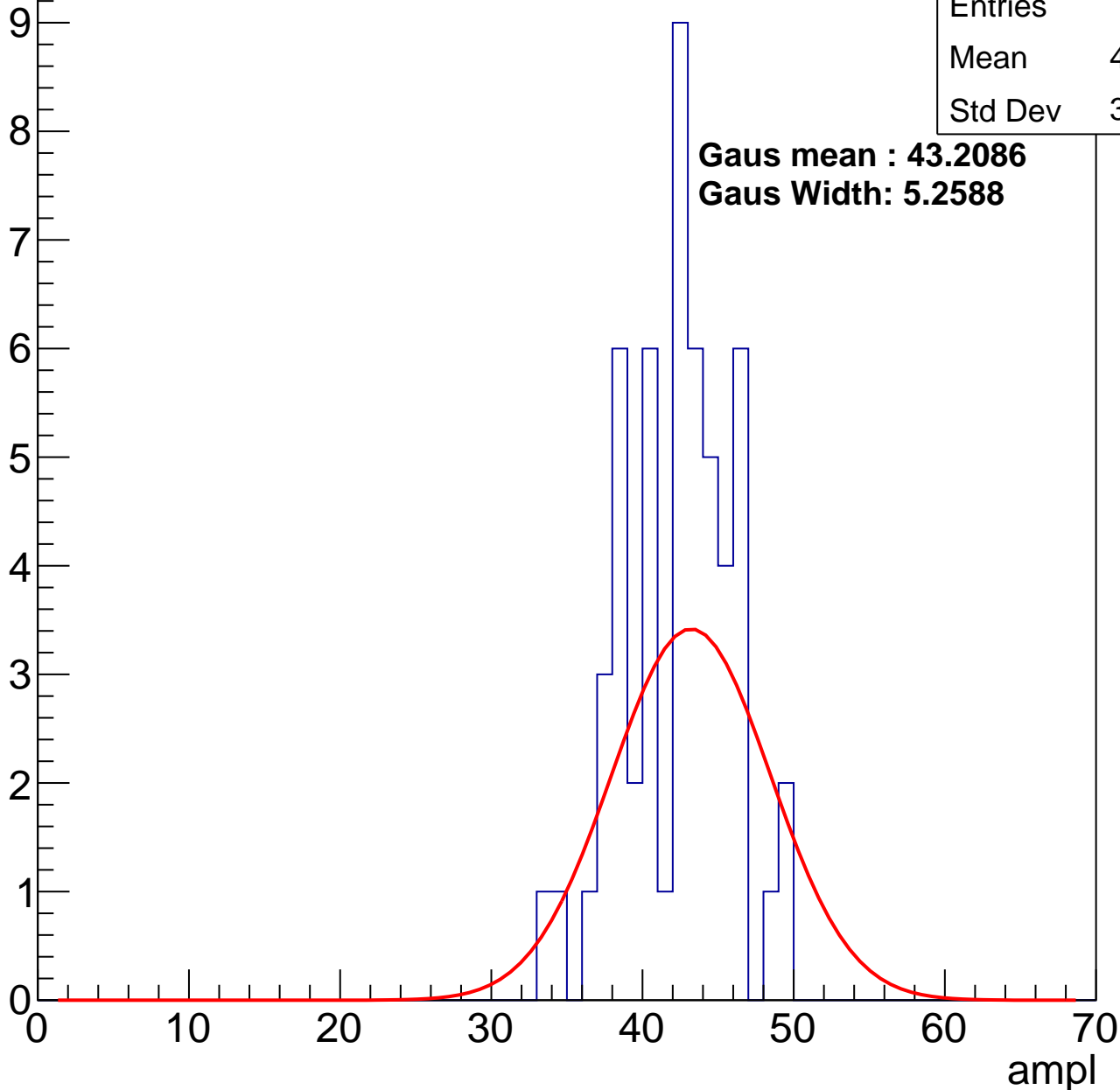
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 41.83 |
| Std Dev | 3.563 |

Gaus mean : 43.2086

Gaus Width: 5.2588

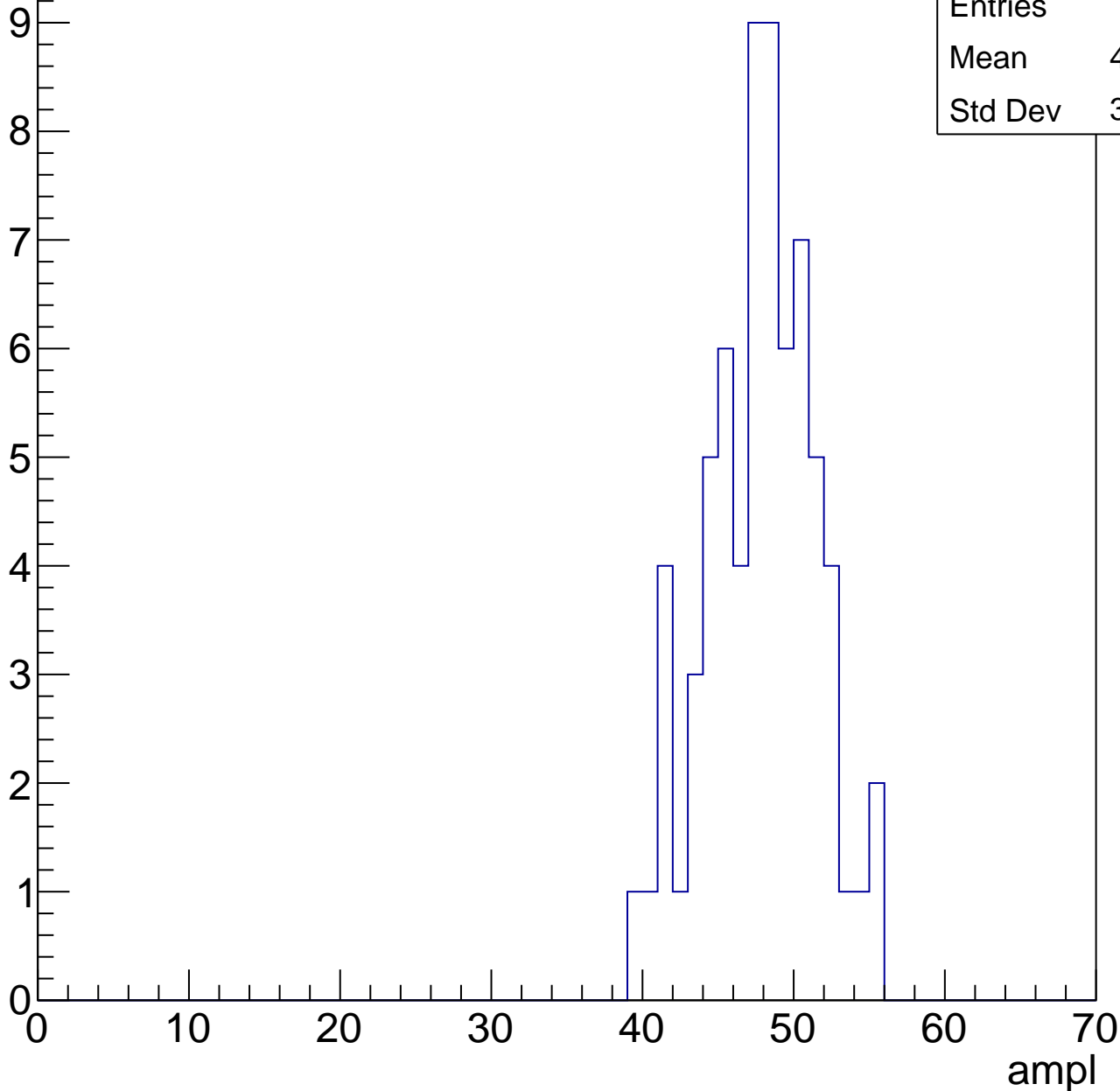


B1L102S, U15-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 47.35 |
| Std Dev | 3.579 |

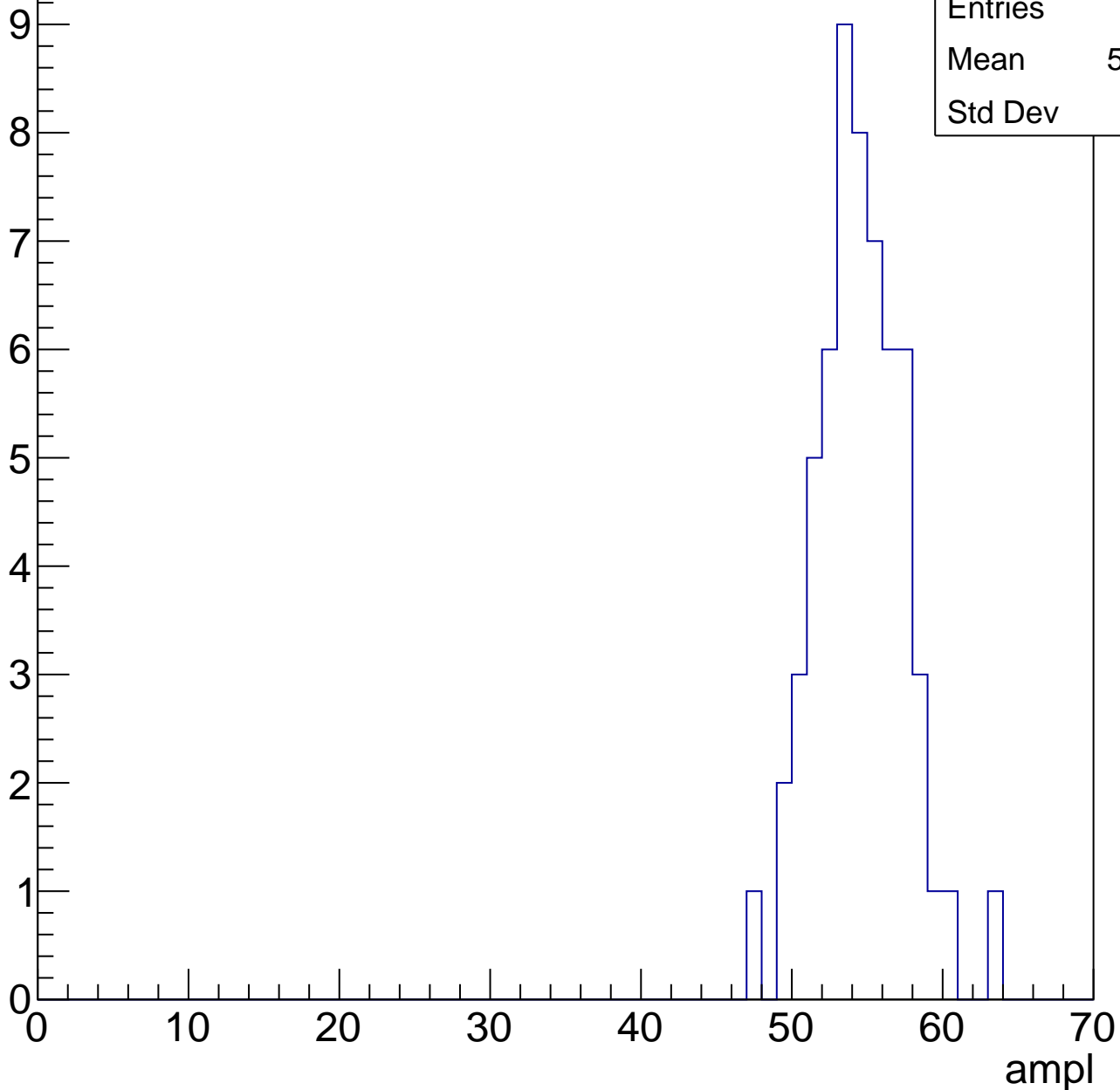


B1L102S, U15-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 54.07 |
| Std Dev | 2.91 |

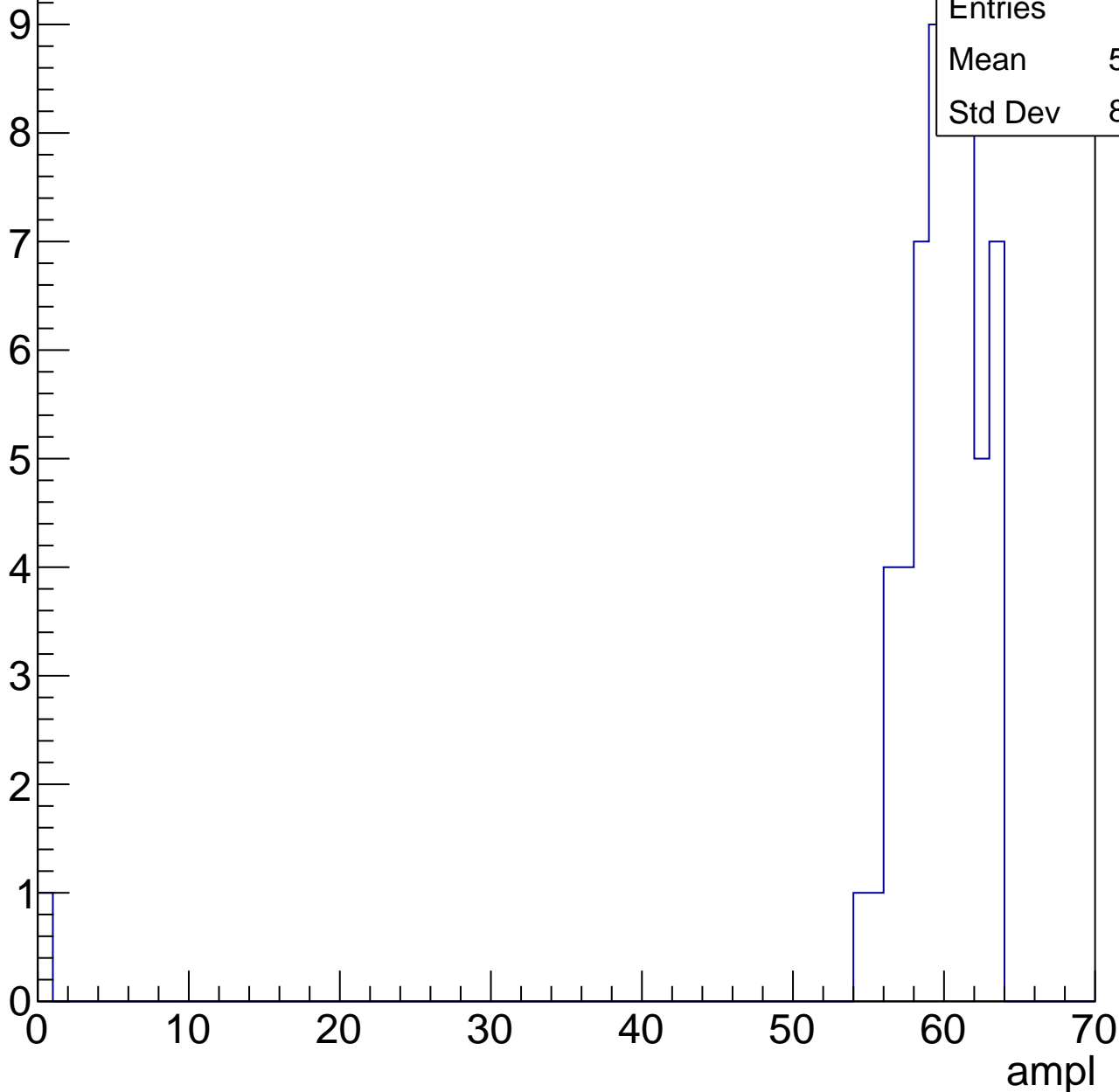


B1L102S, U15-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.54 |
| Std Dev | 8.205 |

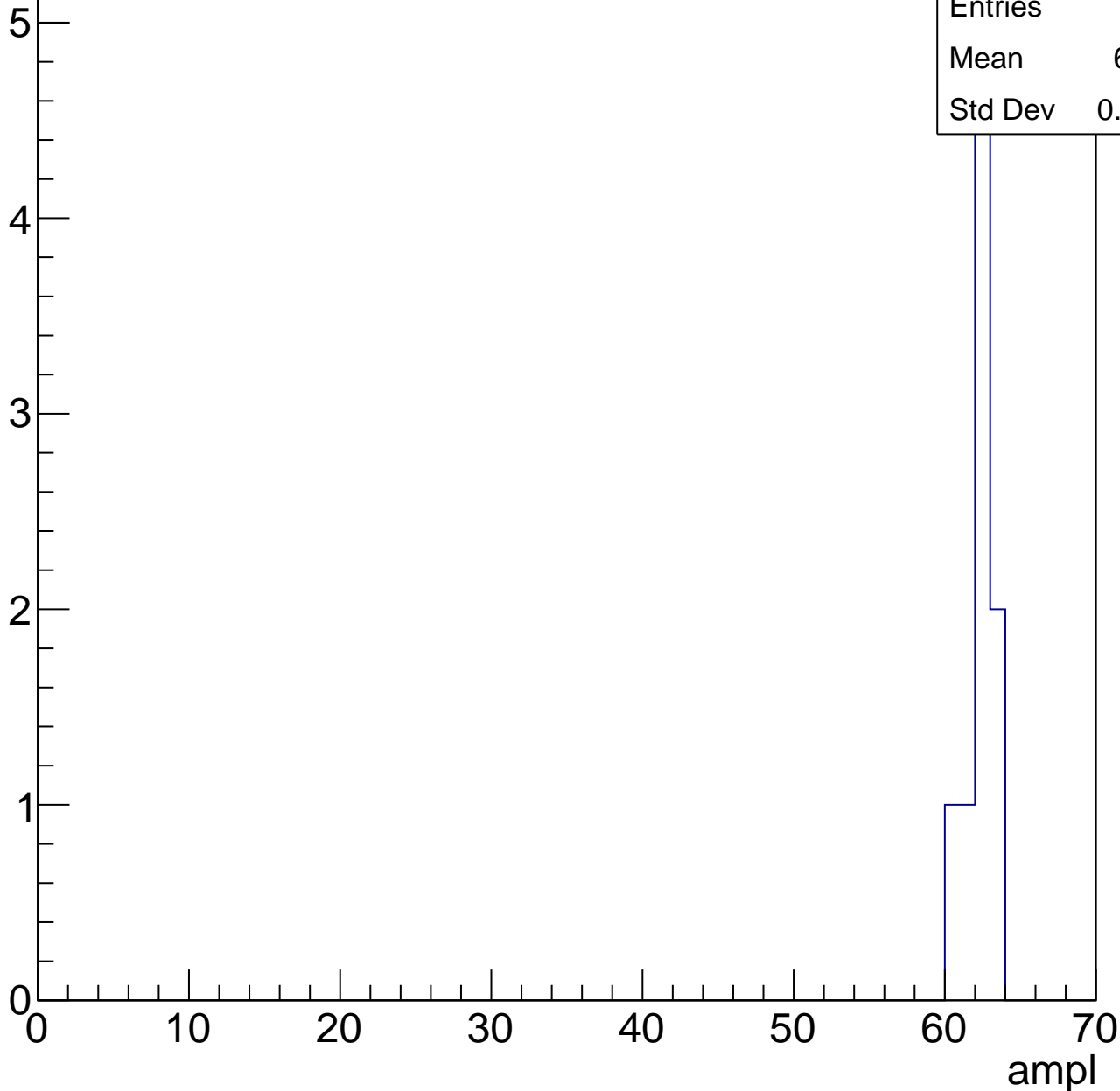


B1L102S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|--------|
| Entries | 9 |
| Mean | 61.89 |
| Std Dev | 0.8749 |



B1L102S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 6.333 |
| Std Dev | 8.957 |

B1L102S, U15-ch78, adc0

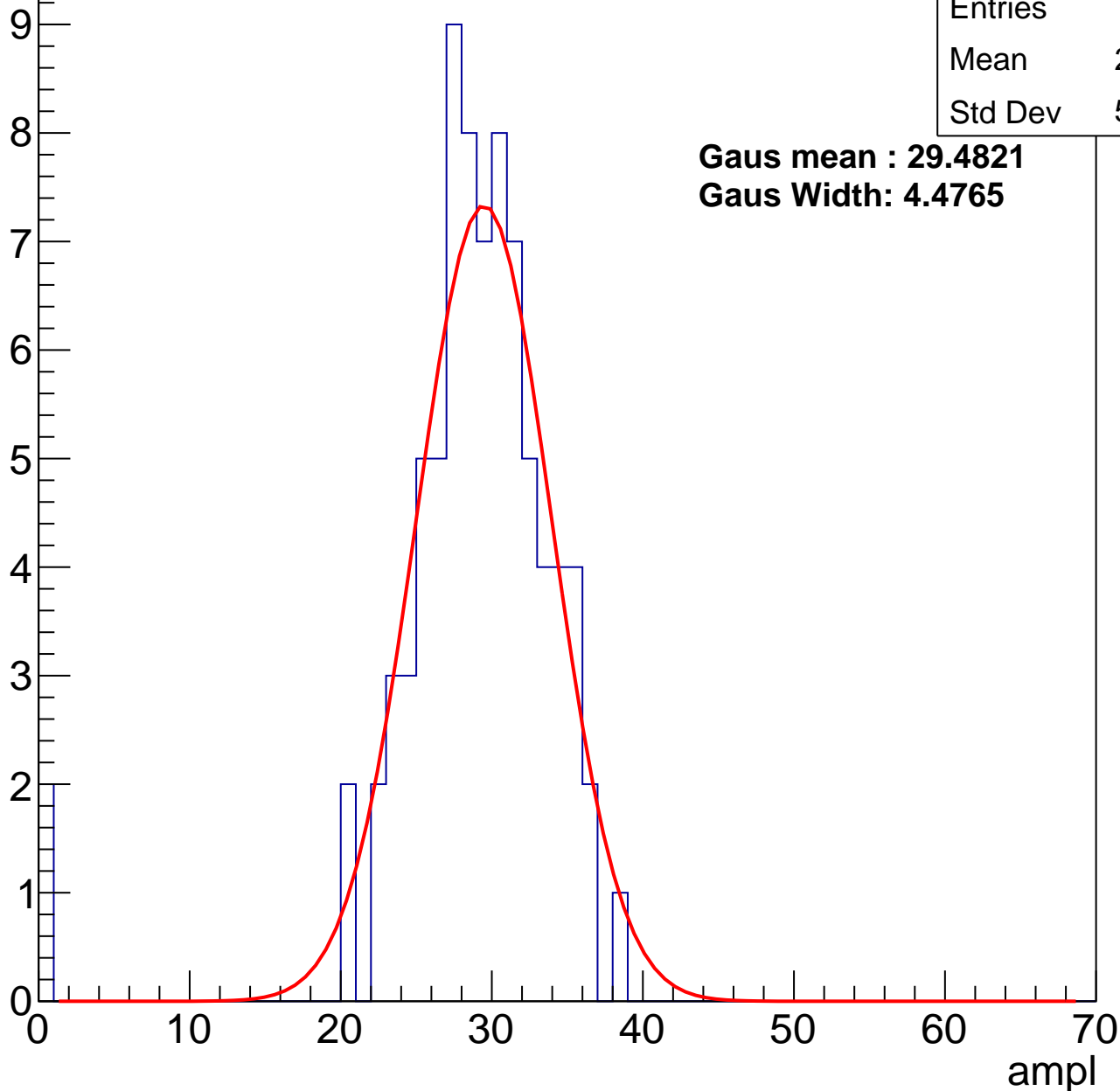
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 28.21 |
| Std Dev | 5.891 |

Gaus mean : 29.4821

Gaus Width: 4.4765



B1L102S, U15-ch78, adc1

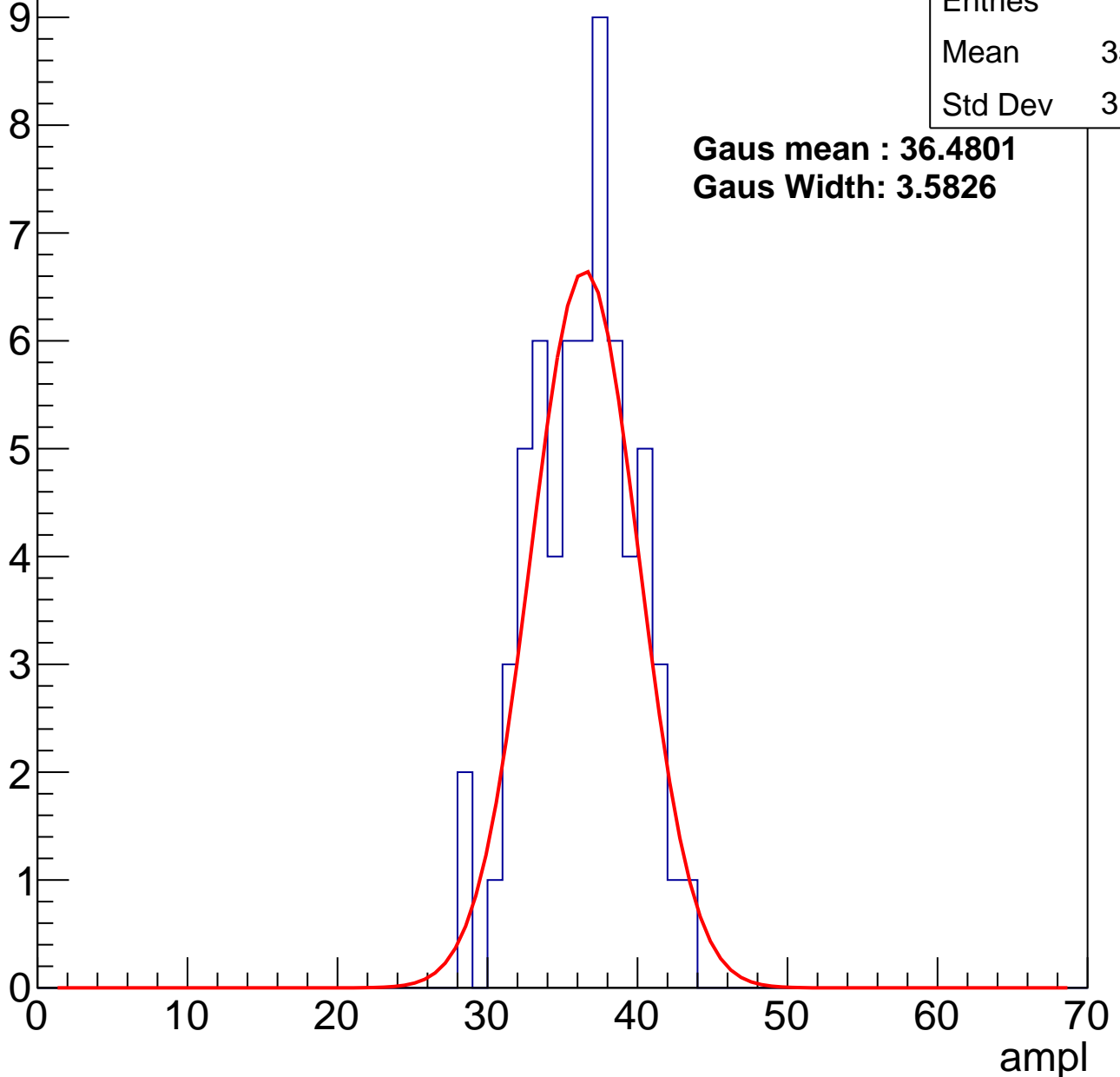
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.87 |
| Std Dev | 3.367 |

Gaus mean : 36.4801

Gaus Width: 3.5826



B1L102S, U15-ch78, adc2

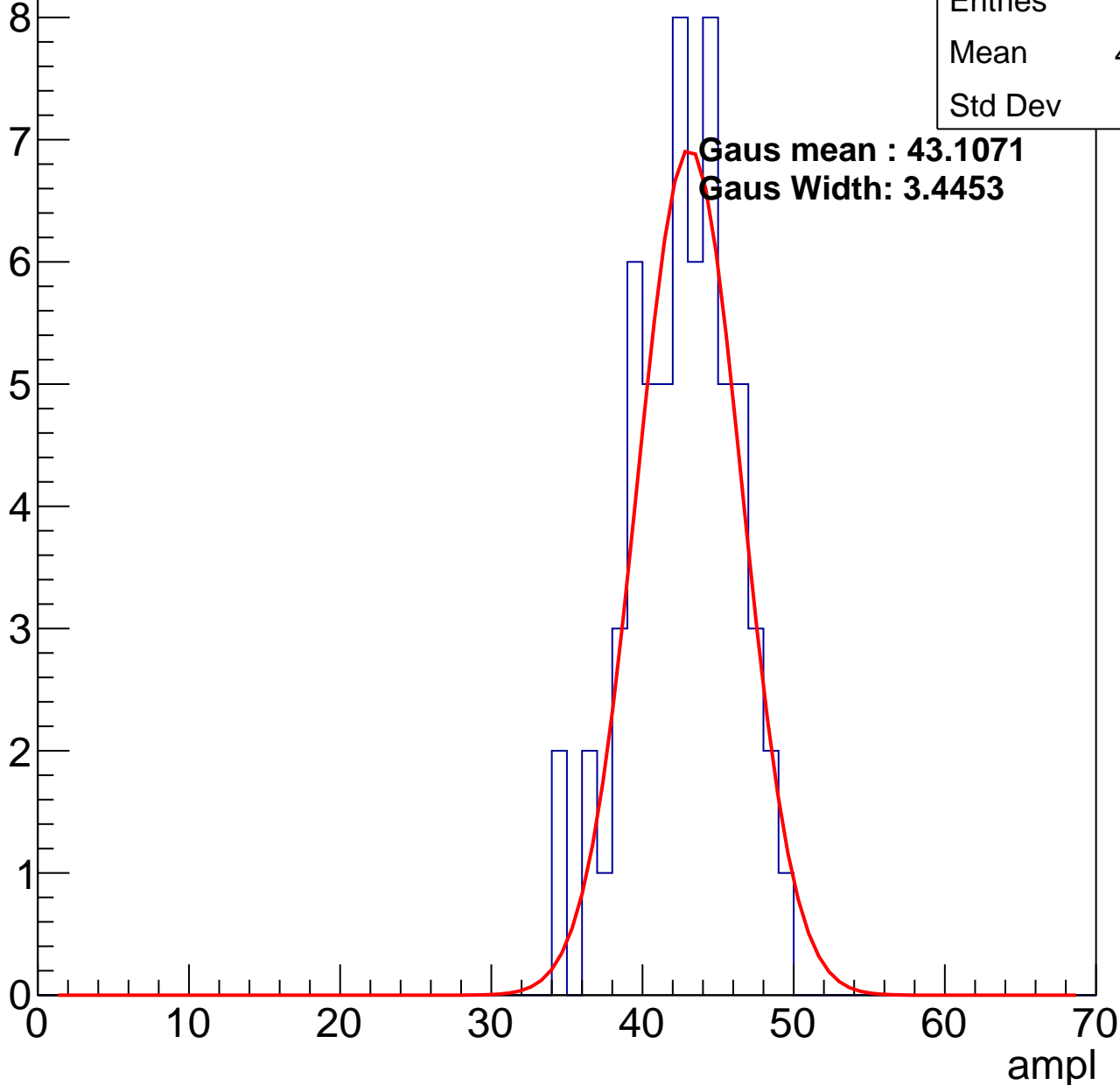
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.21 |
| Std Dev | 3.38 |

Gaus mean : 43.1071

Gaus Width: 3.4453

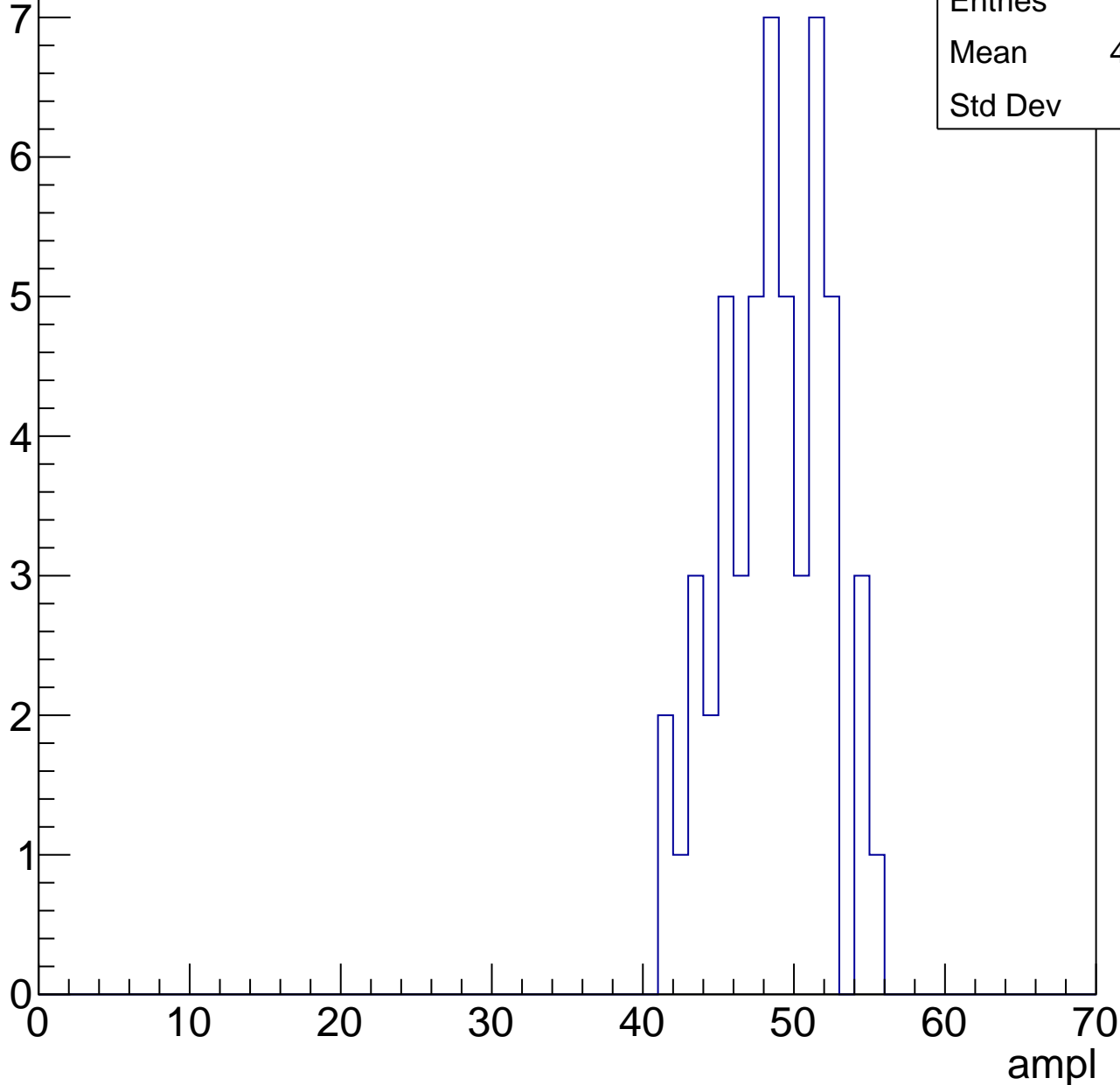


B1L102S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

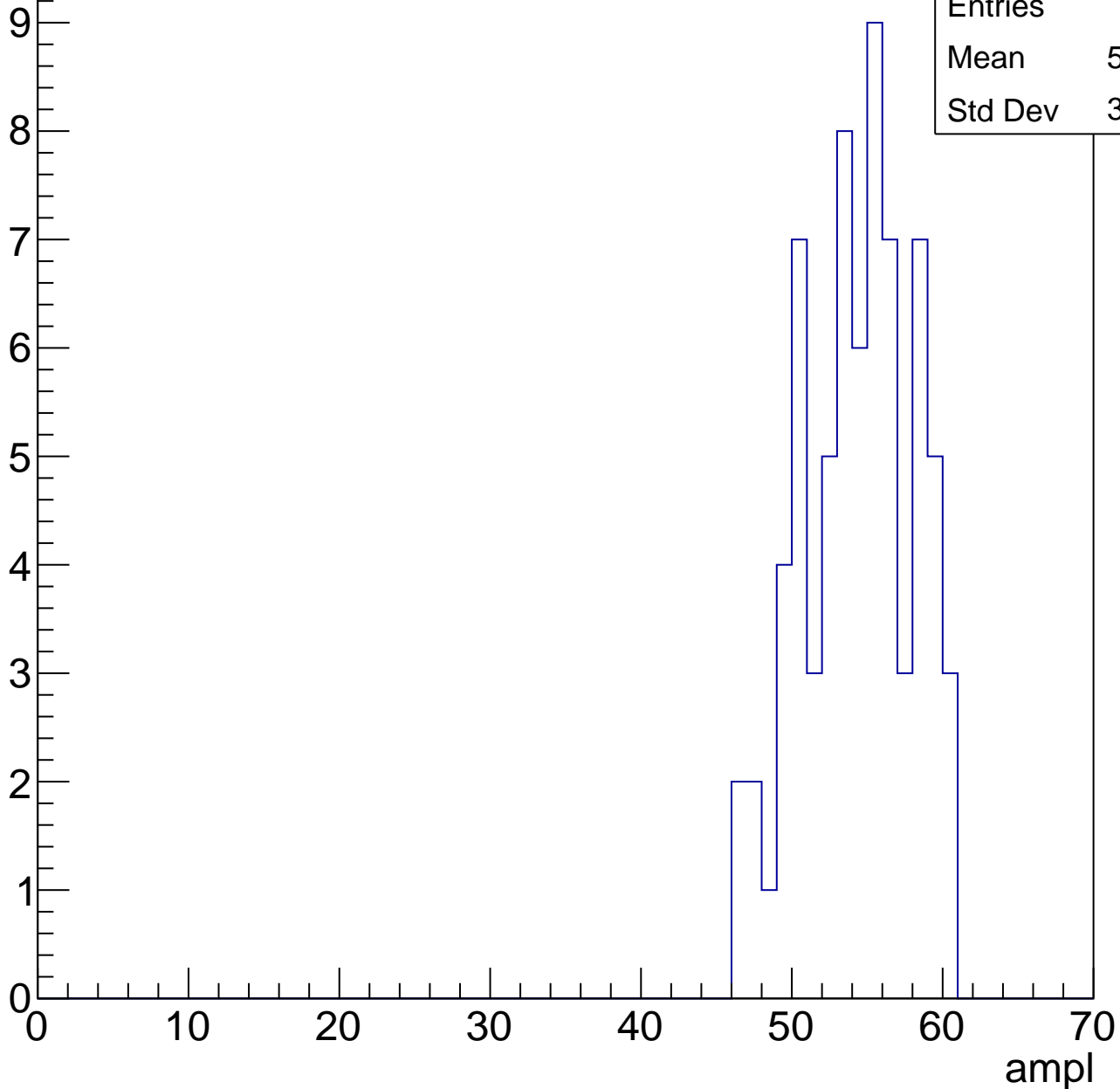
| | |
|---------|-------|
| Entries | 52 |
| Mean | 48.15 |
| Std Dev | 3.45 |



B1L102S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

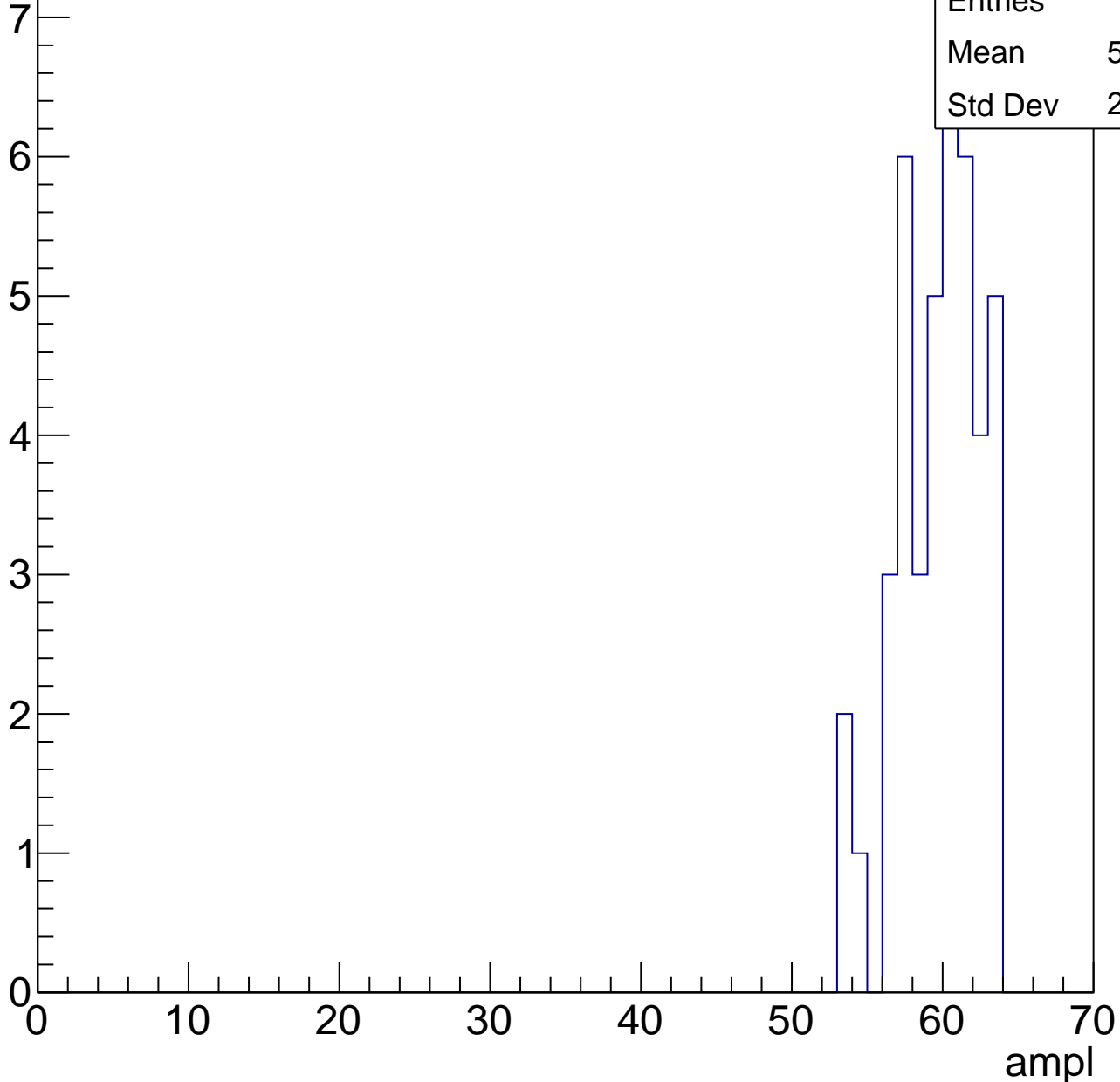


B1L102S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.24 |
| Std Dev | 2.653 |

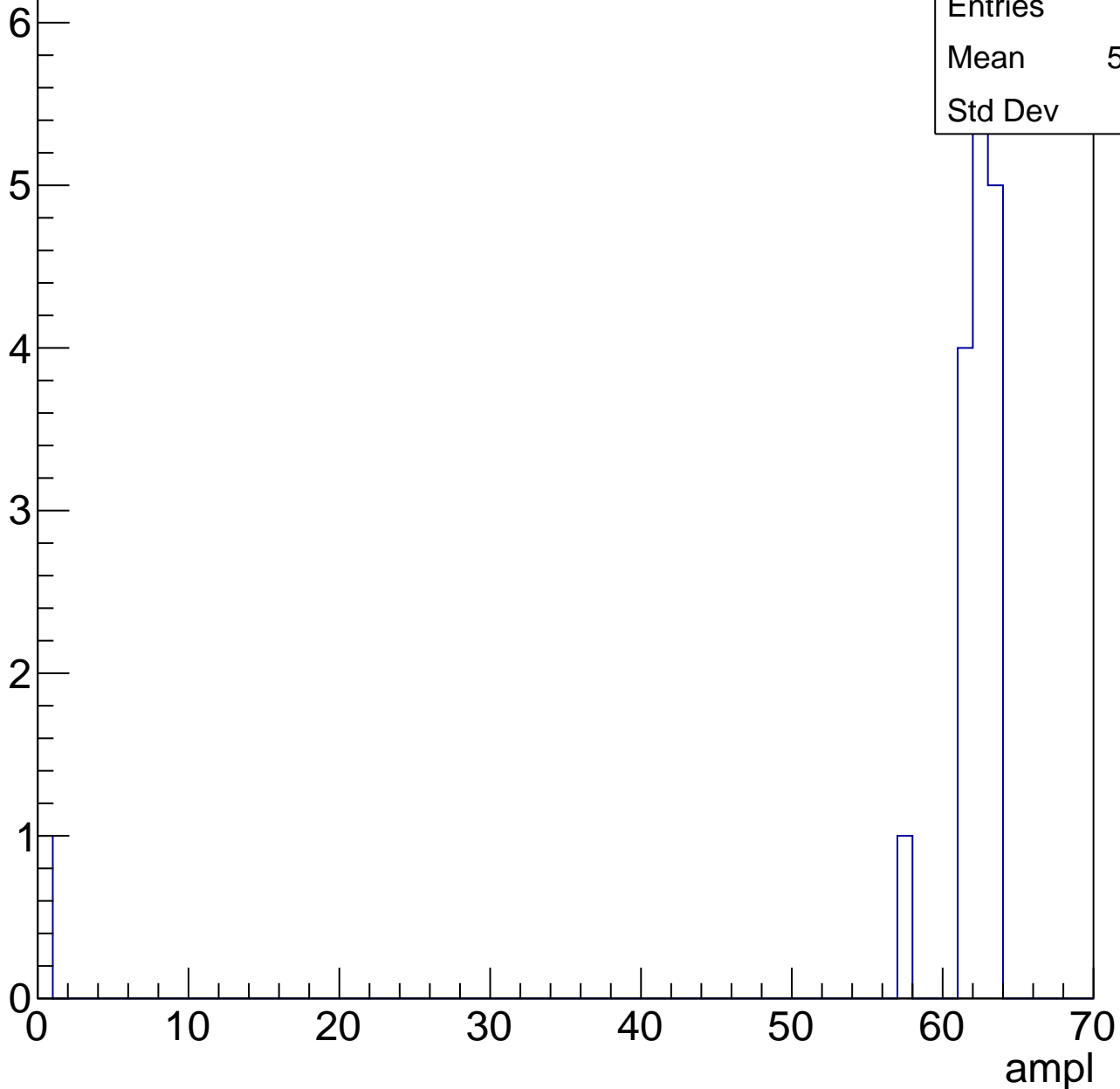


B1L102S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 17 |
| Mean | 58.12 |
| Std Dev | 14.6 |



B1L102S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch79, adc0

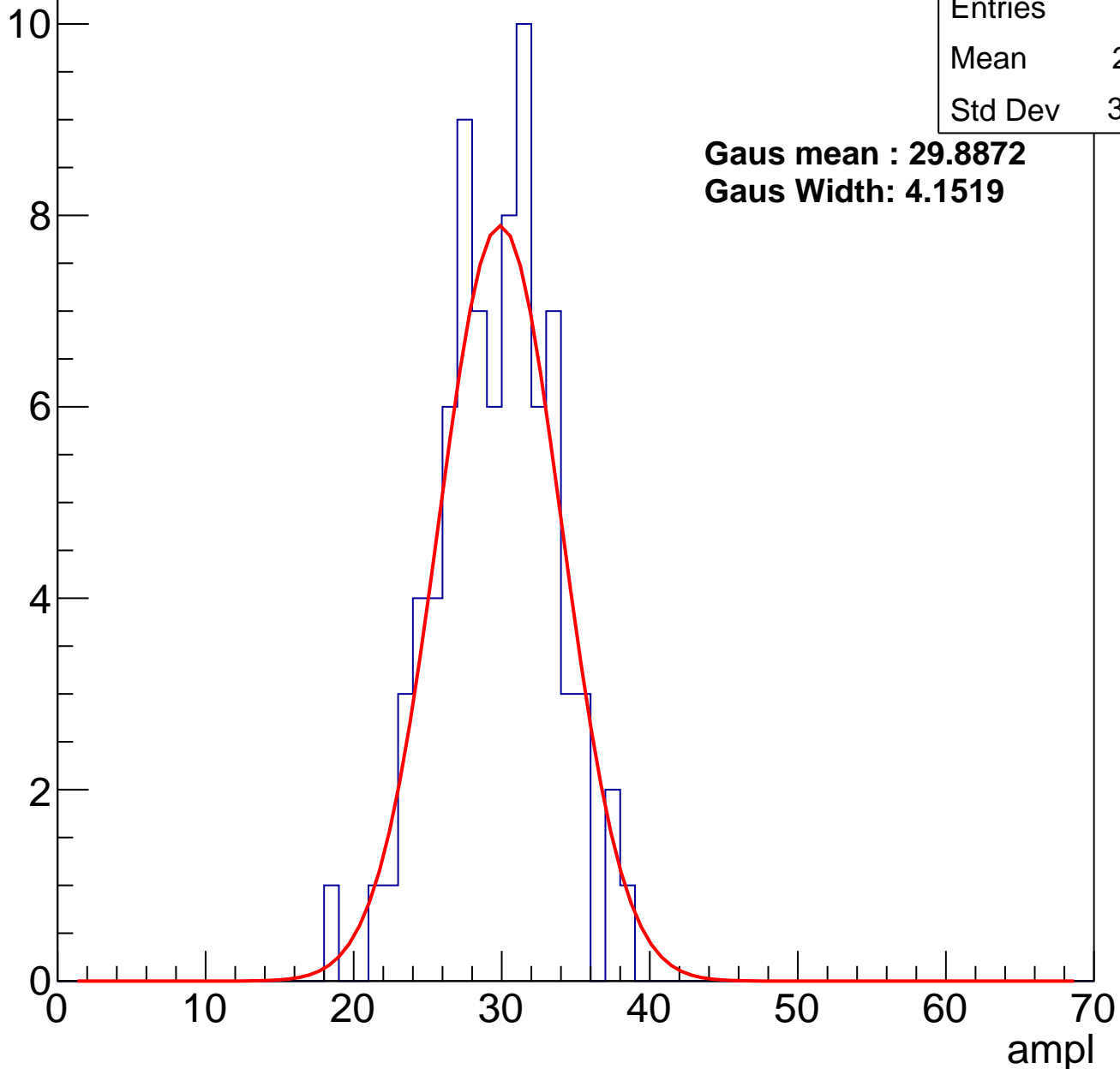
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 82 |
| Mean | 29.11 |
| Std Dev | 3.838 |

Gaus mean : 29.8872

Gaus Width: 4.1519

Entry



B1L102S, U15-ch79, adc1

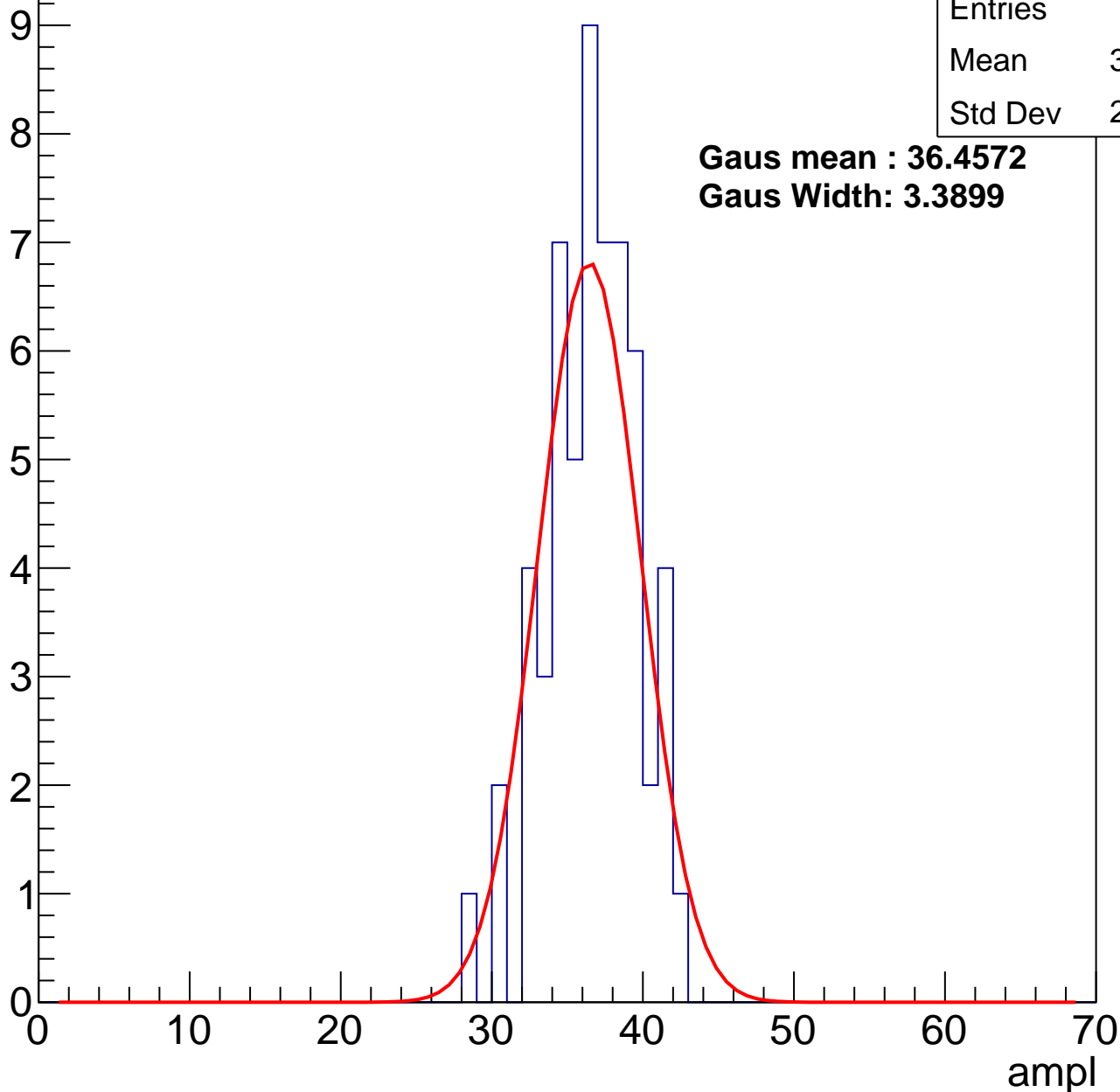
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 36.16 |
| Std Dev | 2.982 |

Gaus mean : 36.4572

Gaus Width: 3.3899



B1L102S, U15-ch79, adc2

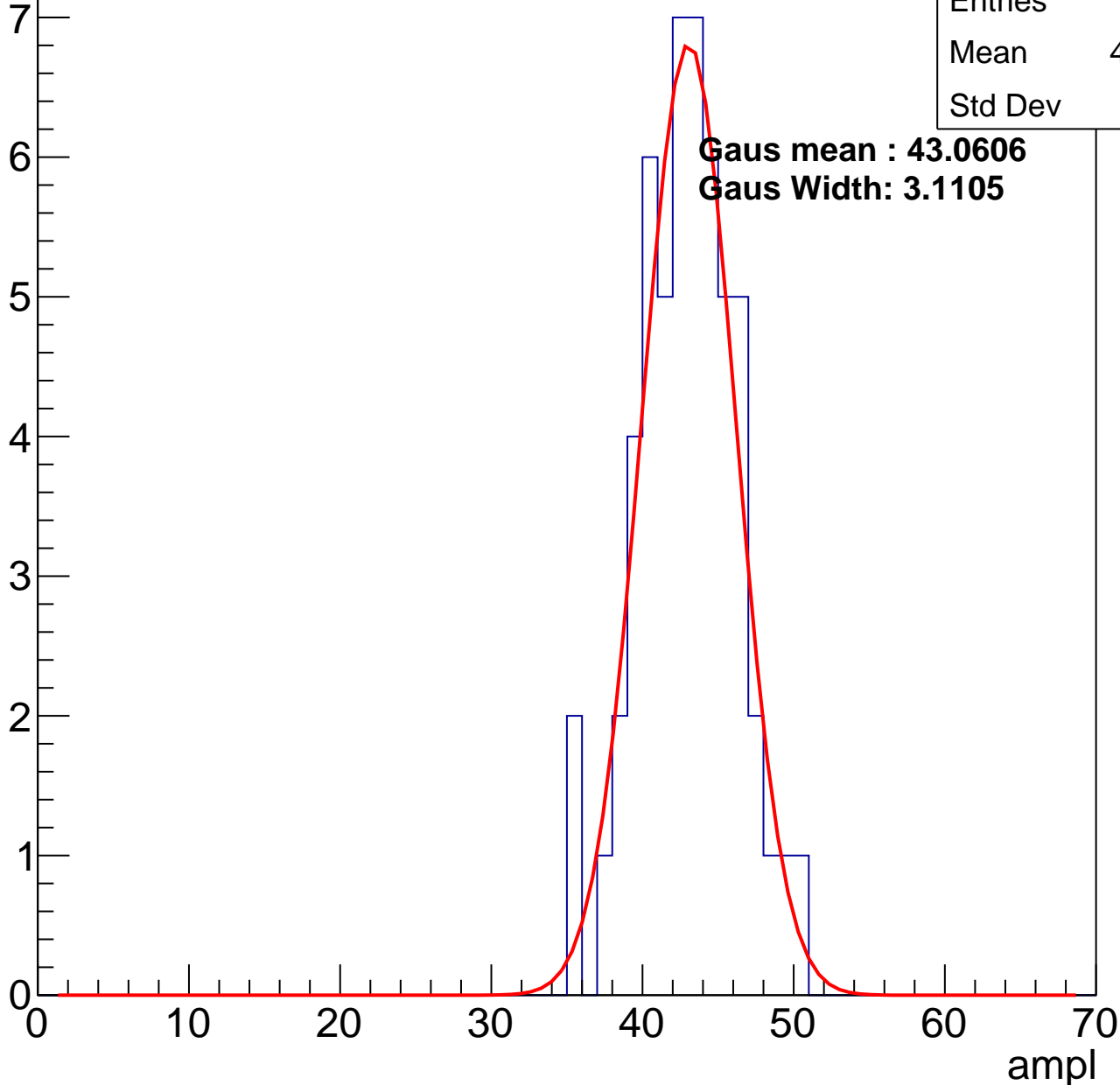
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 42.53 |
| Std Dev | 3.19 |

Gaus mean : 43.0606

Gaus Width: 3.1105

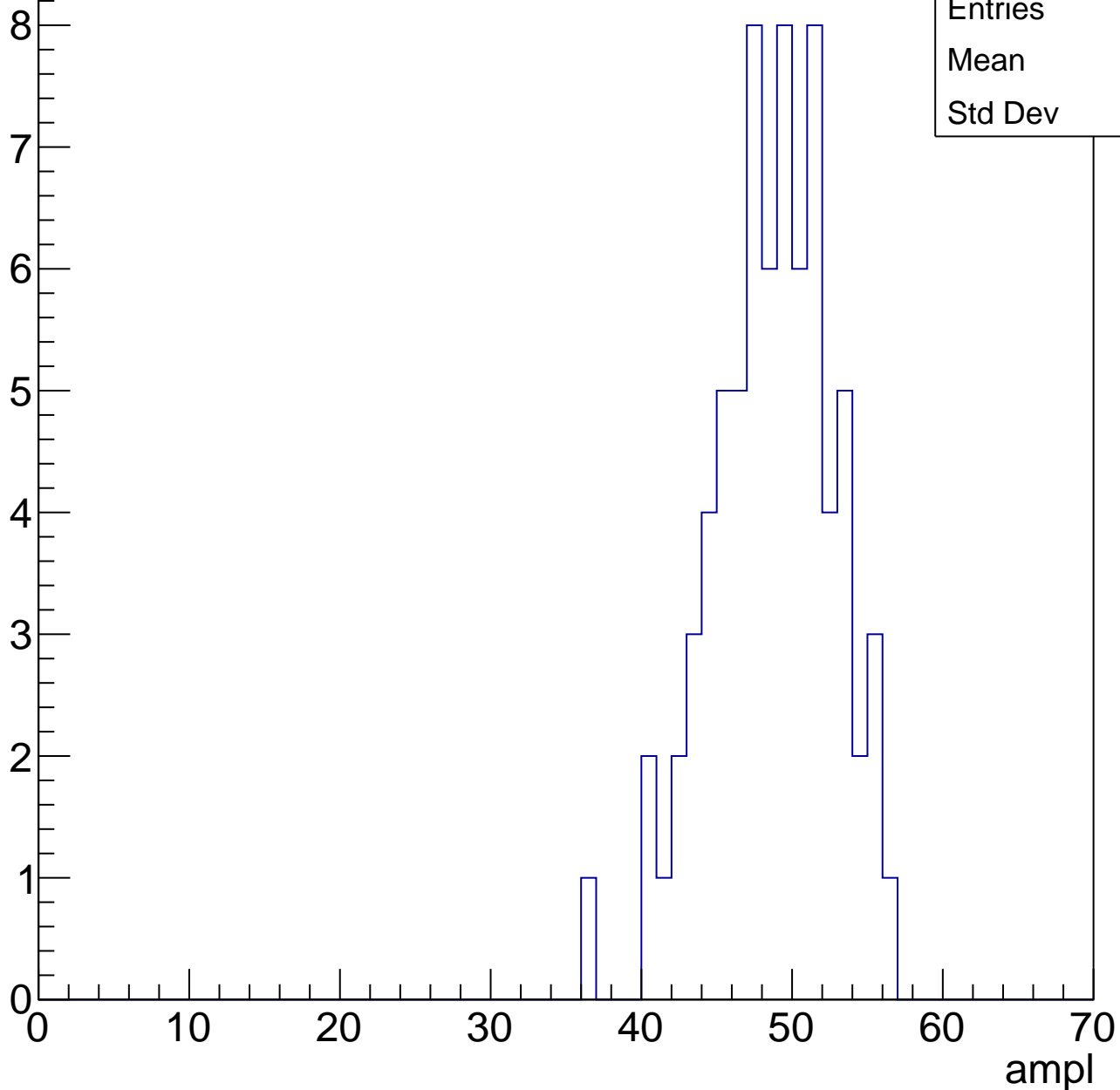


B1L102S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

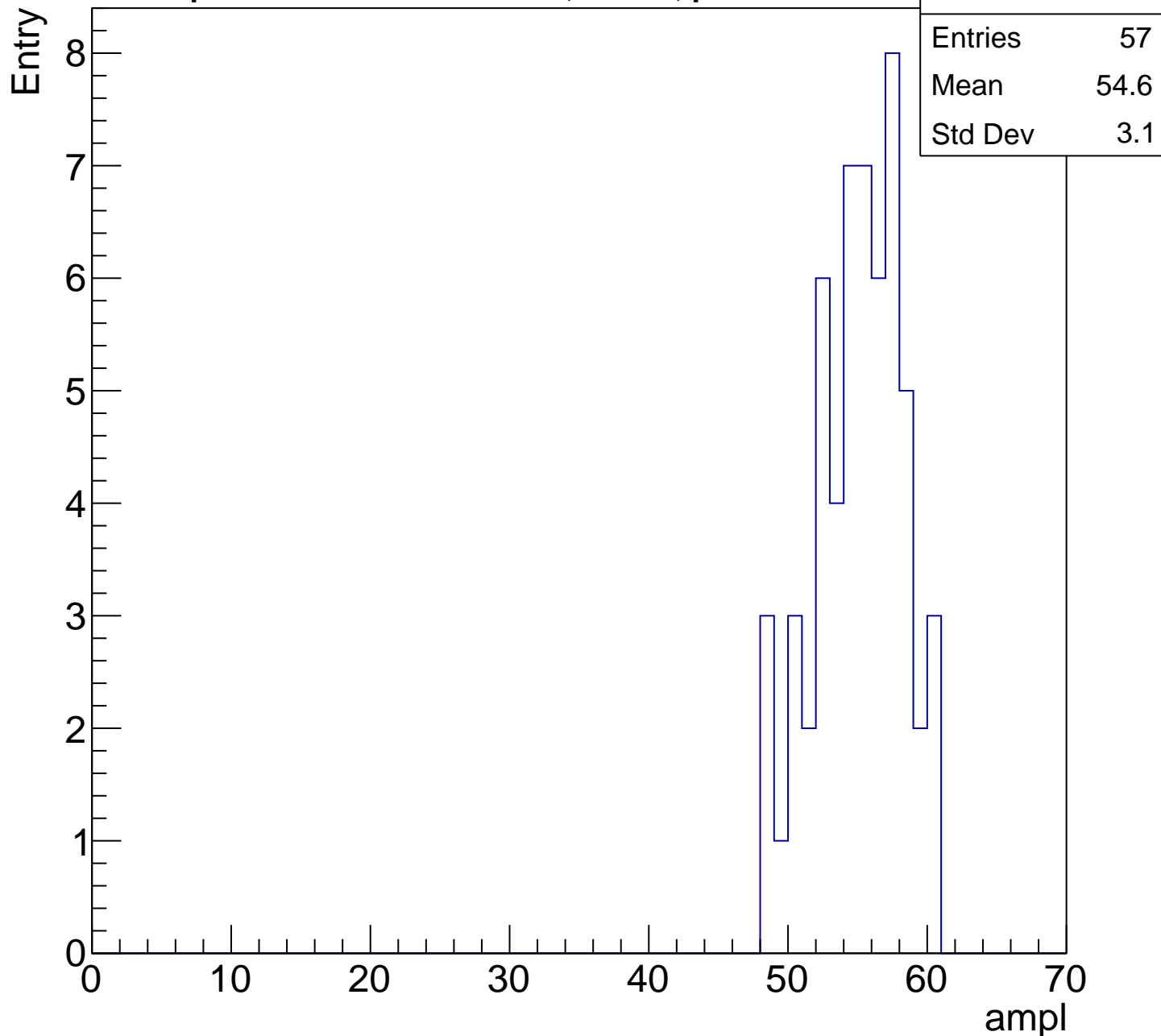
Entry

| | |
|---------|------|
| Entries | 74 |
| Mean | 48.2 |
| Std Dev | 3.99 |



B1L102S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

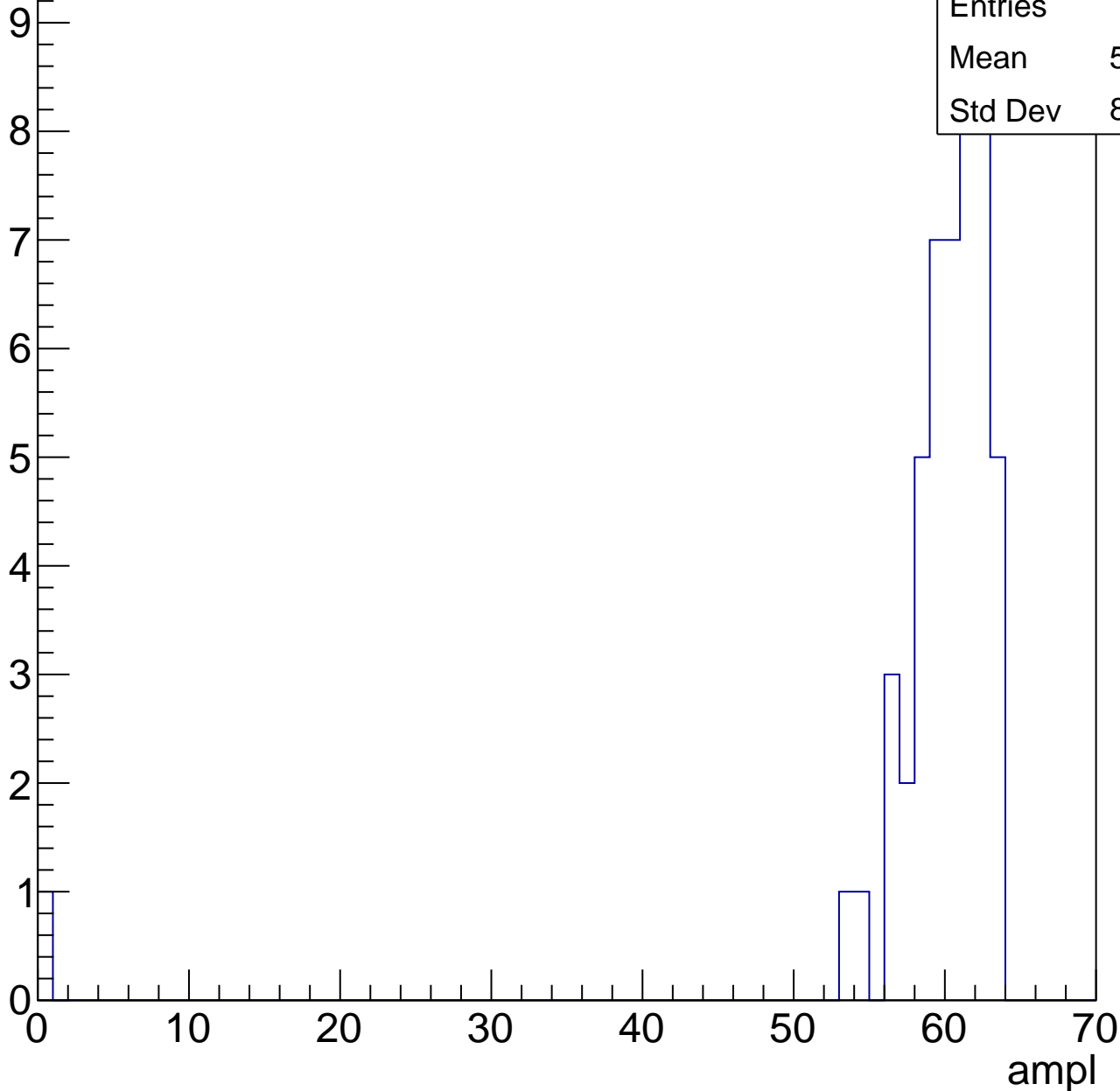


B1L102S, U15-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

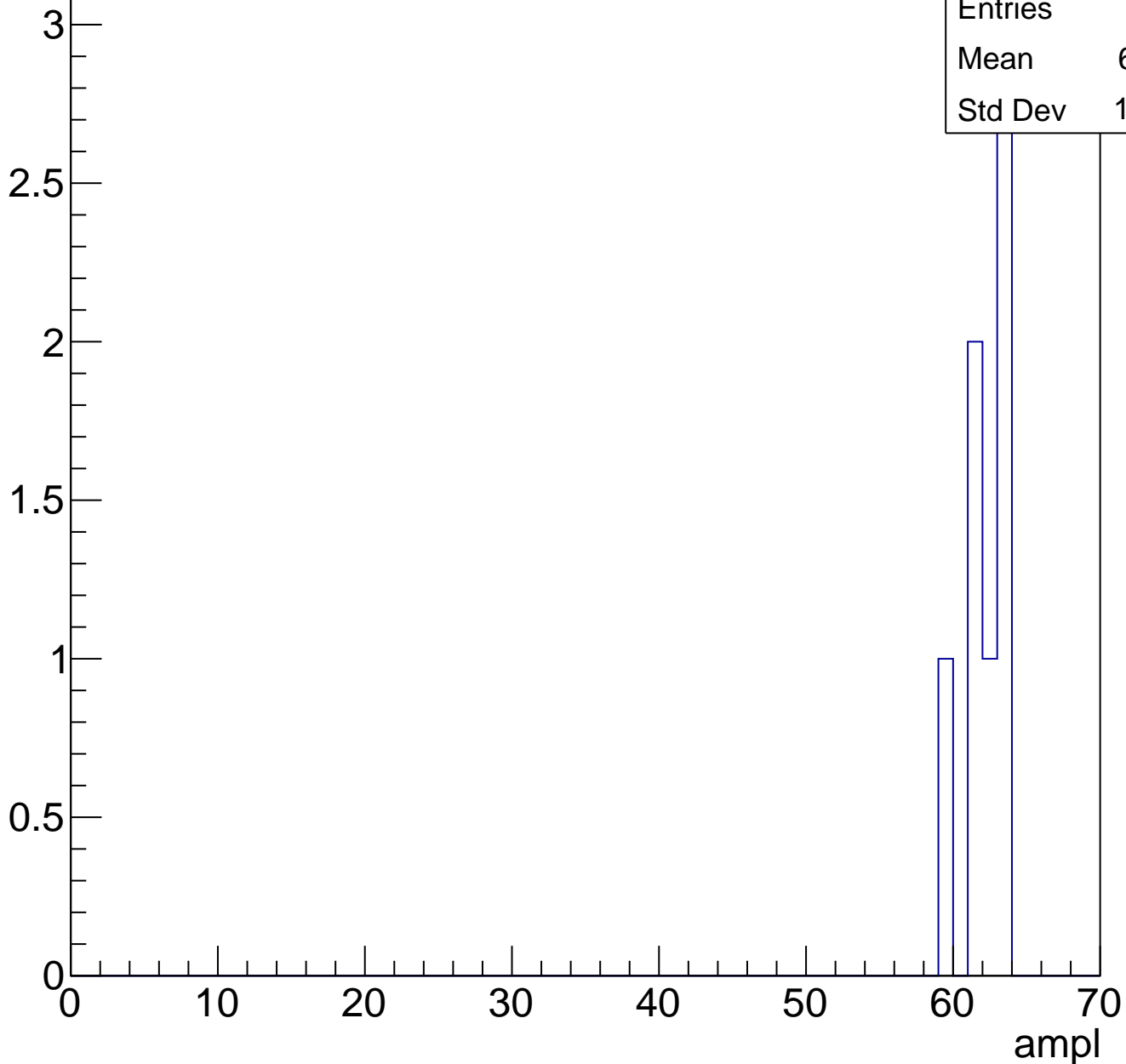
| | |
|---------|-------|
| Entries | 49 |
| Mean | 58.63 |
| Std Dev | 8.778 |



B1L102S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch80, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 89 |
| Mean | 28.29 |
| Std Dev | 4.963 |

Gaus mean : 29.2782

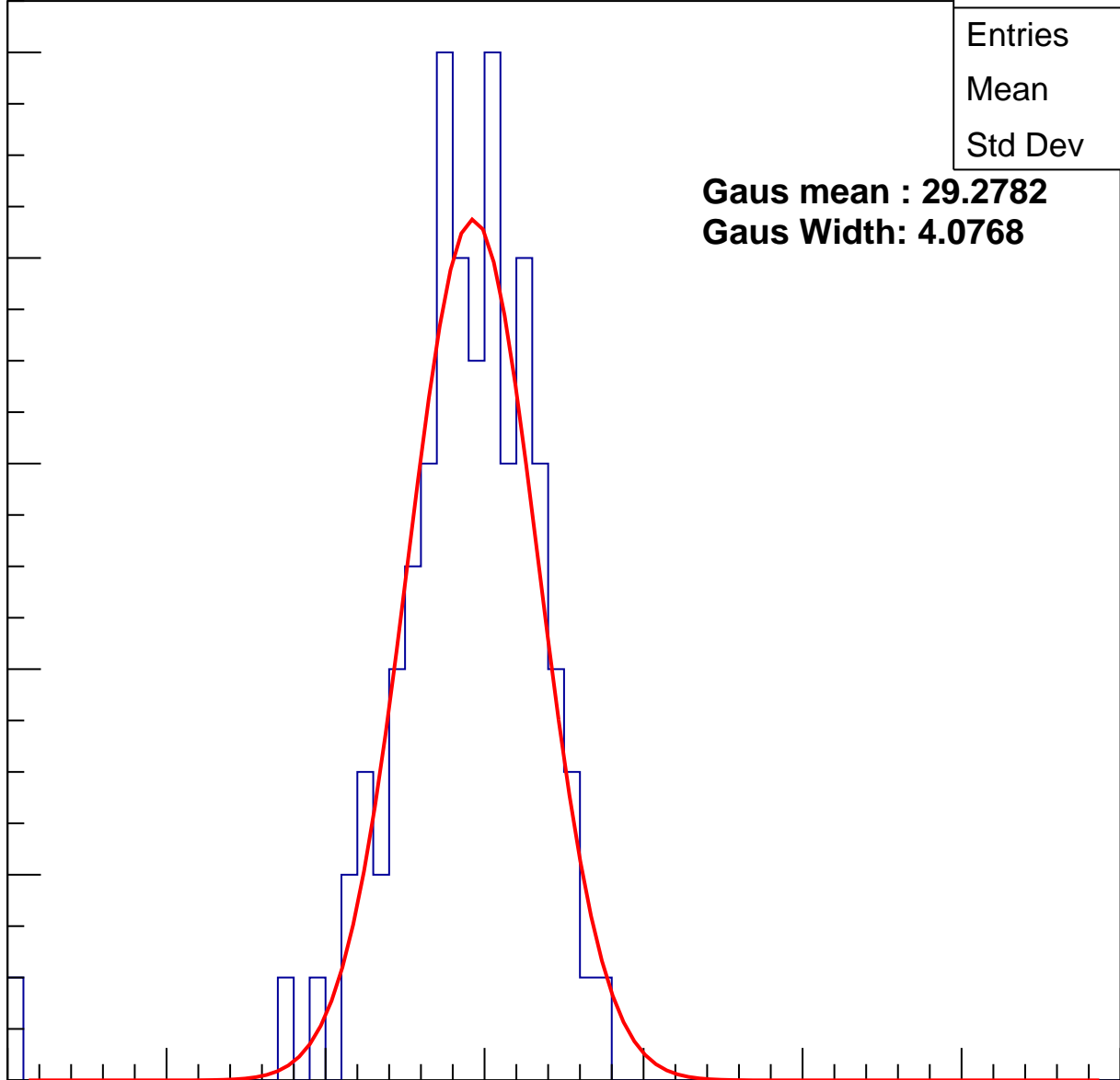
Gaus Width: 4.0768

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch80, adc1

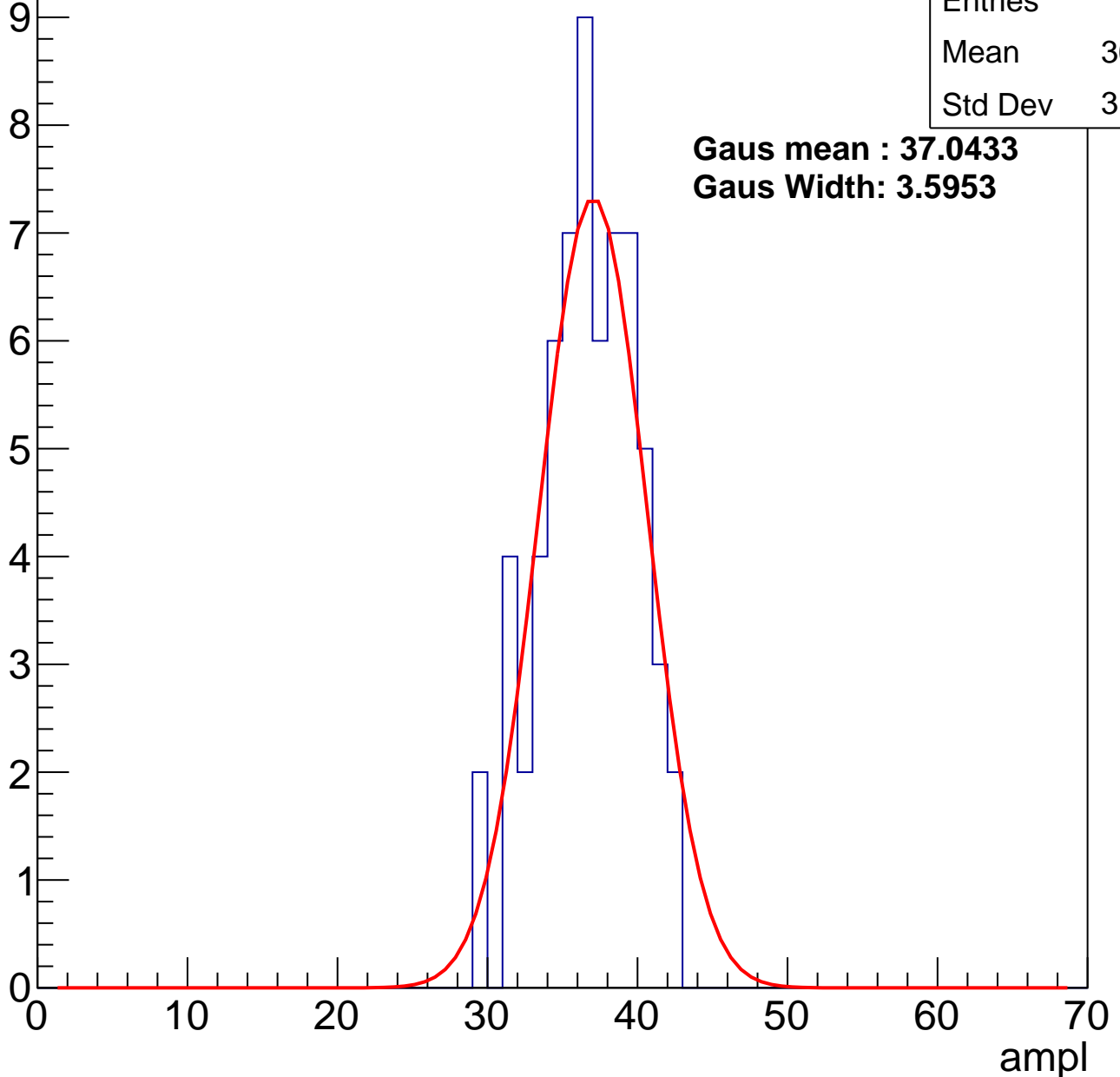
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 36.23 |
| Std Dev | 3.106 |

Gaus mean : 37.0433

Gaus Width: 3.5953



B1L102S, U15-ch80, adc2

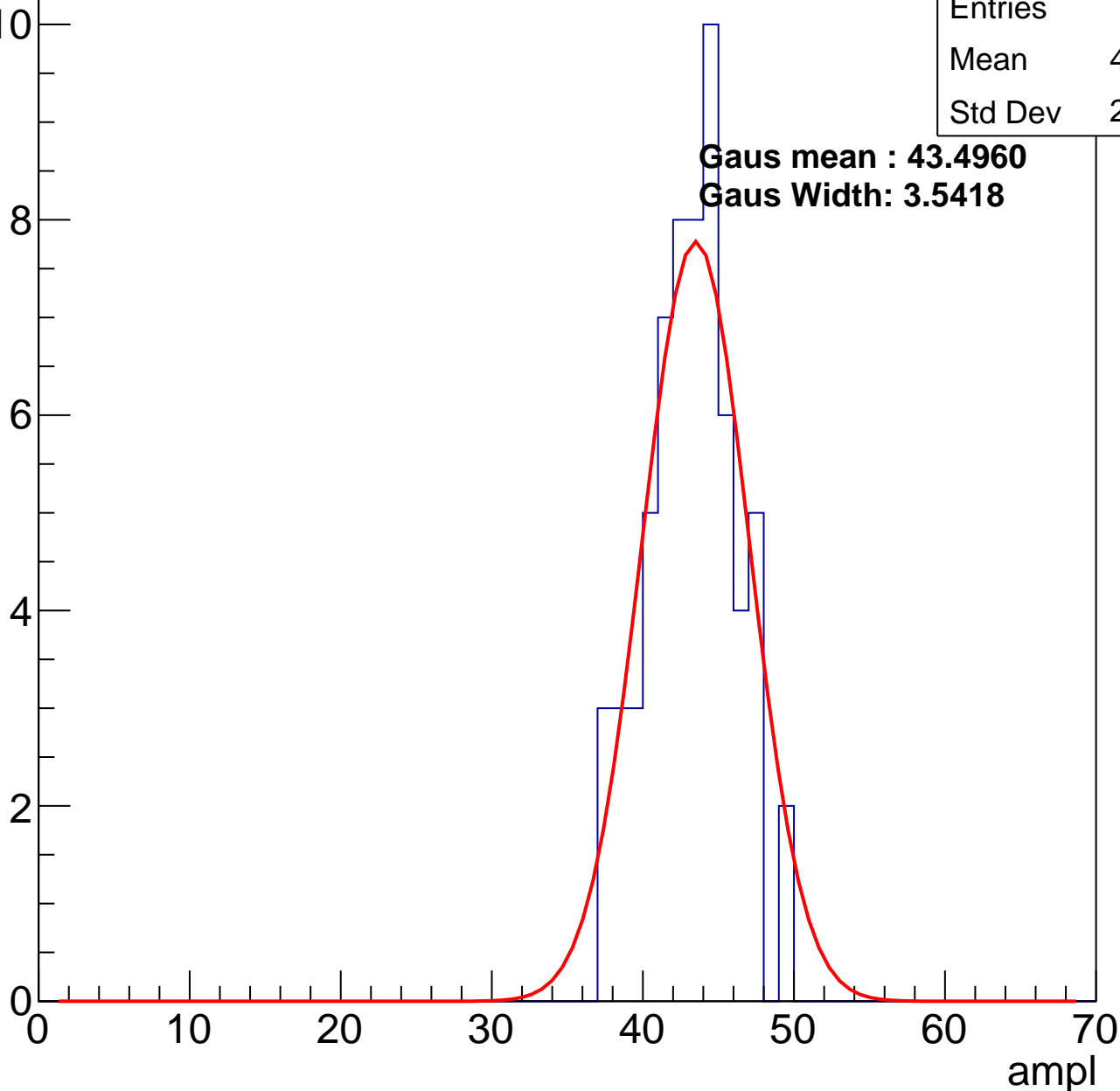
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 42.75 |
| Std Dev | 2.878 |

Gaus mean : 43.4960

Gaus Width: 3.5418

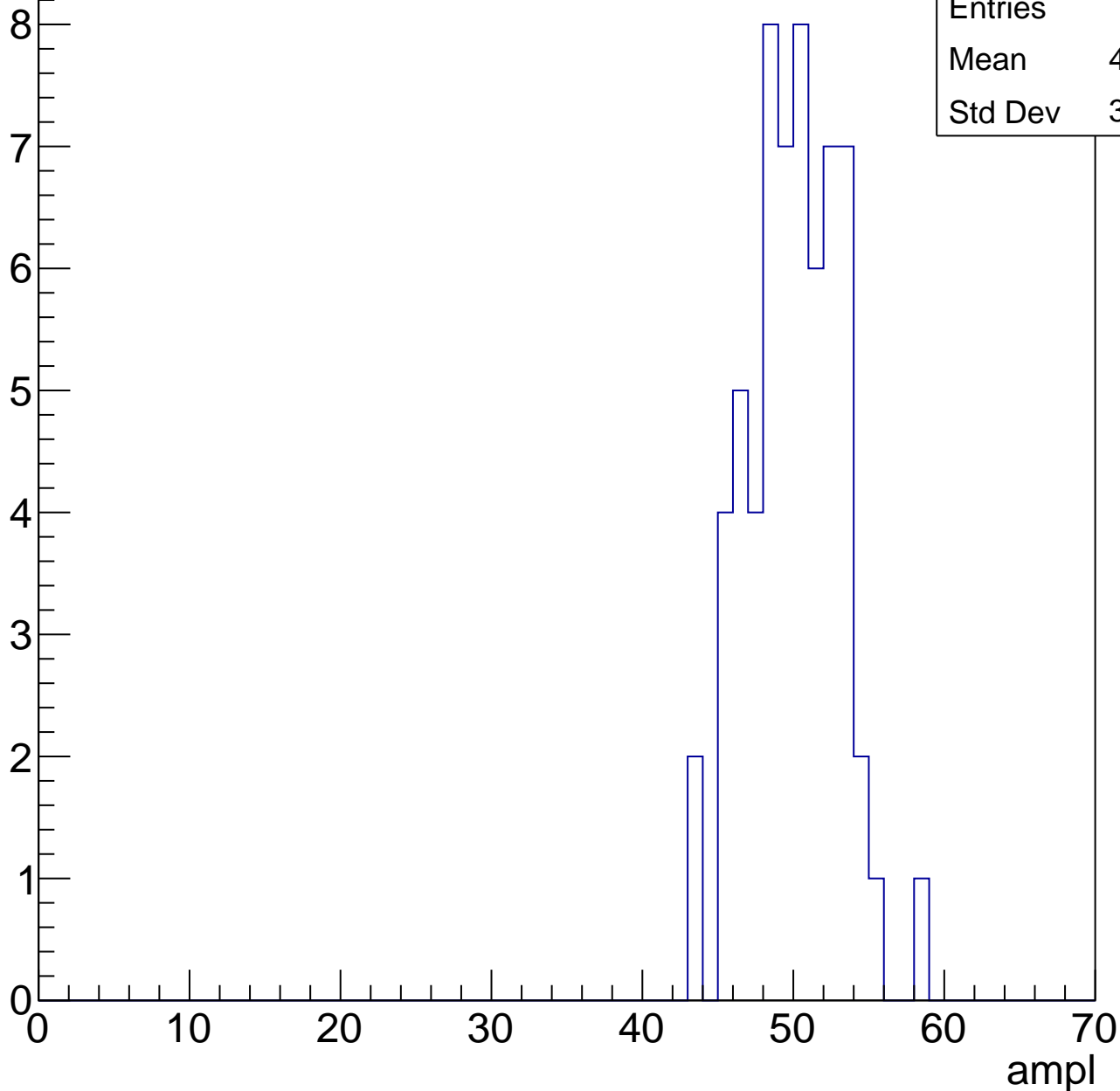


B1L102S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 49.56 |
| Std Dev | 3.003 |

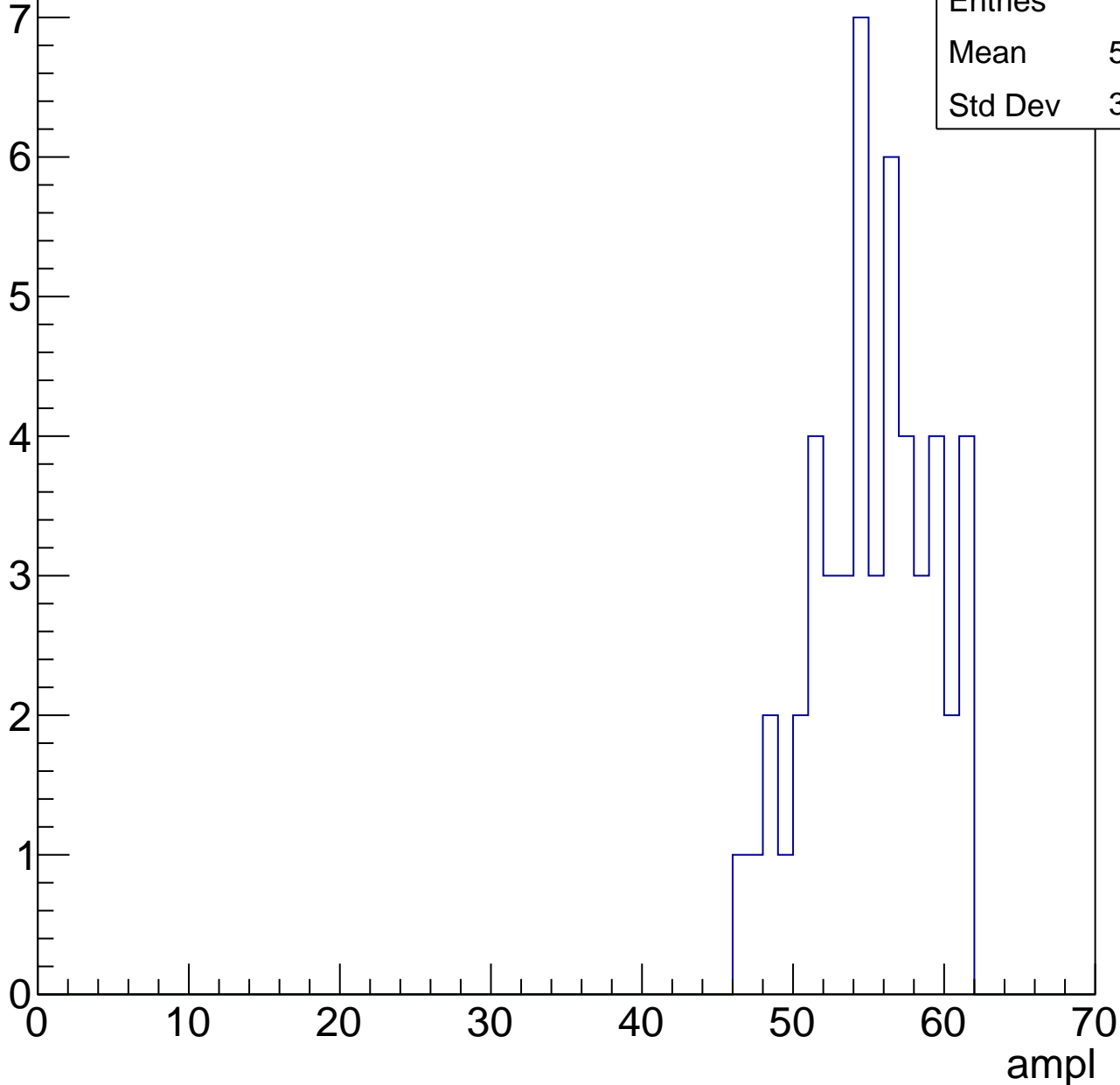


B1L102S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 54.76 |
| Std Dev | 3.855 |

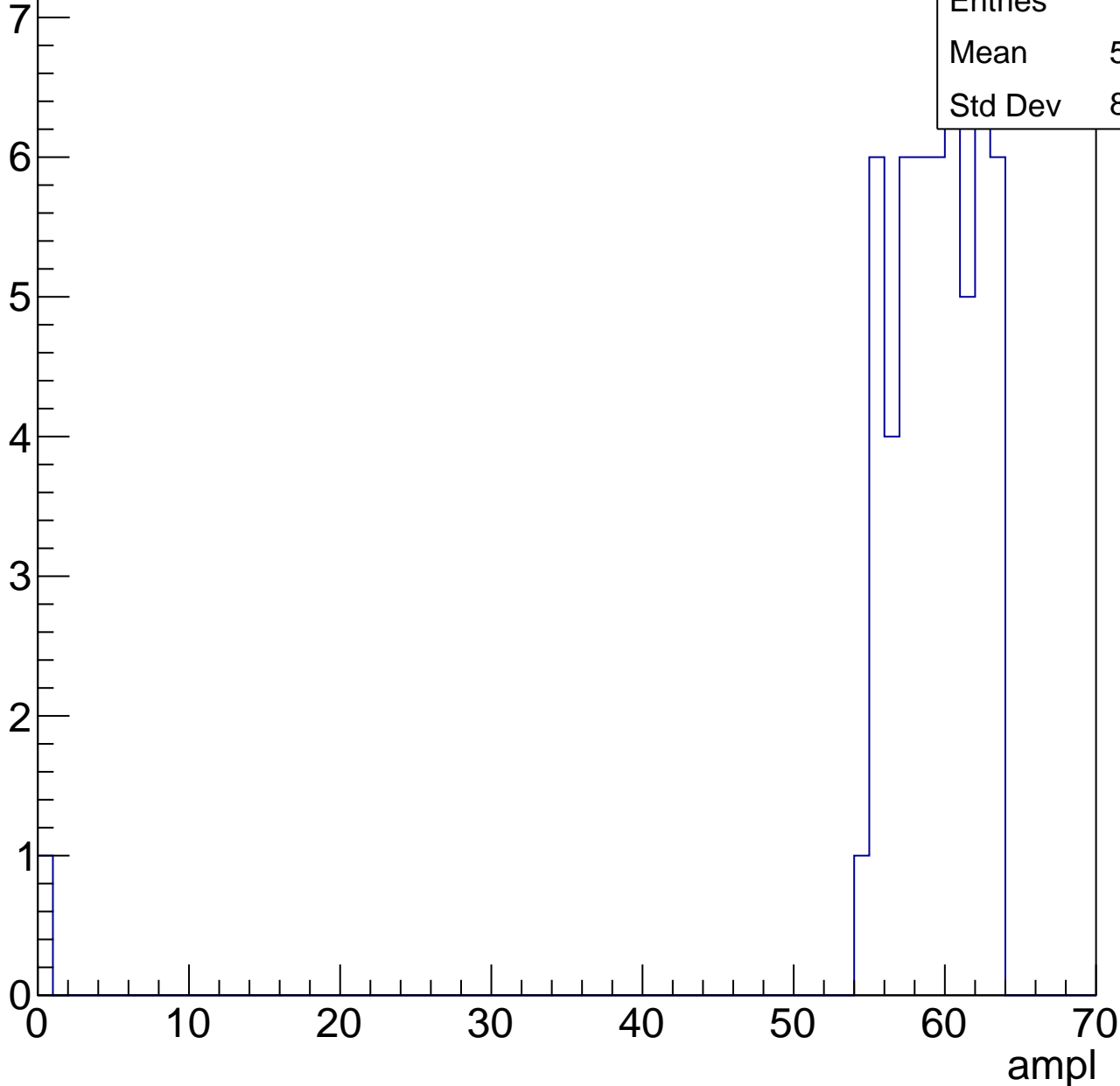


B1L102S, U15-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 57.98 |
| Std Dev | 8.309 |



B1L102S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch81, adc0

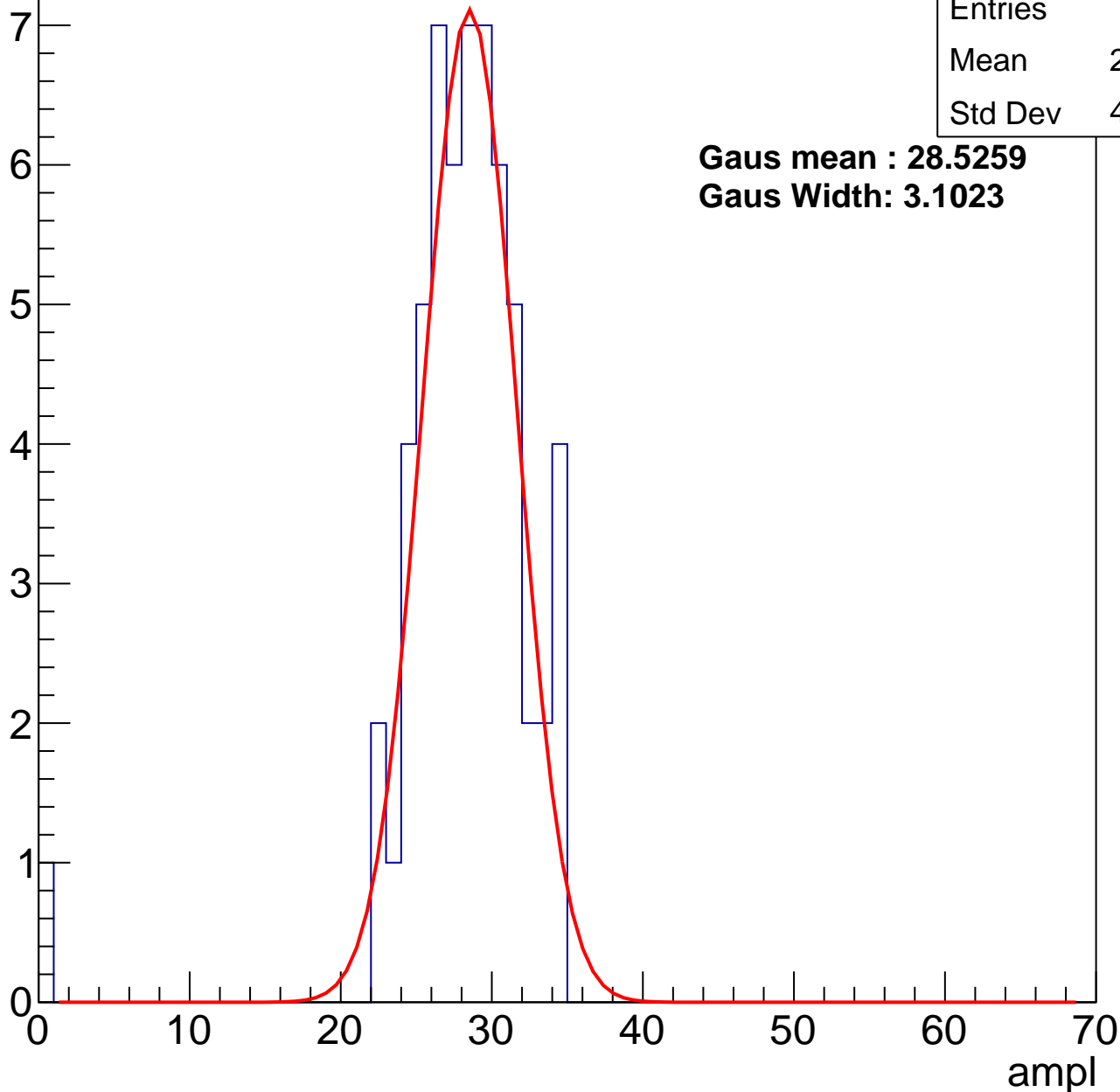
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 27.66 |
| Std Dev | 4.729 |

Gaus mean : 28.5259

Gaus Width: 3.1023



B1L102S, U15-ch81, adc1

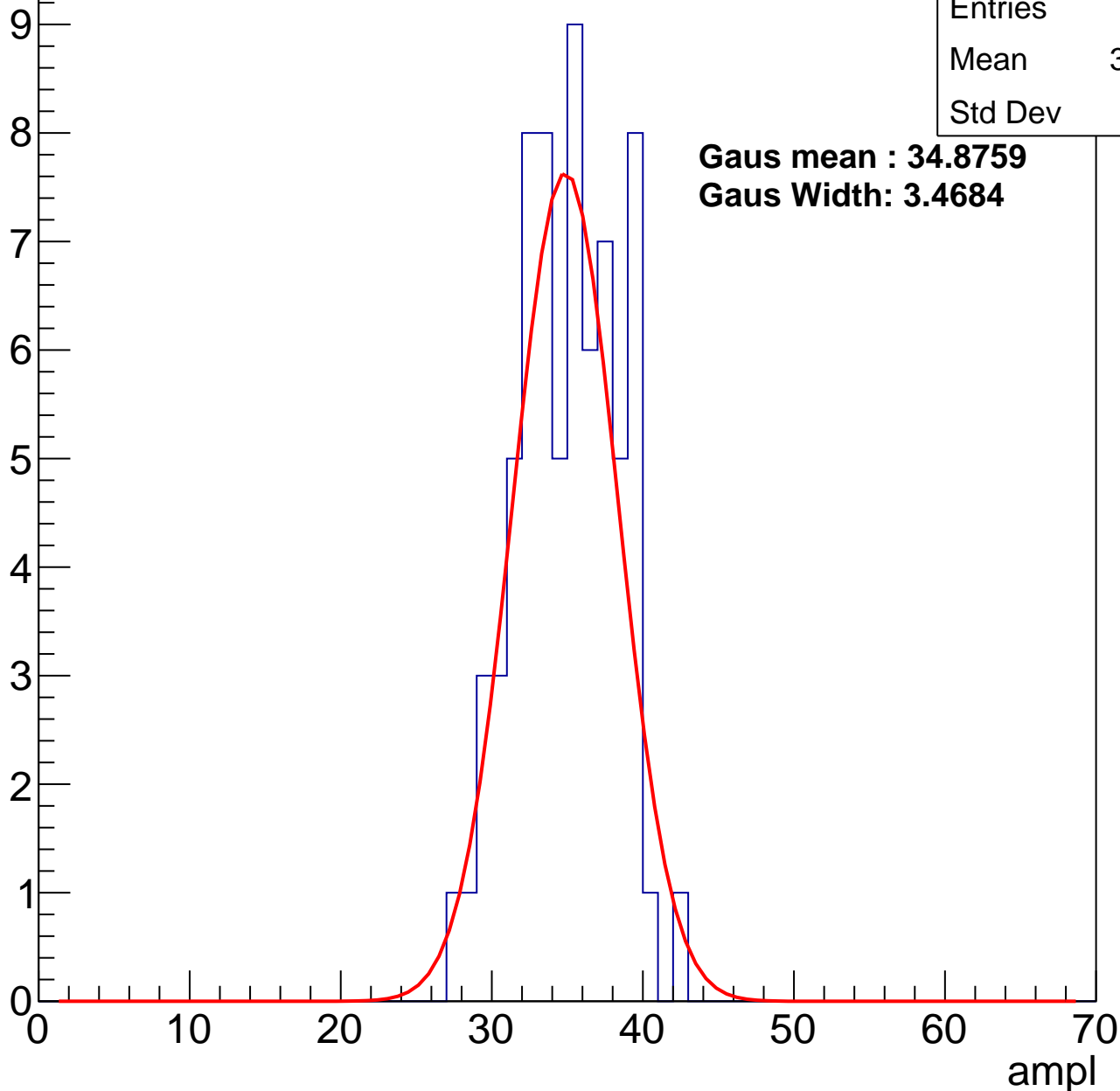
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 34.52 |
| Std Dev | 3.25 |

Gaus mean : 34.8759

Gaus Width: 3.4684



B1L102S, U15-ch81, adc2

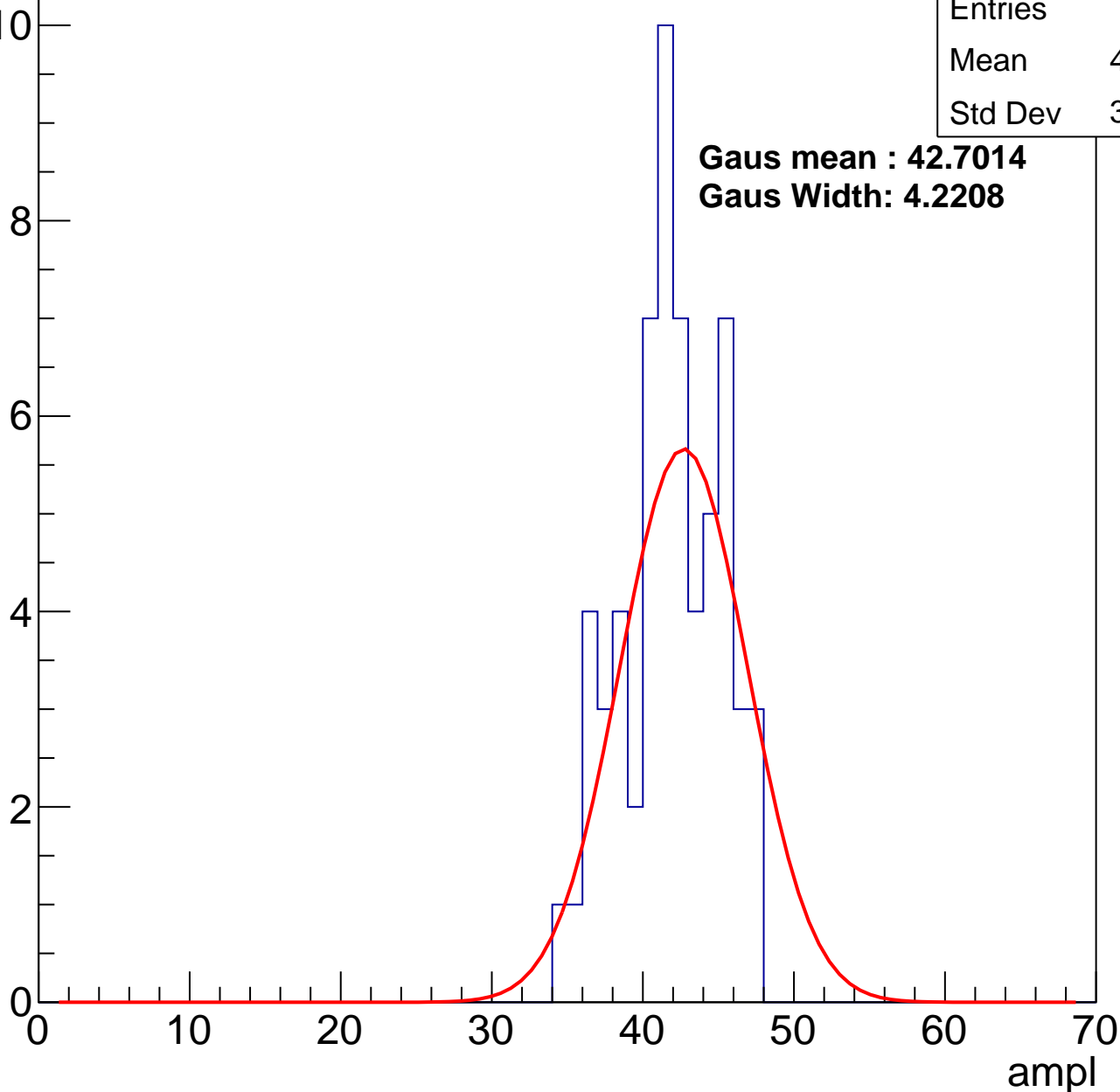
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 41.38 |
| Std Dev | 3.235 |

Gaus mean : 42.7014

Gaus Width: 4.2208

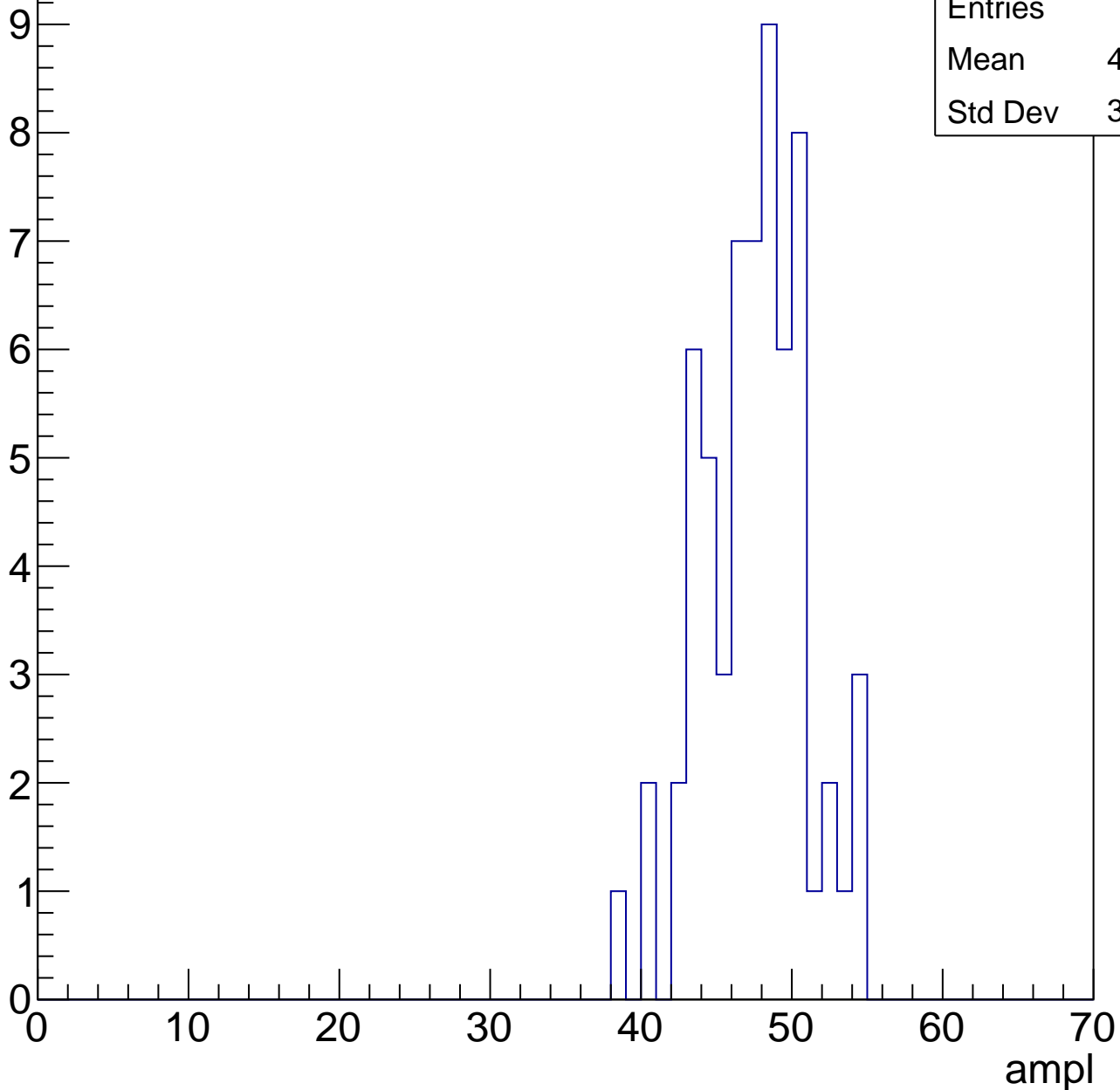


B1L102S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

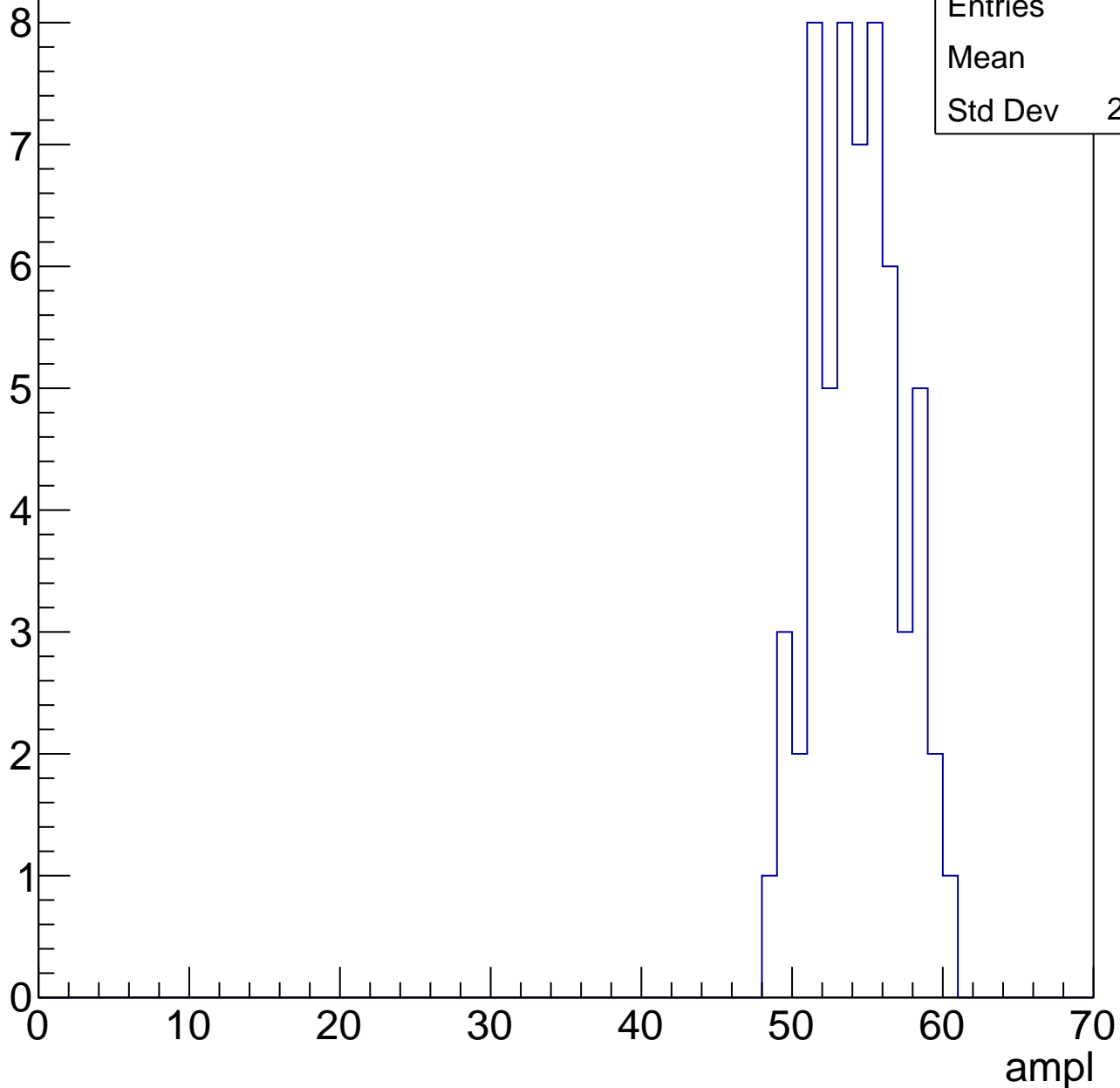
| | |
|---------|-------|
| Entries | 63 |
| Mean | 47.02 |
| Std Dev | 3.434 |



B1L102S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



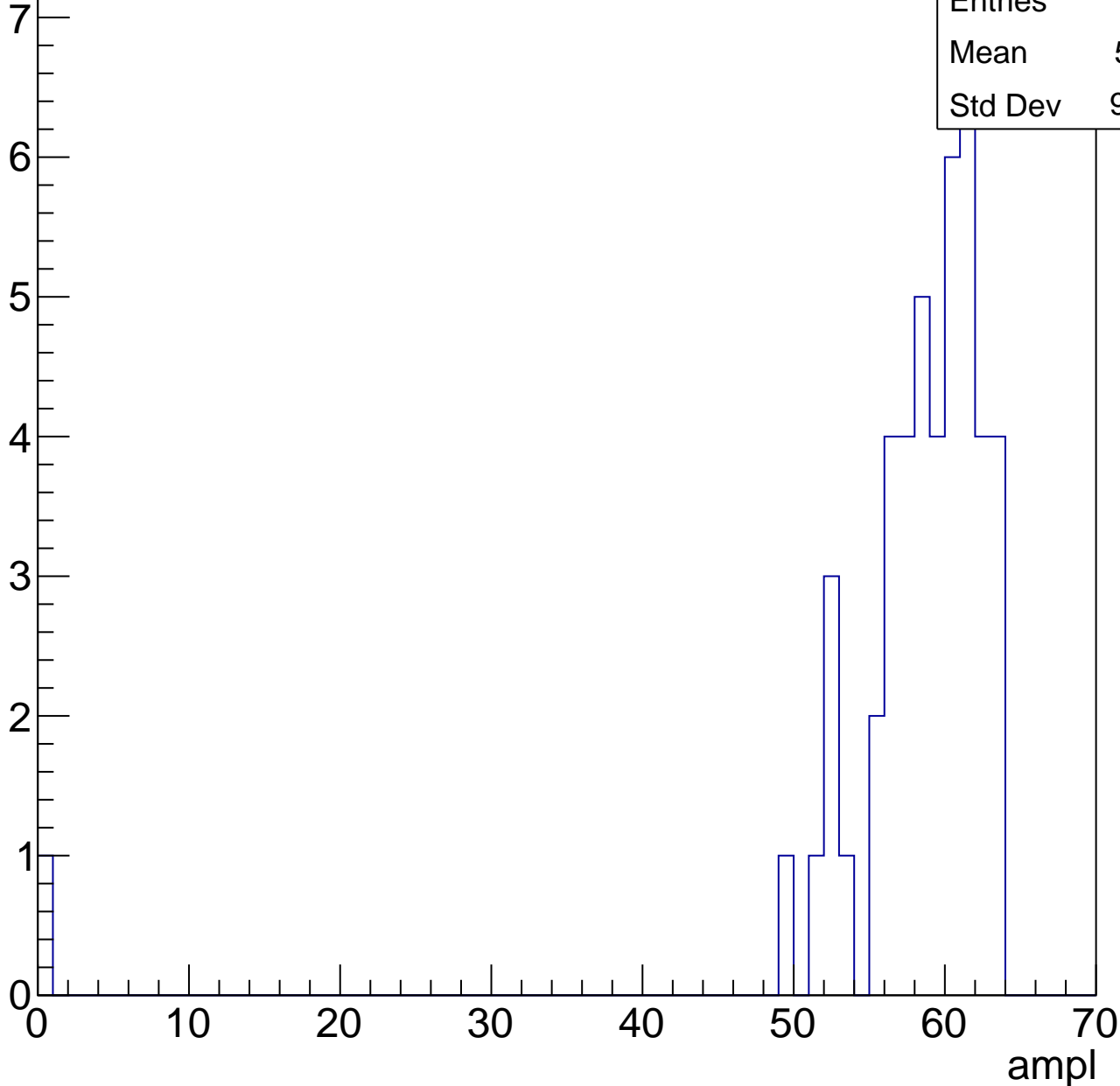
| | |
|---------|-------|
| Entries | 59 |
| Mean | 53.9 |
| Std Dev | 2.815 |

B1L102S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 57.11 |
| Std Dev | 9.089 |

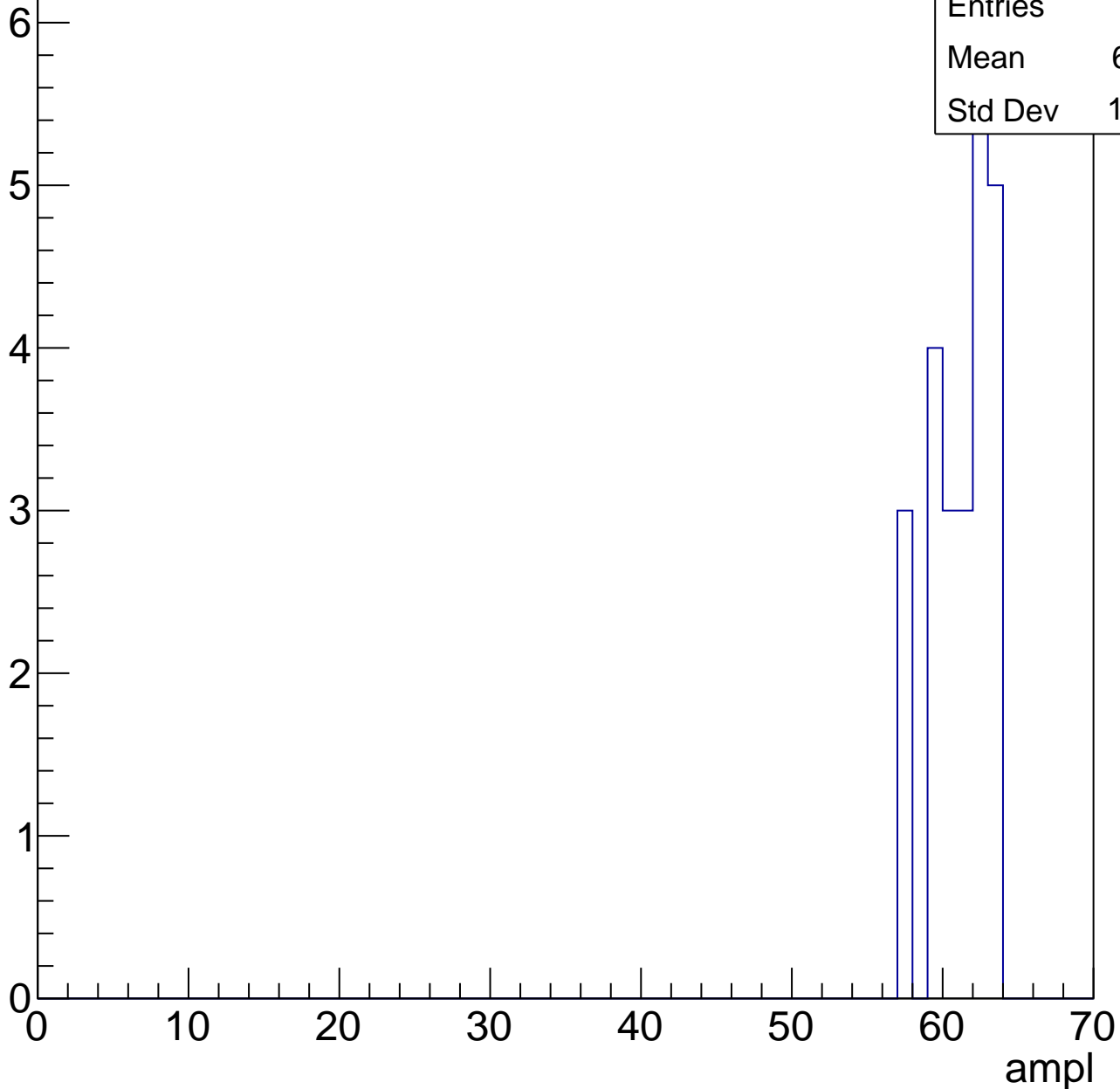


B1L102S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 24 |
| Mean | 60.71 |
| Std Dev | 1.947 |



B1L102S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch82, adc0

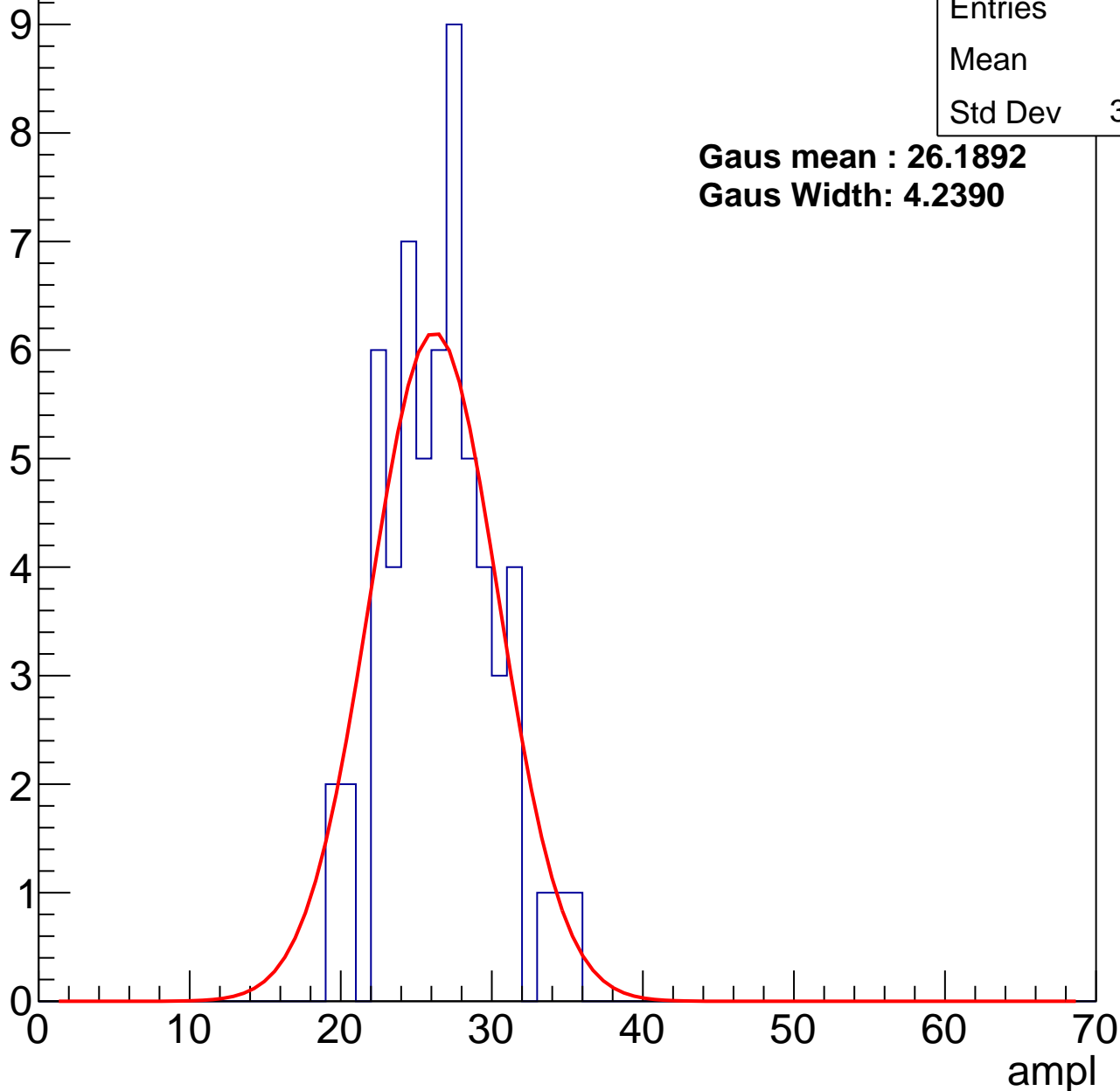
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 26.1 |
| Std Dev | 3.515 |

Gaus mean : 26.1892

Gaus Width: 4.2390



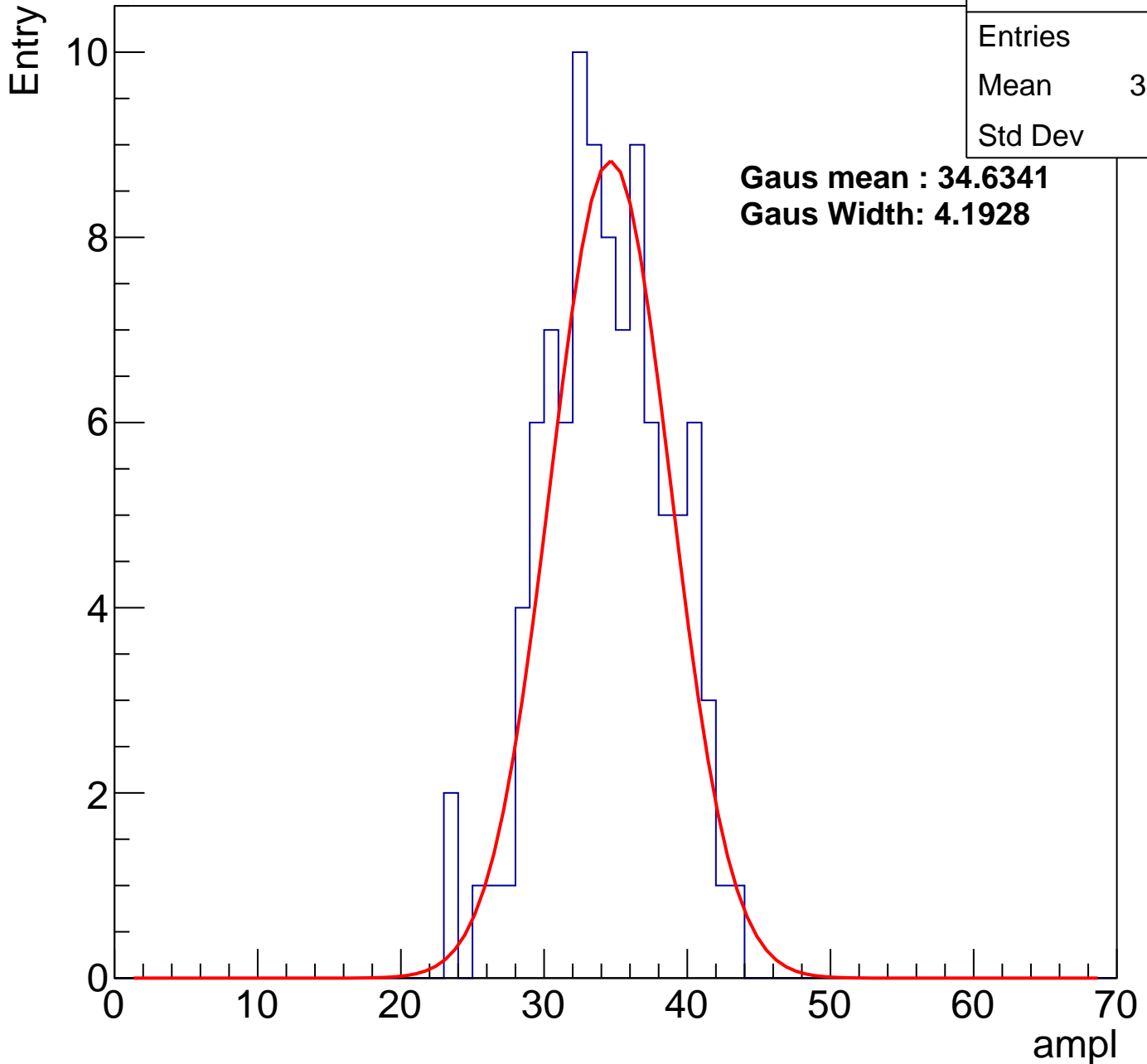
B1L102S, U15-ch82, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 98 |
| Mean | 33.87 |
| Std Dev | 4.23 |

Gaus mean : 34.6341

Gaus Width: 4.1928



B1L102S, U15-ch82, adc2

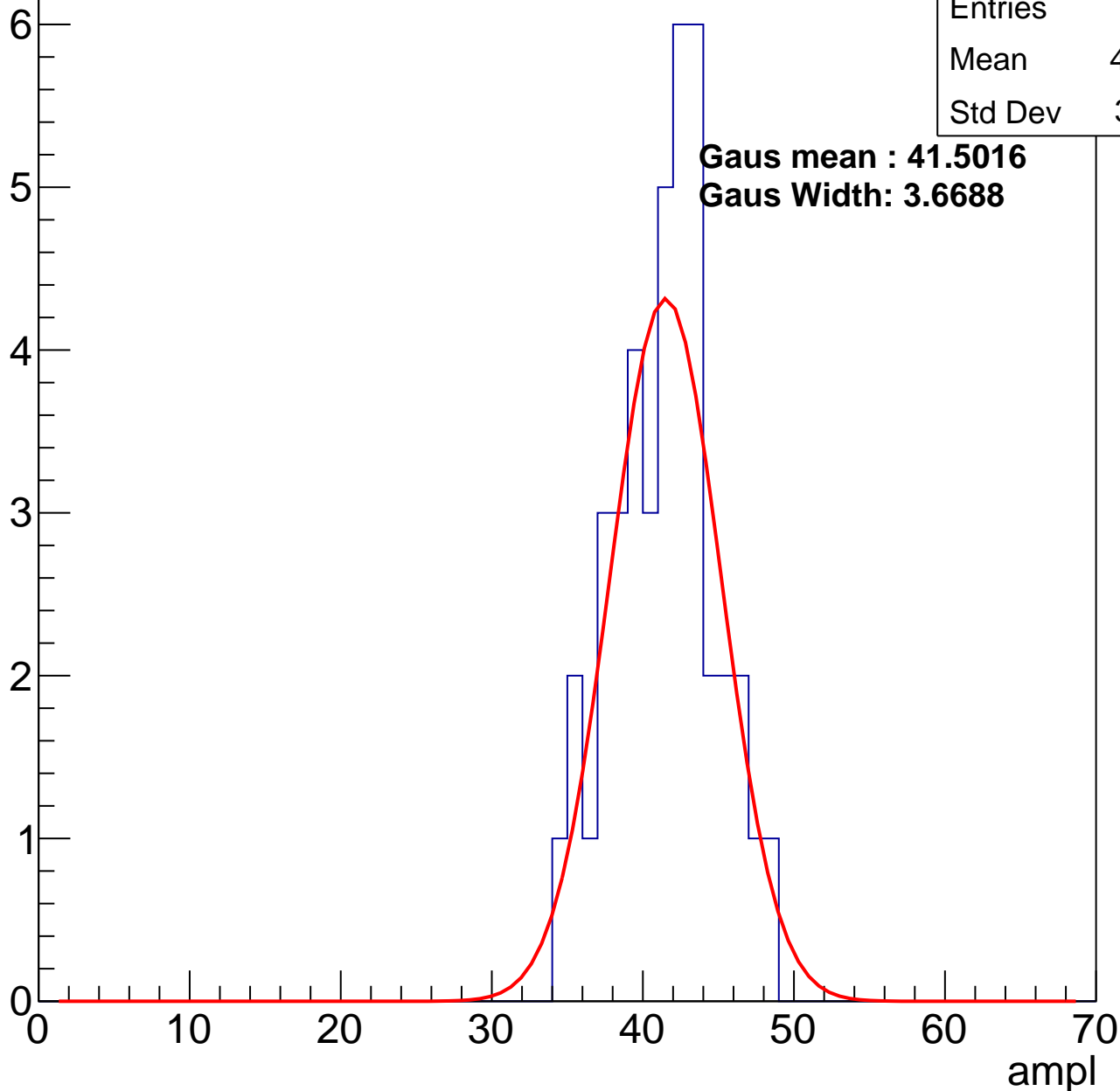
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 42 |
| Mean | 40.98 |
| Std Dev | 3.291 |

Gaus mean : 41.5016

Gaus Width: 3.6688

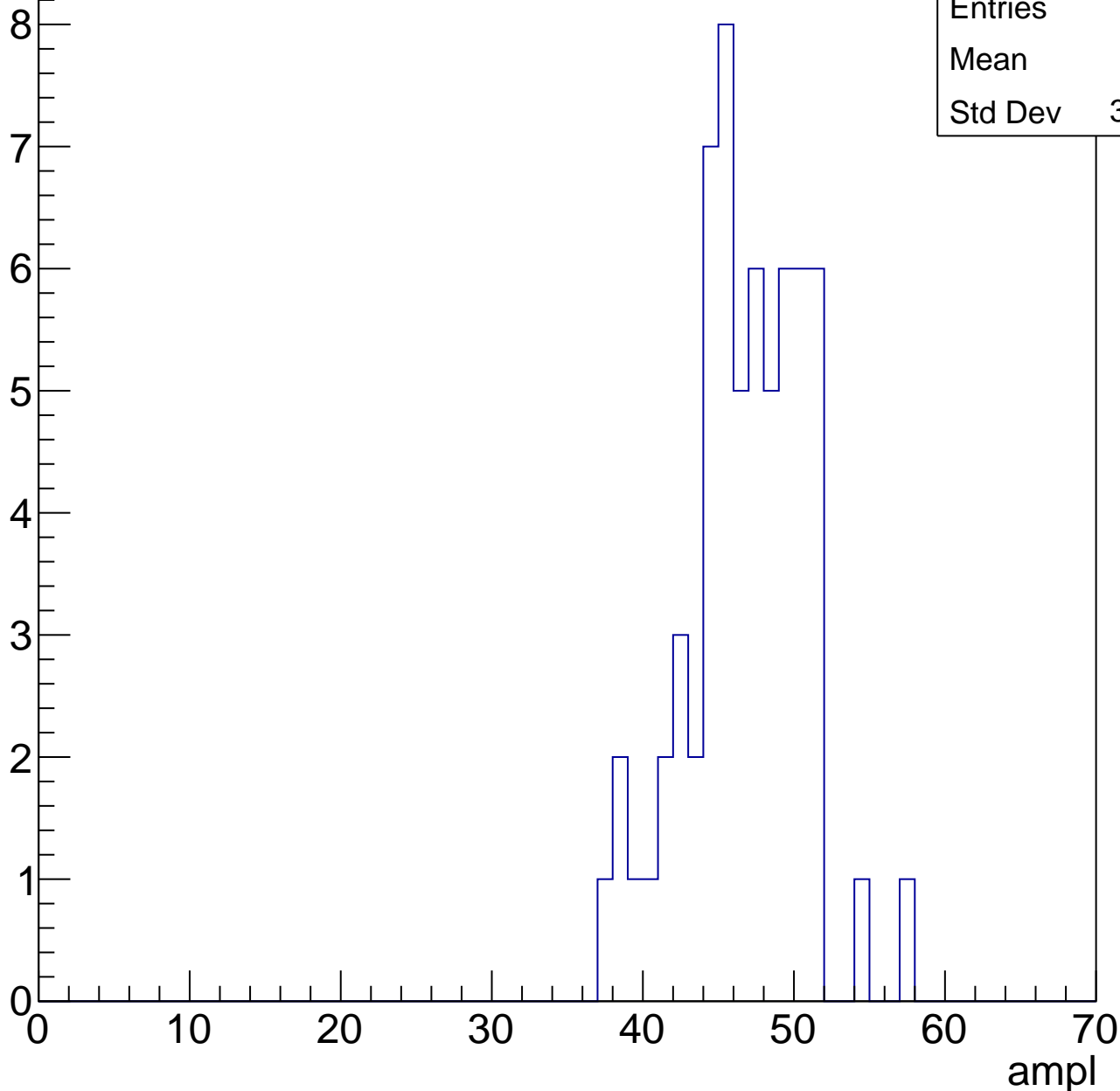


B1L102S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 46.3 |
| Std Dev | 3.886 |

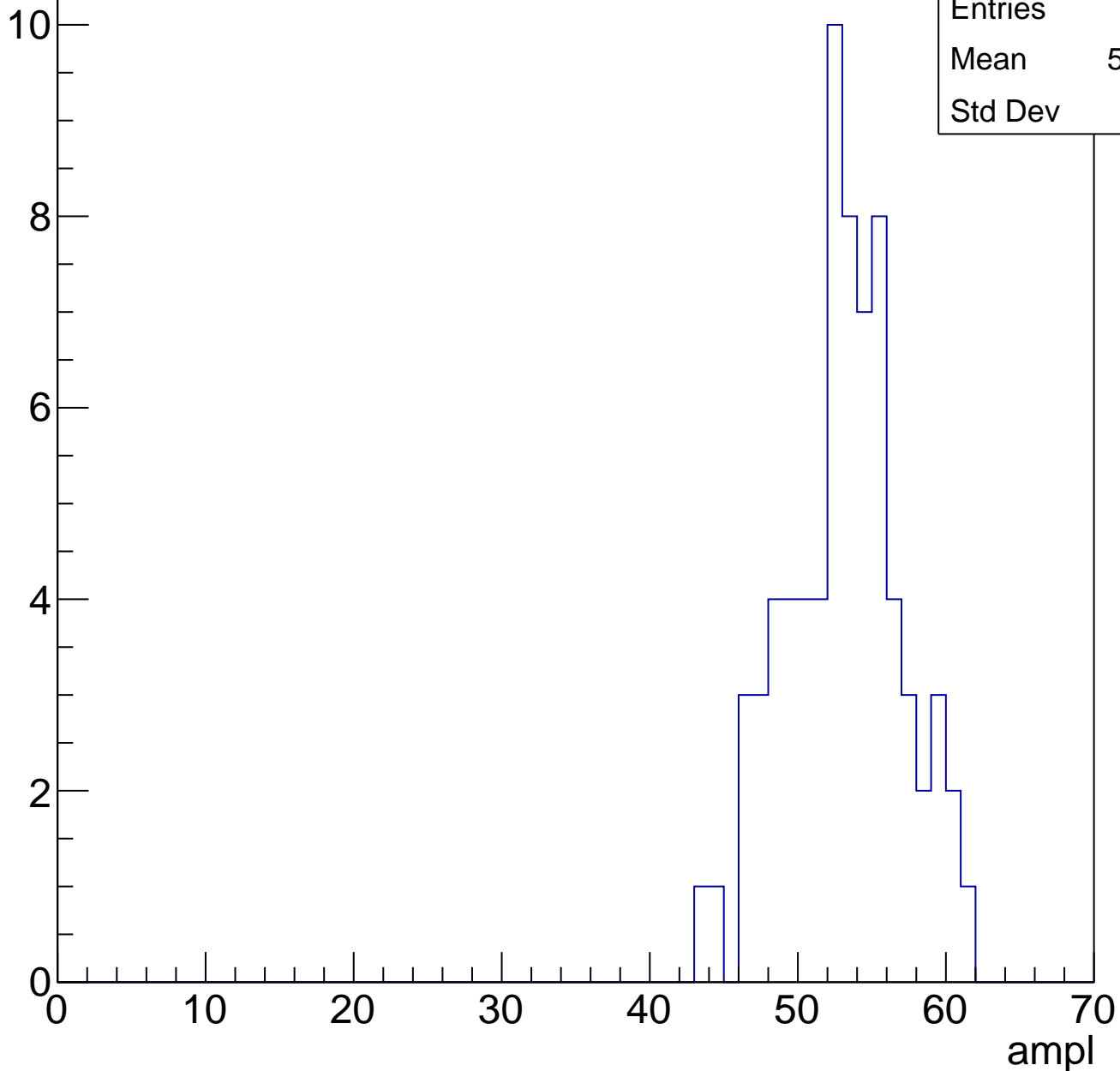


B1L102S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 72 |
| Mean | 52.62 |
| Std Dev | 3.9 |

Entry



B1L102S, U15-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 45

Mean 58.18

Std Dev 2.497

2

1

0

0

1

2

3

4

5

6

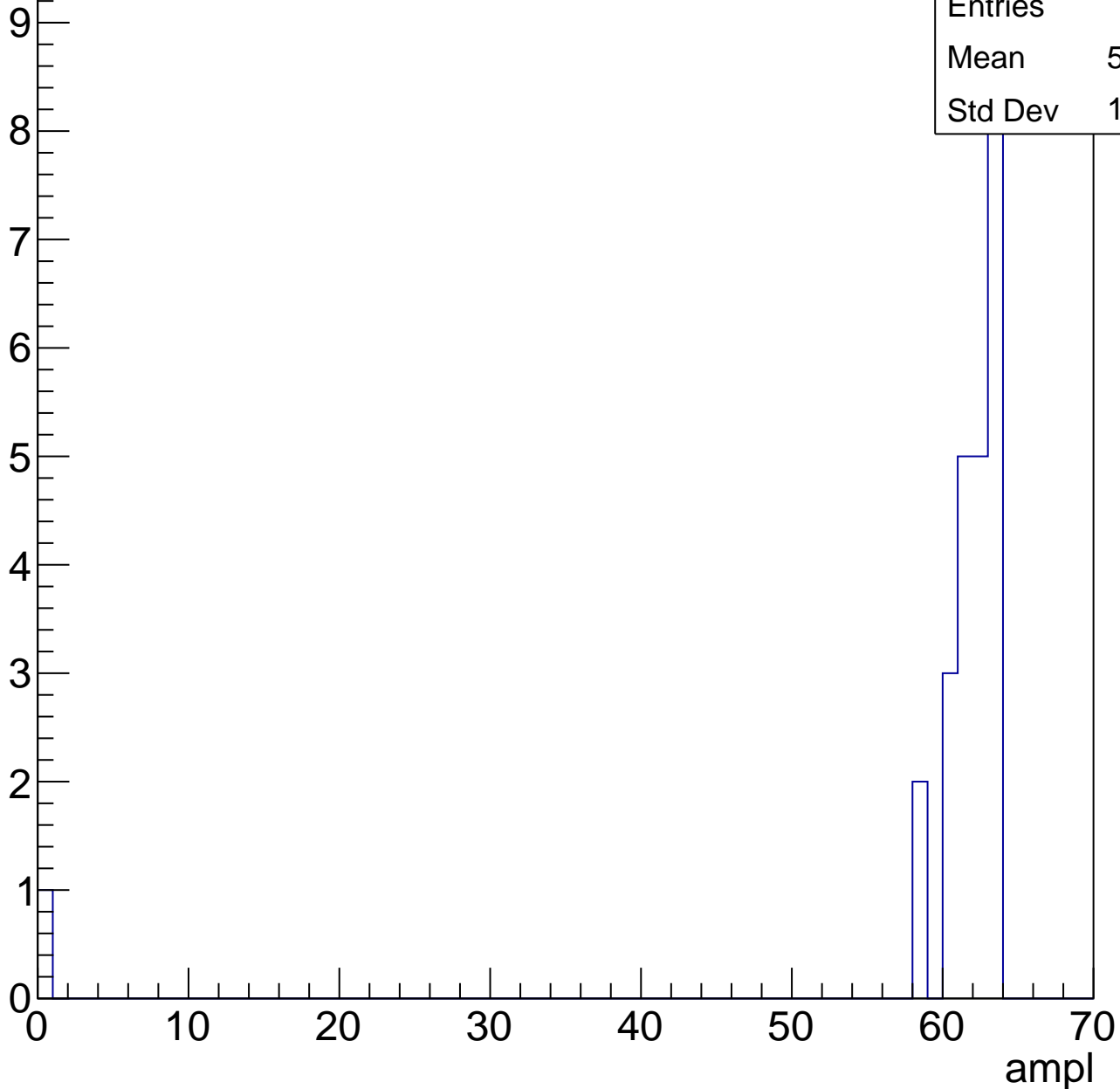
7

B1L102S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 25 |
| Mean | 59.12 |
| Std Dev | 12.16 |



B1L102S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch83, adc0

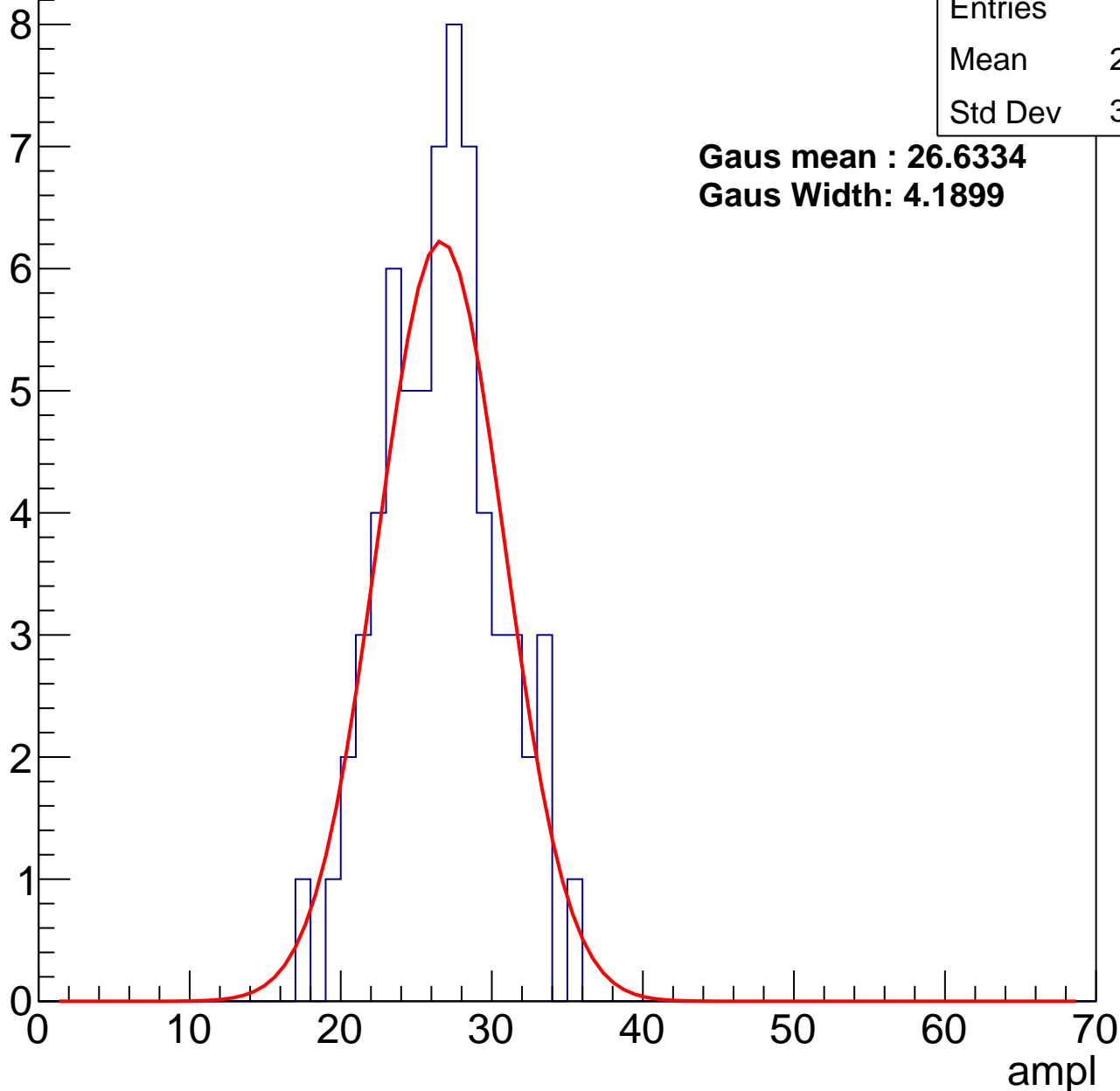
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 26.17 |
| Std Dev | 3.748 |

Gaus mean : 26.6334

Gaus Width: 4.1899



B1L102S, U15-ch83, adc1

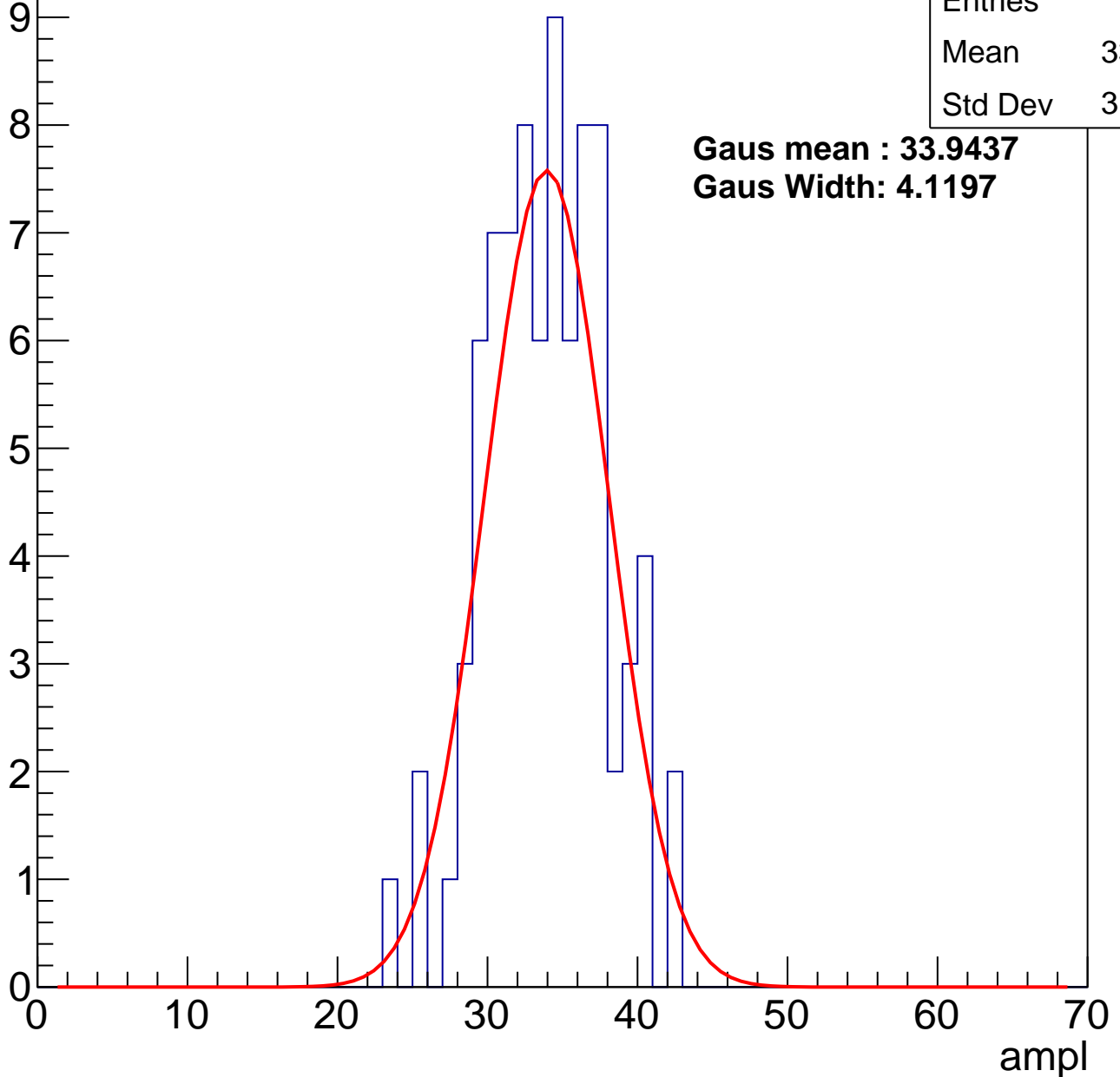
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 33.45 |
| Std Dev | 3.906 |

Gaus mean : 33.9437

Gaus Width: 4.1197



B1L102S, U15-ch83, adc2

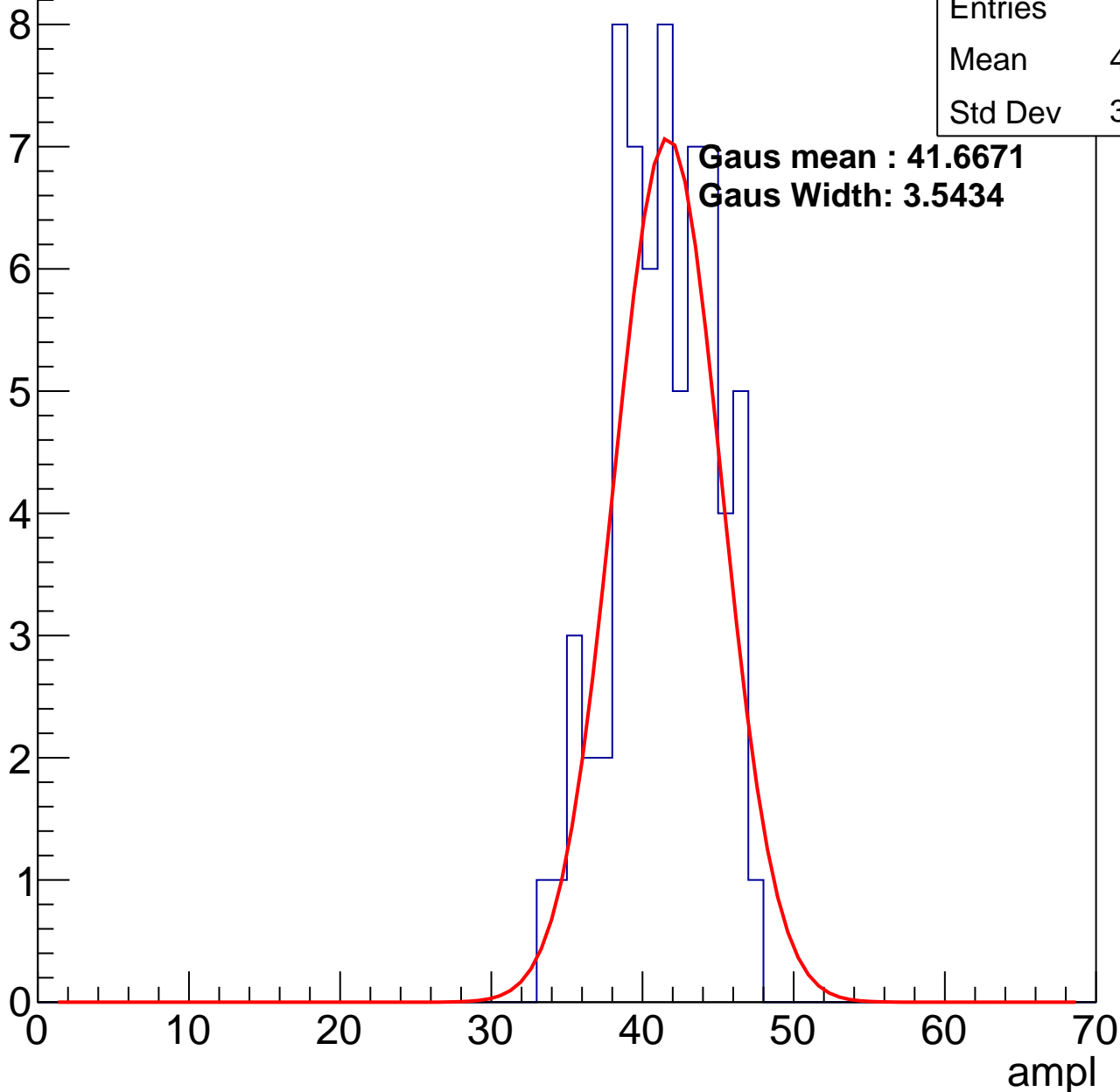
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 40.88 |
| Std Dev | 3.299 |

Gaus mean : 41.6671

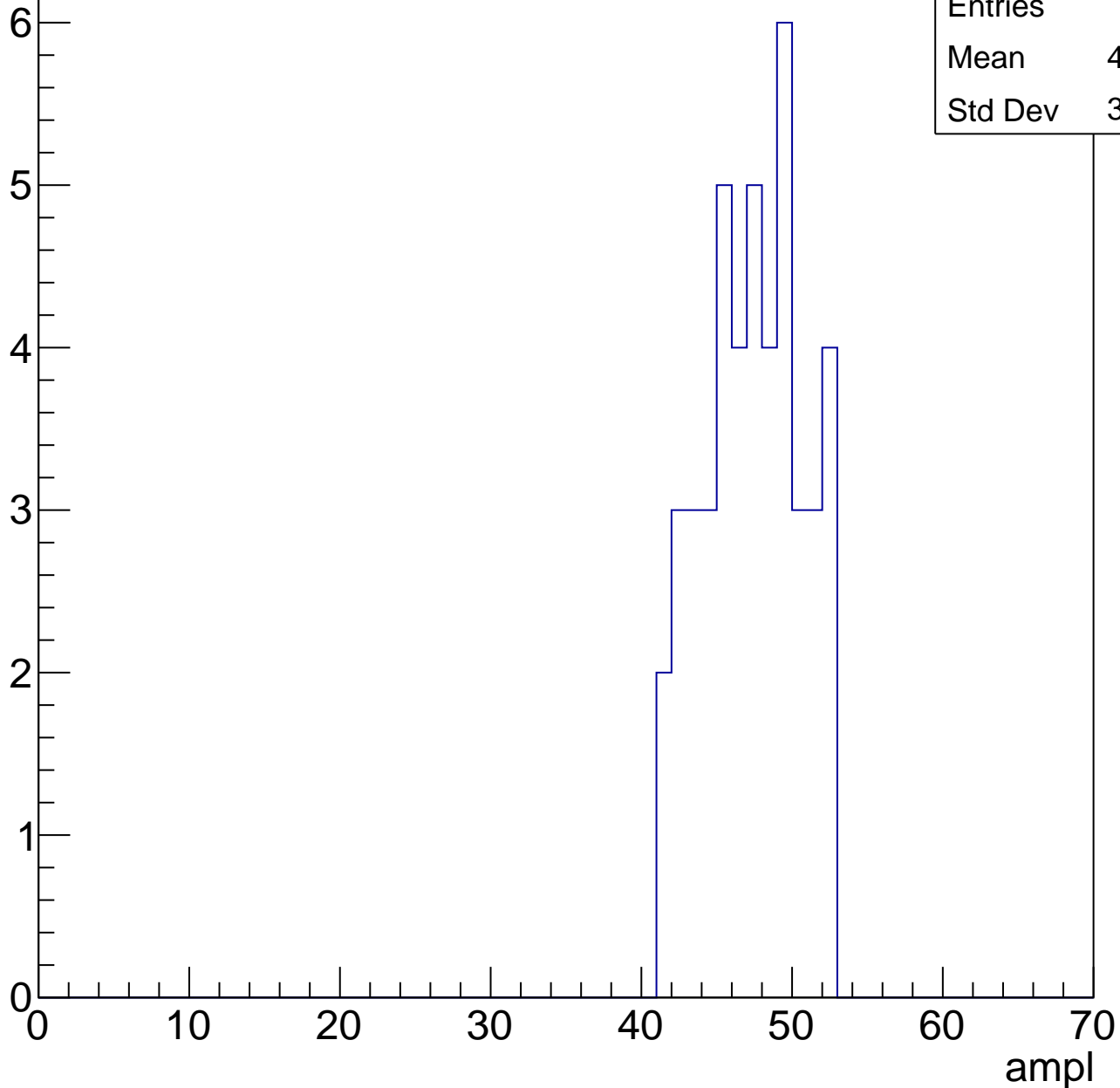
Gaus Width: 3.5434



B1L102S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

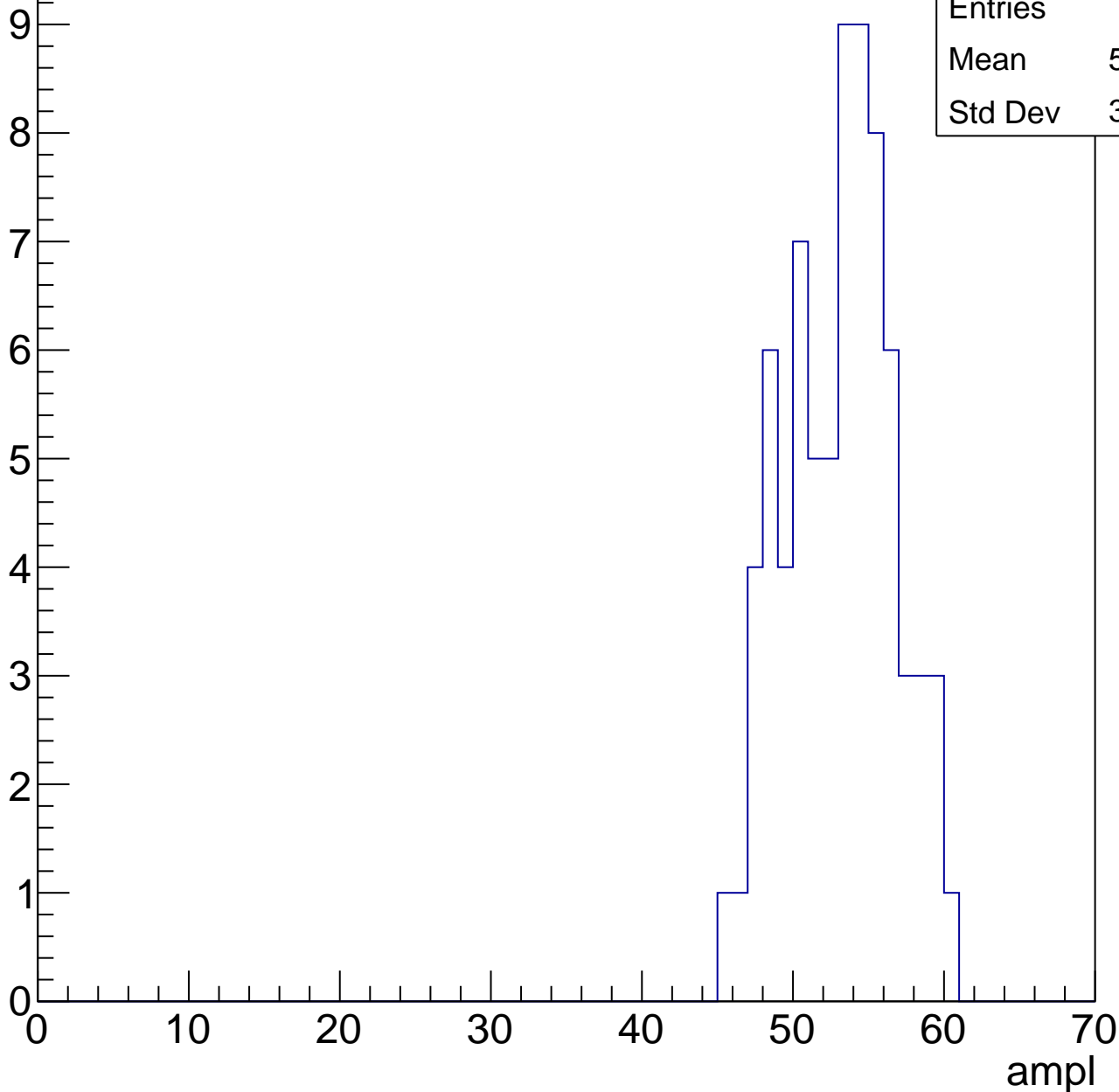


B1L102S, U15-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

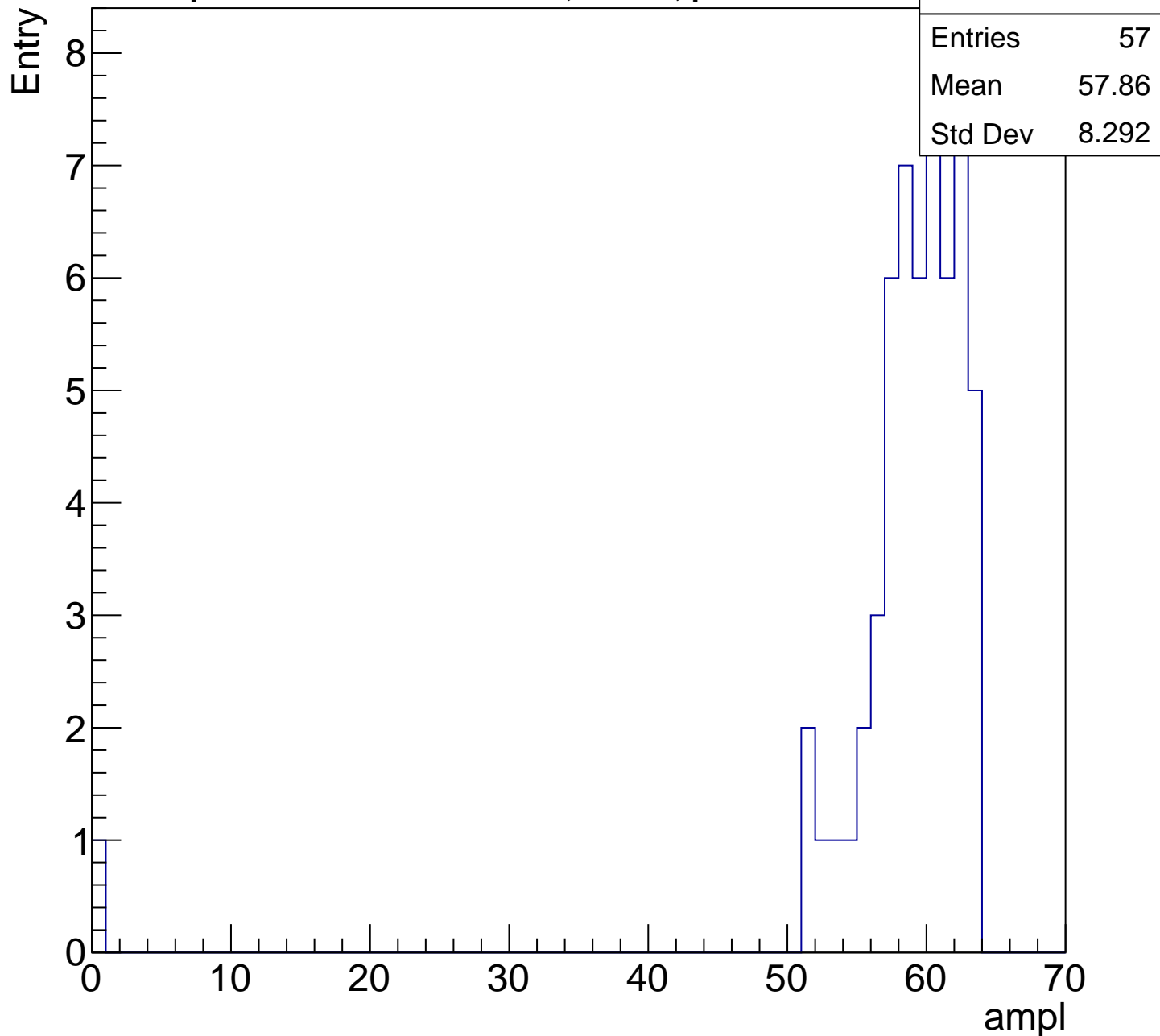
Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 52.65 |
| Std Dev | 3.512 |



B1L102S, U15-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

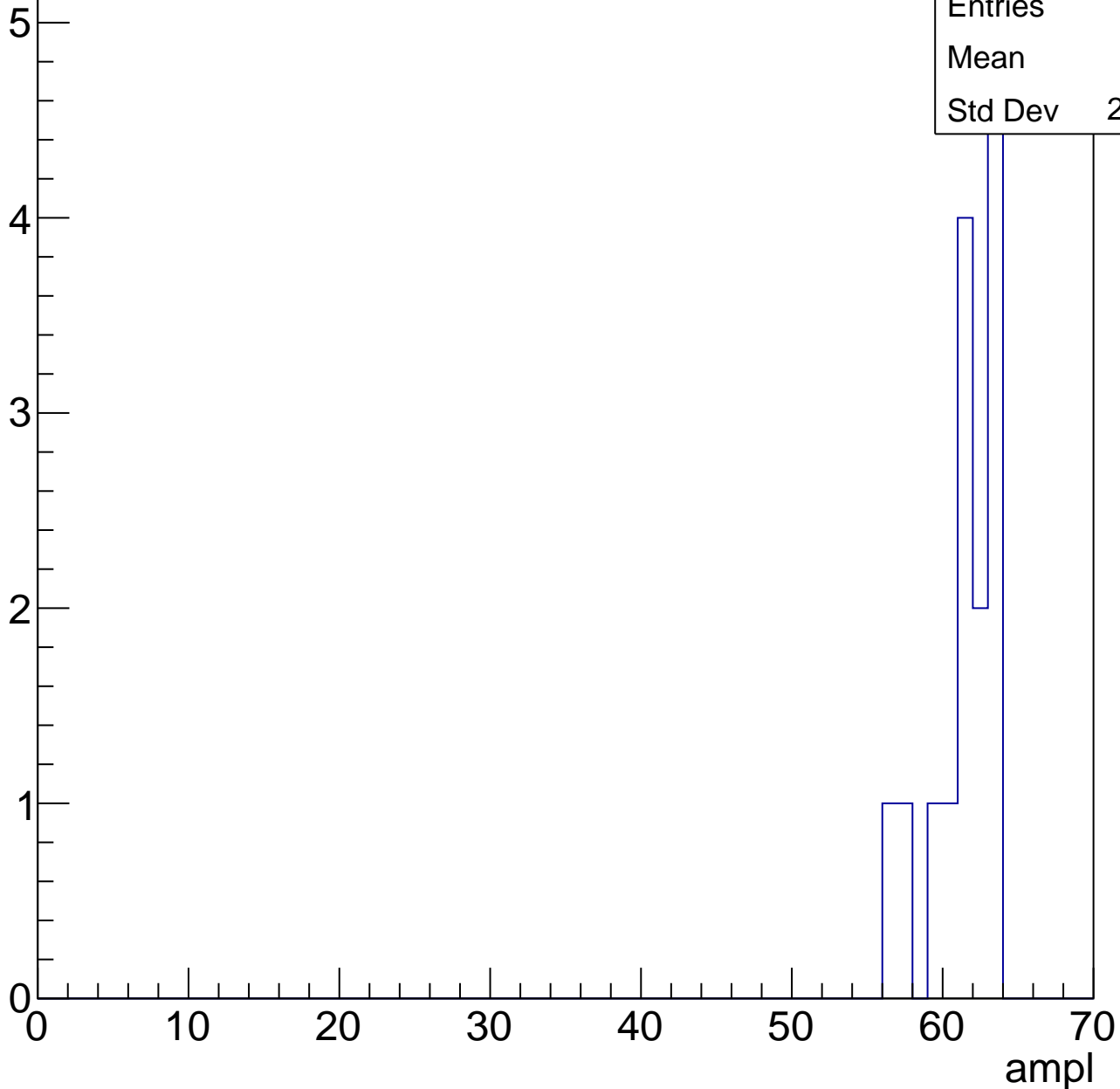


B1L102S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 15 |
| Mean | 61 |
| Std Dev | 2.129 |



B1L102S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch84, adc0

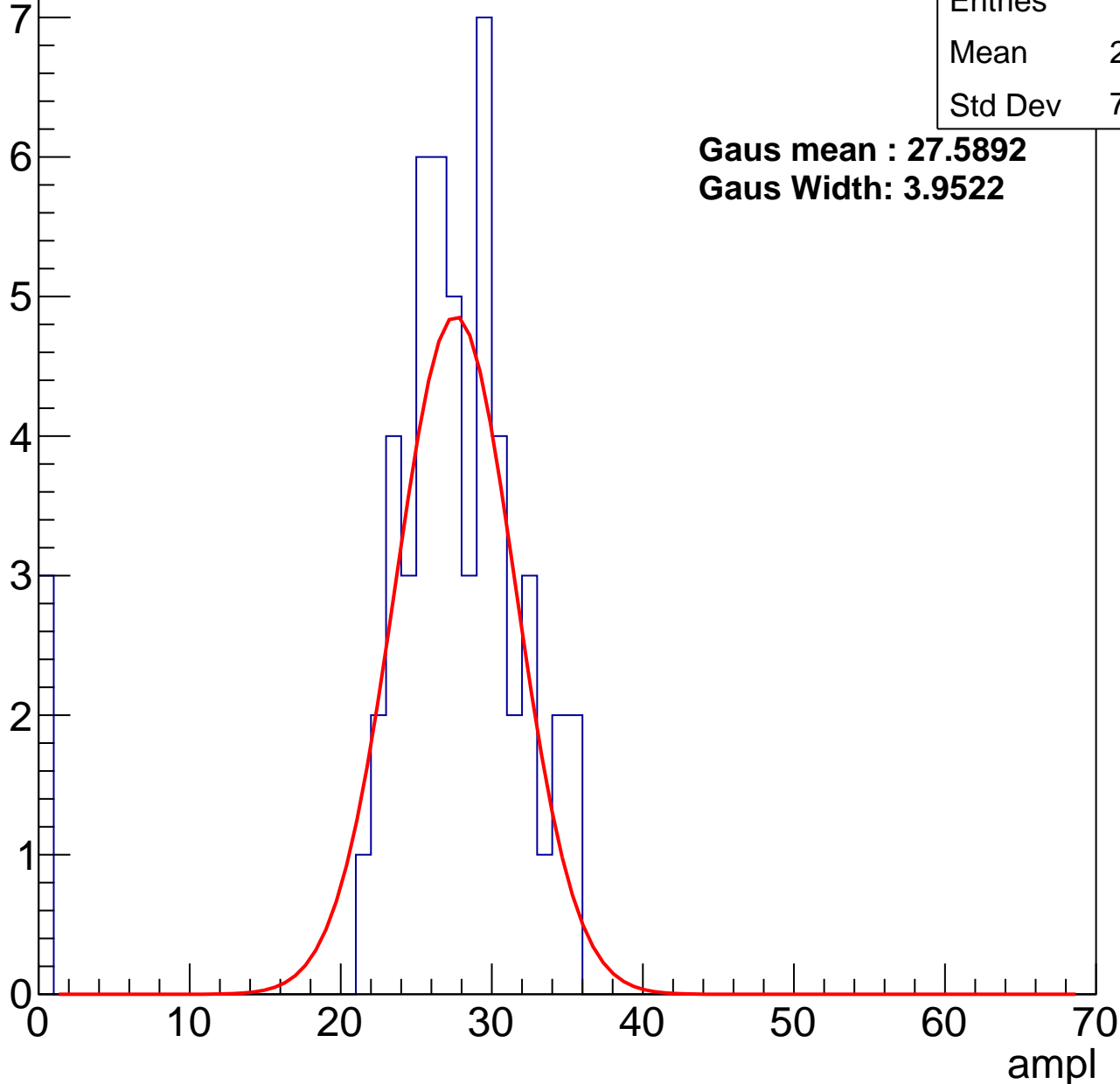
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 26.04 |
| Std Dev | 7.175 |

Gaus mean : 27.5892

Gaus Width: 3.9522



B1L102S, U15-ch84, adc1

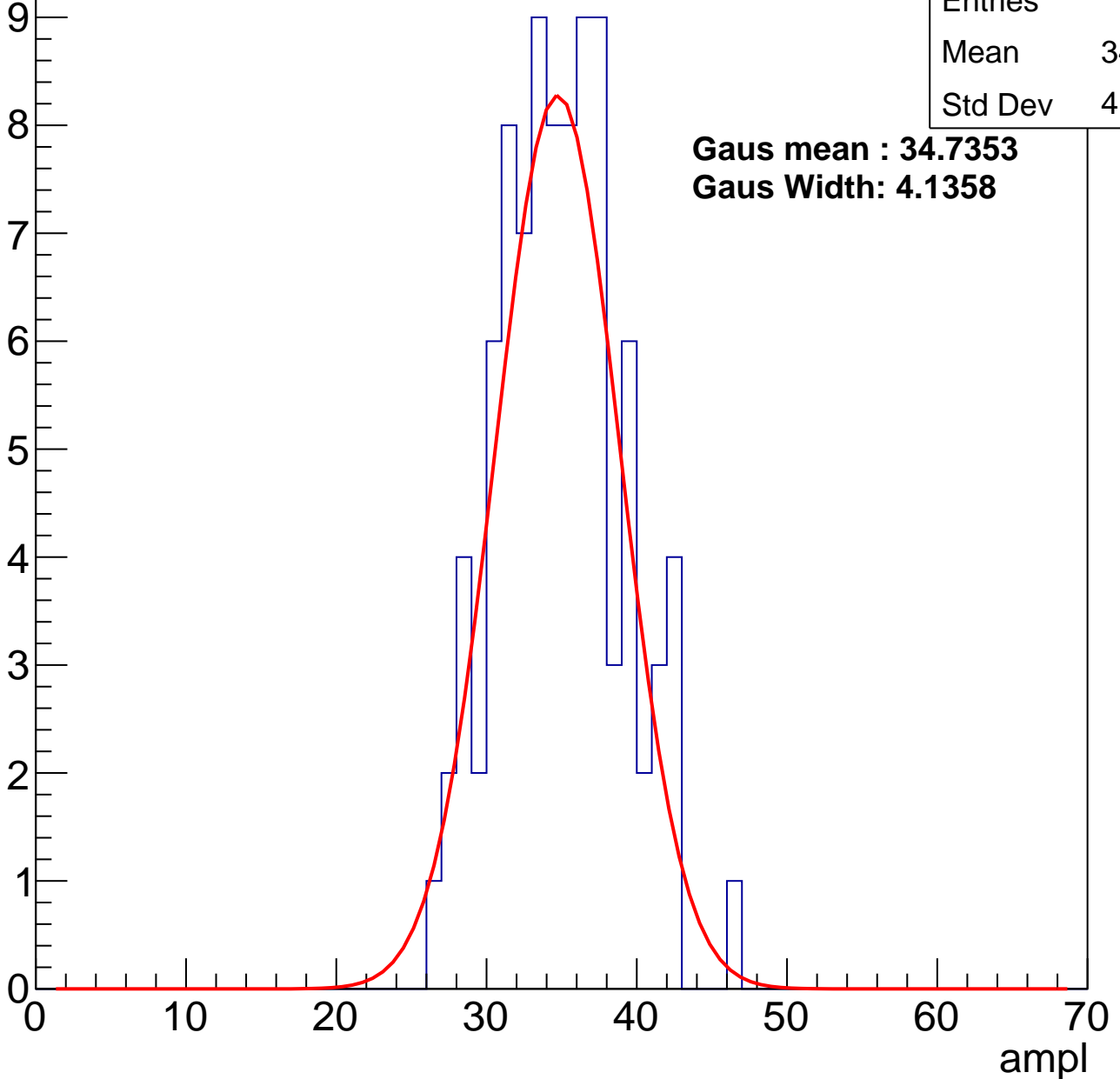
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 92 |
| Mean | 34.49 |
| Std Dev | 4.012 |

Gaus mean : 34.7353

Gaus Width: 4.1358



B1L102S, U15-ch84, adc2

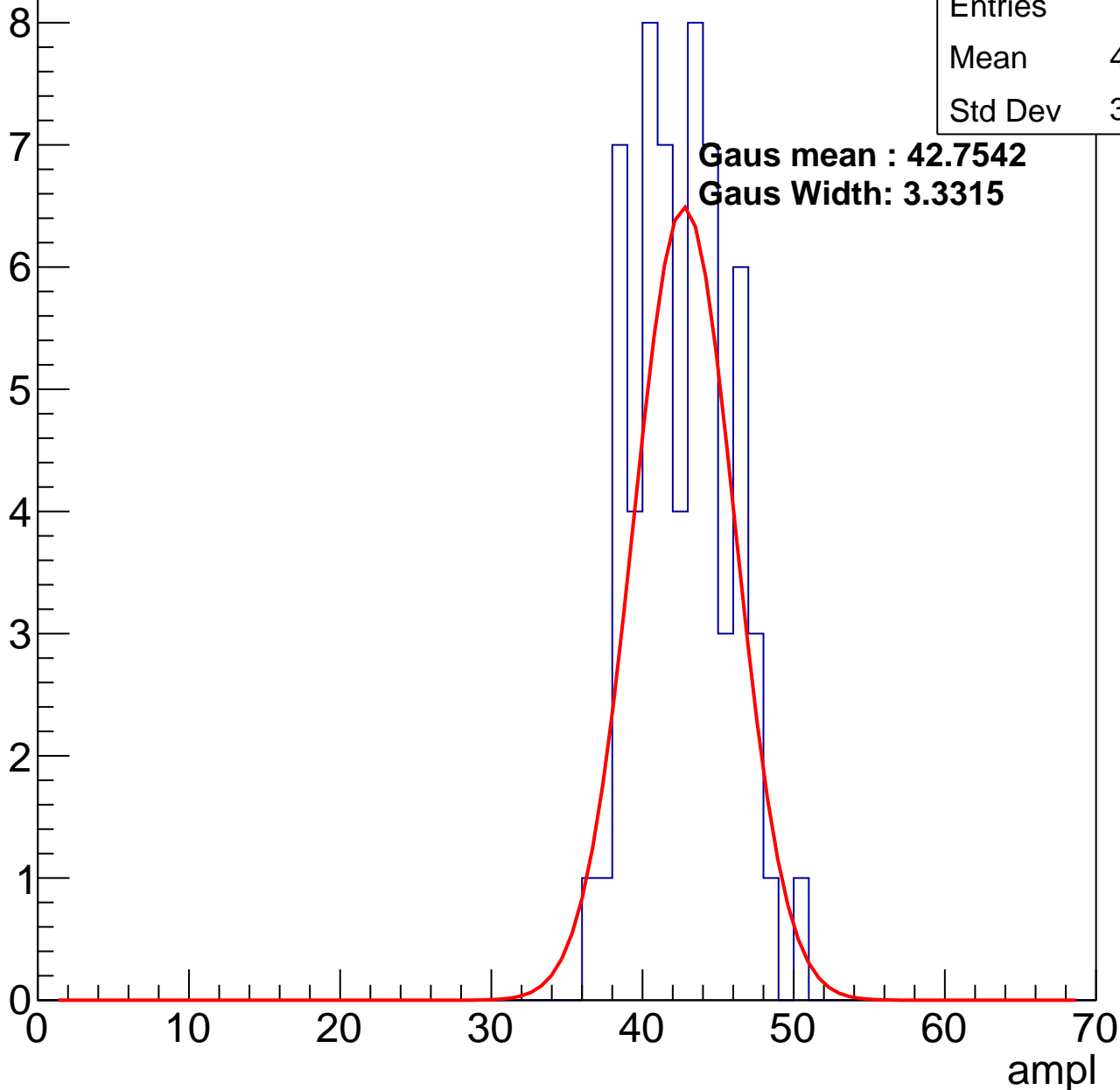
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.16 |
| Std Dev | 3.084 |

Gaus mean : 42.7542

Gaus Width: 3.3315

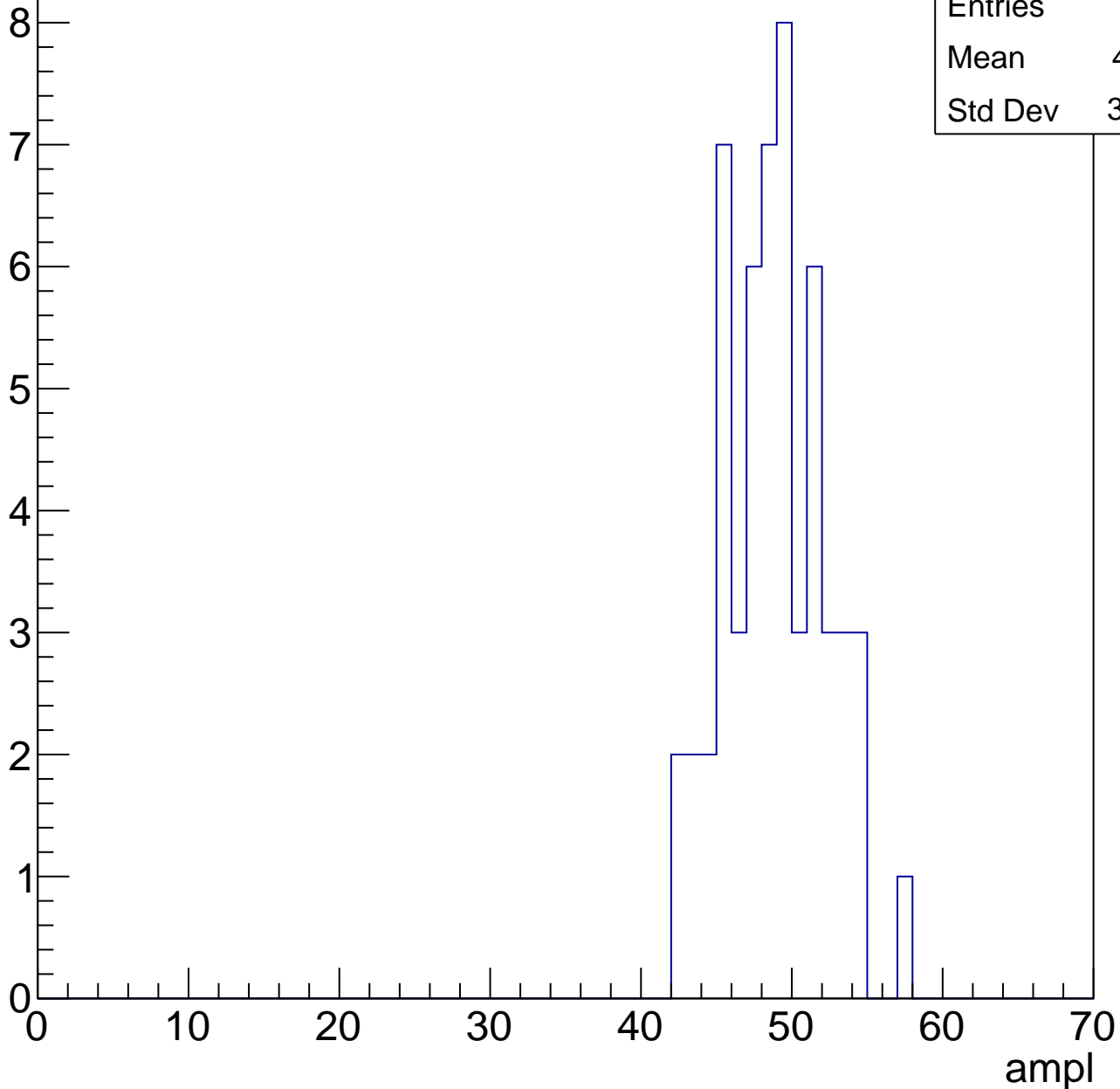


B1L102S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 48.41 |
| Std Dev | 3.305 |

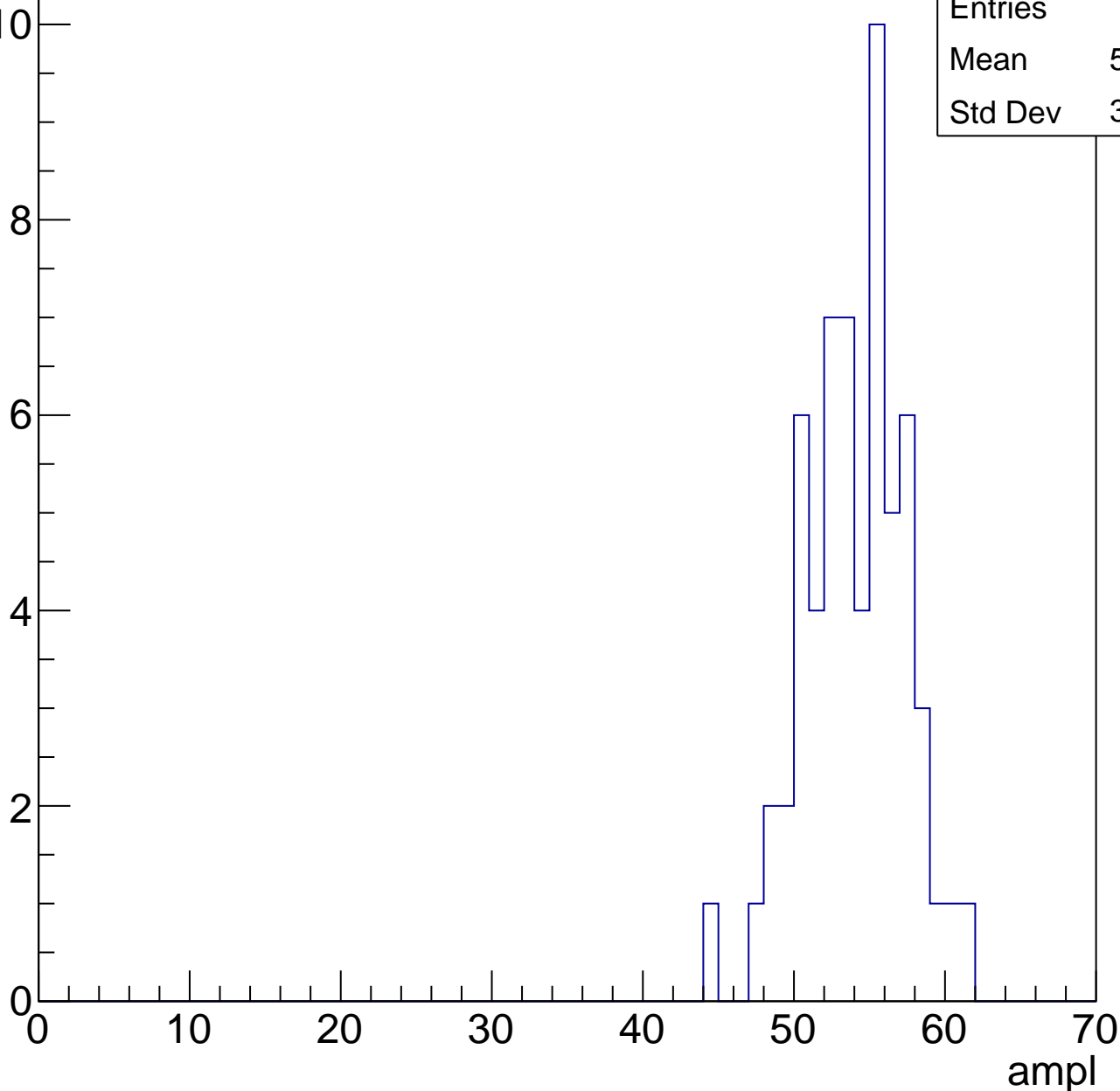


B1L102S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 53.54 |
| Std Dev | 3.307 |

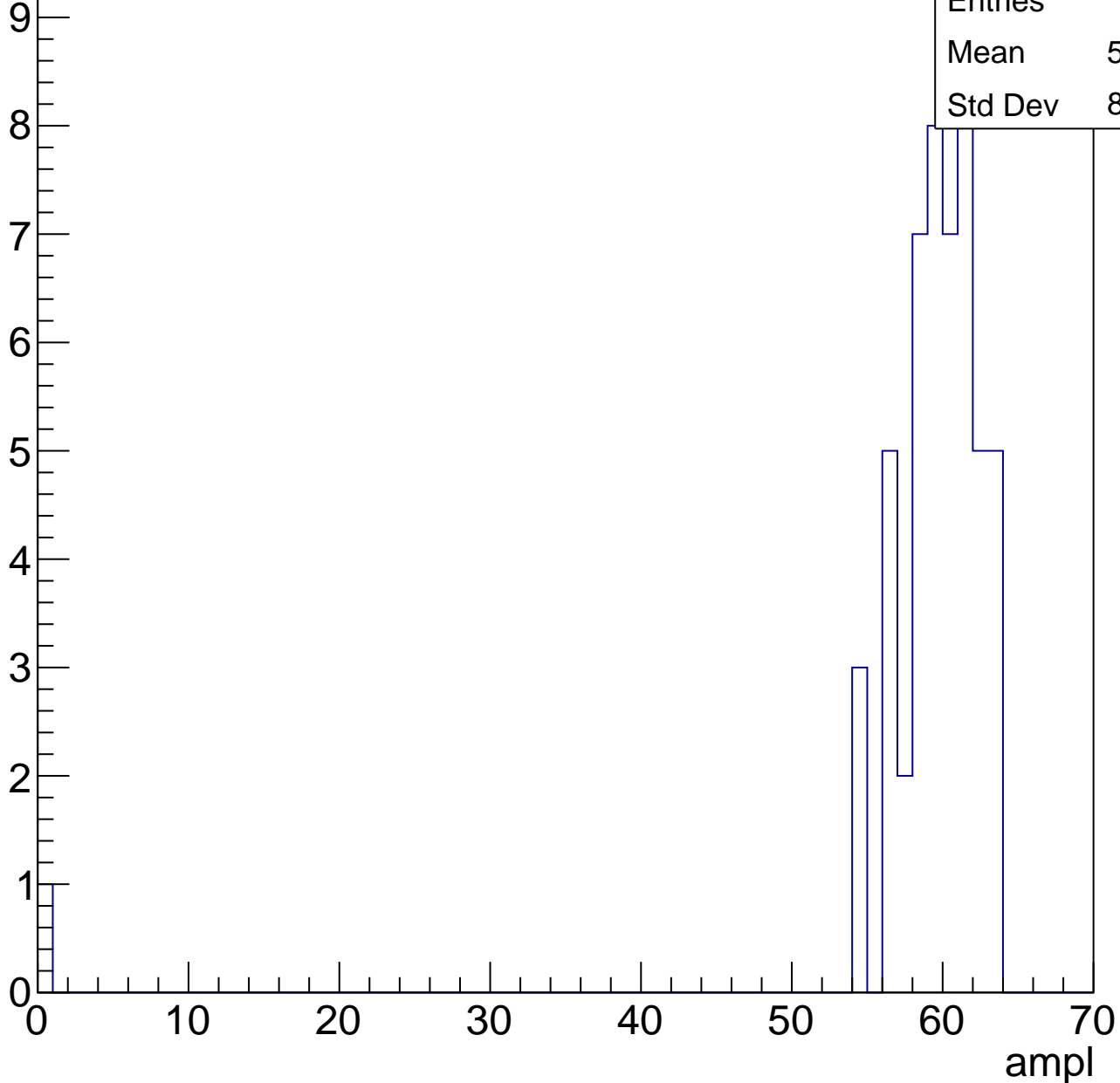


B1L102S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.23 |
| Std Dev | 8.496 |

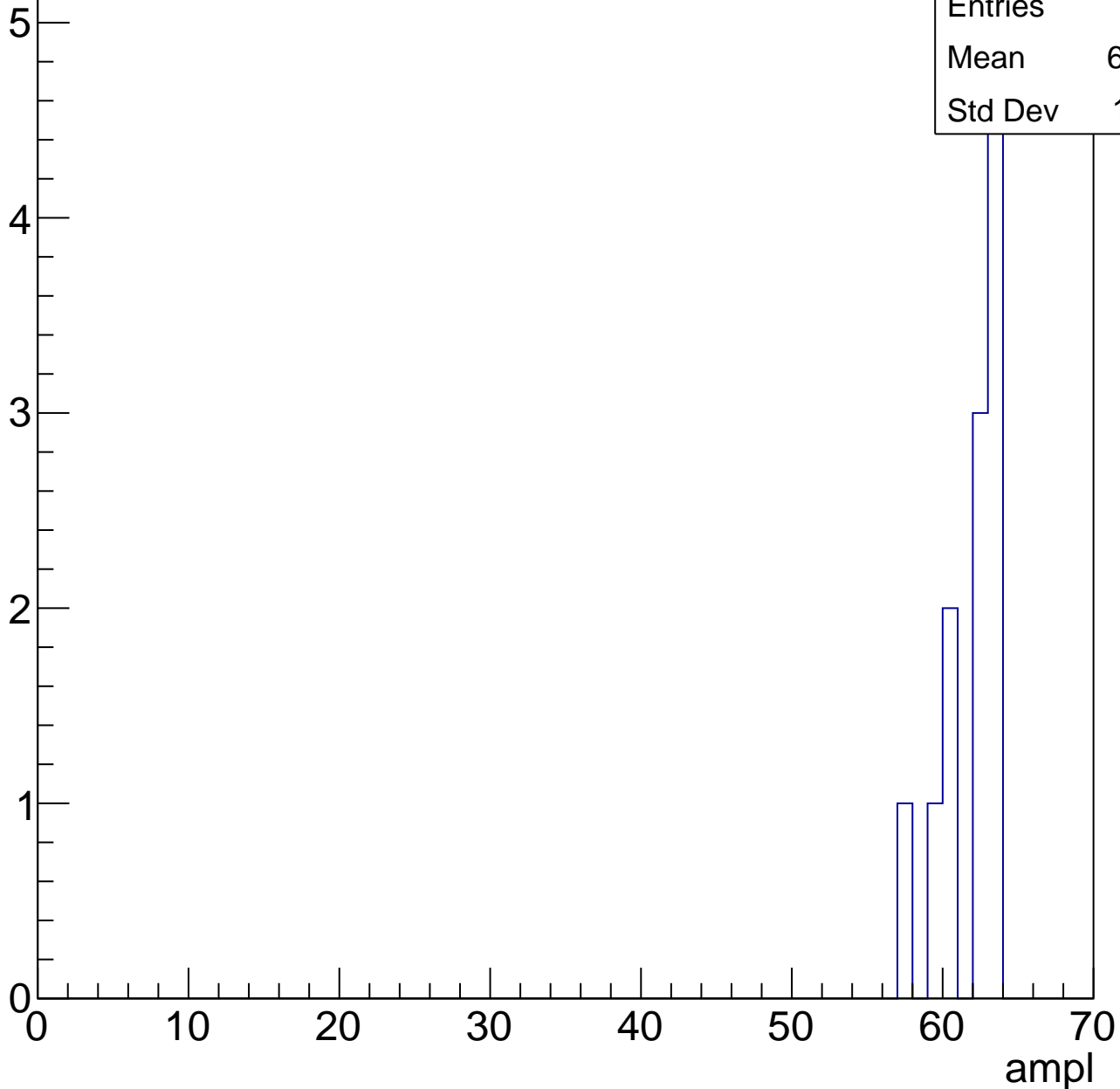


B1L102S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

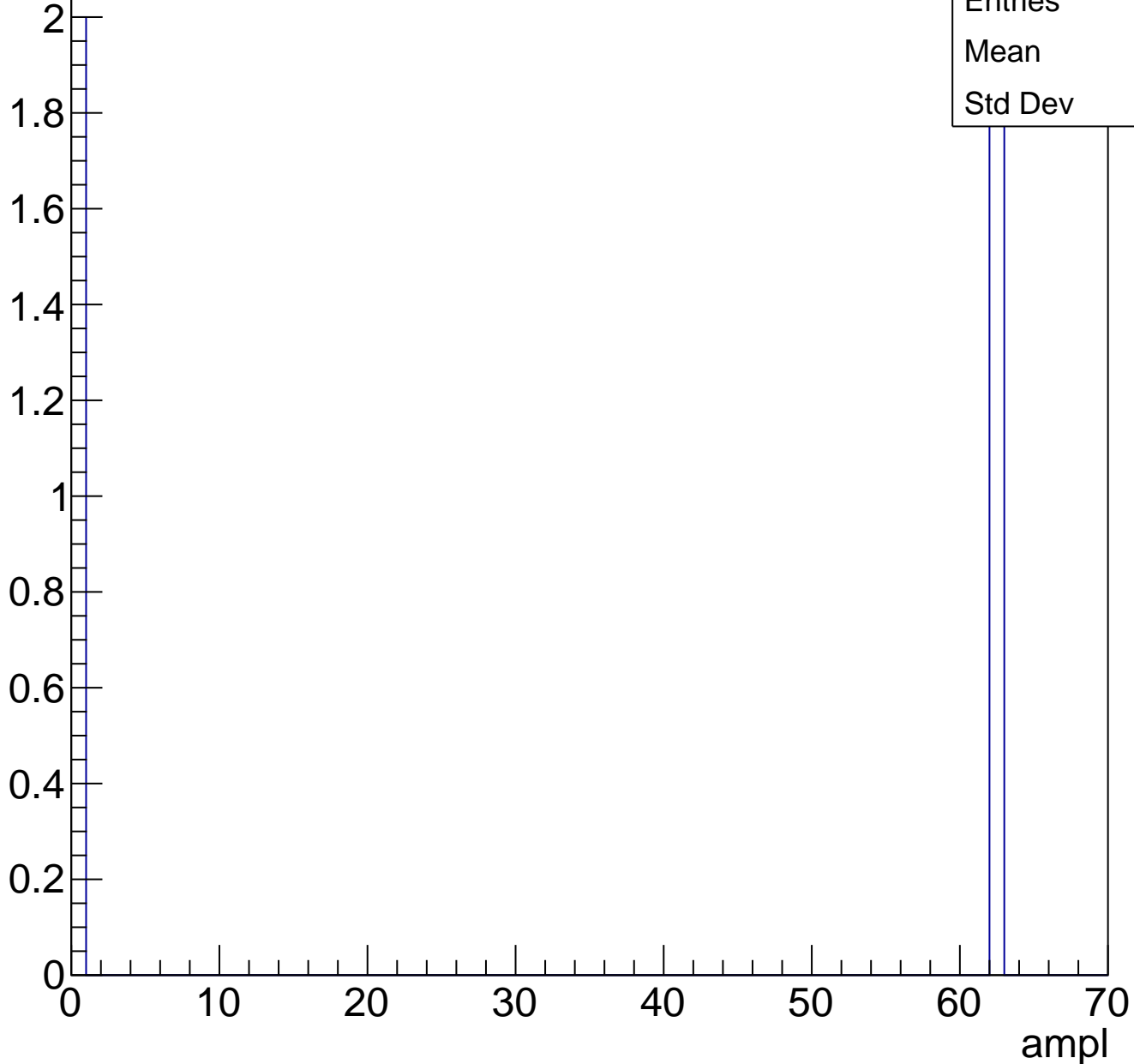
| | |
|---------|-------|
| Entries | 12 |
| Mean | 61.42 |
| Std Dev | 1.891 |



B1L102S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch85, adc0

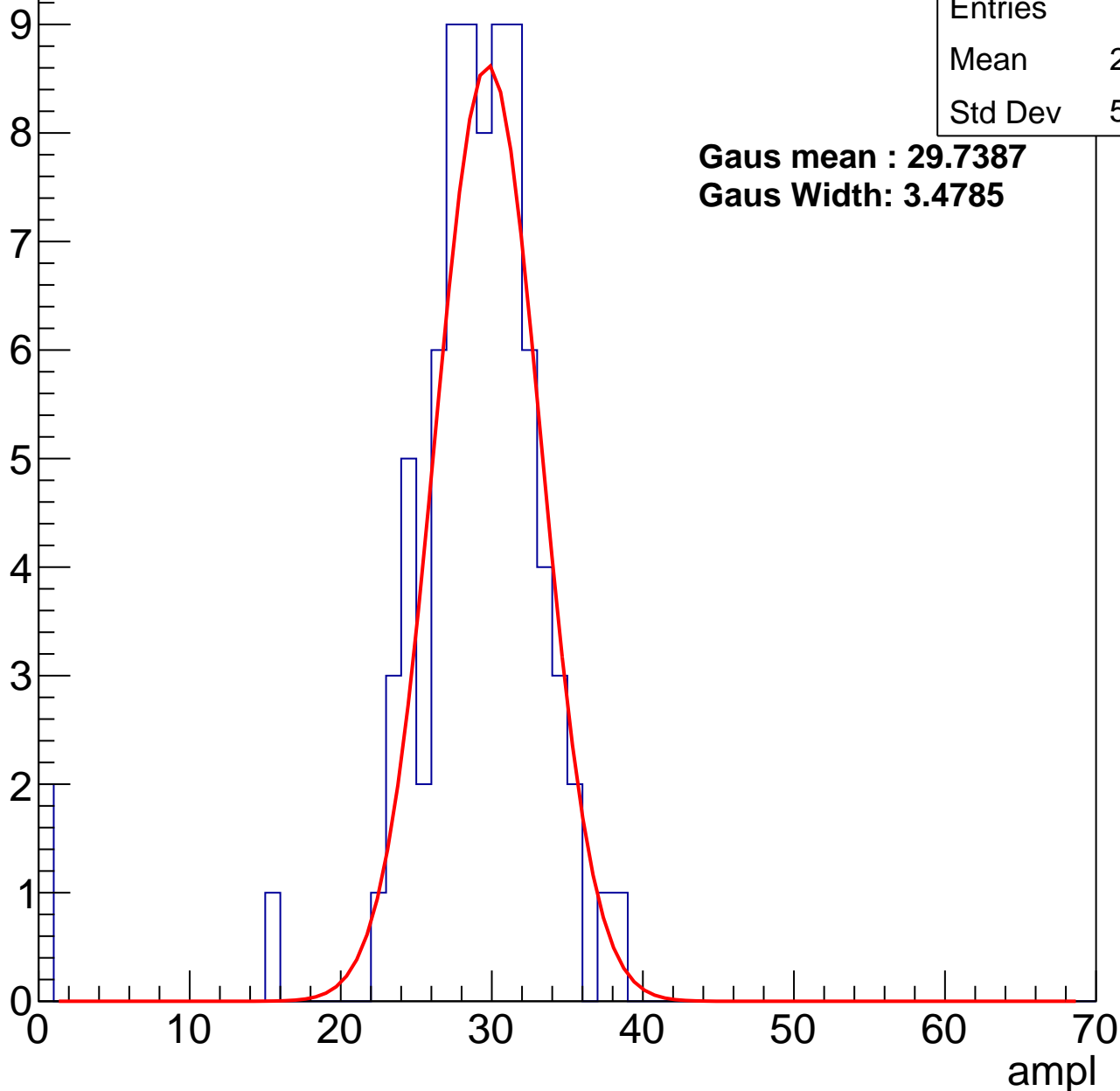
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 81 |
| Mean | 28.14 |
| Std Dev | 5.758 |

Gaus mean : 29.7387

Gaus Width: 3.4785



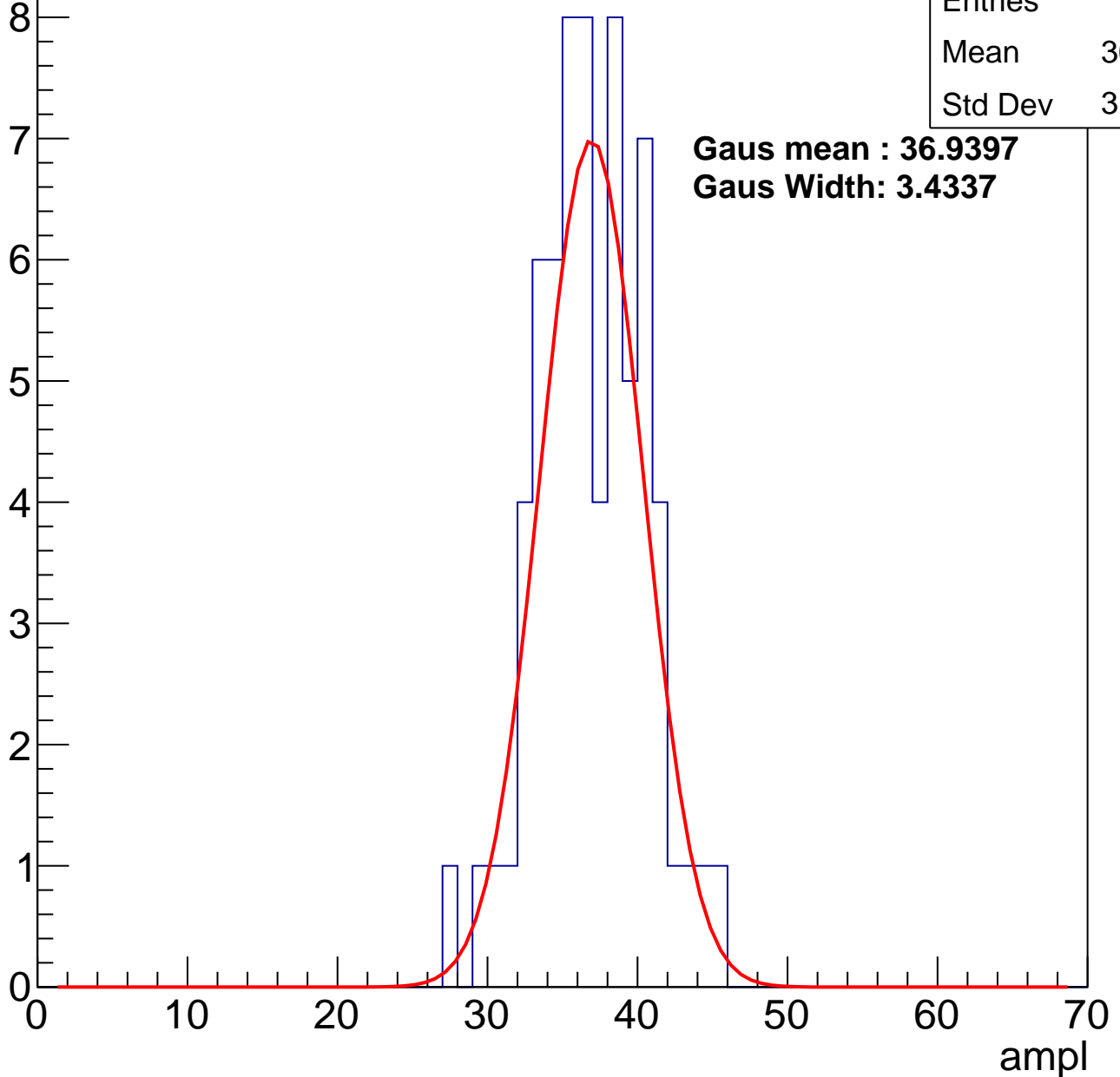
B1L102S, U15-ch85, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 36.47 |
| Std Dev | 3.533 |

Gaus mean : 36.9397
Gaus Width: 3.4337



B1L102S, U15-ch85, adc2

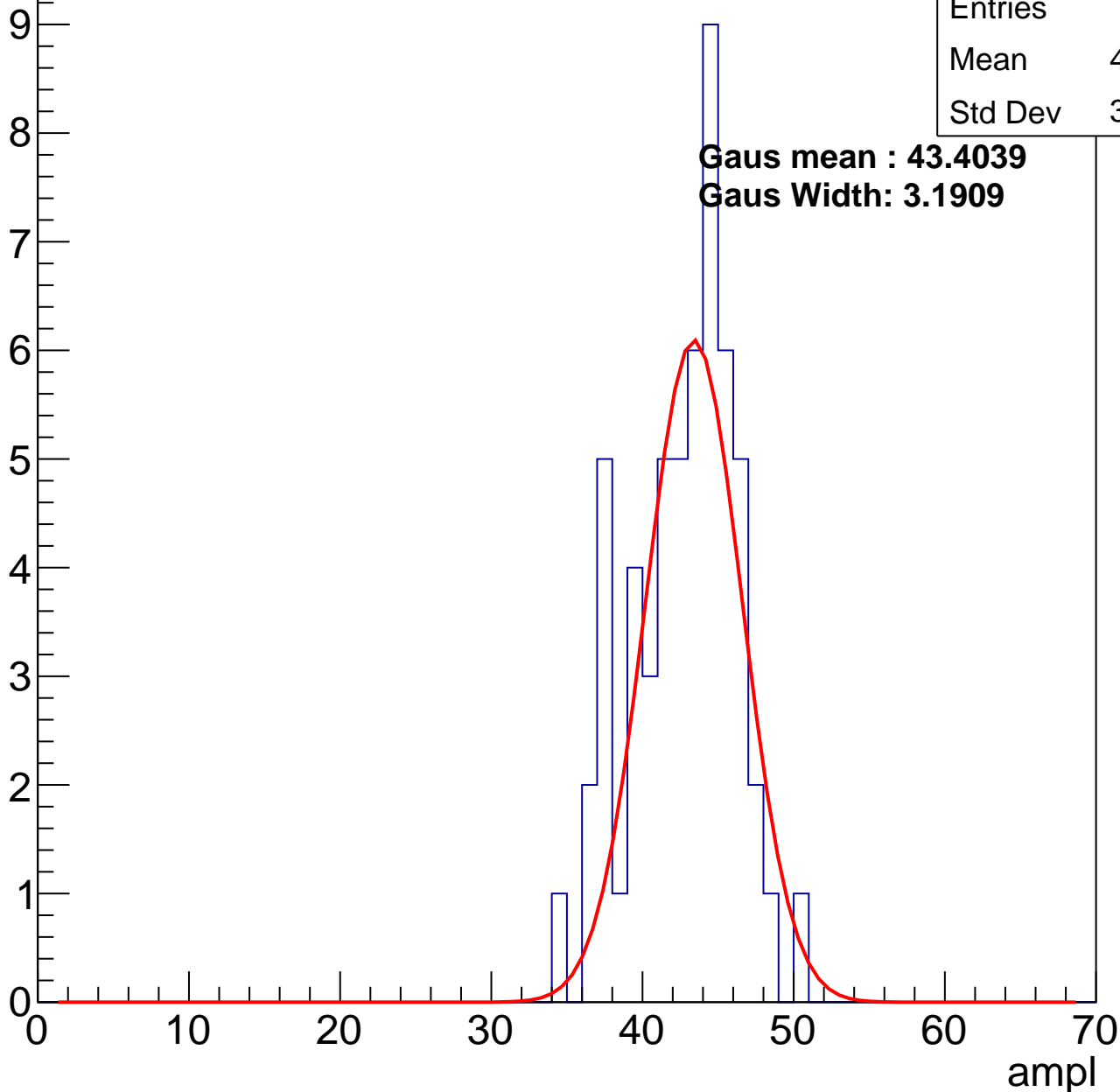
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 42.25 |
| Std Dev | 3.413 |

Gaus mean : 43.4039

Gaus Width: 3.1909

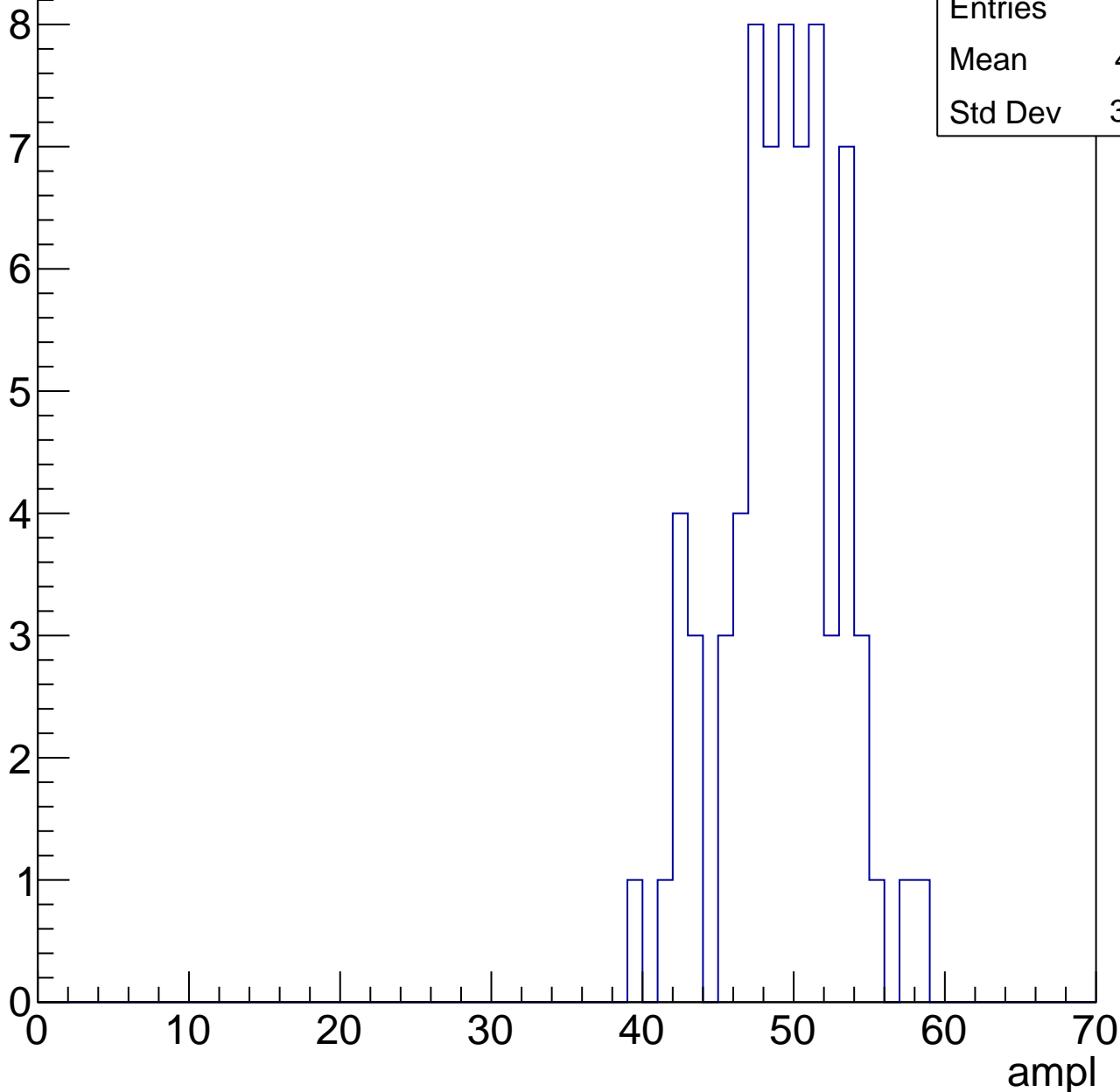


B1L102S, U15-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

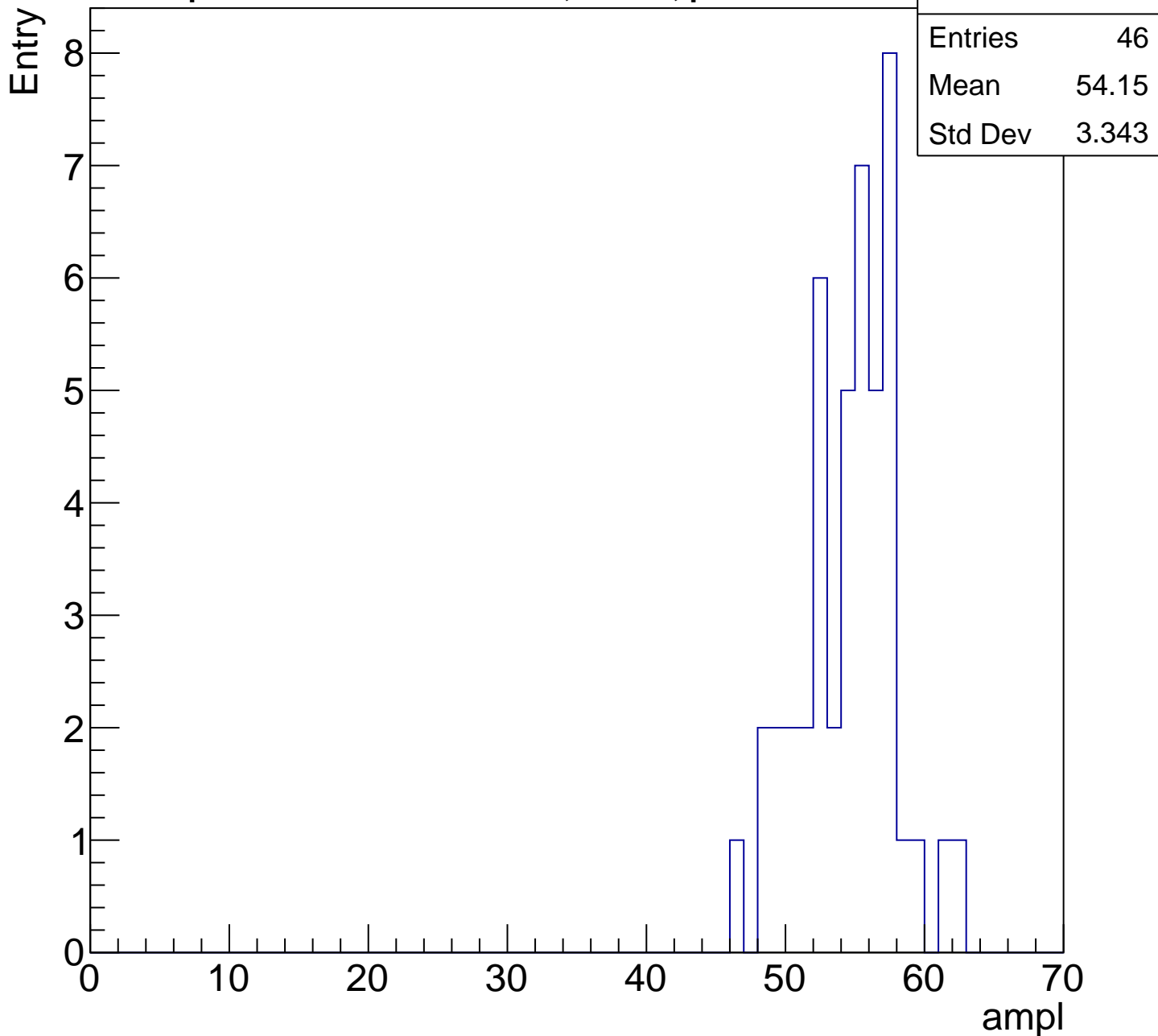
Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 48.81 |
| Std Dev | 3.822 |



B1L102S, U15-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

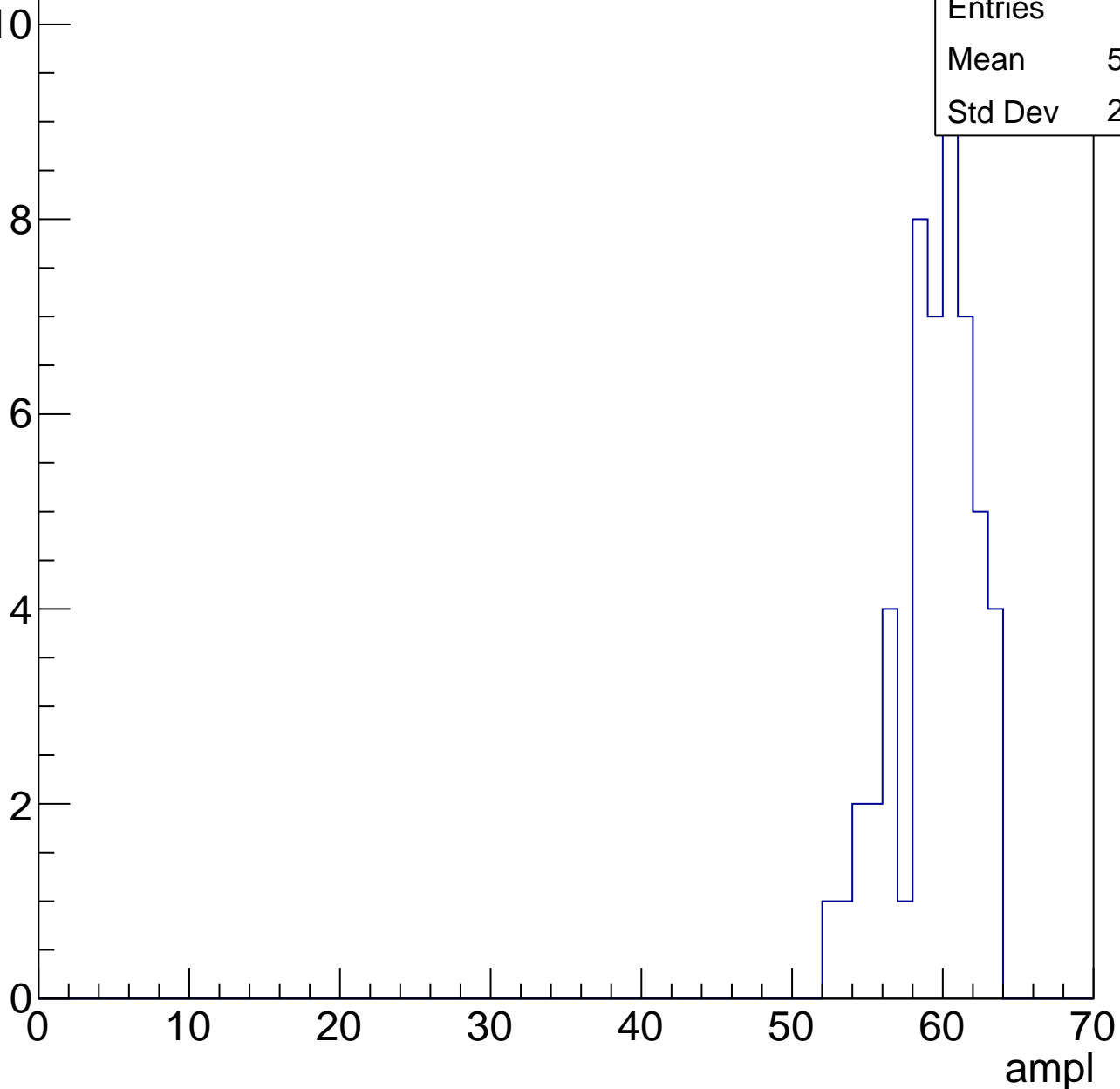


B1L102S, U15-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 59.04 |
| Std Dev | 2.638 |

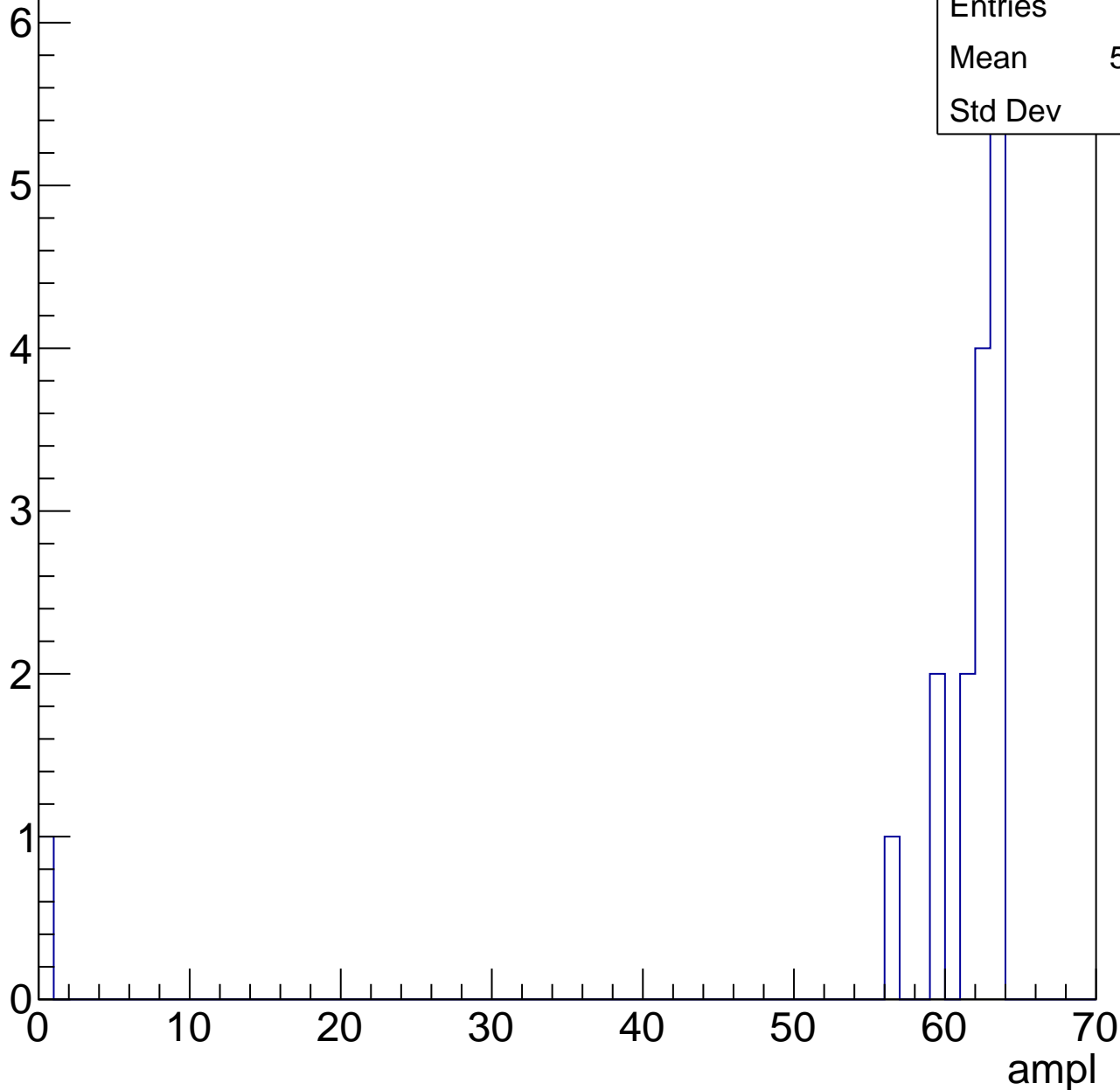


B1L102S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 16 |
| Mean | 57.62 |
| Std Dev | 15 |



B1L102S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch86, adc0

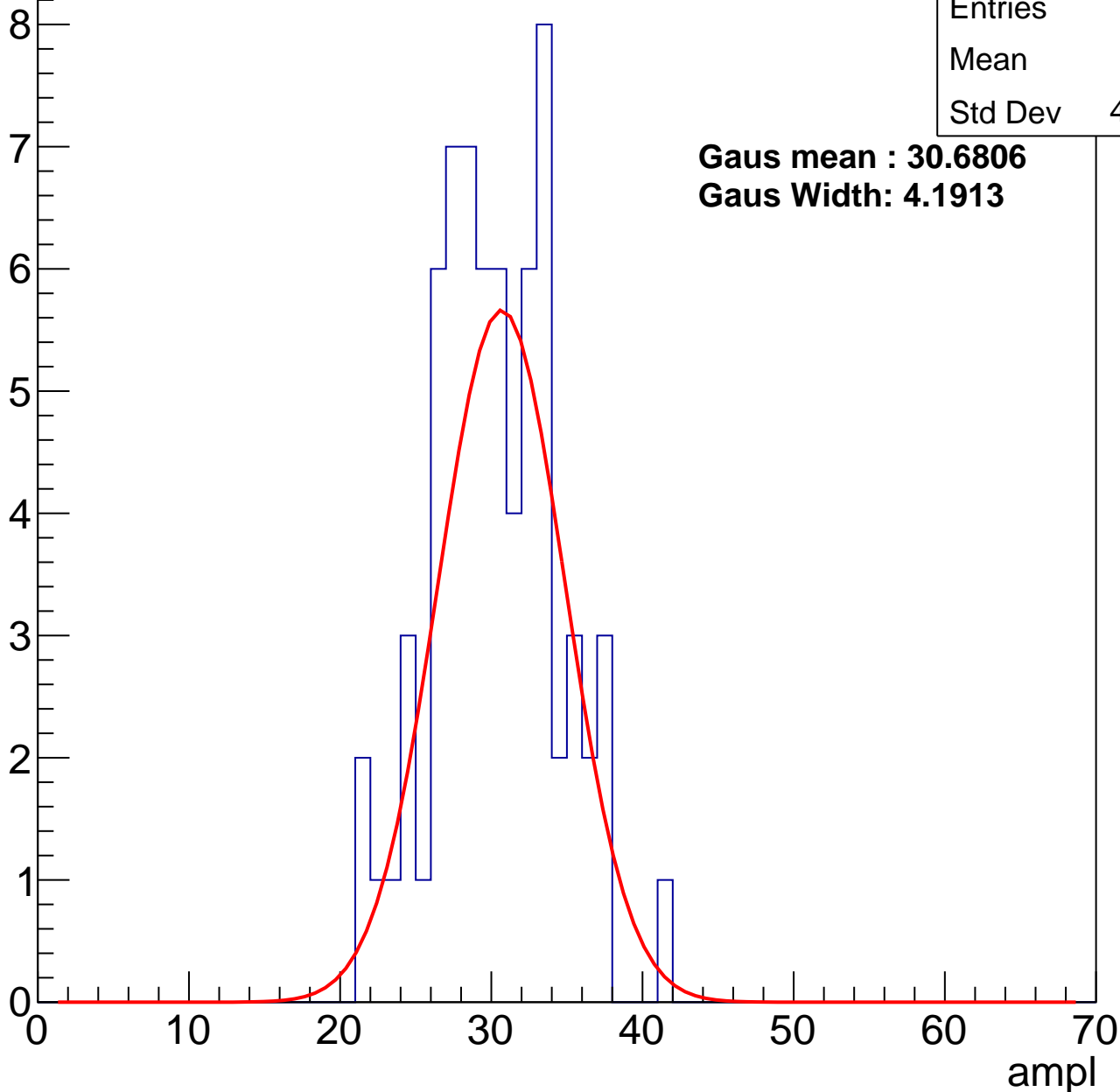
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 29.8 |
| Std Dev | 4.074 |

Gaus mean : 30.6806

Gaus Width: 4.1913



B1L102S, U15-ch86, adc1

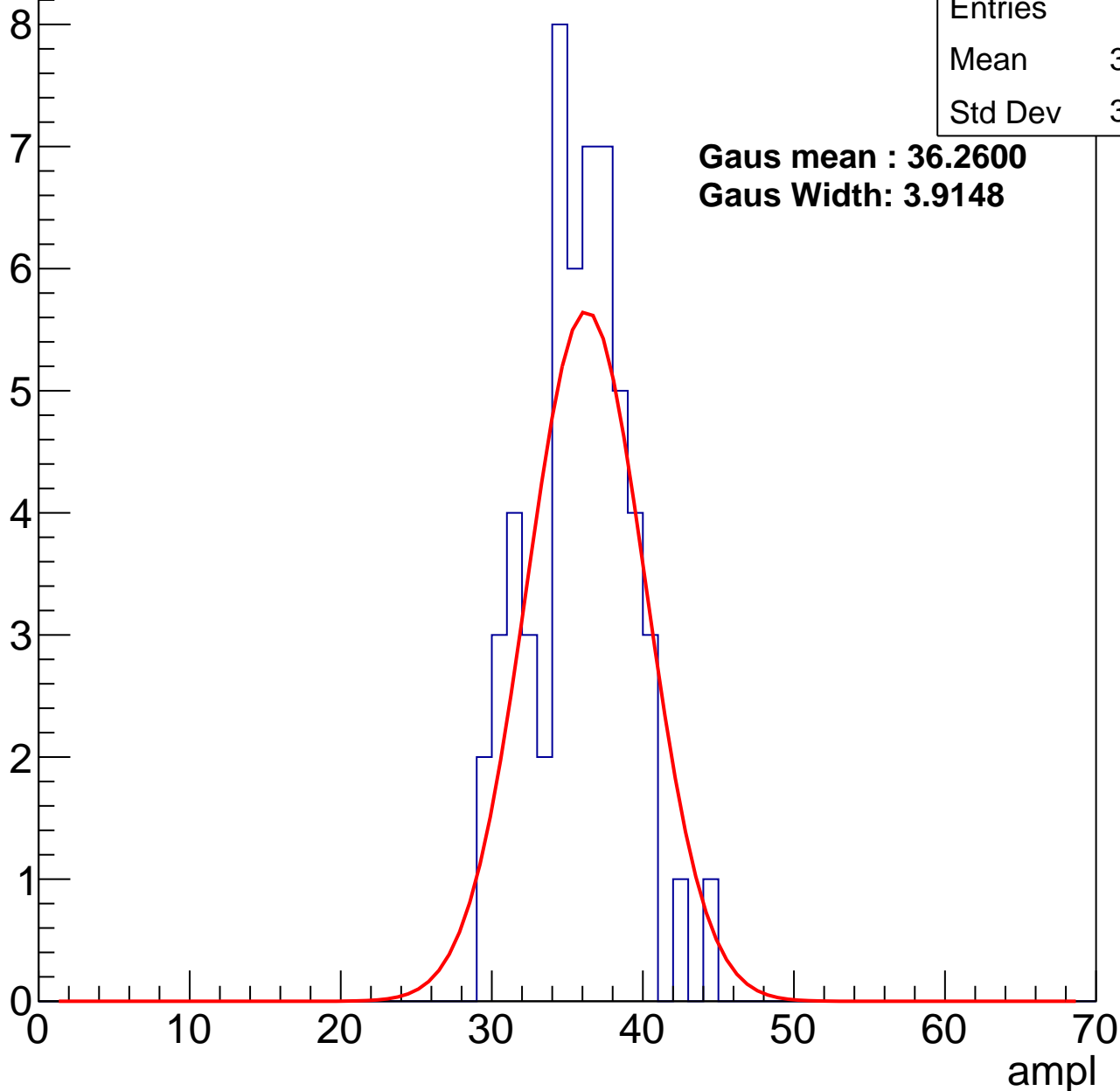
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 35.34 |
| Std Dev | 3.264 |

Gaus mean : 36.2600

Gaus Width: 3.9148



B1L102S, U15-ch86, adc2

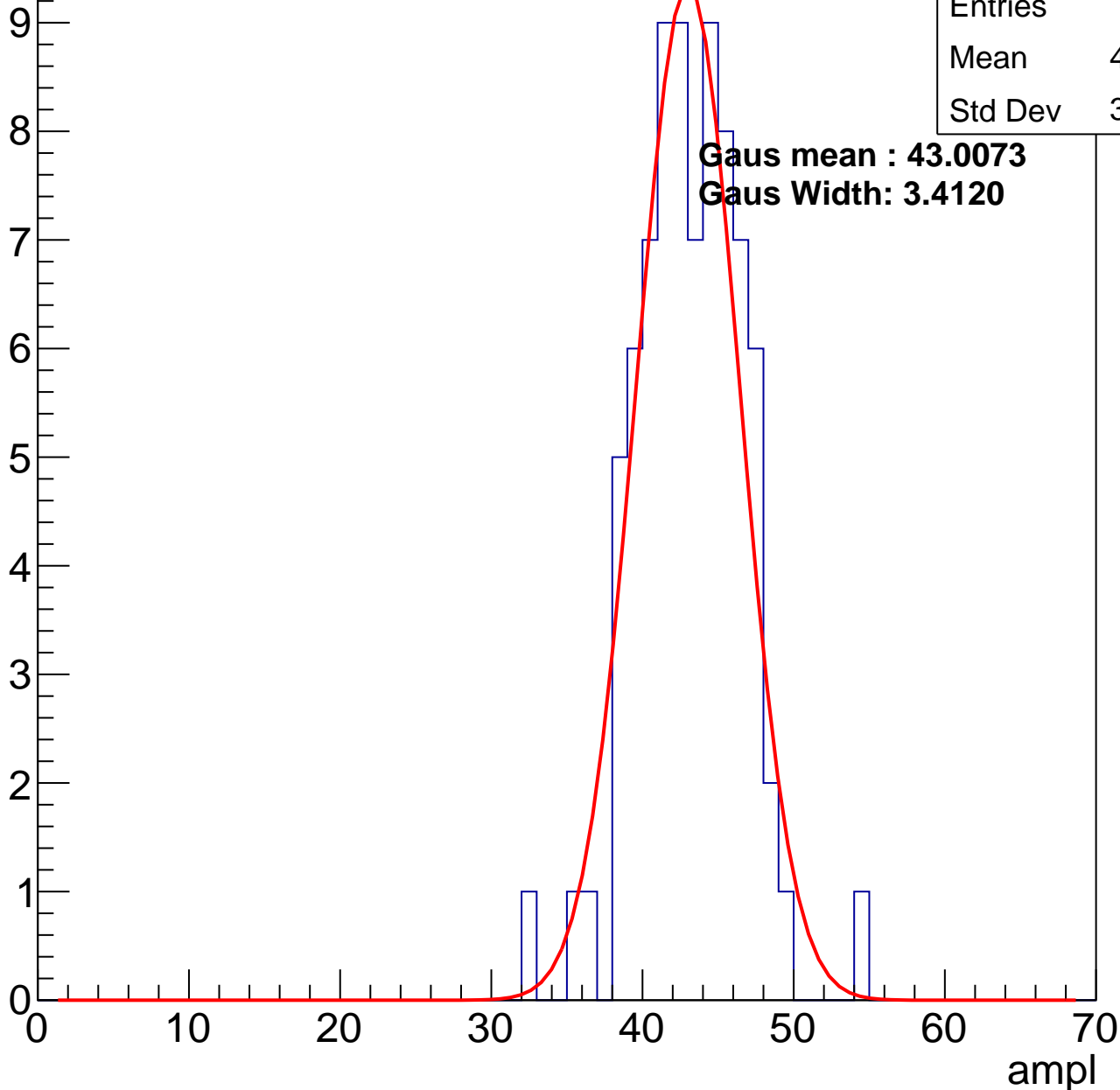
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 42.67 |
| Std Dev | 3.467 |

Gaus mean : 43.0073

Gaus Width: 3.4120

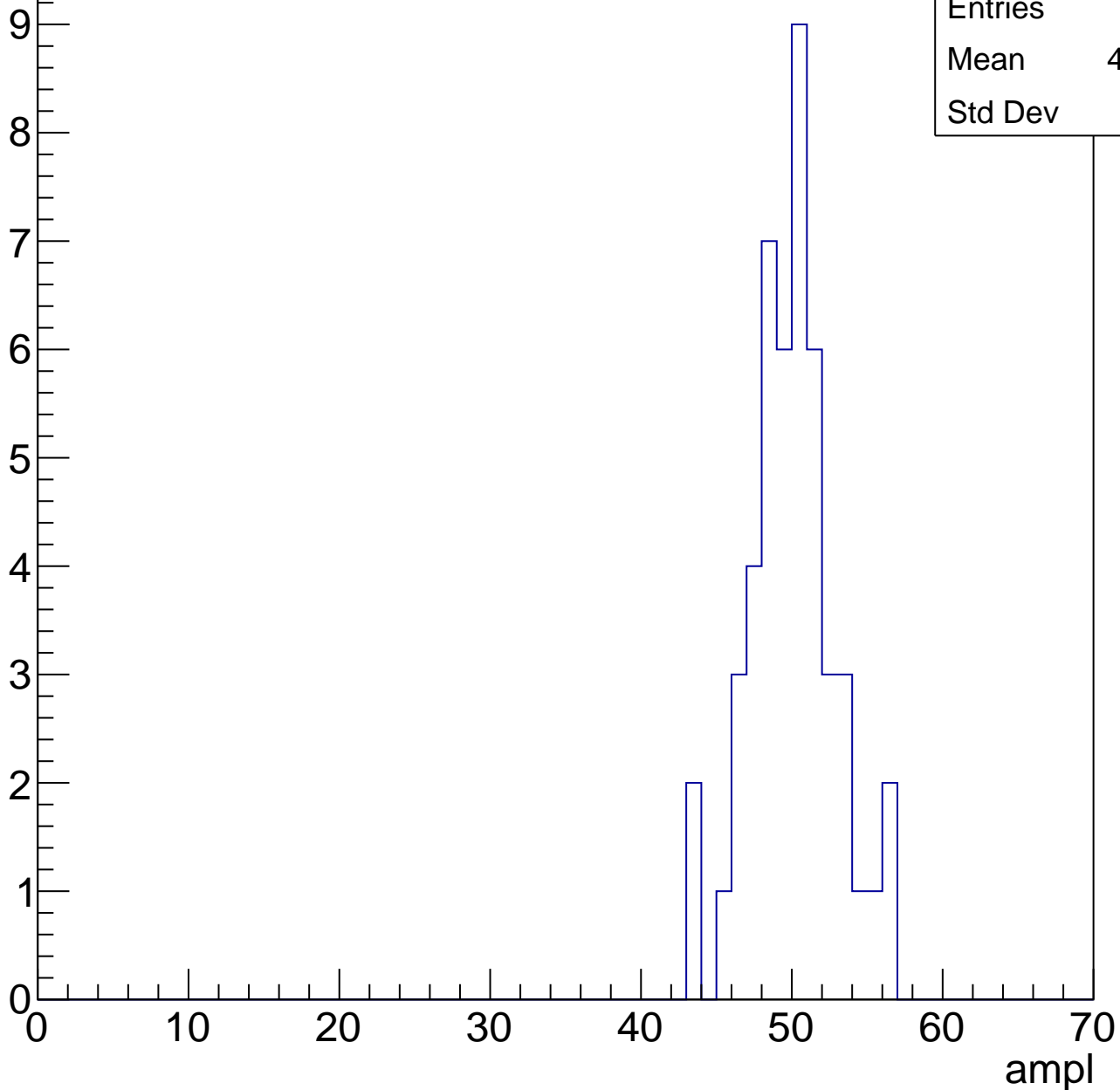


B1L102S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 49.56 |
| Std Dev | 2.85 |

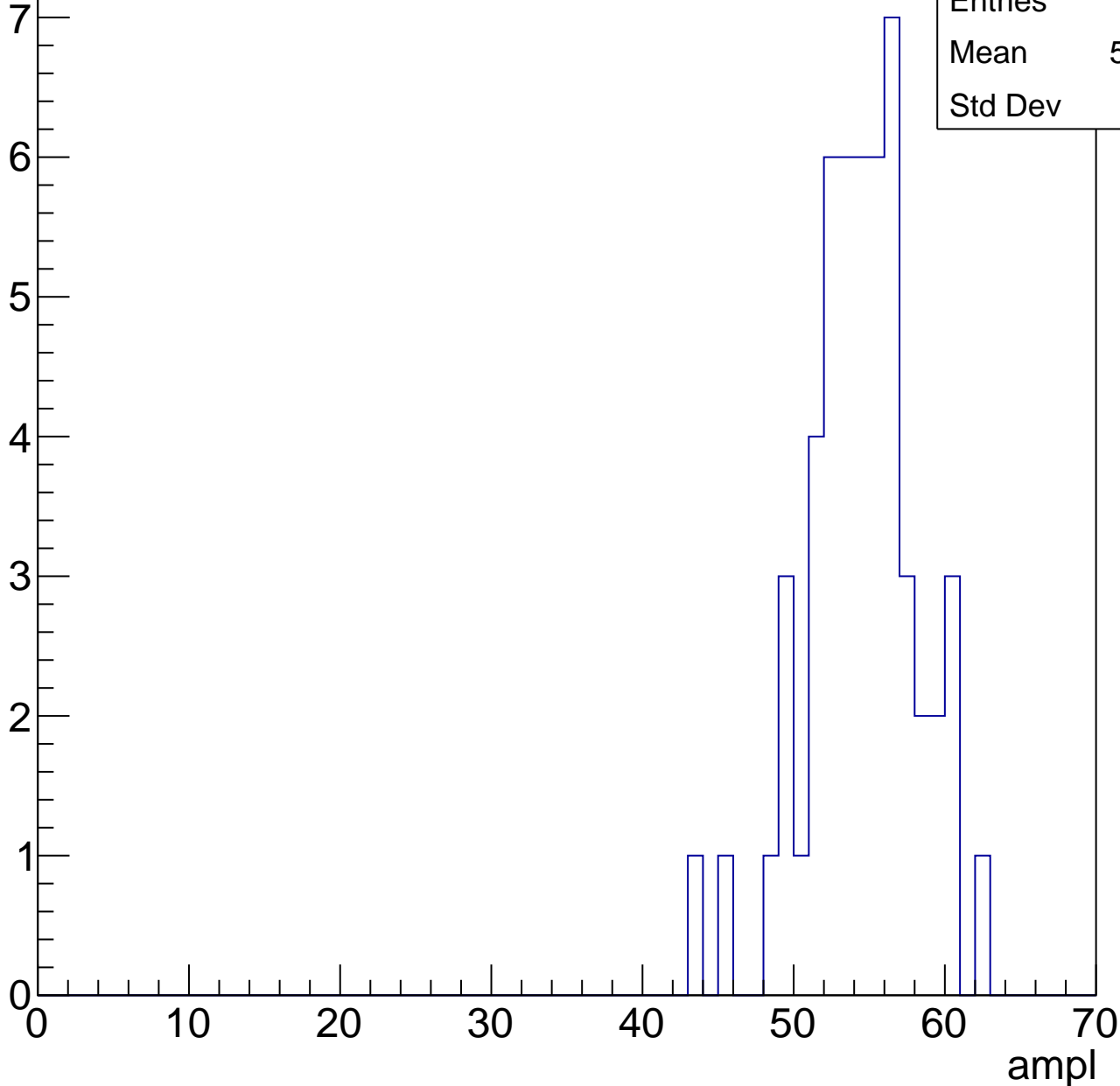


B1L102S, U15-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

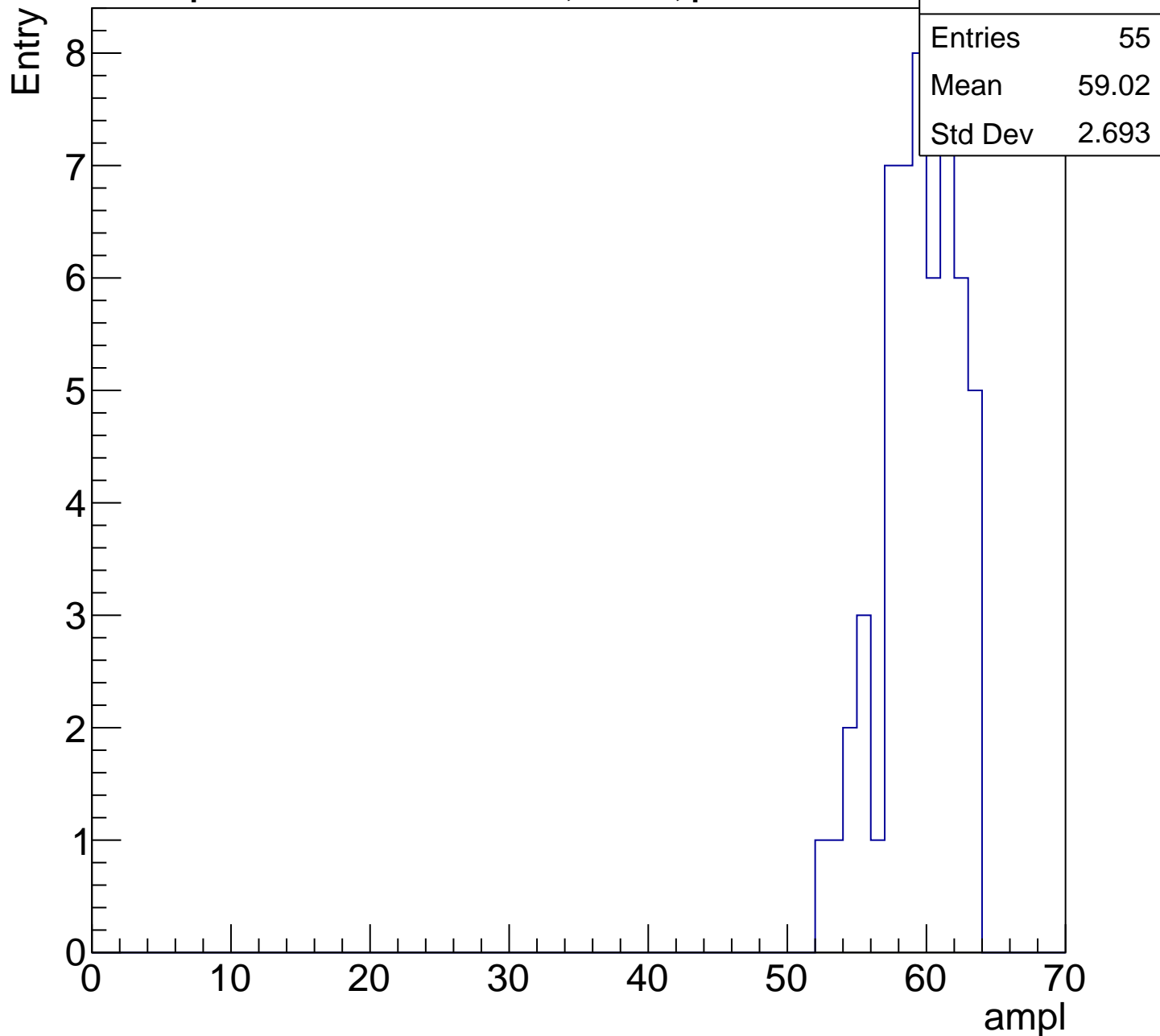
Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 53.96 |
| Std Dev | 3.66 |



B1L102S, U15-ch86, adc5

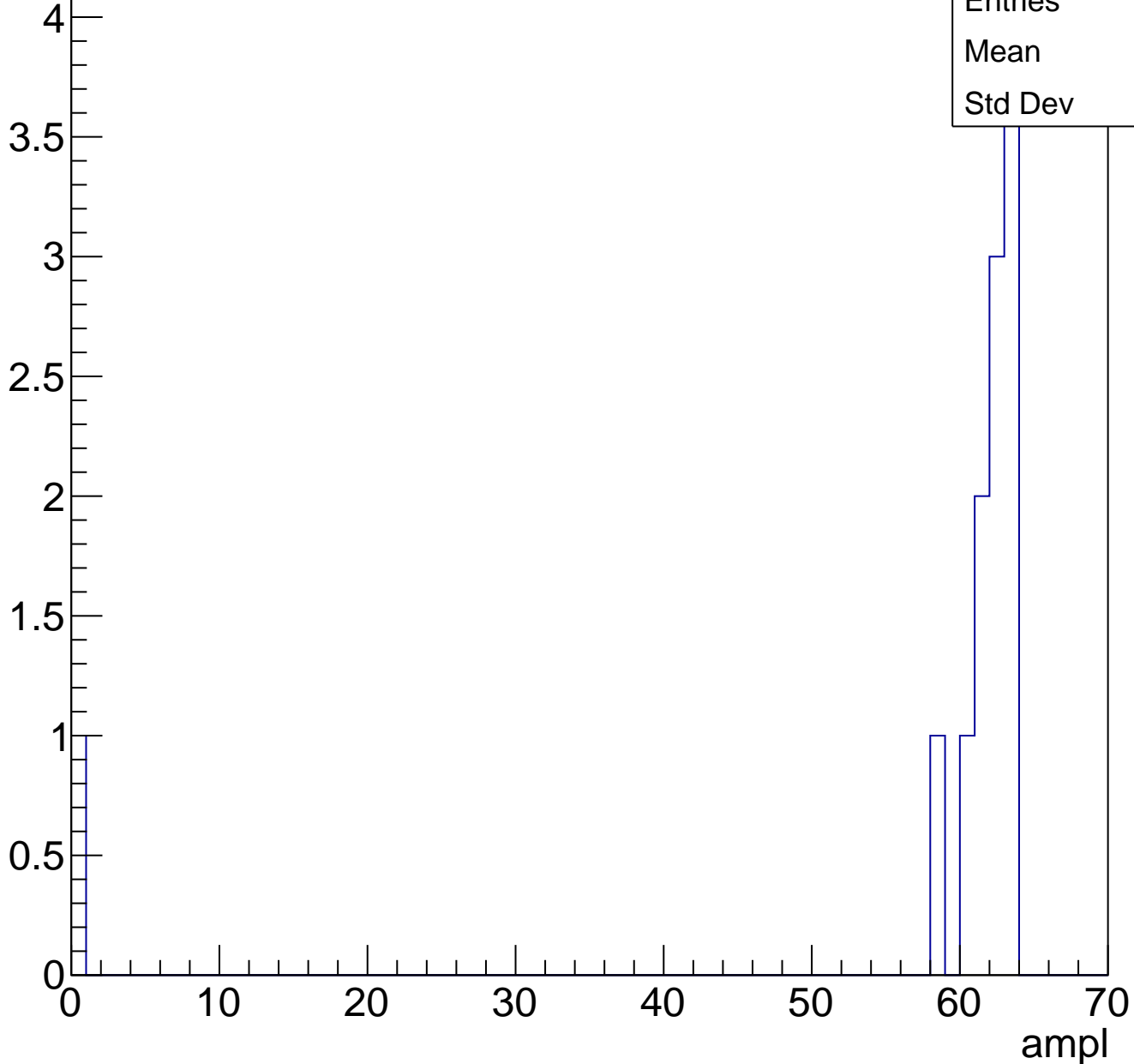
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

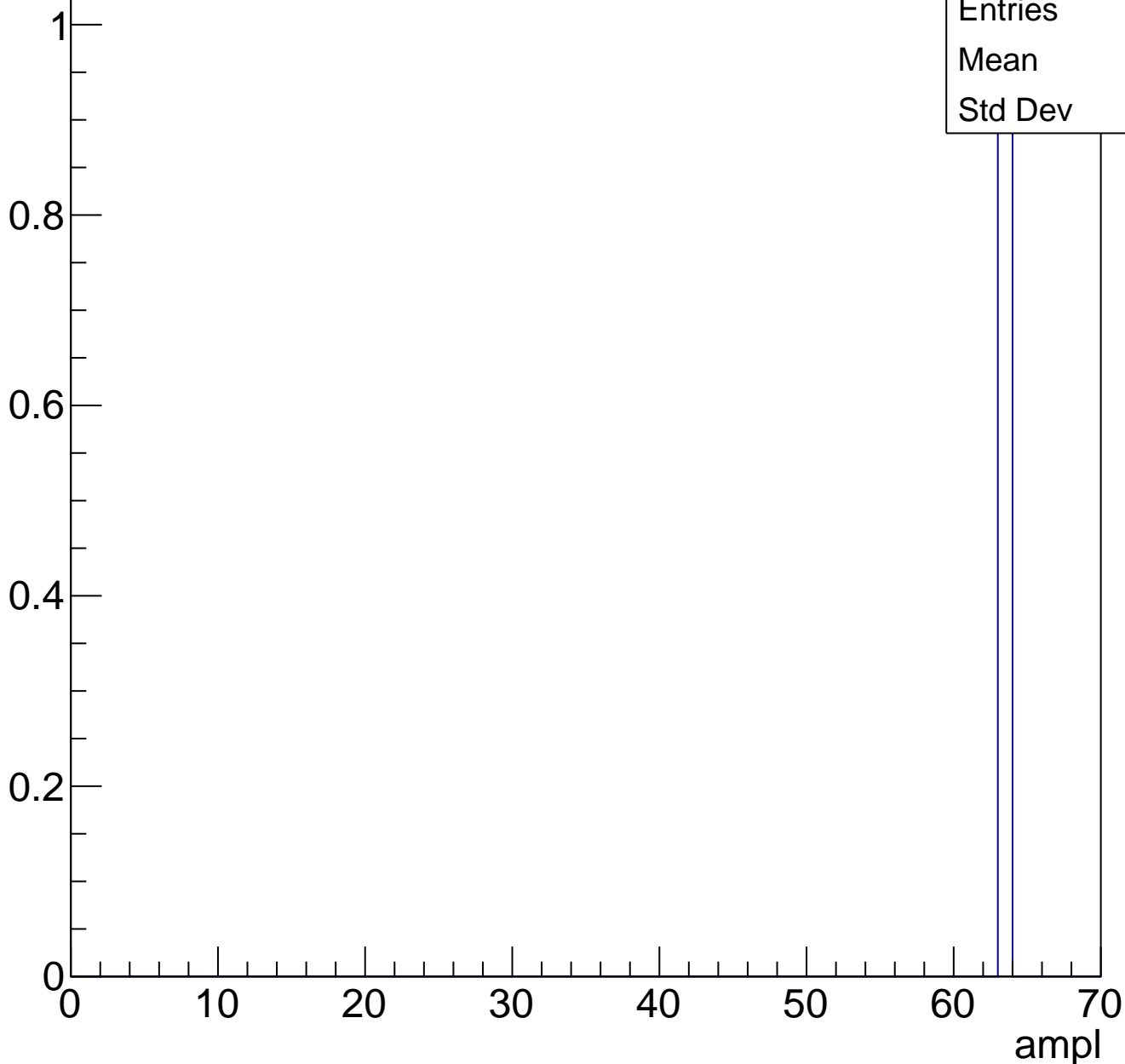
Entry



B1L102S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch87, adc0

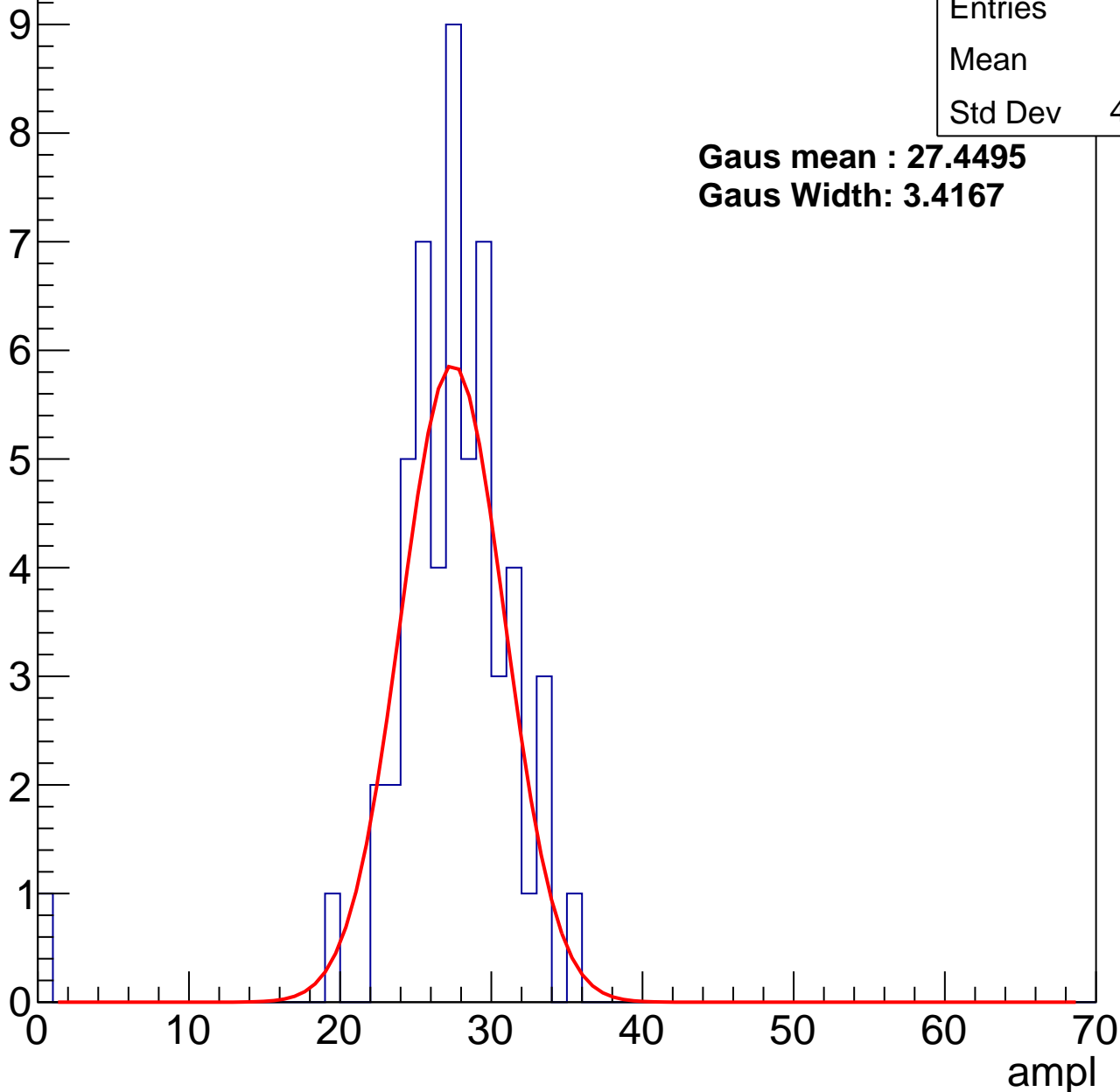
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 26.8 |
| Std Dev | 4.807 |

Gaus mean : 27.4495

Gaus Width: 3.4167



B1L102S, U15-ch87, adc1

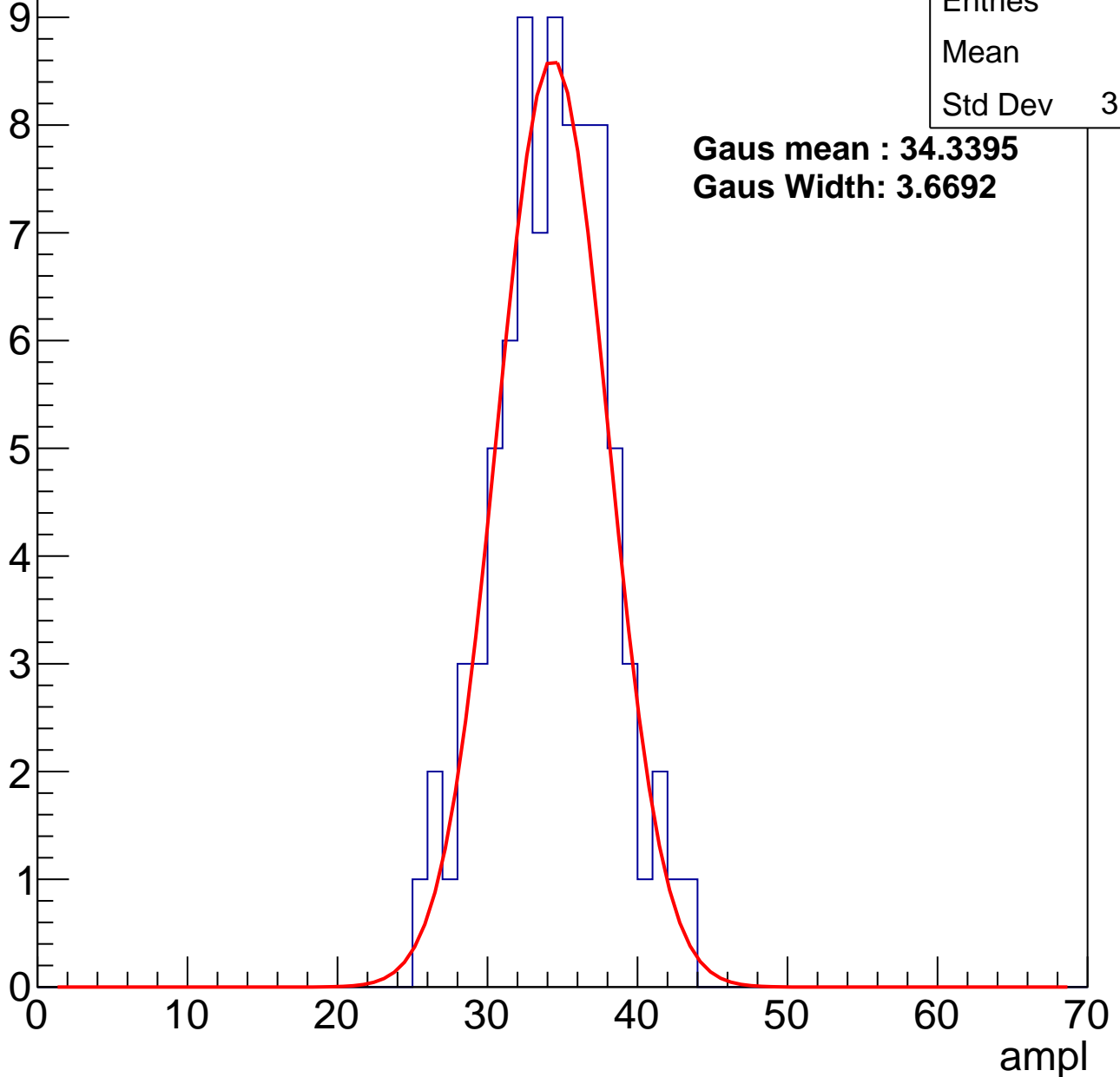
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 33.9 |
| Std Dev | 3.744 |

Gaus mean : 34.3395

Gaus Width: 3.6692



B1L102S, U15-ch87, adc2

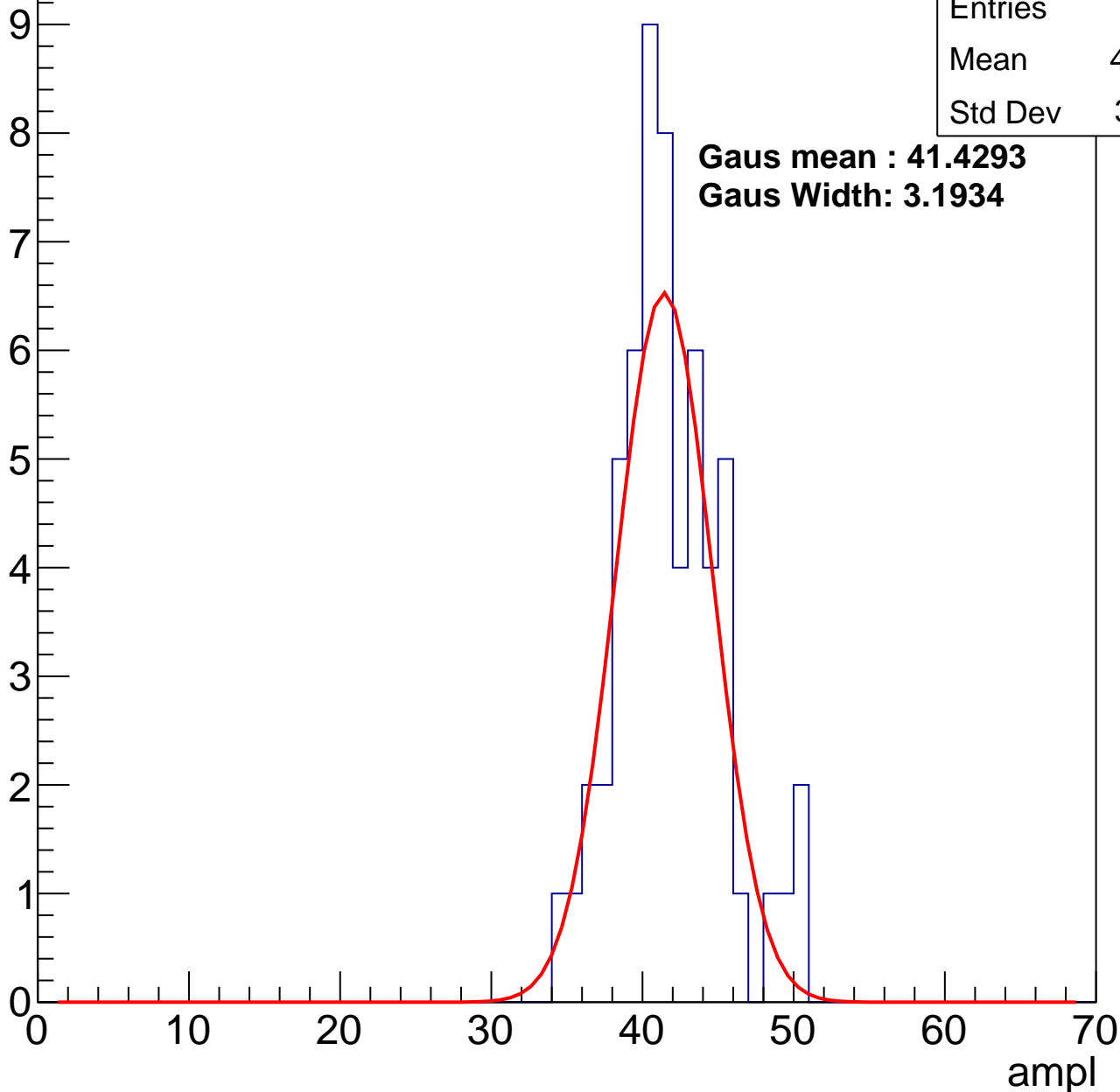
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 41.33 |
| Std Dev | 3.431 |

Gaus mean : 41.4293

Gaus Width: 3.1934



B1L102S, U15-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

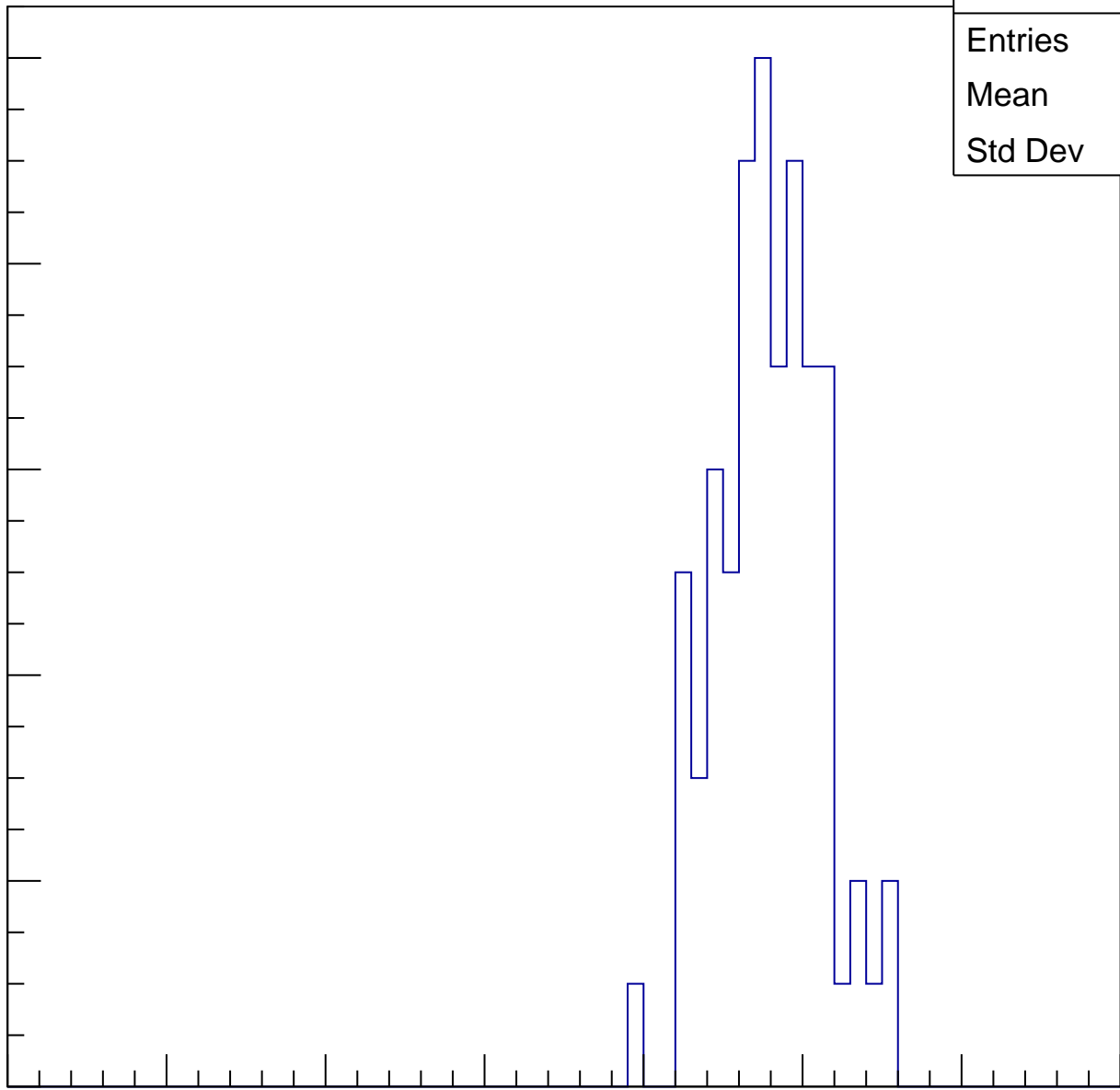
| | |
|---------|-------|
| Entries | 75 |
| Mean | 47.43 |
| Std Dev | 3.263 |

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

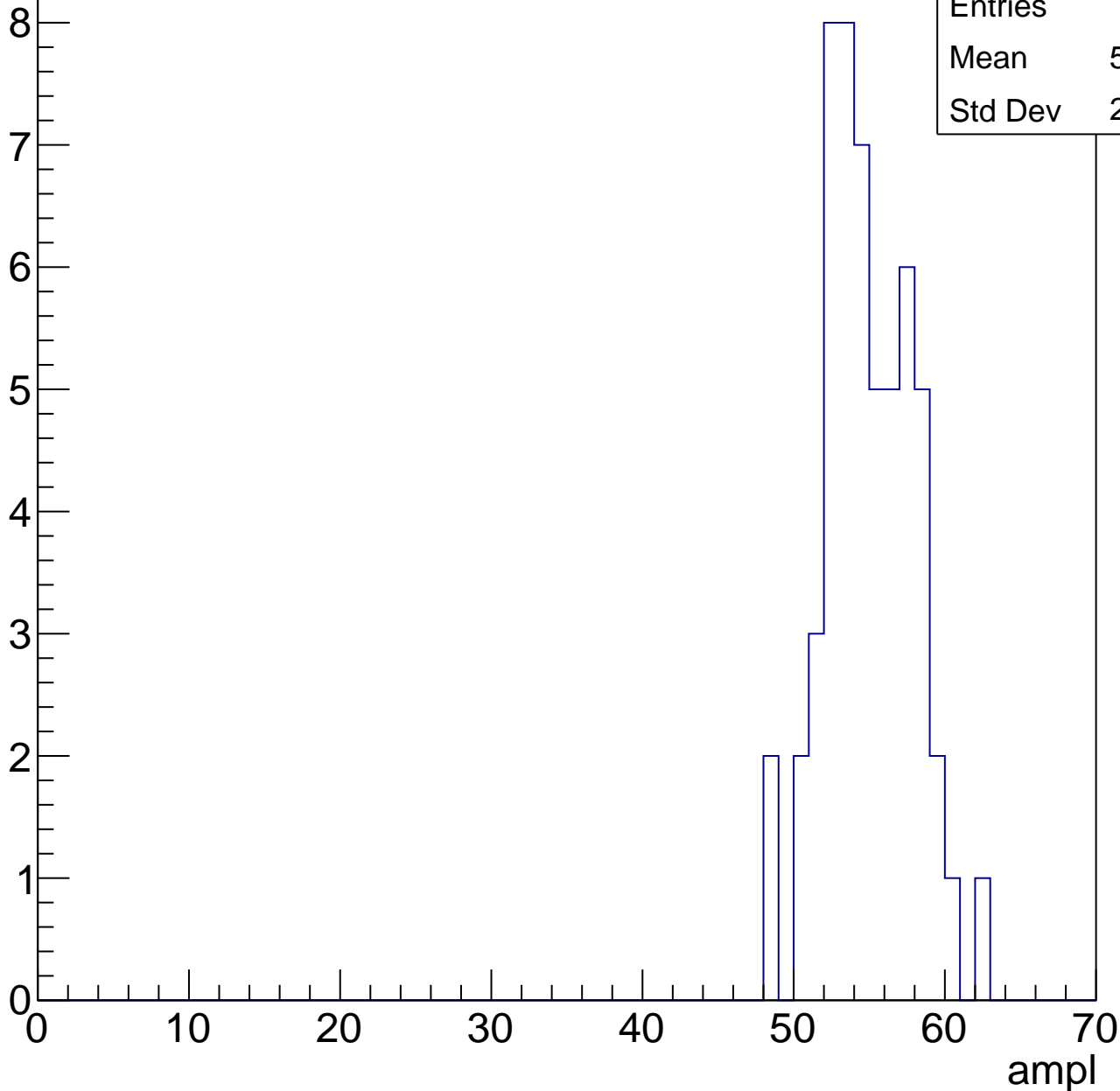


B1L102S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 54.44 |
| Std Dev | 2.922 |

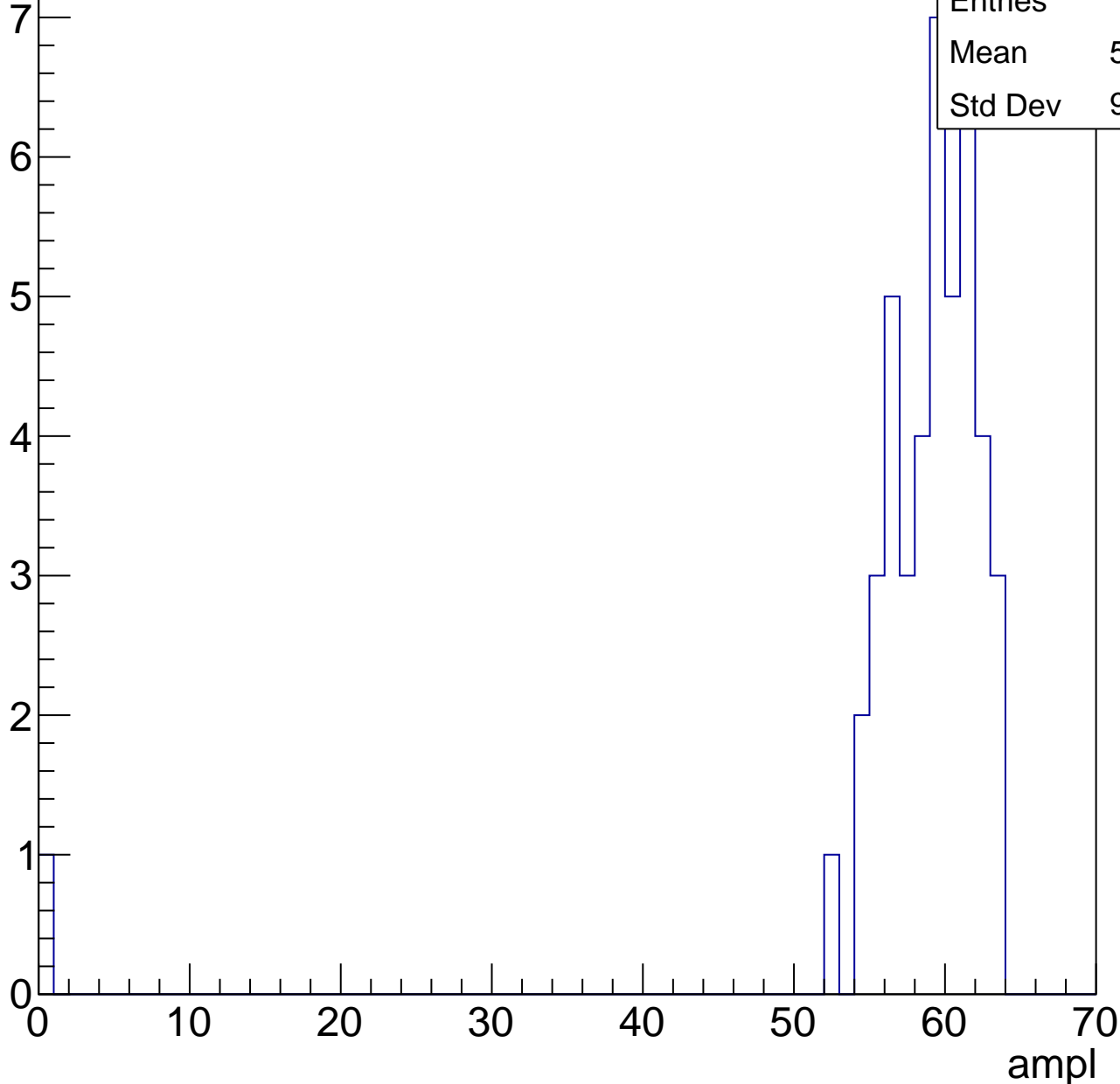


B1L102S, U15-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 57.44 |
| Std Dev | 9.064 |

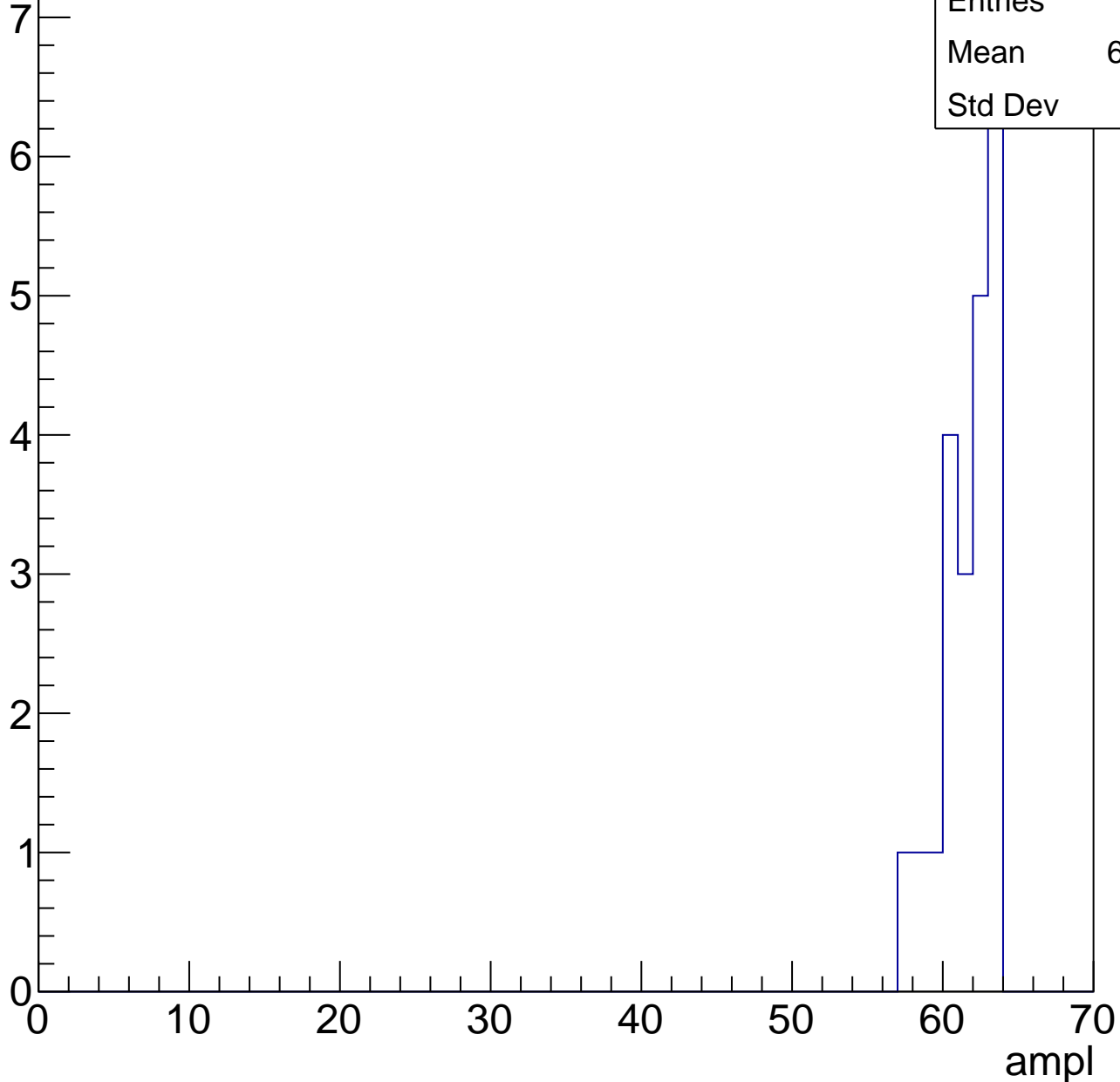


B1L102S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 22 |
| Mean | 61.27 |
| Std Dev | 1.71 |



B1L102S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch88, adc0

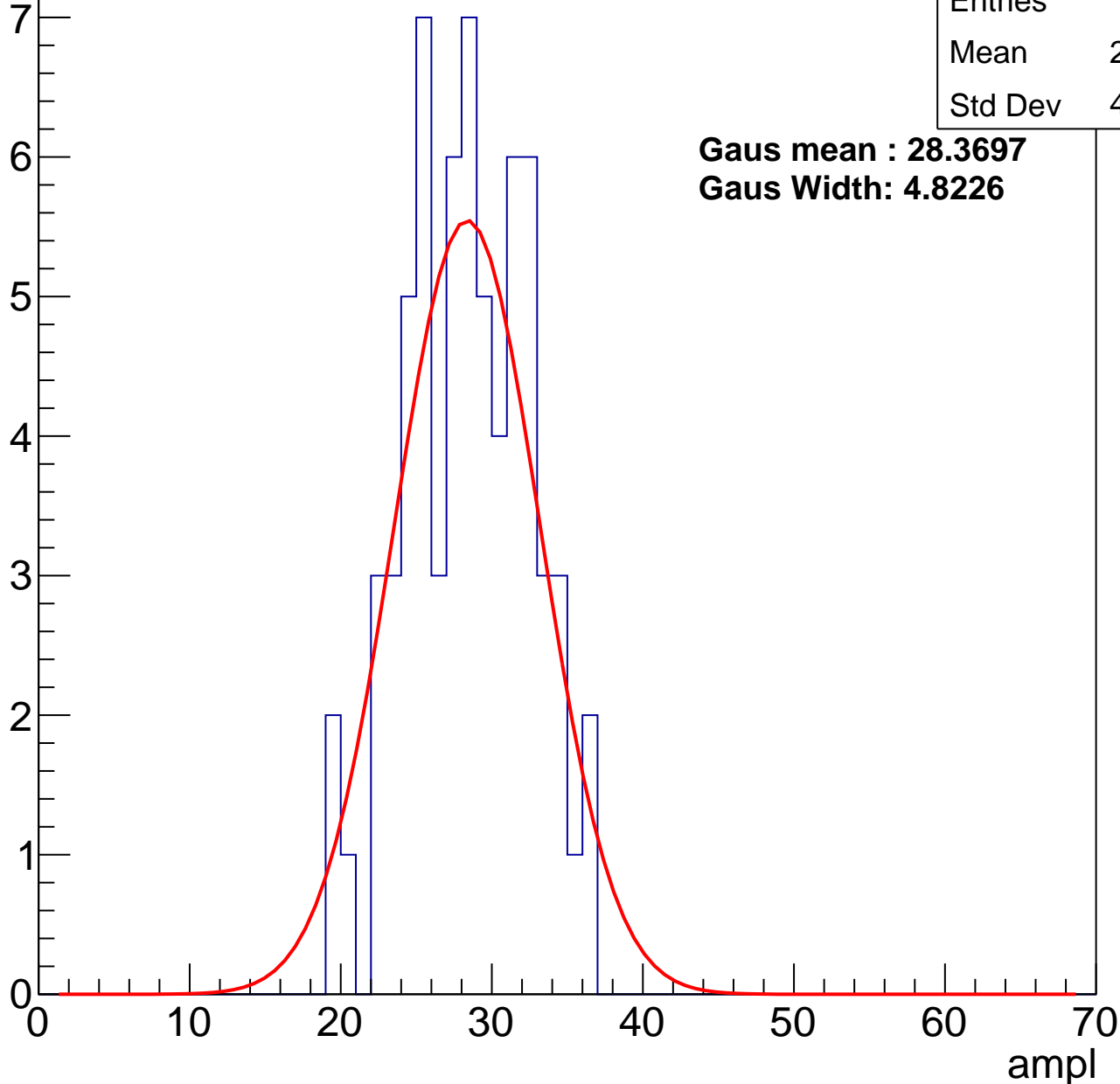
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.99 |
| Std Dev | 4.054 |

Gaus mean : 28.3697

Gaus Width: 4.8226



B1L102S, U15-ch88, adc1

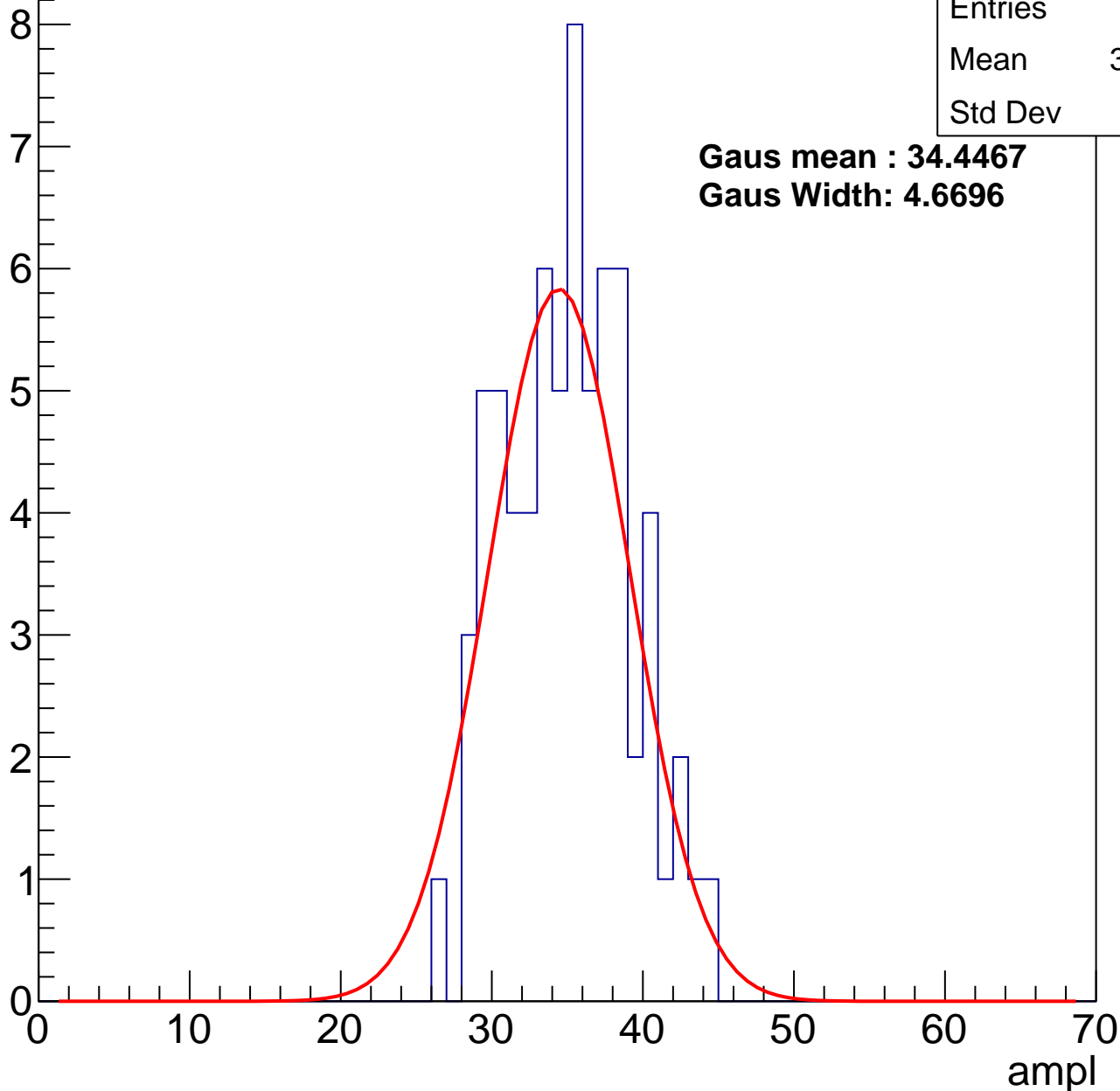
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.57 |
| Std Dev | 4.07 |

Gaus mean : 34.4467

Gaus Width: 4.6696



B1L102S, U15-ch88, adc2

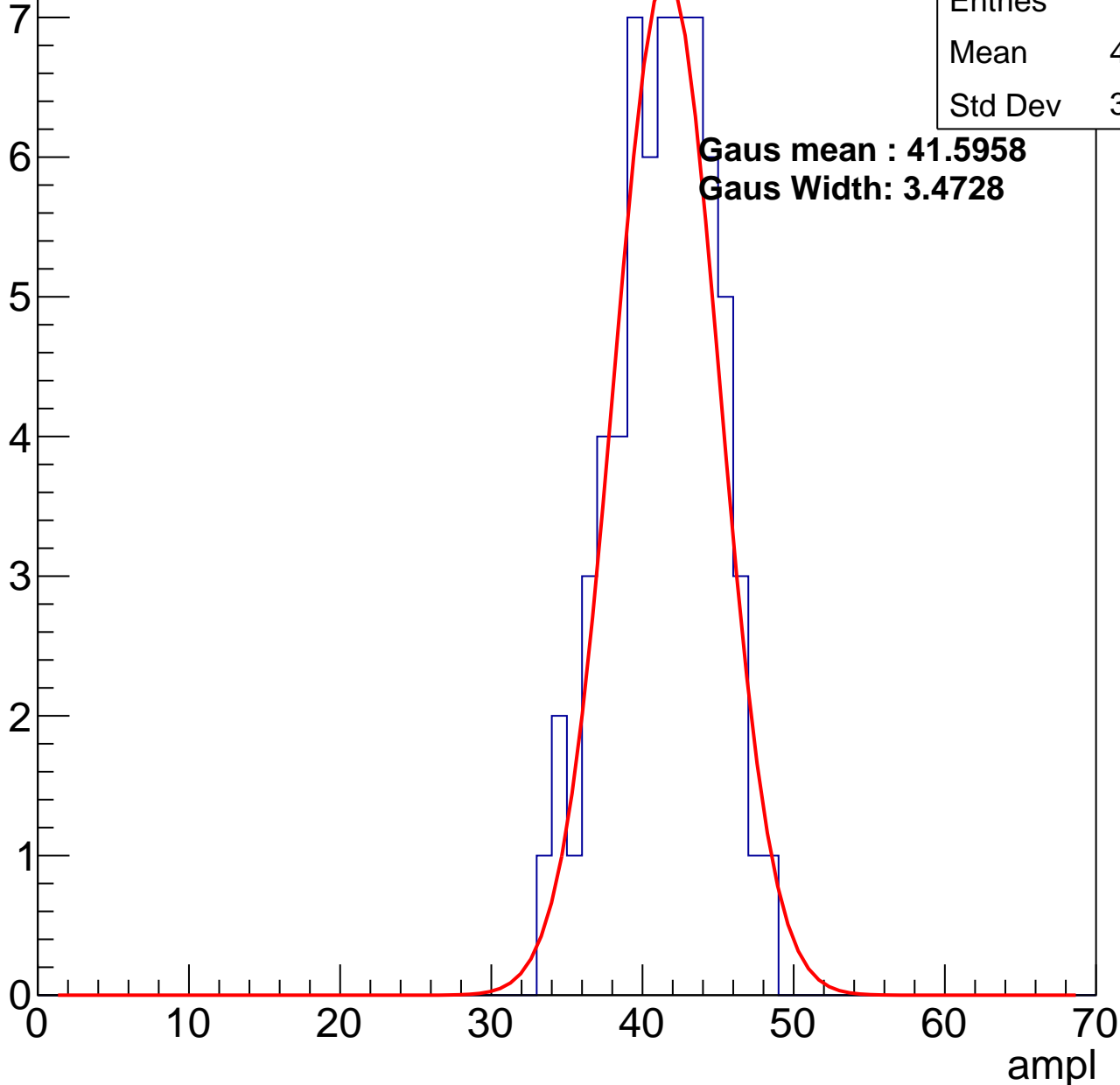
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 40.94 |
| Std Dev | 3.364 |

Gaus mean : 41.5958

Gaus Width: 3.4728

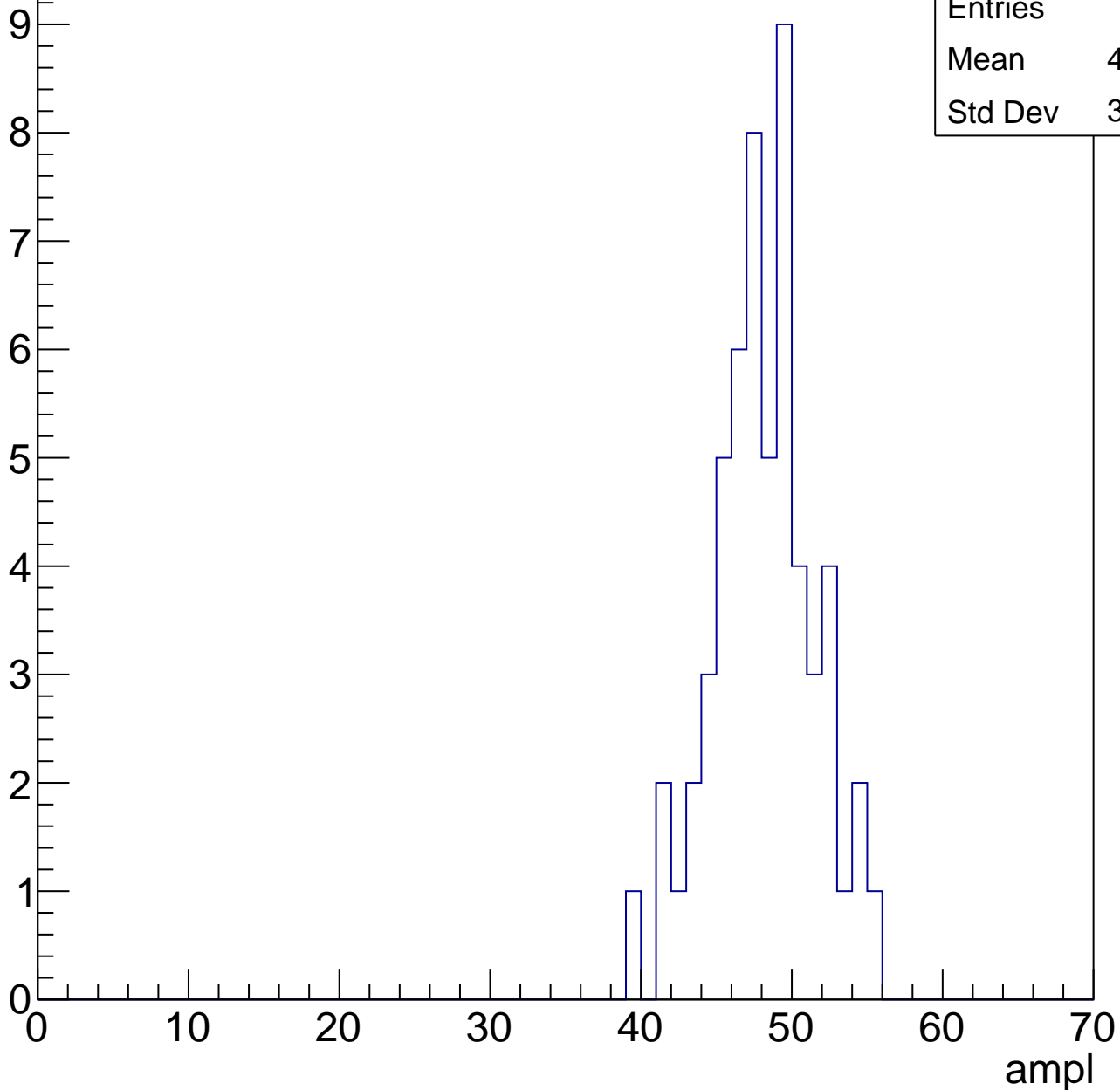


B1L102S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 47.65 |
| Std Dev | 3.364 |

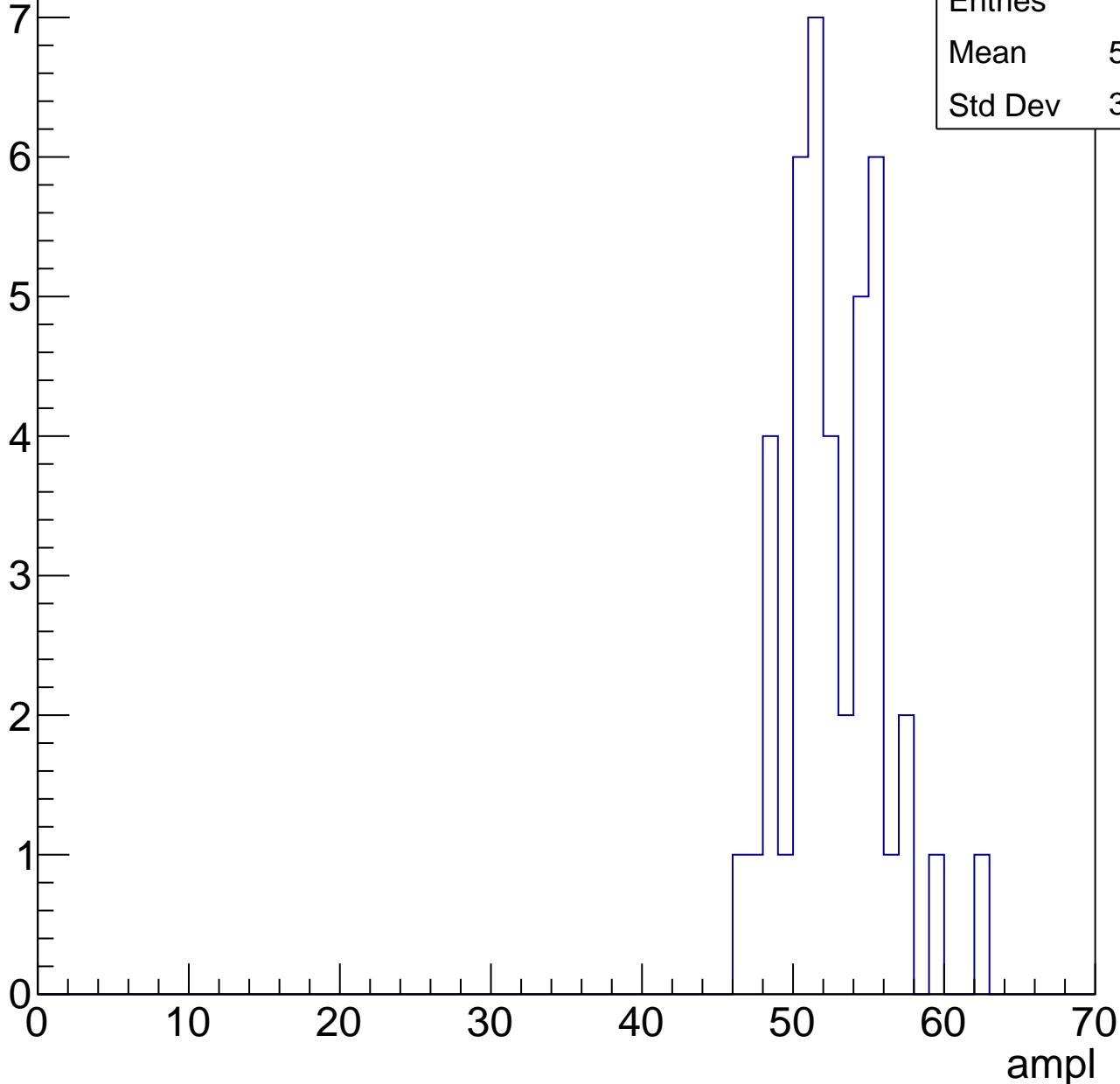


B1L102S, U15-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

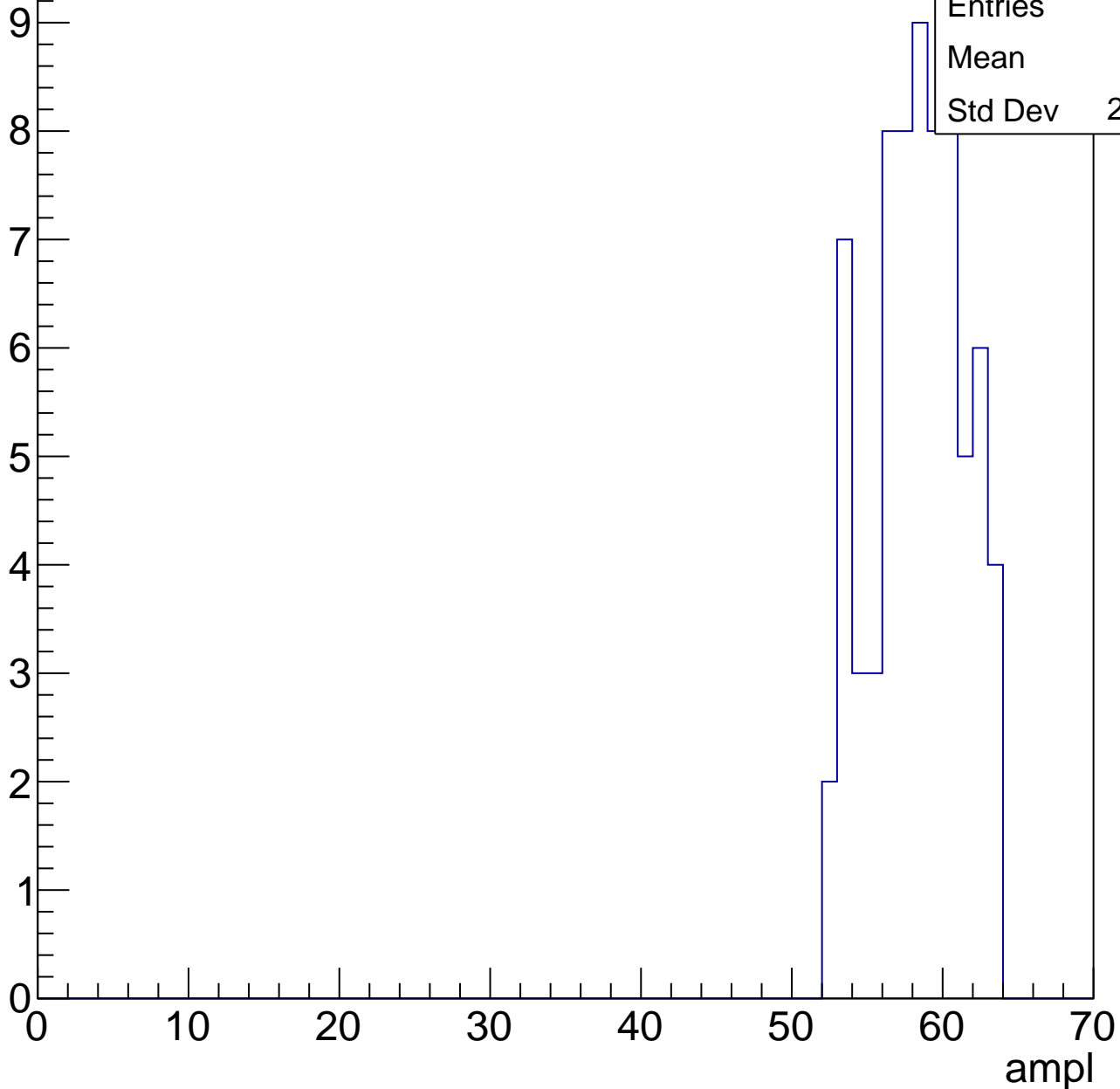
| | |
|---------|-------|
| Entries | 42 |
| Mean | 52.29 |
| Std Dev | 3.283 |



B1L102S, U15-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

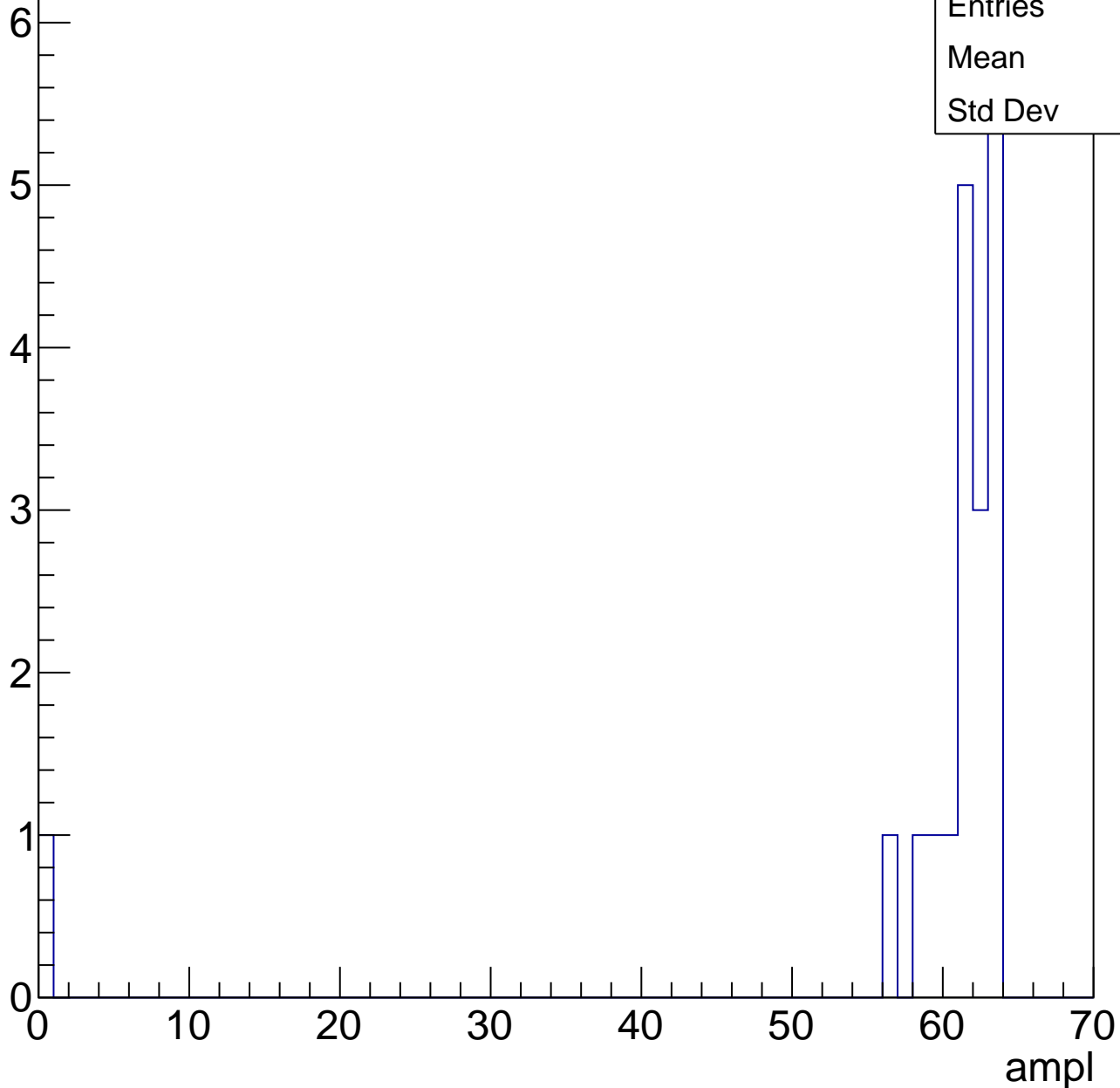


B1L102S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 19 |
| Mean | 58 |
| Std Dev | 13.8 |



B1L102S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch89, adc0

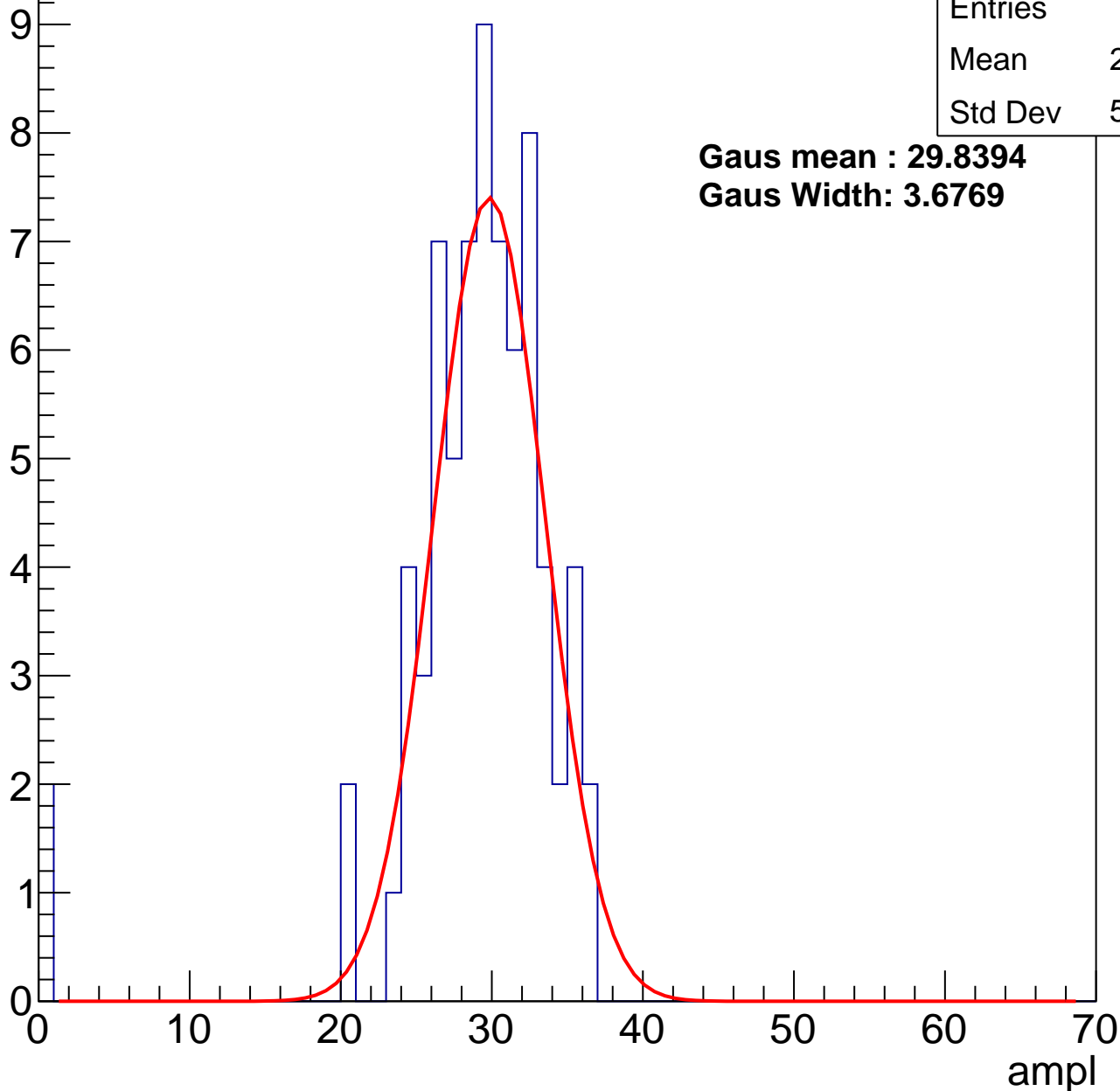
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 28.38 |
| Std Dev | 5.909 |

Gaus mean : 29.8394

Gaus Width: 3.6769



B1L102S, U15-ch89, adc1

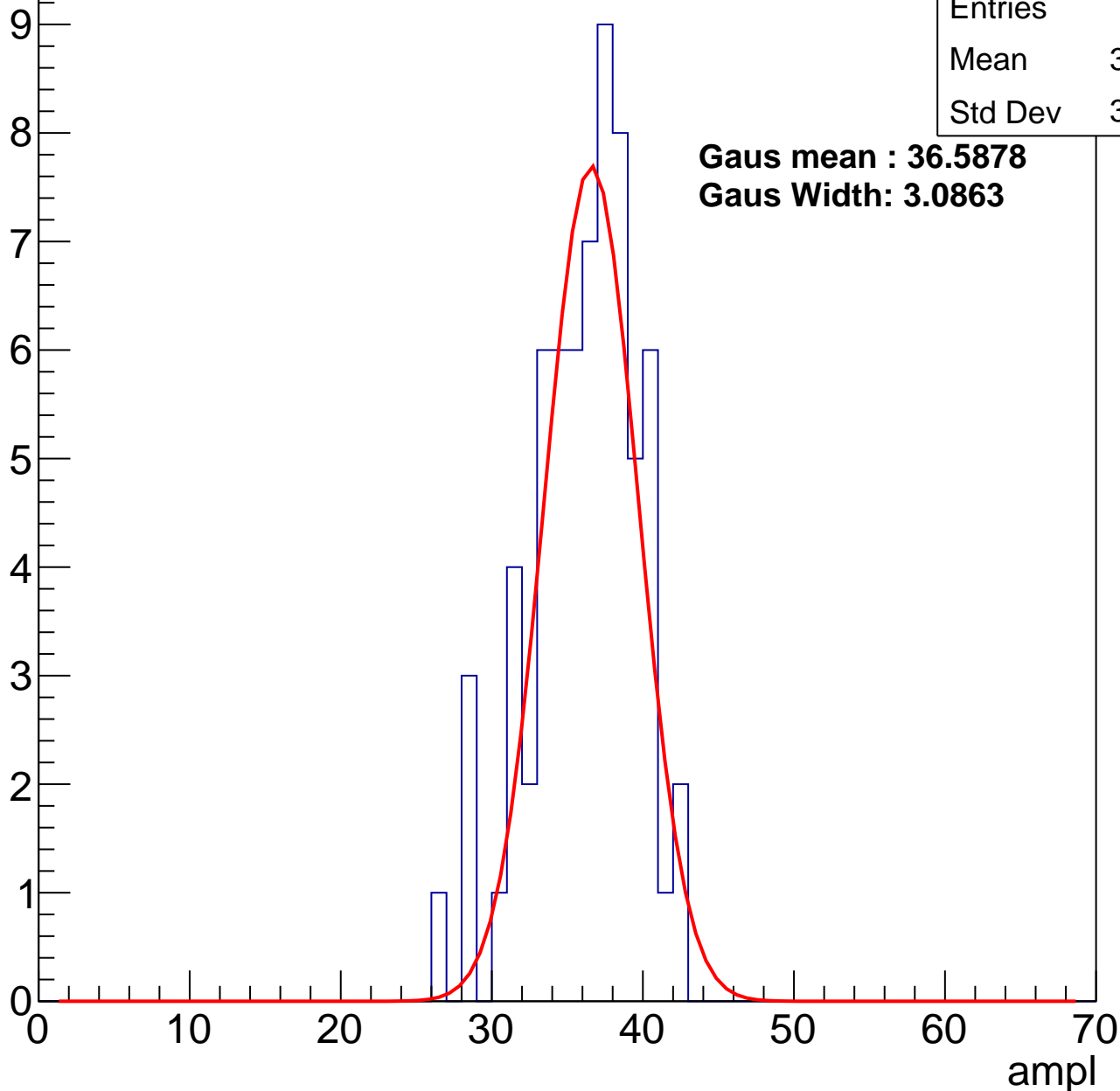
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 35.66 |
| Std Dev | 3.488 |

Gaus mean : 36.5878

Gaus Width: 3.0863



B1L102S, U15-ch89, adc2

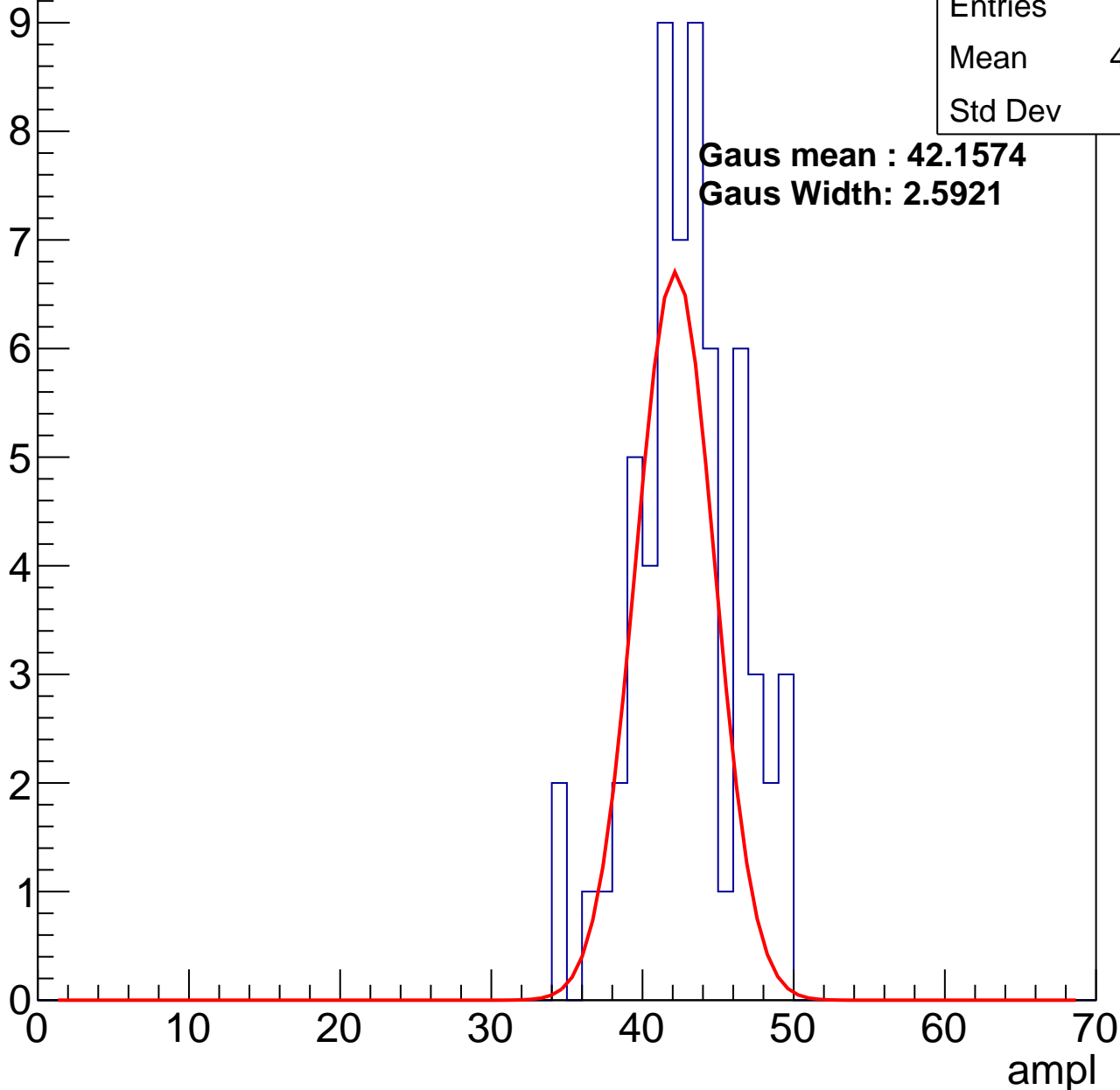
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 42.48 |
| Std Dev | 3.41 |

Gaus mean : 42.1574

Gaus Width: 2.5921

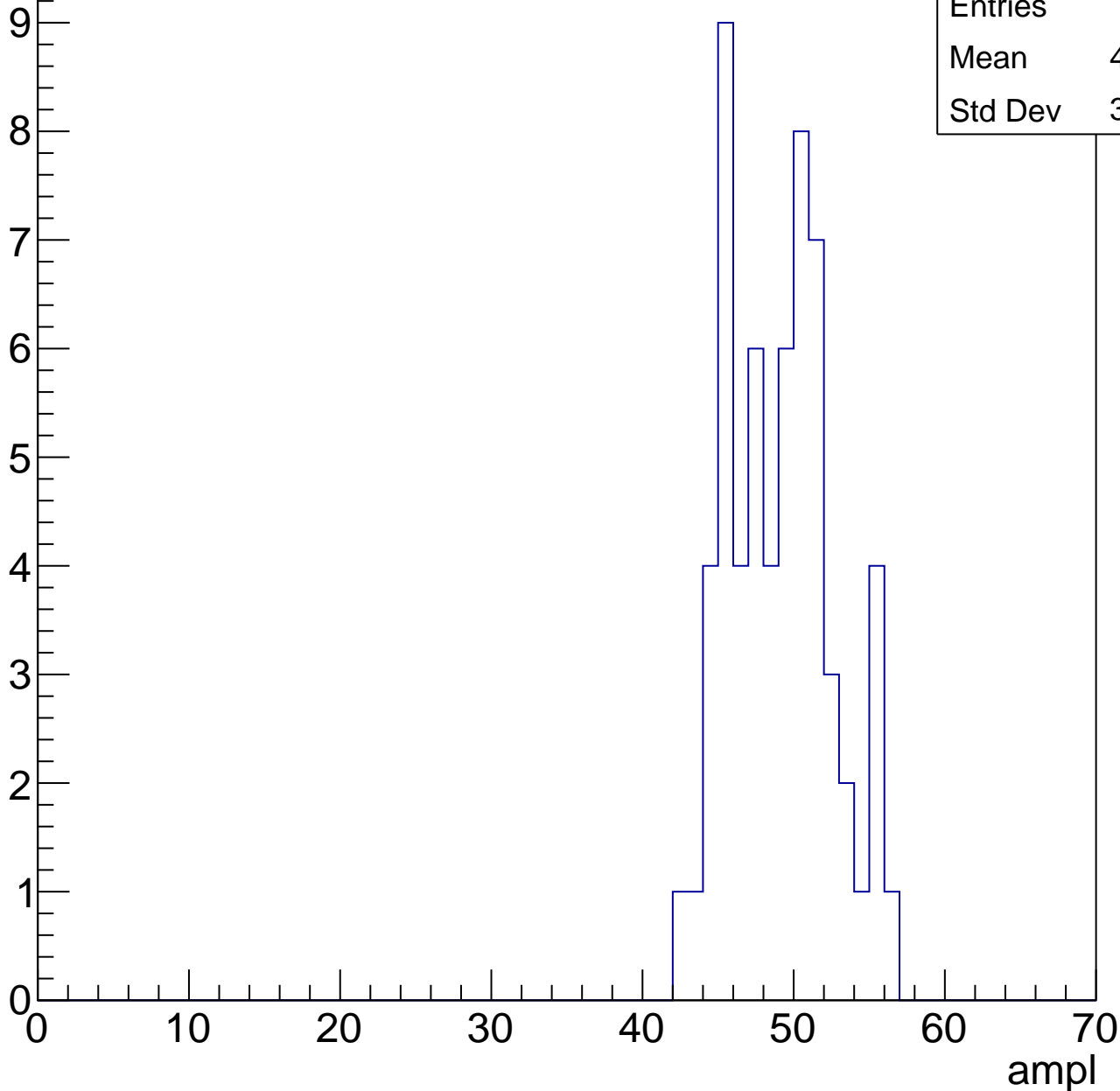


B1L102S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

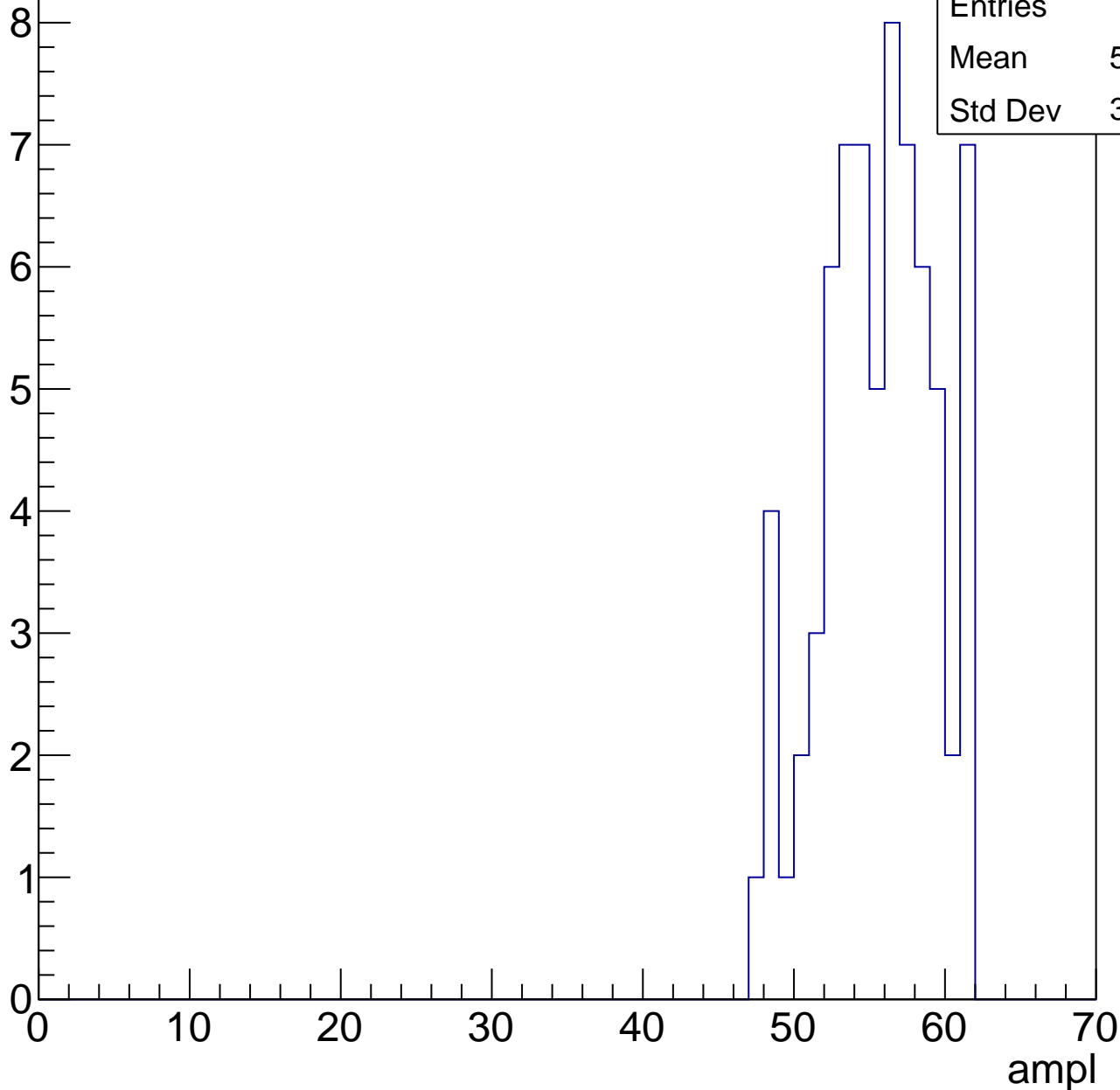
| | |
|---------|-------|
| Entries | 61 |
| Mean | 48.64 |
| Std Dev | 3.383 |



B1L102S, U15-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



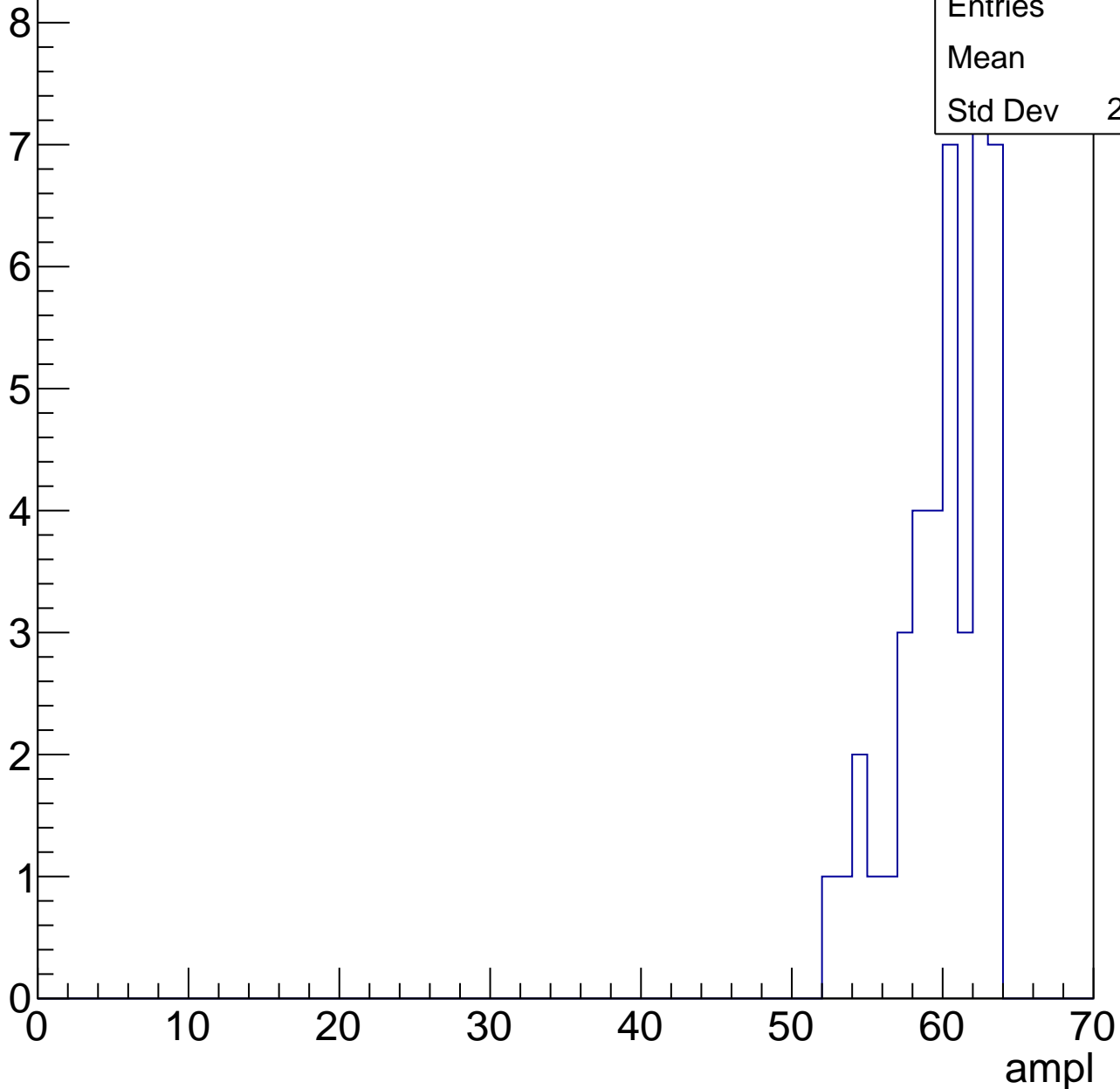
| | |
|---------|-------|
| Entries | 71 |
| Mean | 55.13 |
| Std Dev | 3.665 |

B1L102S, U15-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

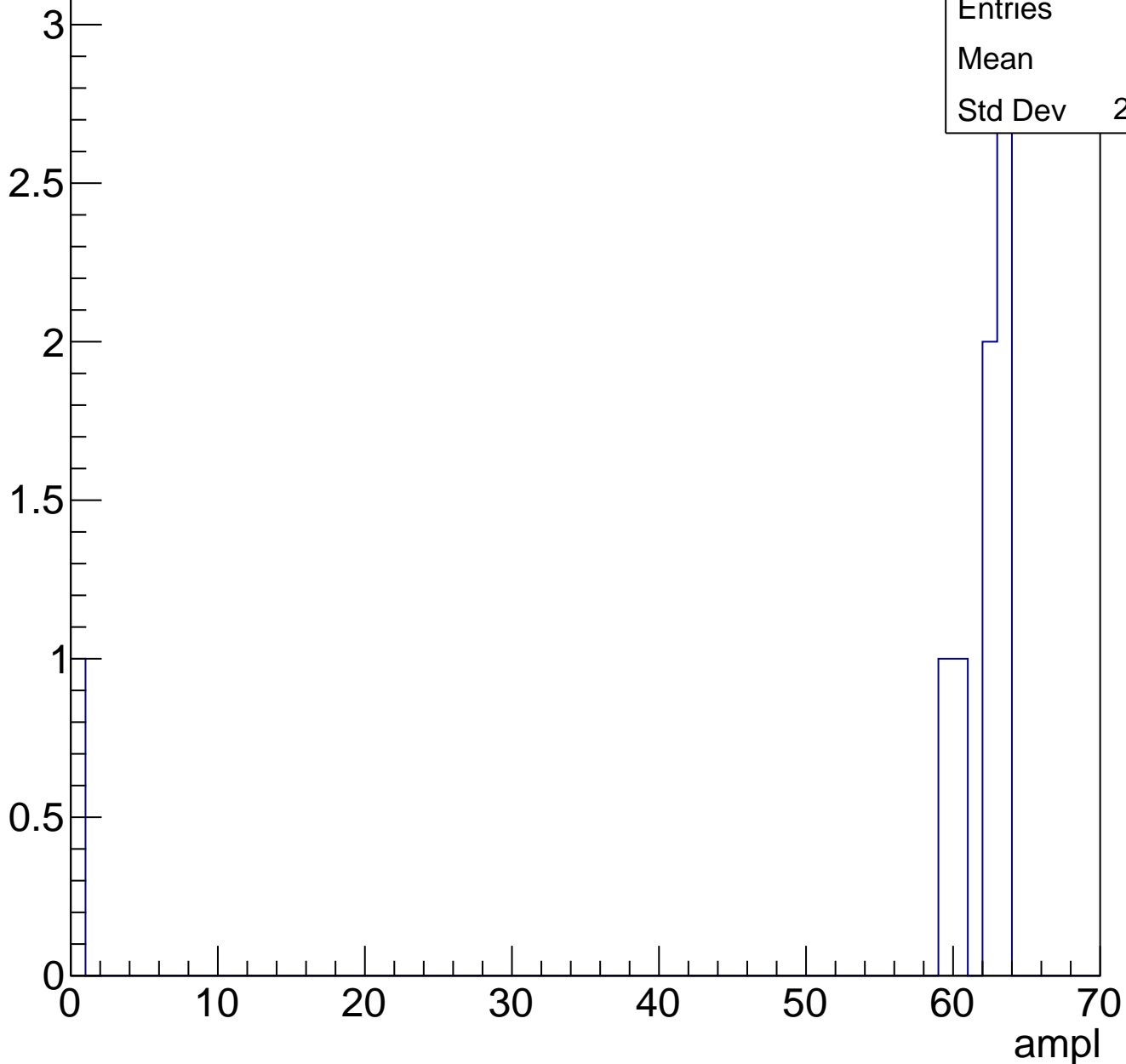
| | |
|---------|-------|
| Entries | 42 |
| Mean | 59.6 |
| Std Dev | 2.944 |



B1L102S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch90, adc0

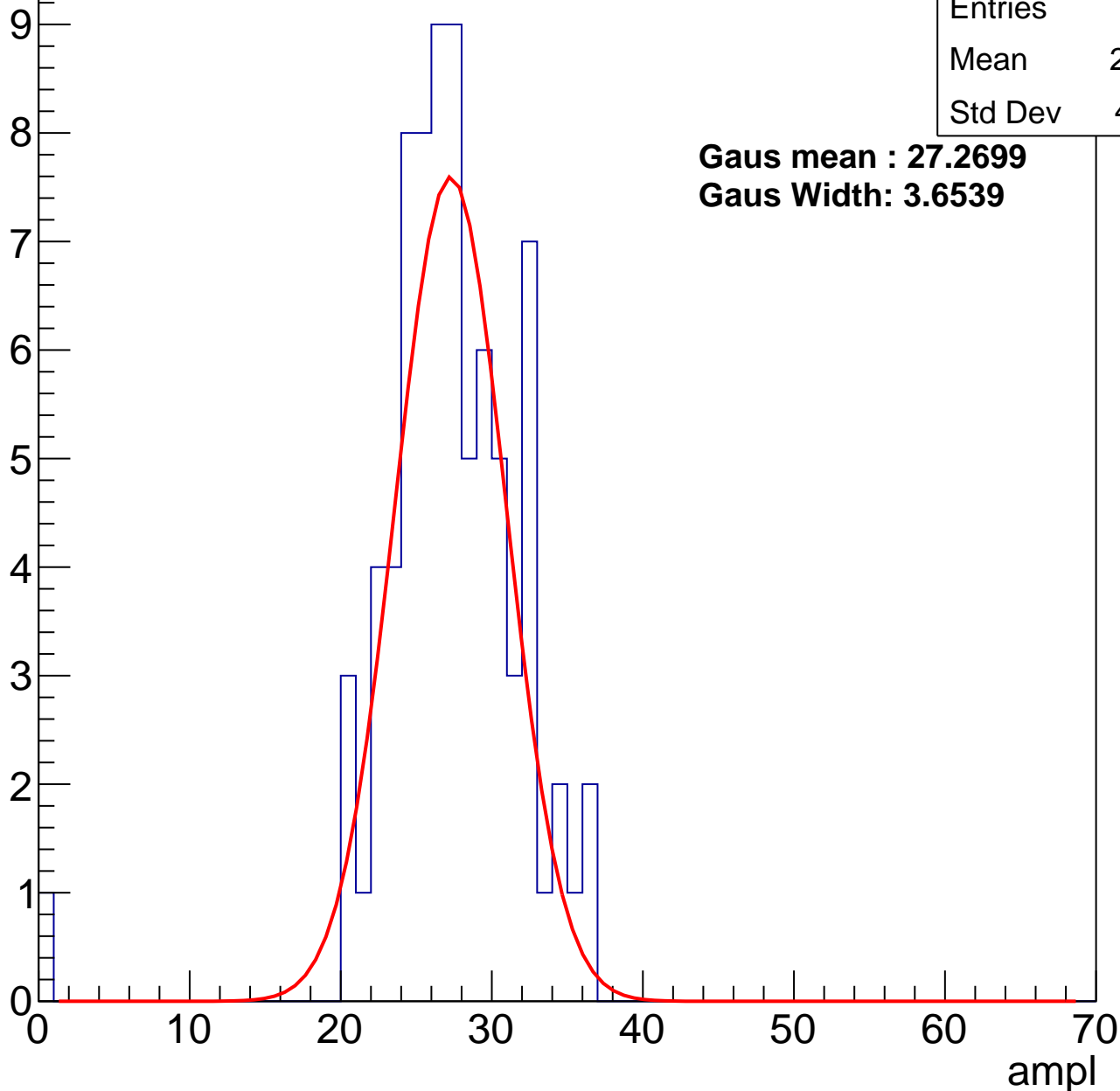
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 79 |
| Mean | 26.82 |
| Std Dev | 4.831 |

Gaus mean : 27.2699

Gaus Width: 3.6539



B1L102S, U15-ch90, adc1

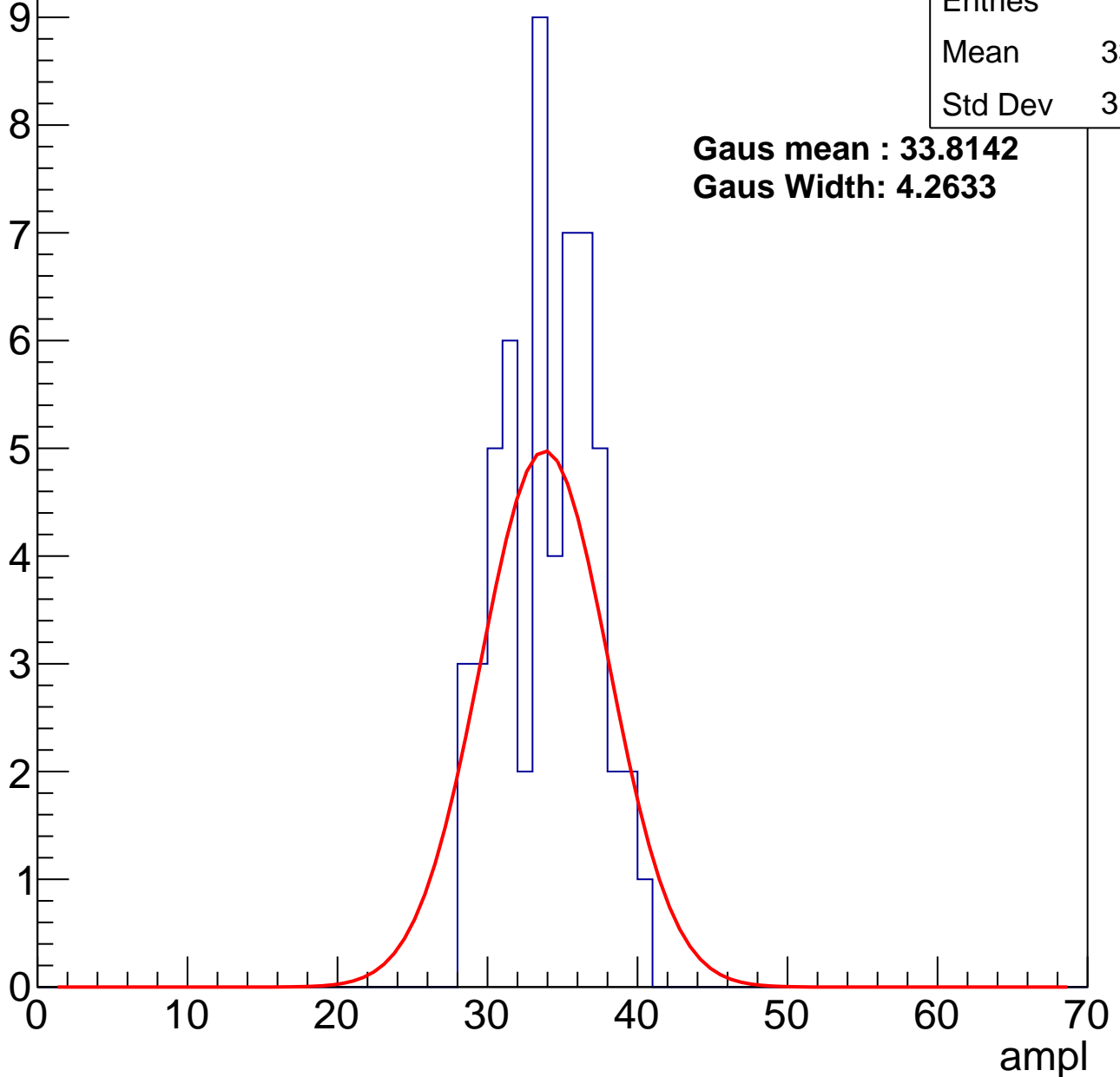
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 33.57 |
| Std Dev | 3.052 |

Gaus mean : 33.8142

Gaus Width: 4.2633



B1L102S, U15-ch90, adc2

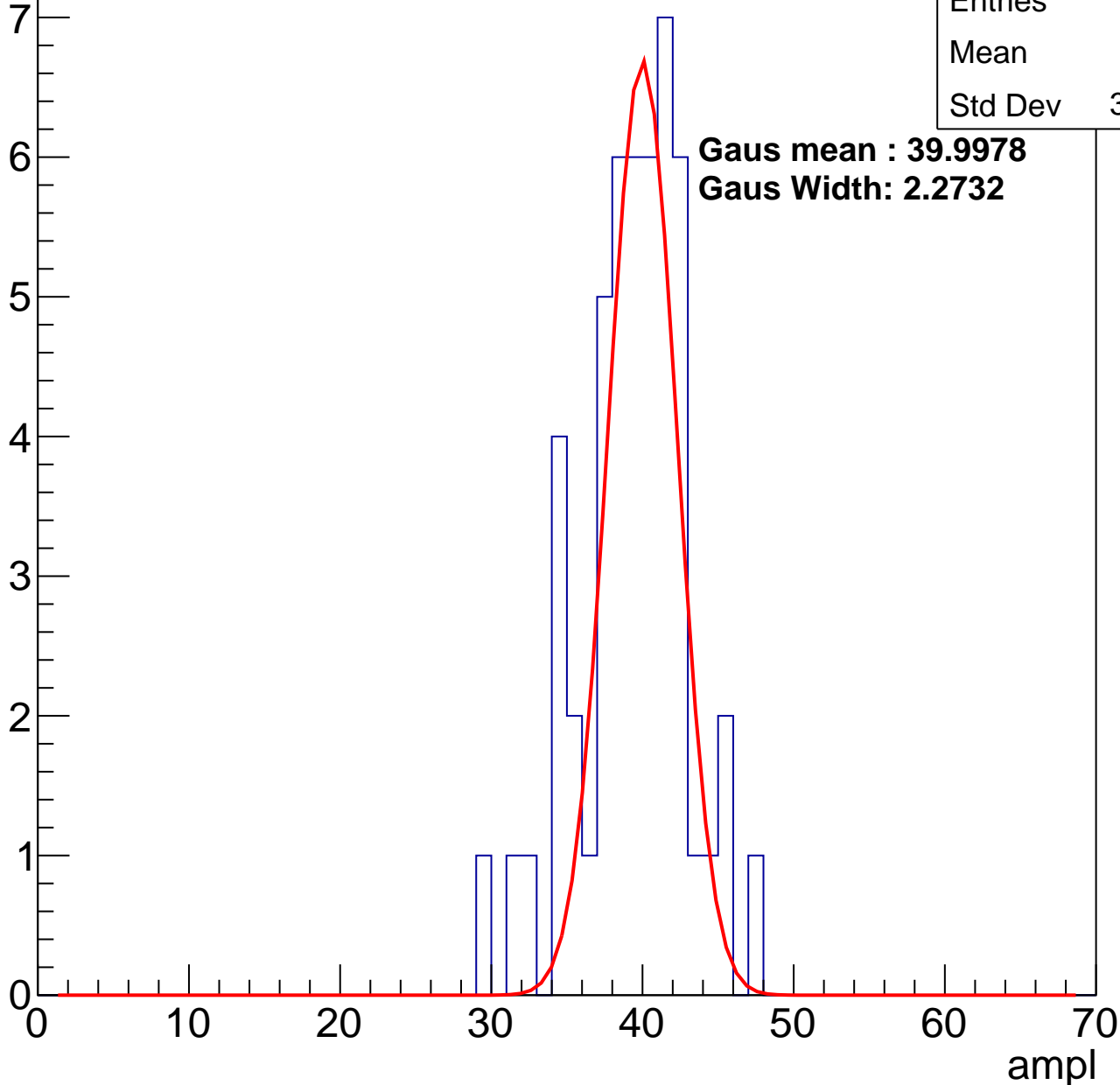
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 38.9 |
| Std Dev | 3.555 |

Gaus mean : 39.9978

Gaus Width: 2.2732

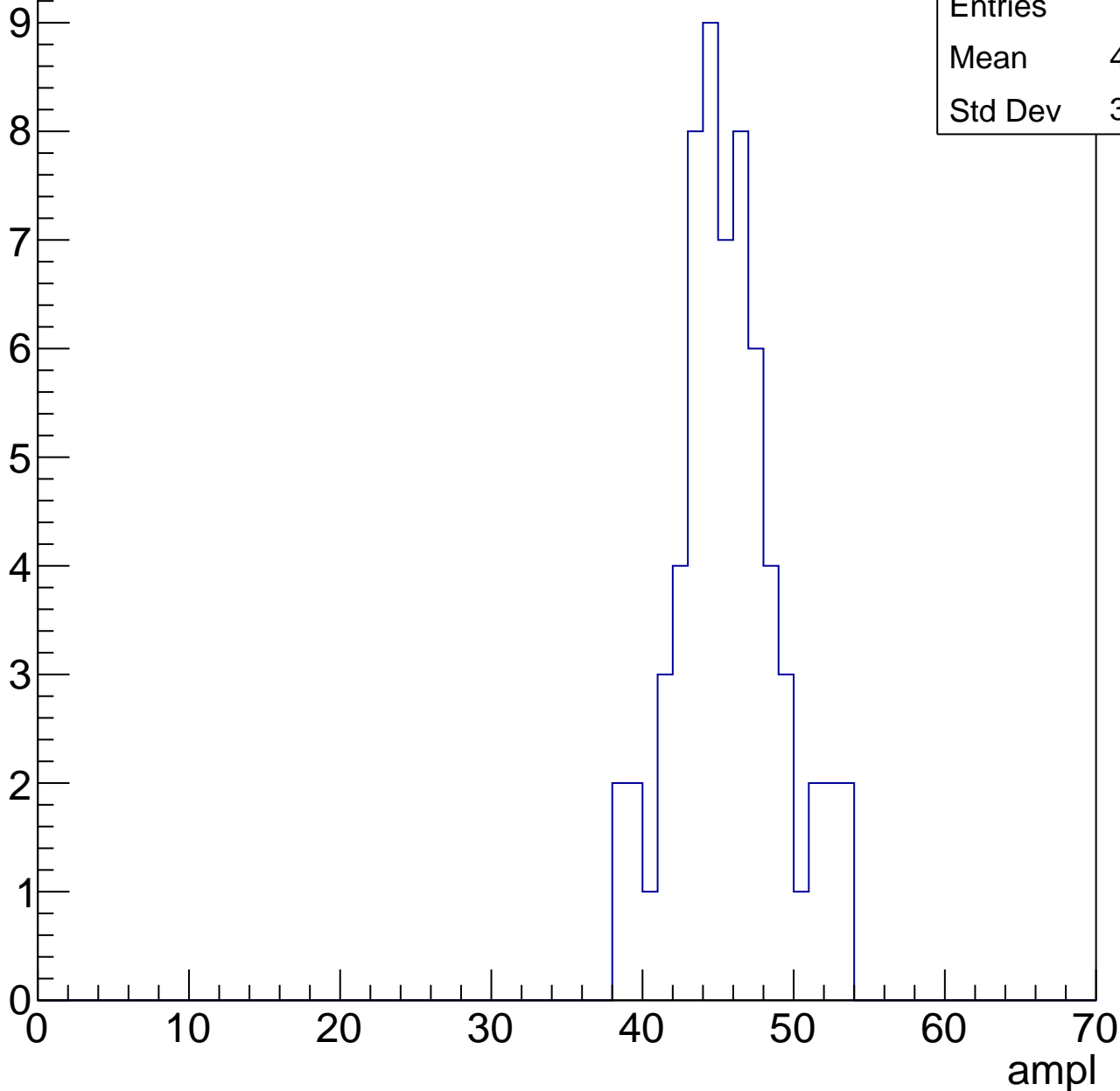


B1L102S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.17 |
| Std Dev | 3.439 |

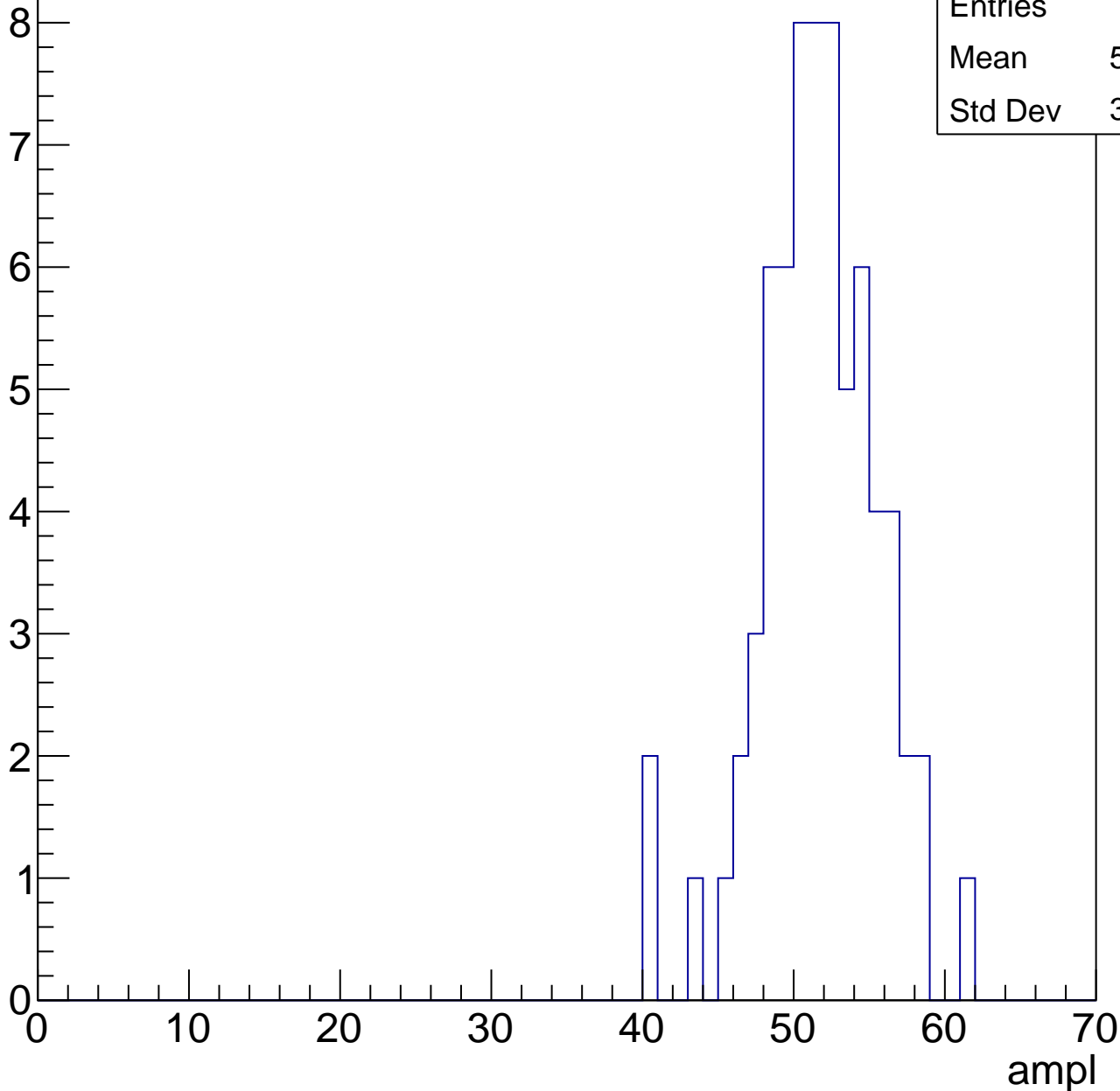


B1L102S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 51.17 |
| Std Dev | 3.882 |

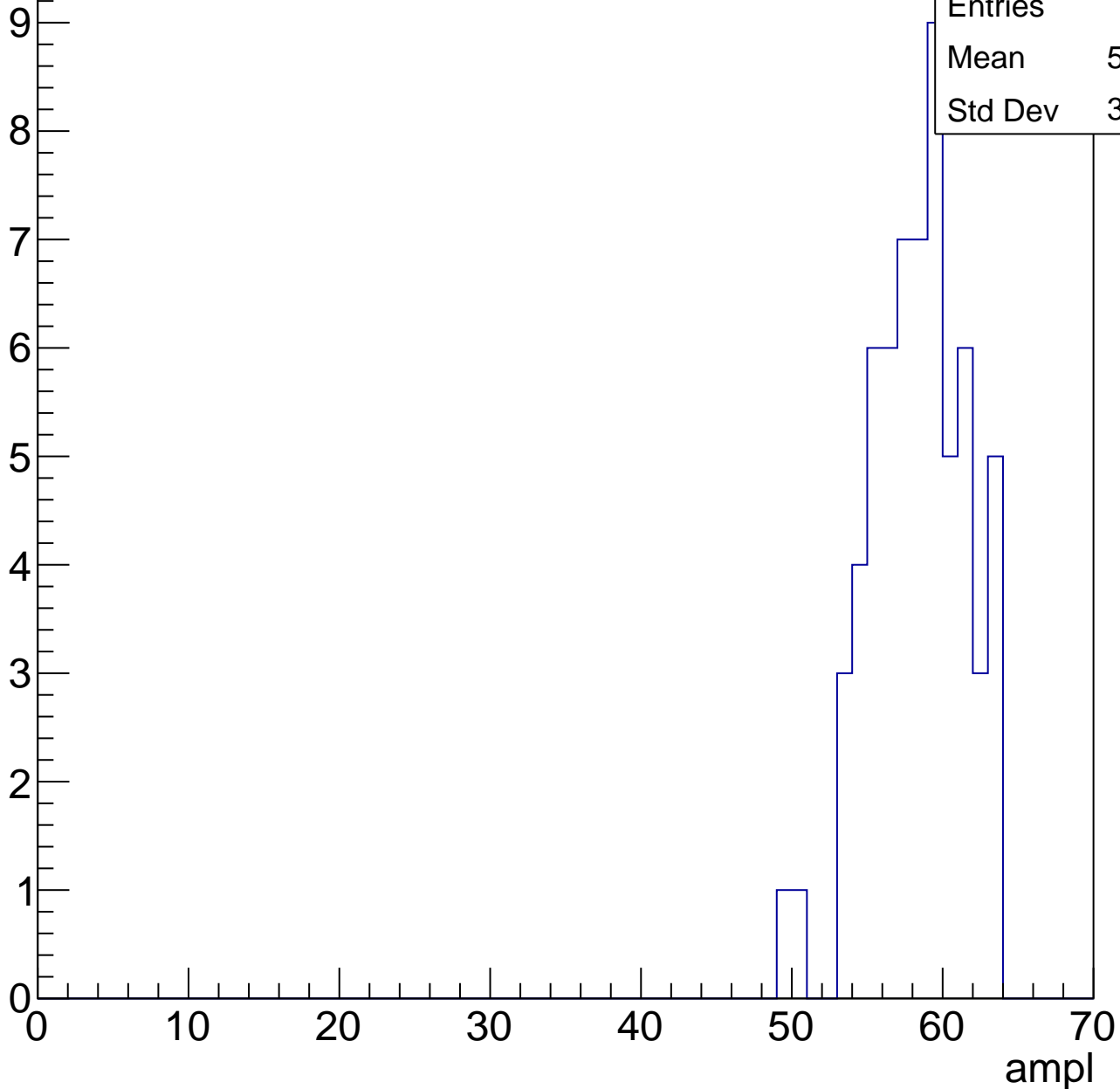


B1L102S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 57.83 |
| Std Dev | 3.145 |

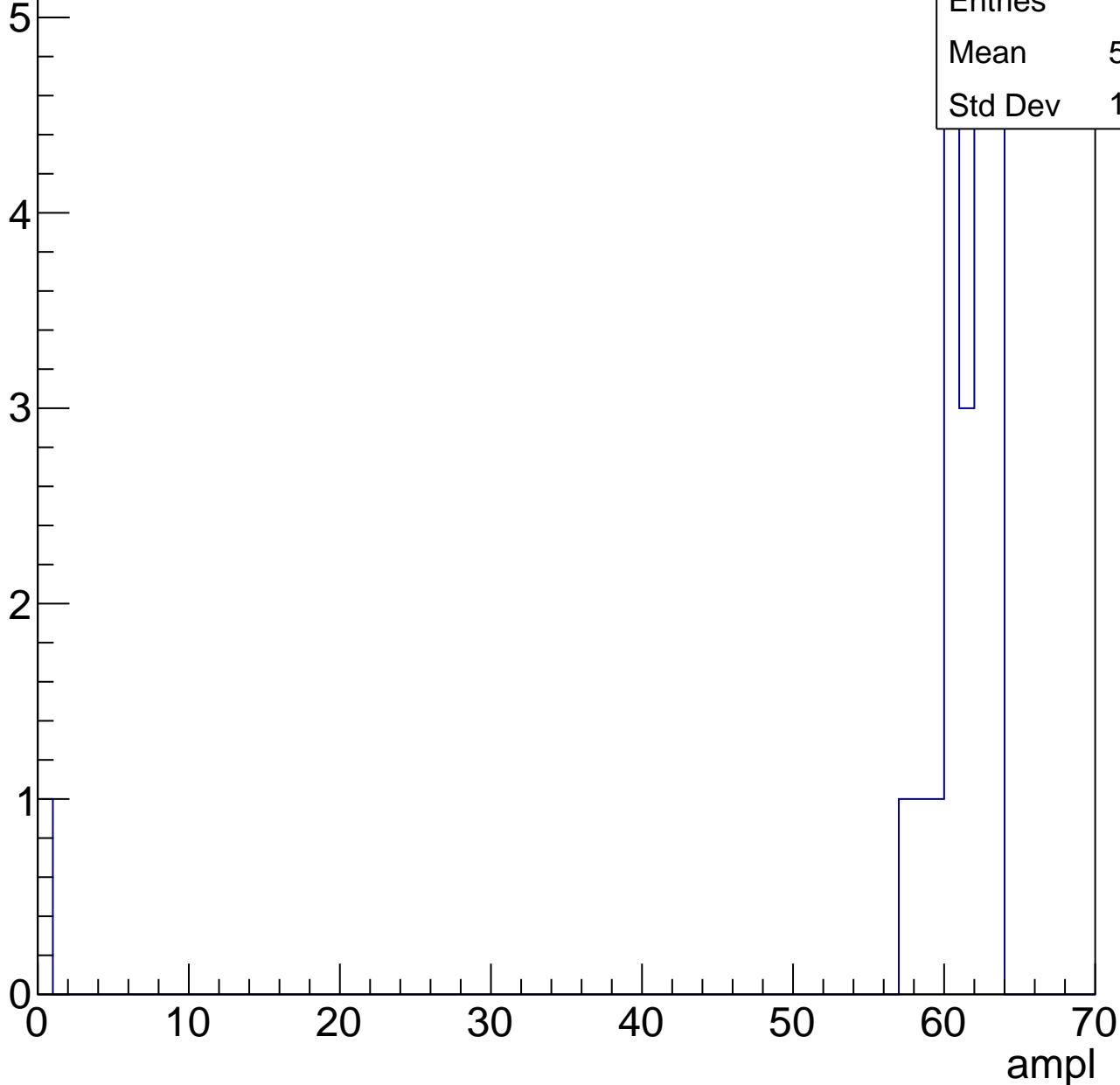


B1L102S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 22 |
| Mean | 58.27 |
| Std Dev | 12.82 |



B1L102S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch91, adc0

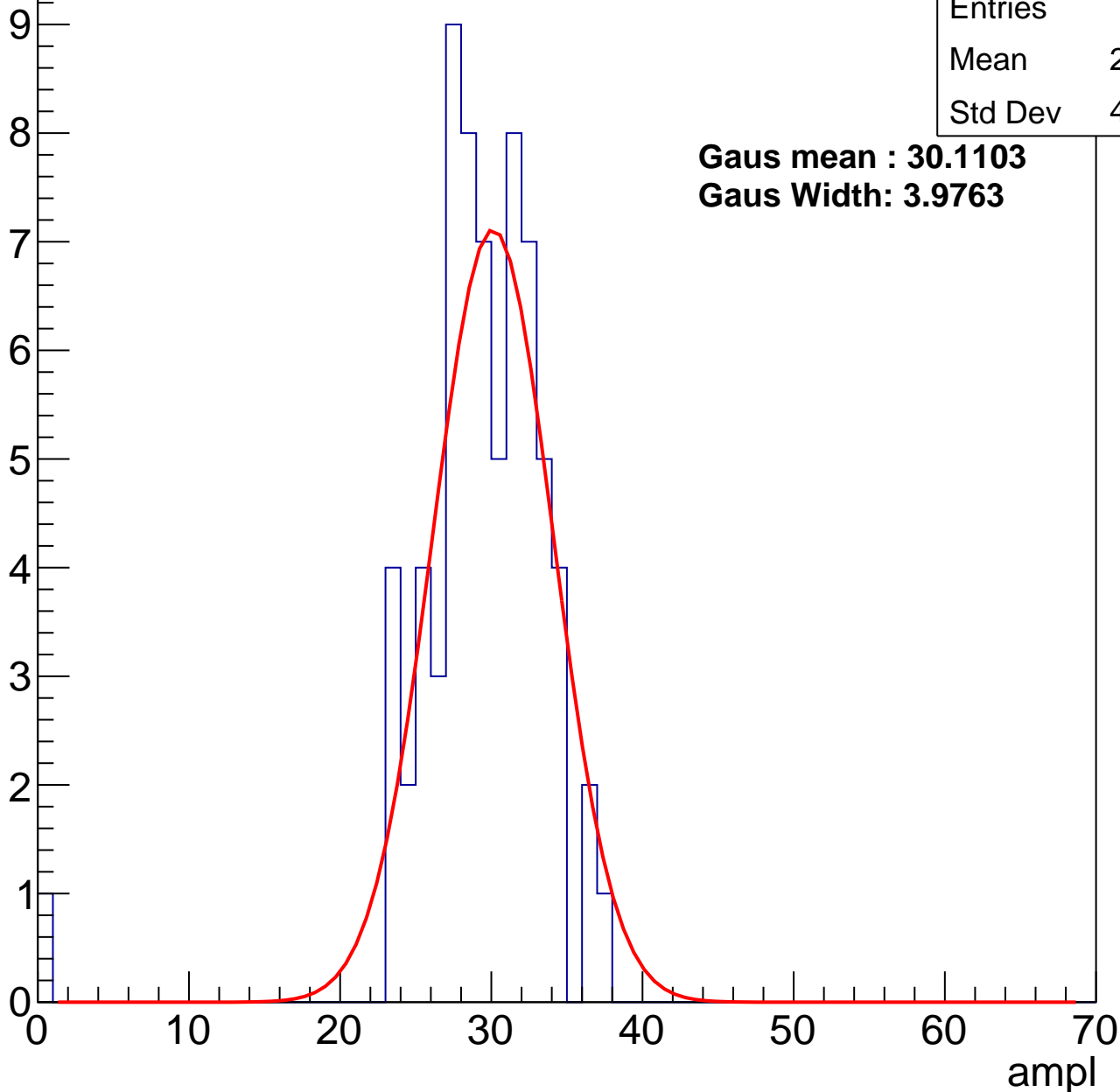
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 28.86 |
| Std Dev | 4.797 |

Gaus mean : 30.1103

Gaus Width: 3.9763



B1L102S, U15-ch91, adc1

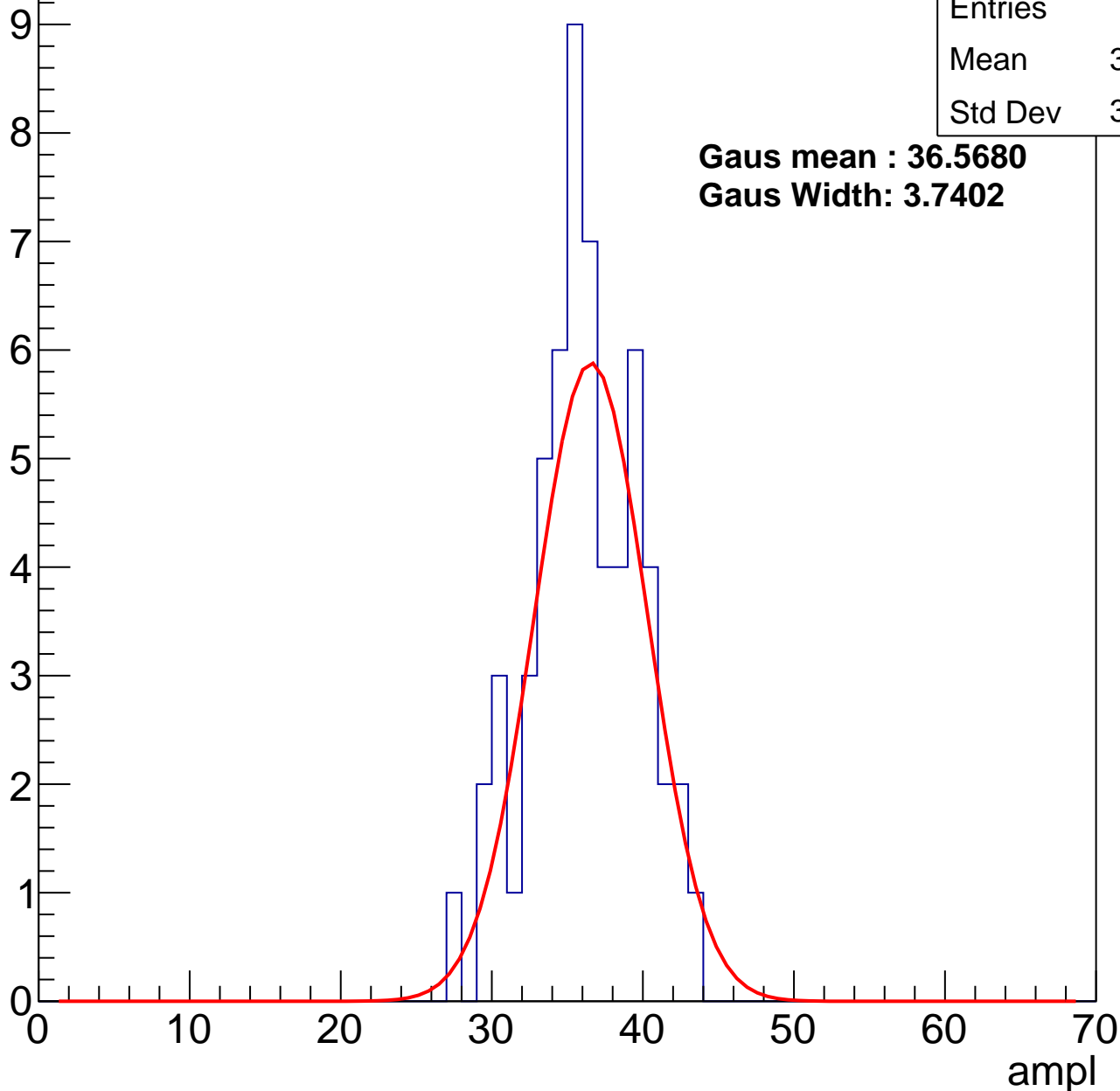
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 35.68 |
| Std Dev | 3.514 |

Gaus mean : 36.5680

Gaus Width: 3.7402



B1L102S, U15-ch91, adc2

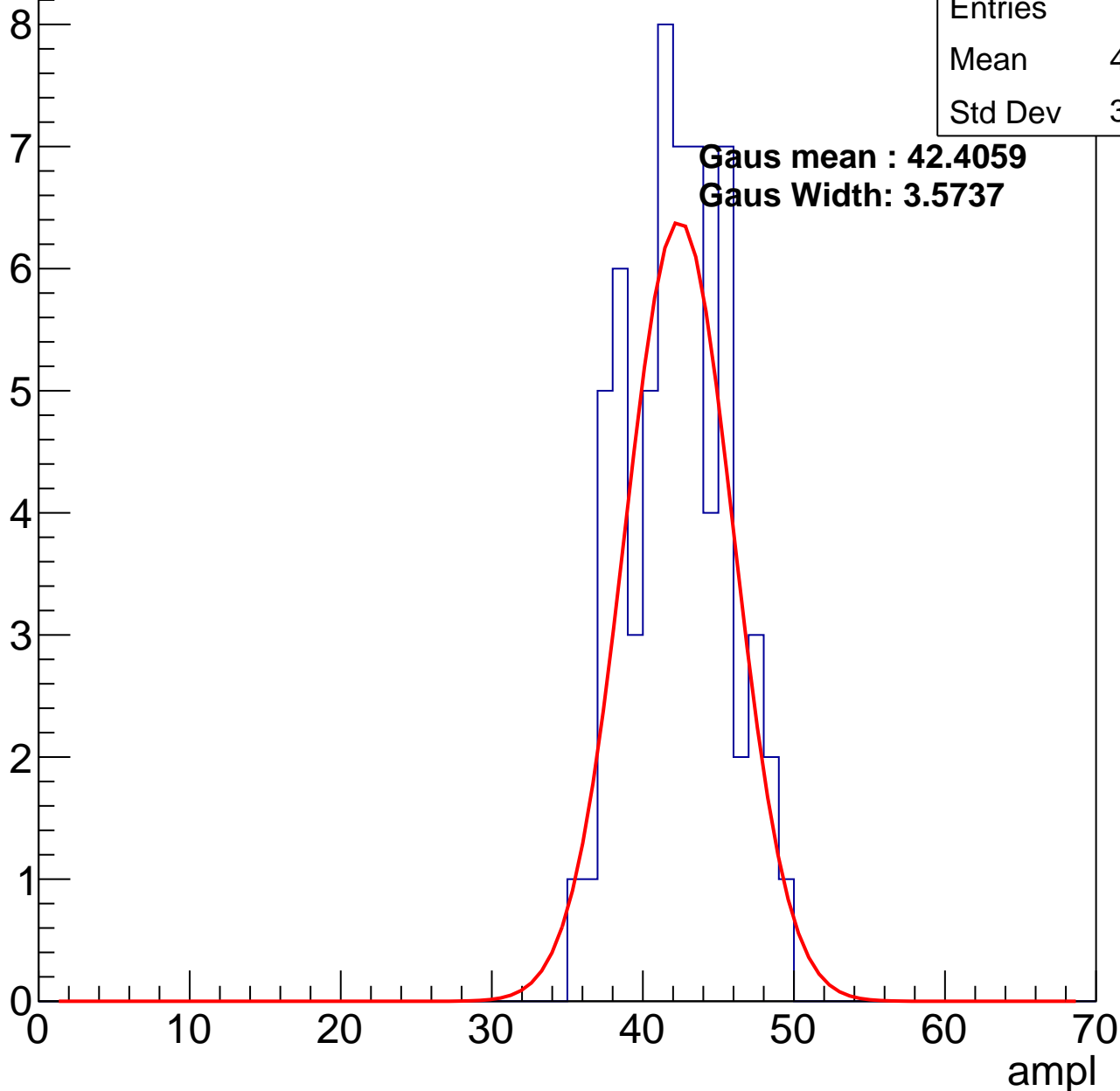
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 41.82 |
| Std Dev | 3.295 |

Gaus mean : 42.4059

Gaus Width: 3.5737

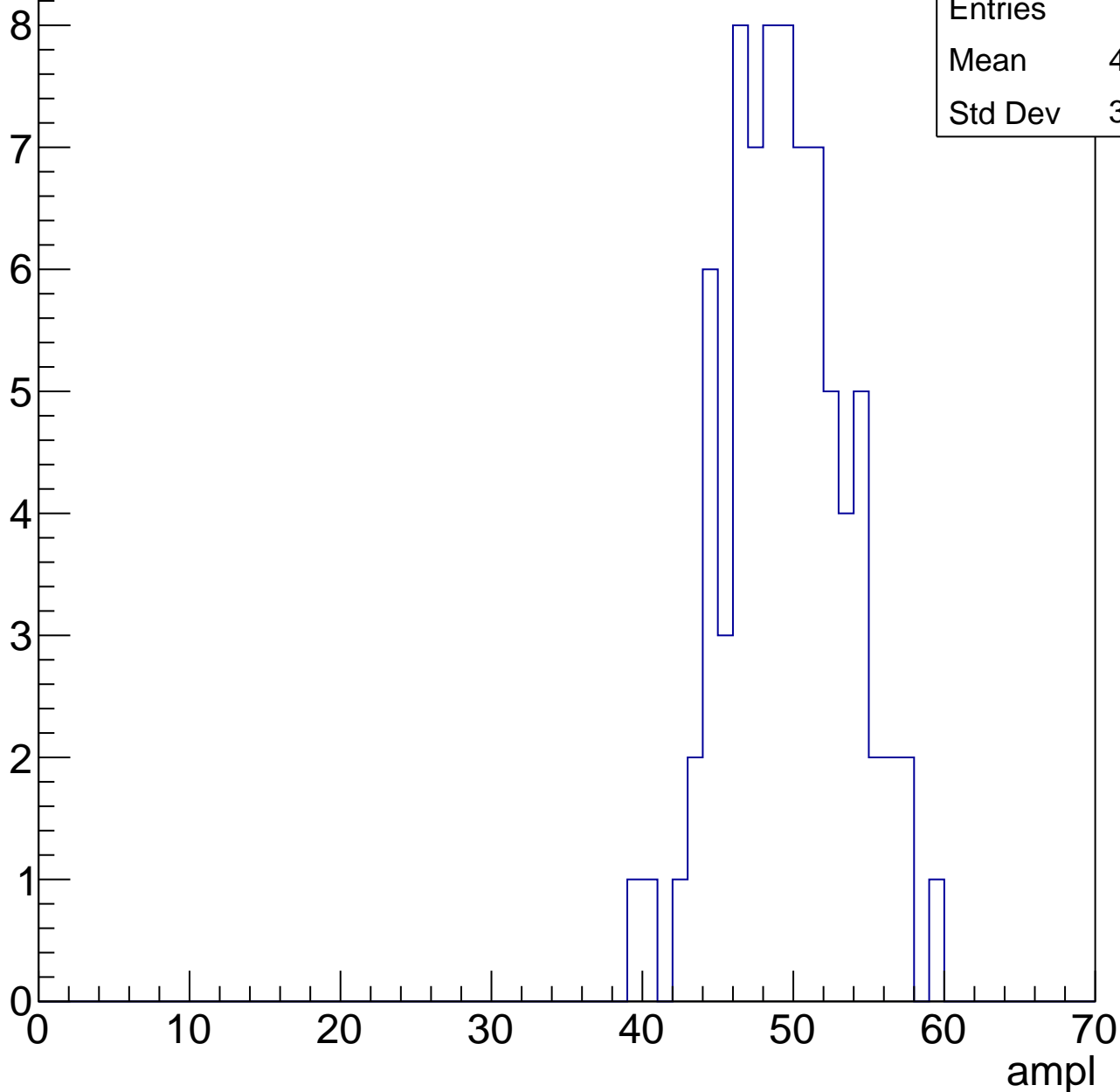


B1L102S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 80 |
| Mean | 49.04 |
| Std Dev | 3.992 |

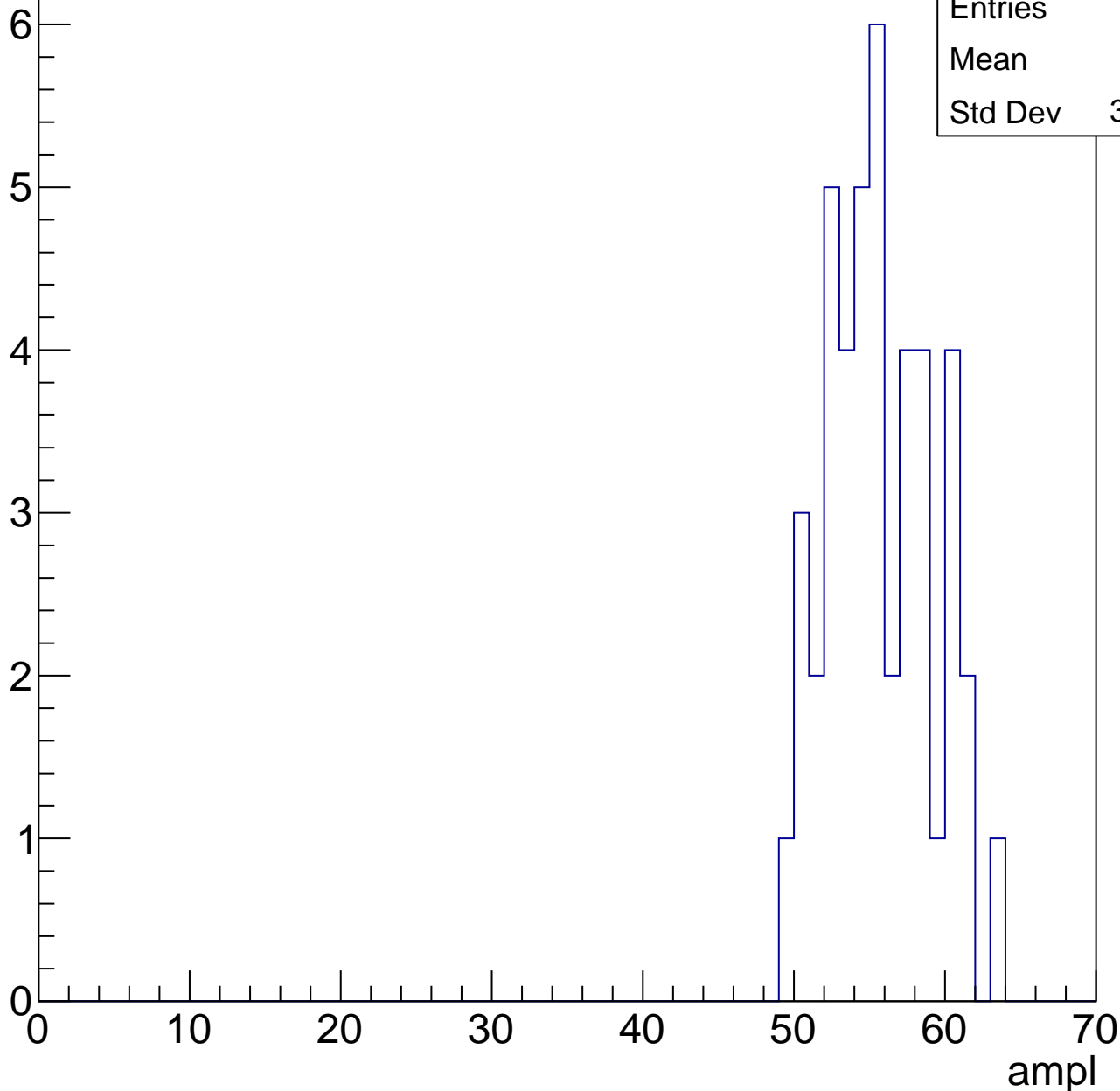


B1L102S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 55.2 |
| Std Dev | 3.415 |

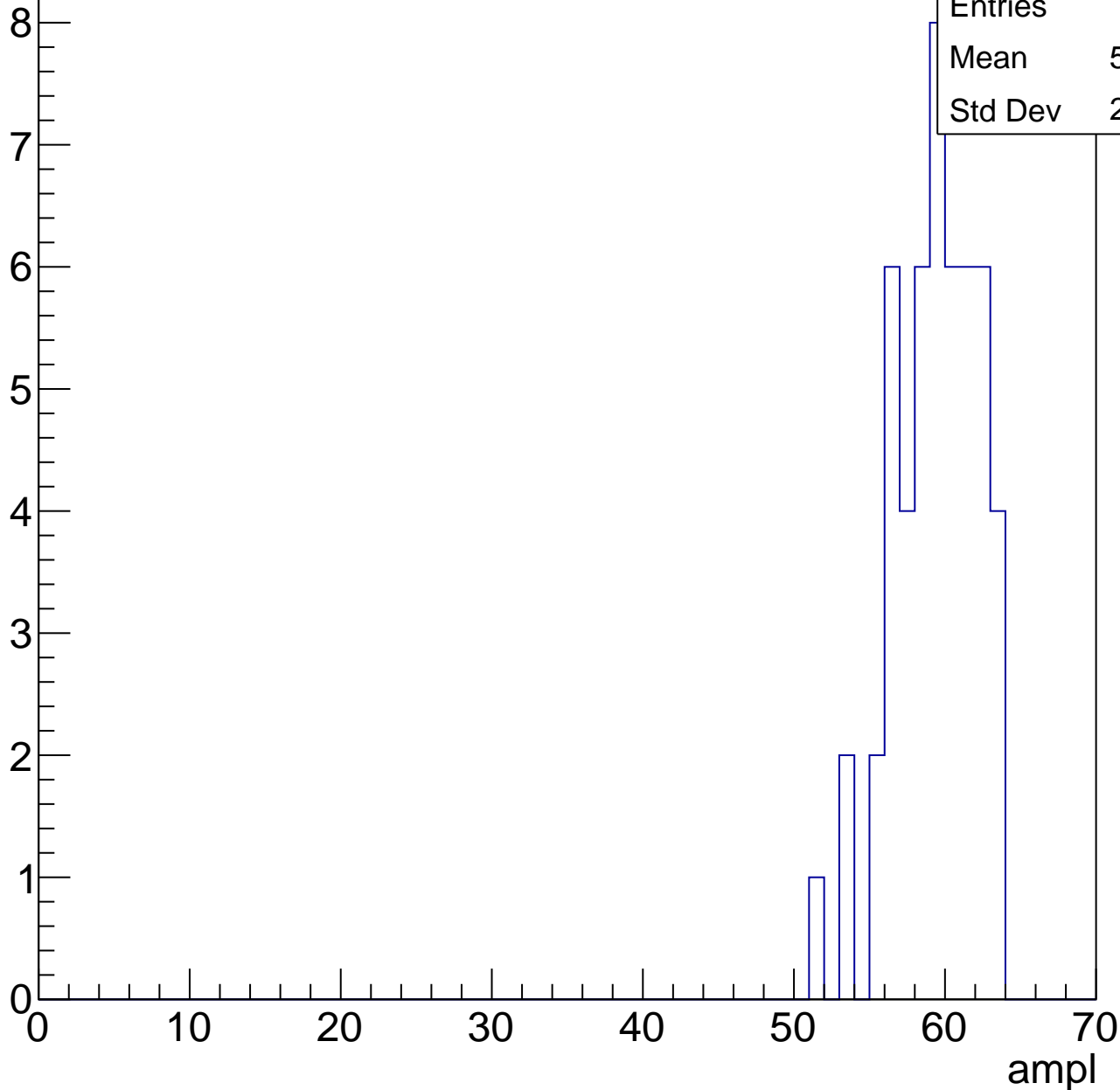


B1L102S, U15-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

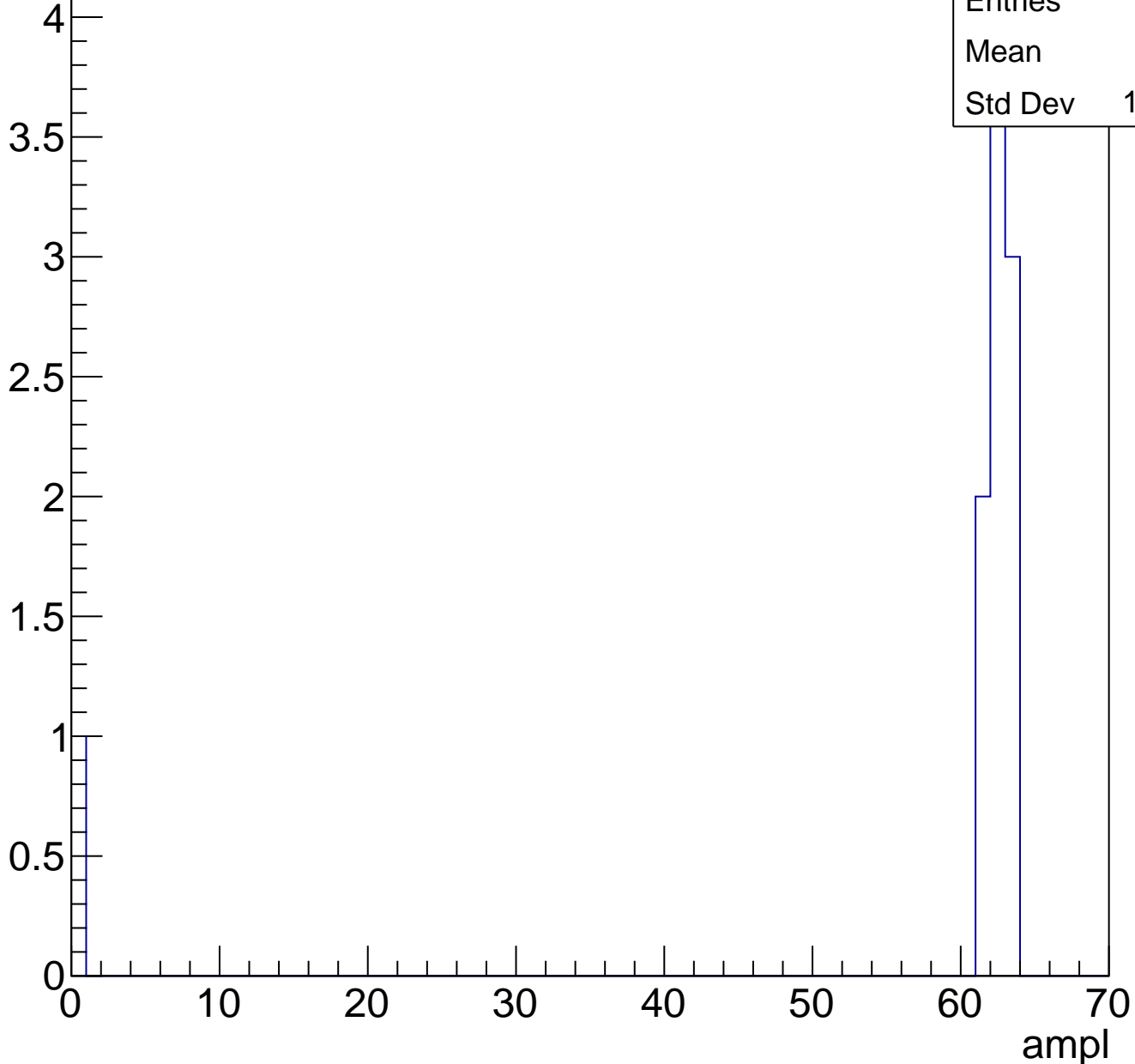
| | |
|---------|-------|
| Entries | 51 |
| Mean | 58.84 |
| Std Dev | 2.768 |



B1L102S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

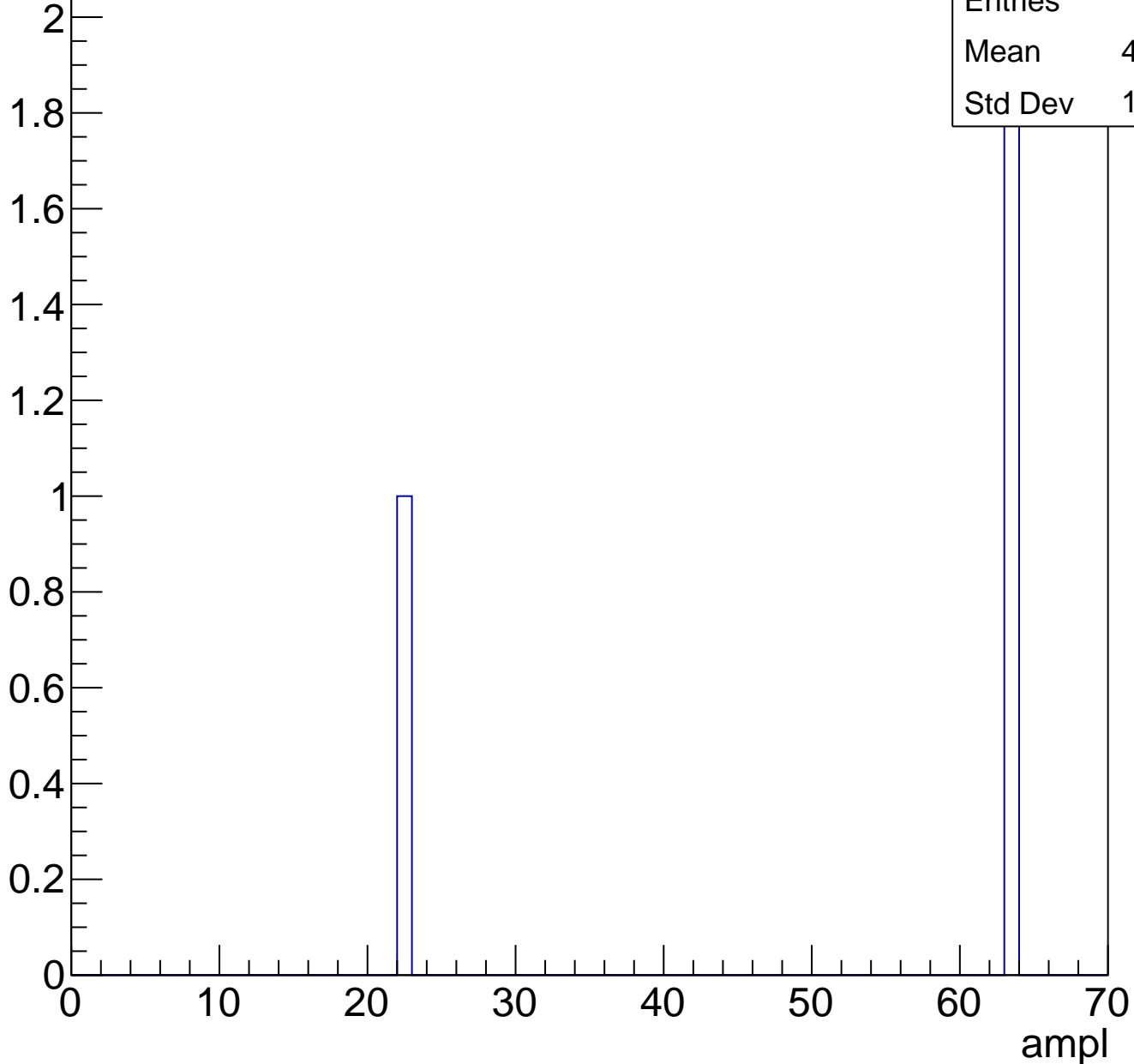
Entry



B1L102S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch92, adc0

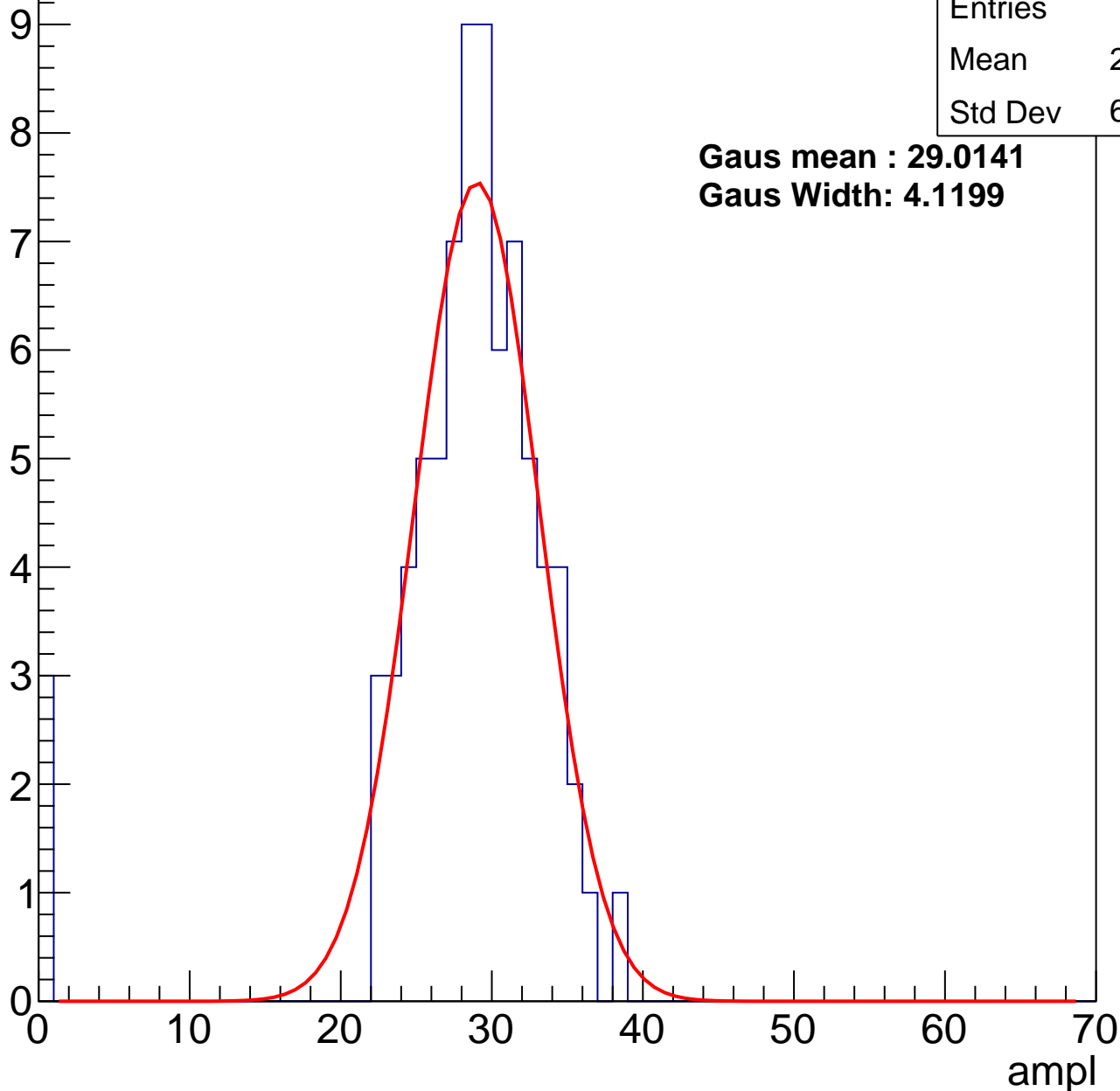
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 27.65 |
| Std Dev | 6.547 |

Gaus mean : 29.0141

Gaus Width: 4.1199



B1L102S, U15-ch92, adc1

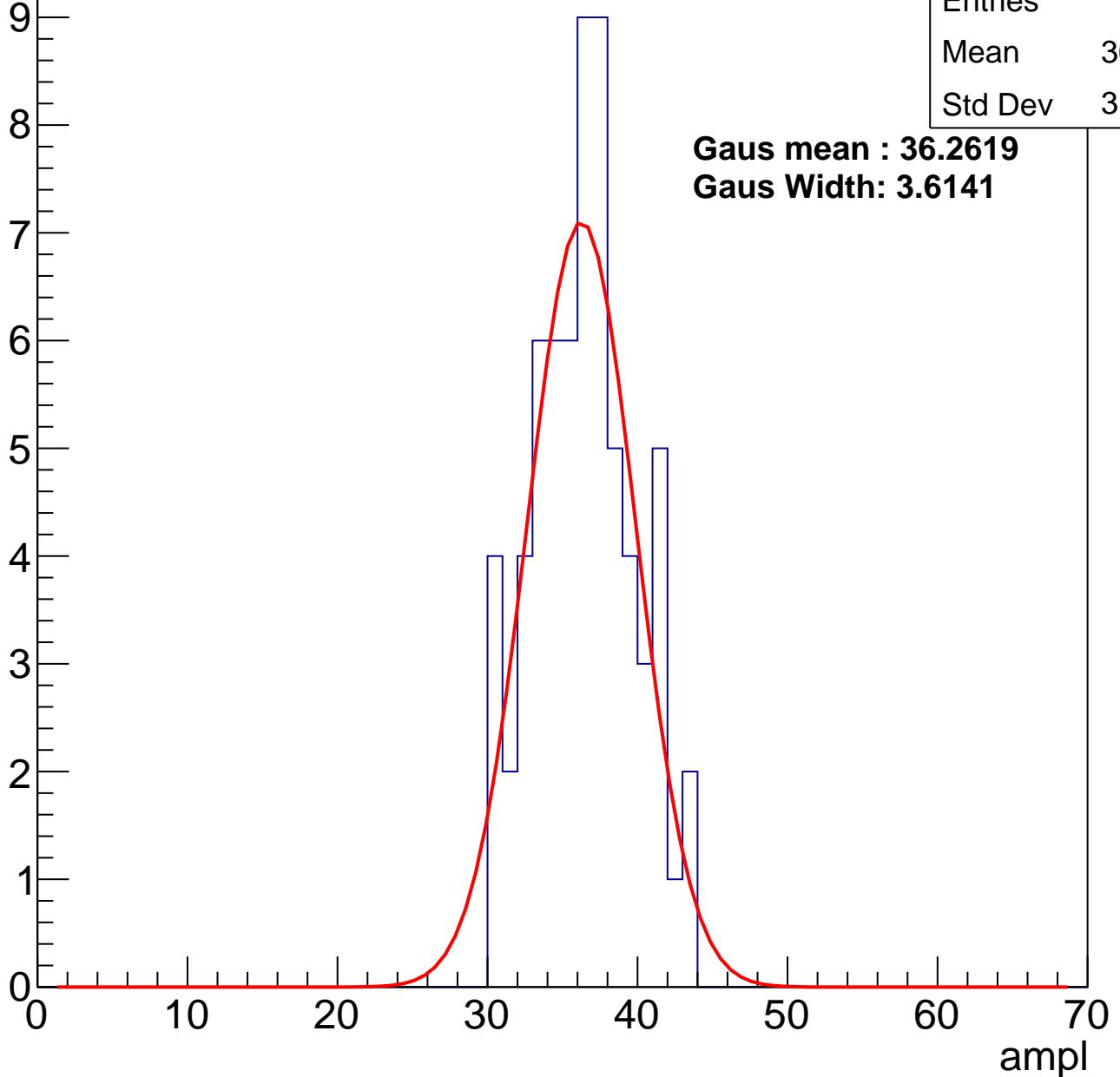
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 36.03 |
| Std Dev | 3.289 |

Gaus mean : 36.2619

Gaus Width: 3.6141



B1L102S, U15-ch92, adc2

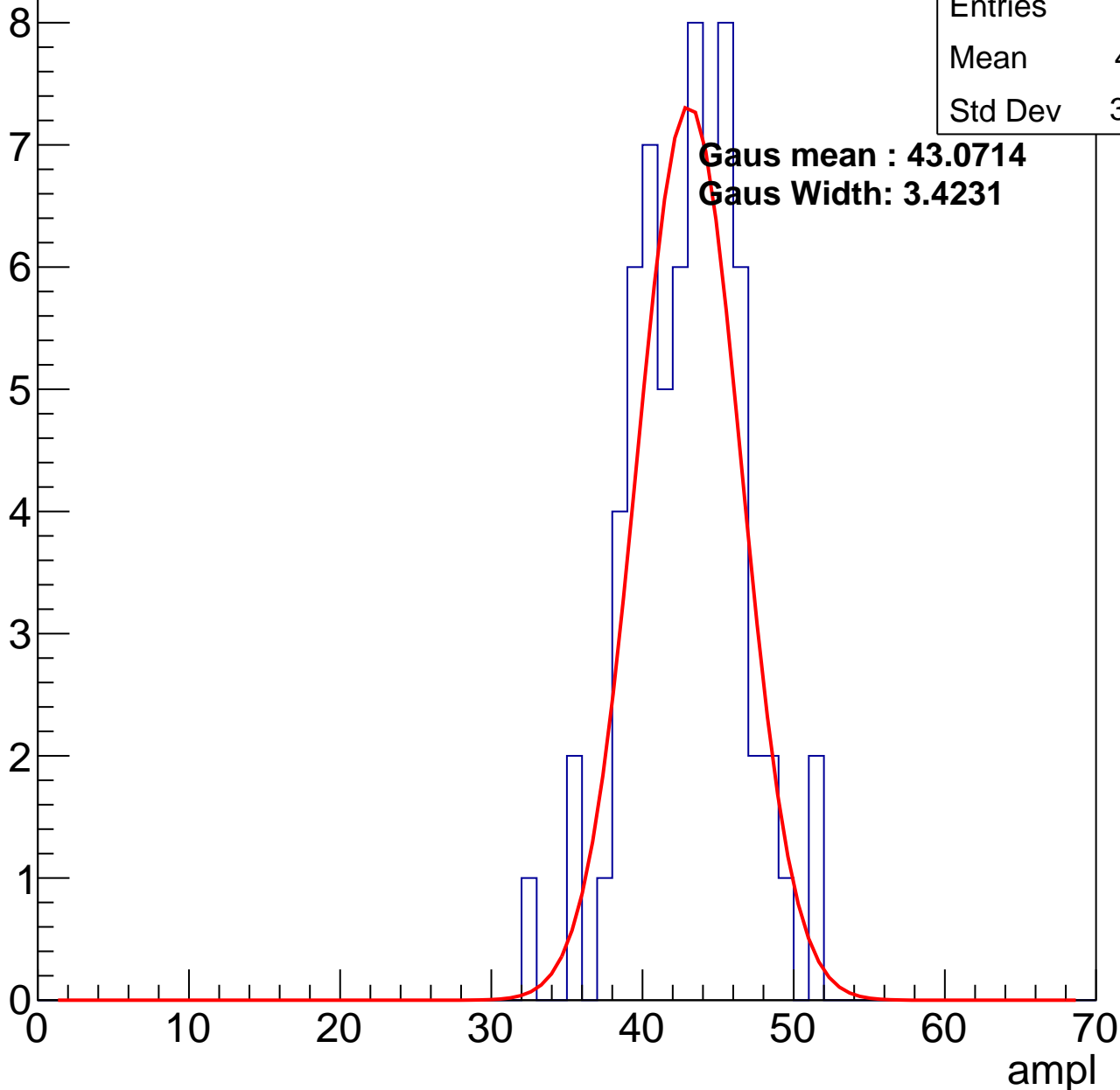
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.51 |
| Std Dev | 3.624 |

Gaus mean : 43.0714

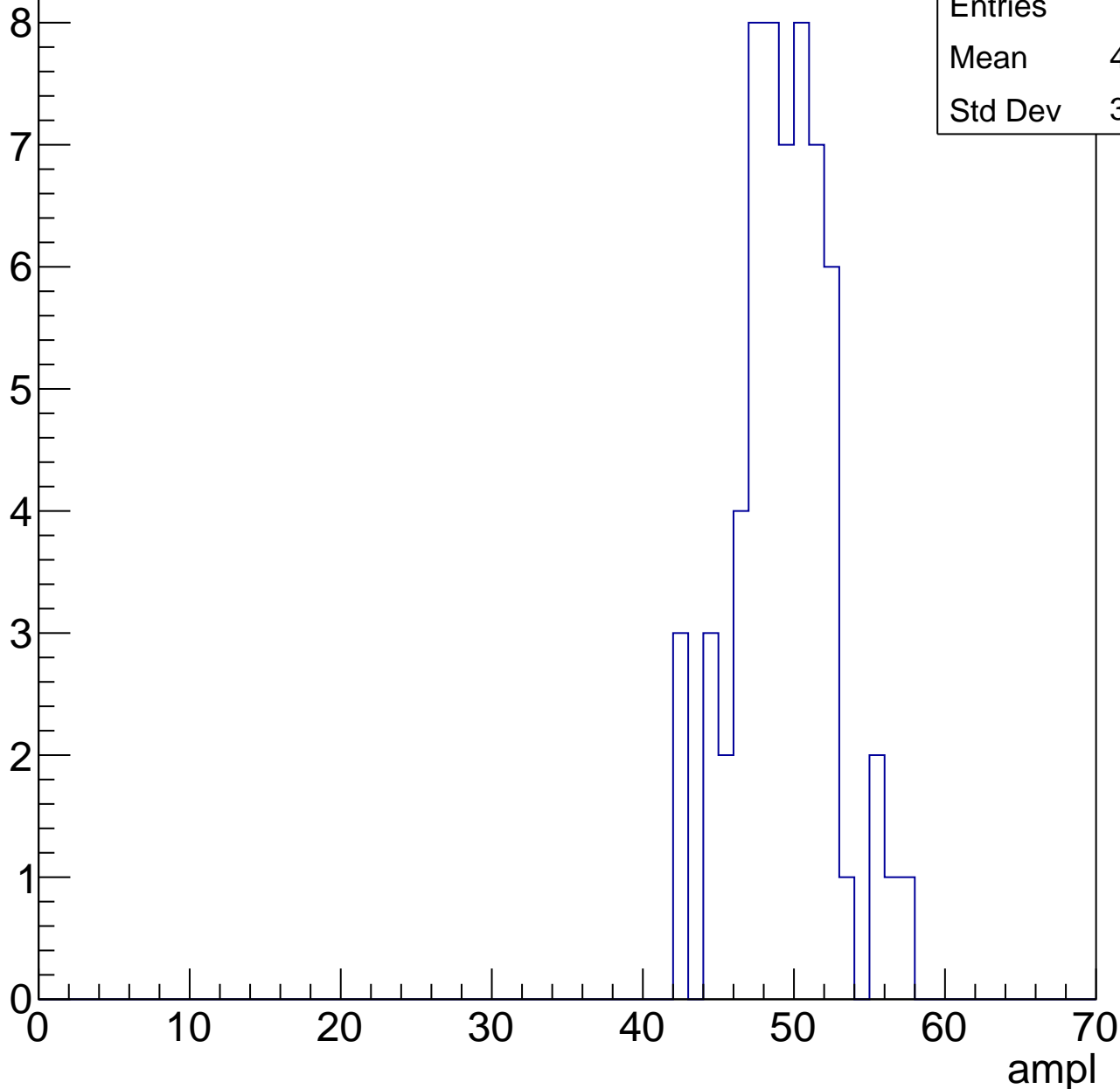
Gaus Width: 3.4231



B1L102S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

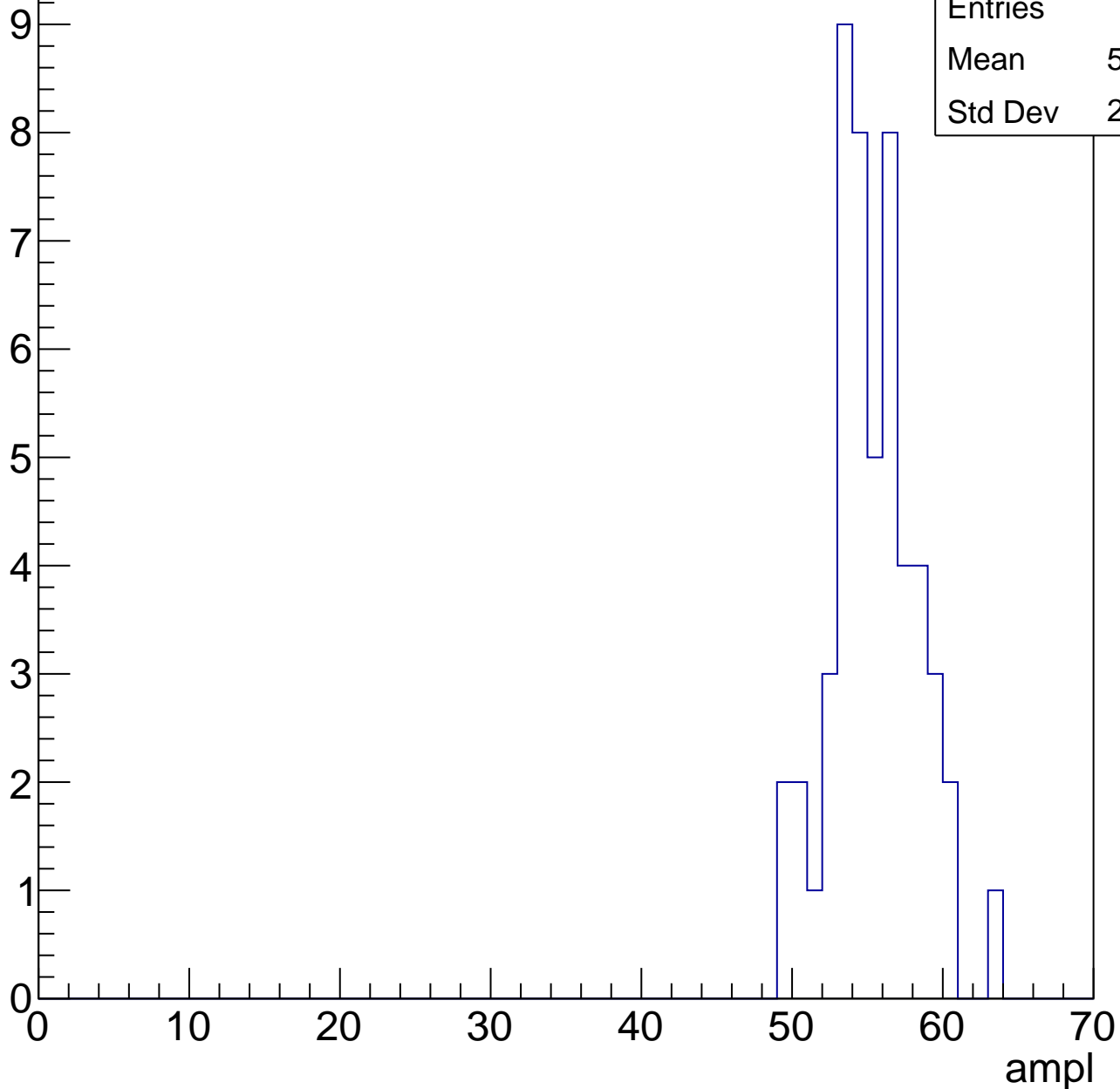
Entry



B1L102S, U15-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

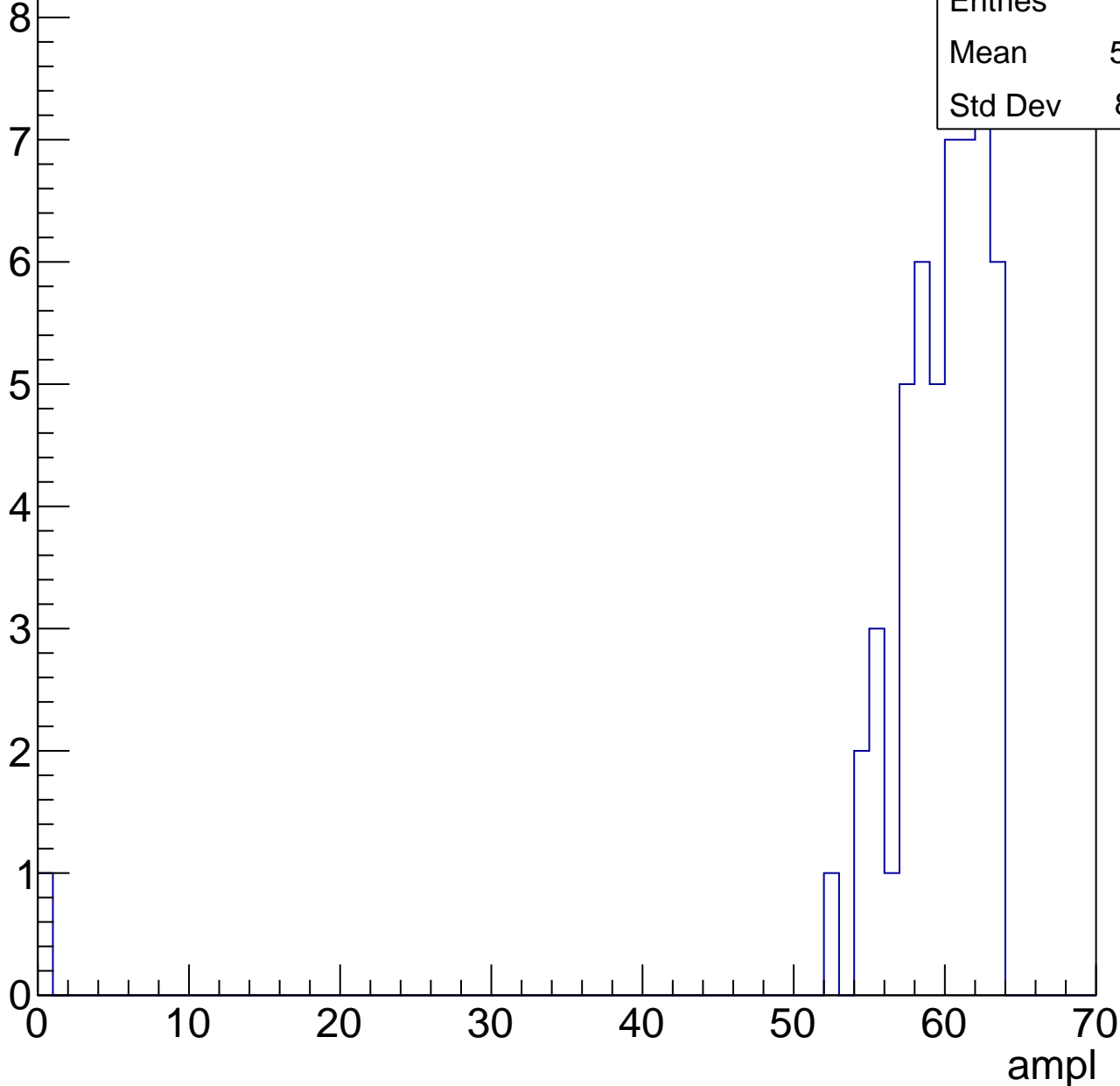


B1L102S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

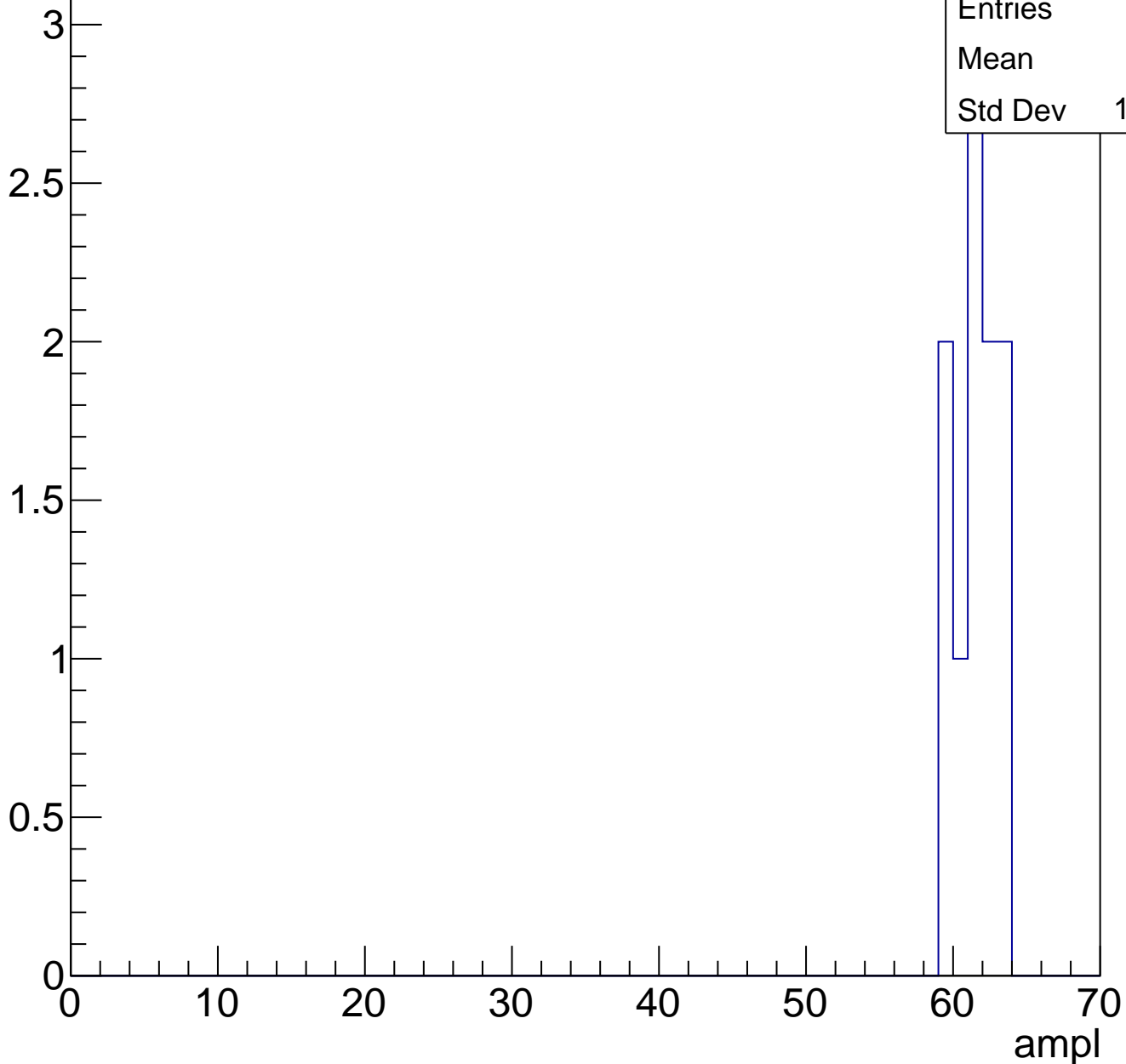
| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.27 |
| Std Dev | 8.591 |



B1L102S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

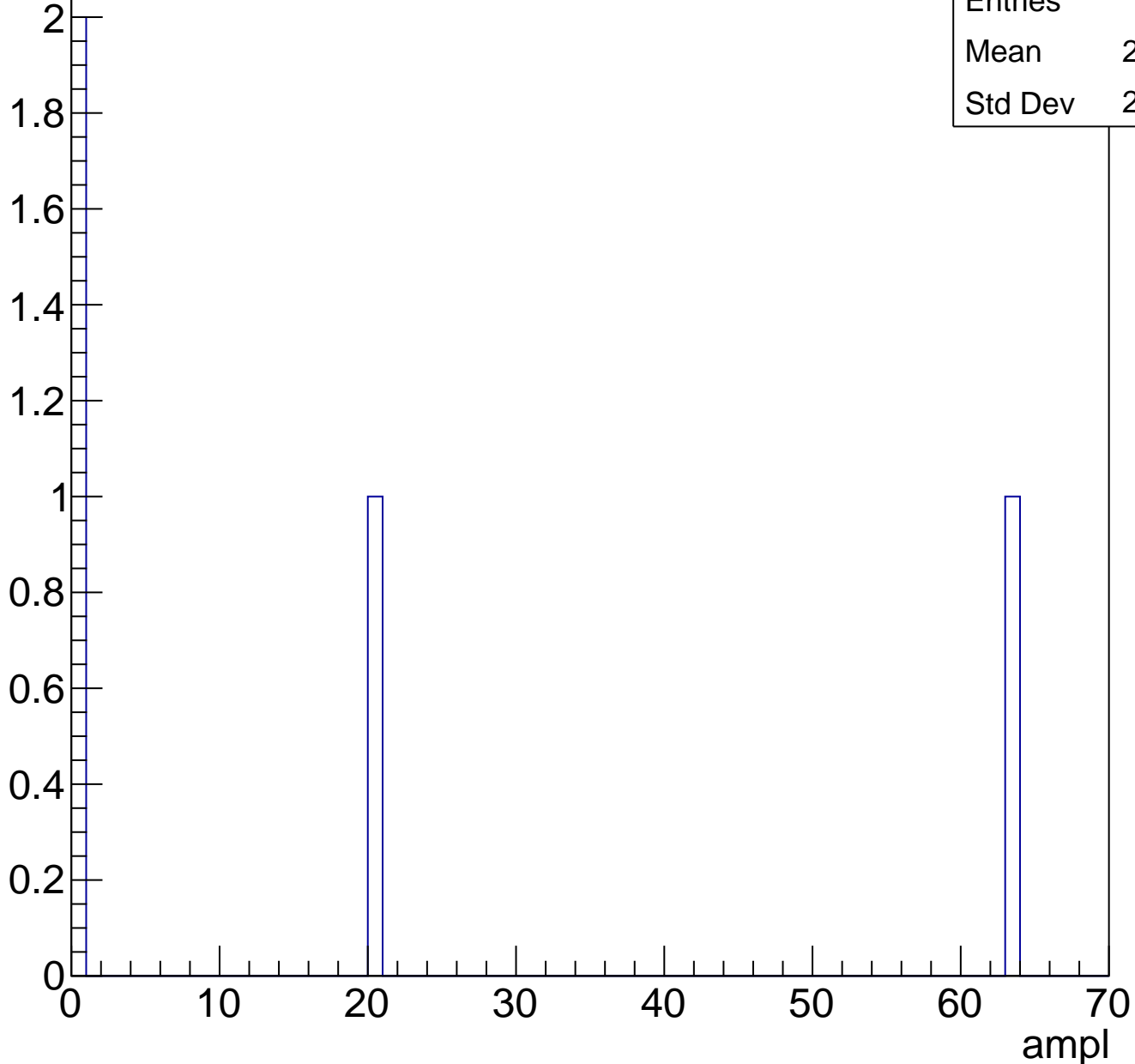
Entry



B1L102S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 4 |
| Mean | 20.75 |
| Std Dev | 25.72 |

B1L102S, U15-ch93, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 87 |
| Mean | 28.52 |
| Std Dev | 5.056 |

Gaus mean : 29.5309

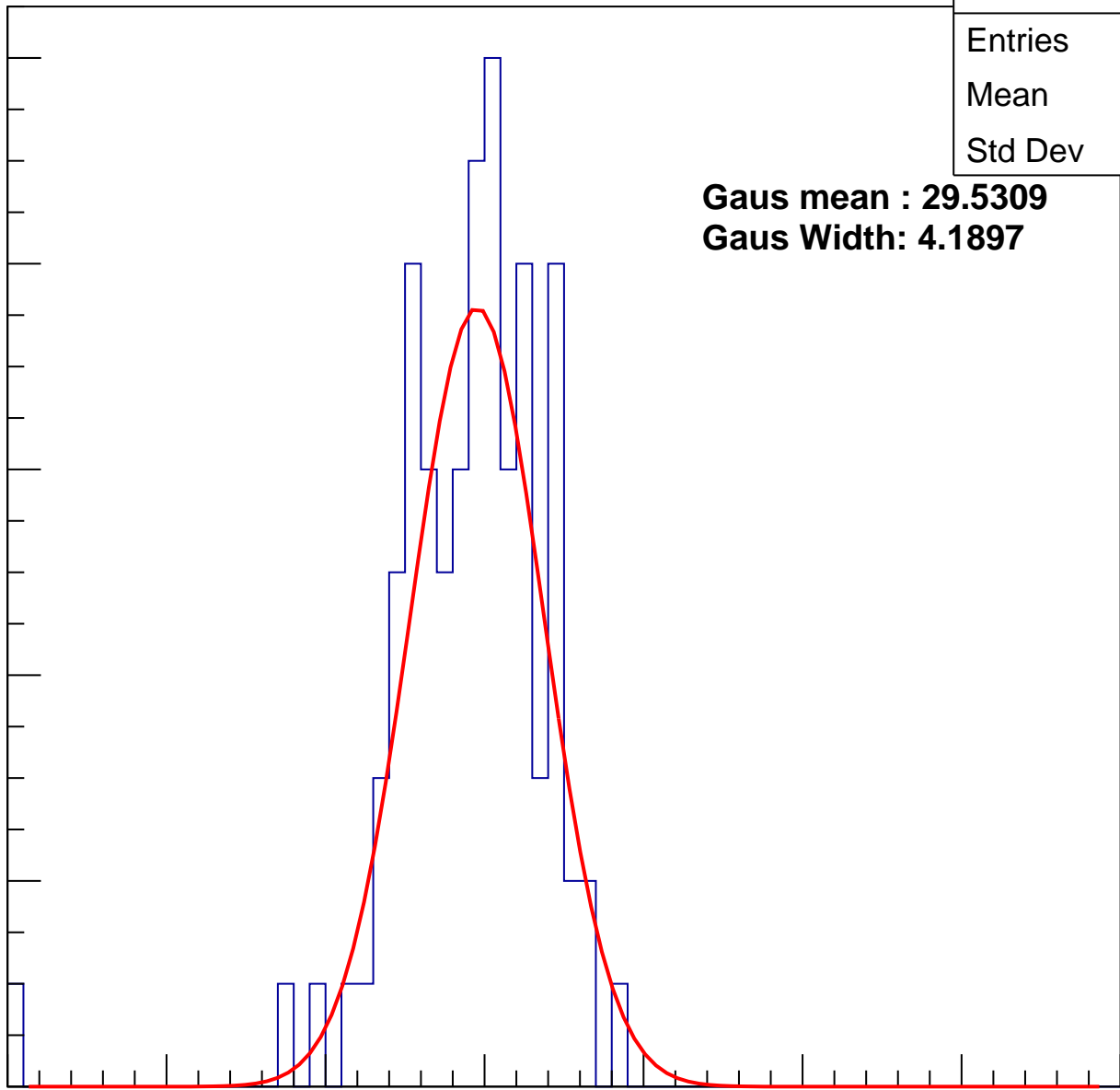
Gaus Width: 4.1897

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch93, adc1

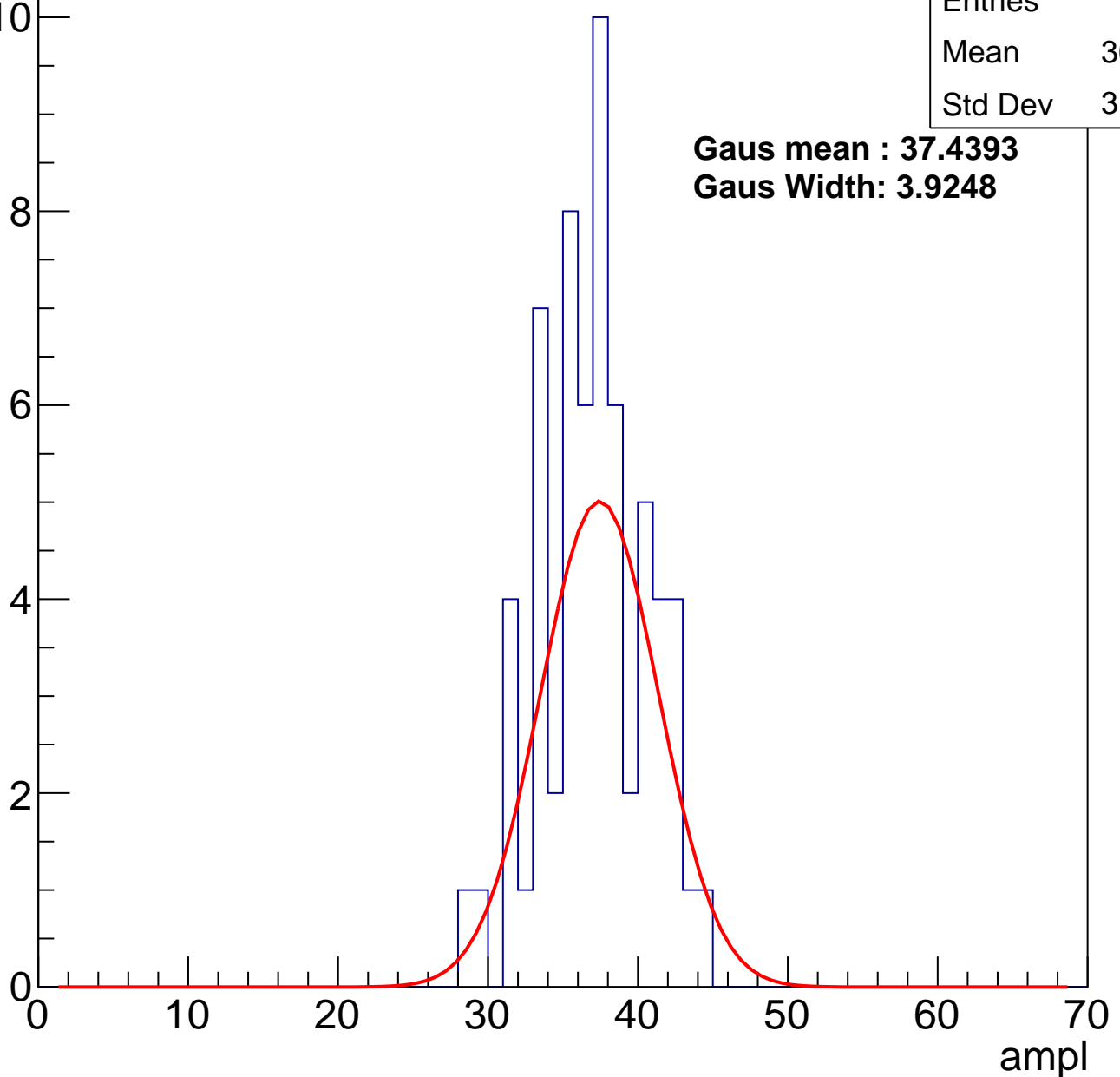
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.56 |
| Std Dev | 3.513 |

Gaus mean : 37.4393

Gaus Width: 3.9248



B1L102S, U15-ch93, adc2

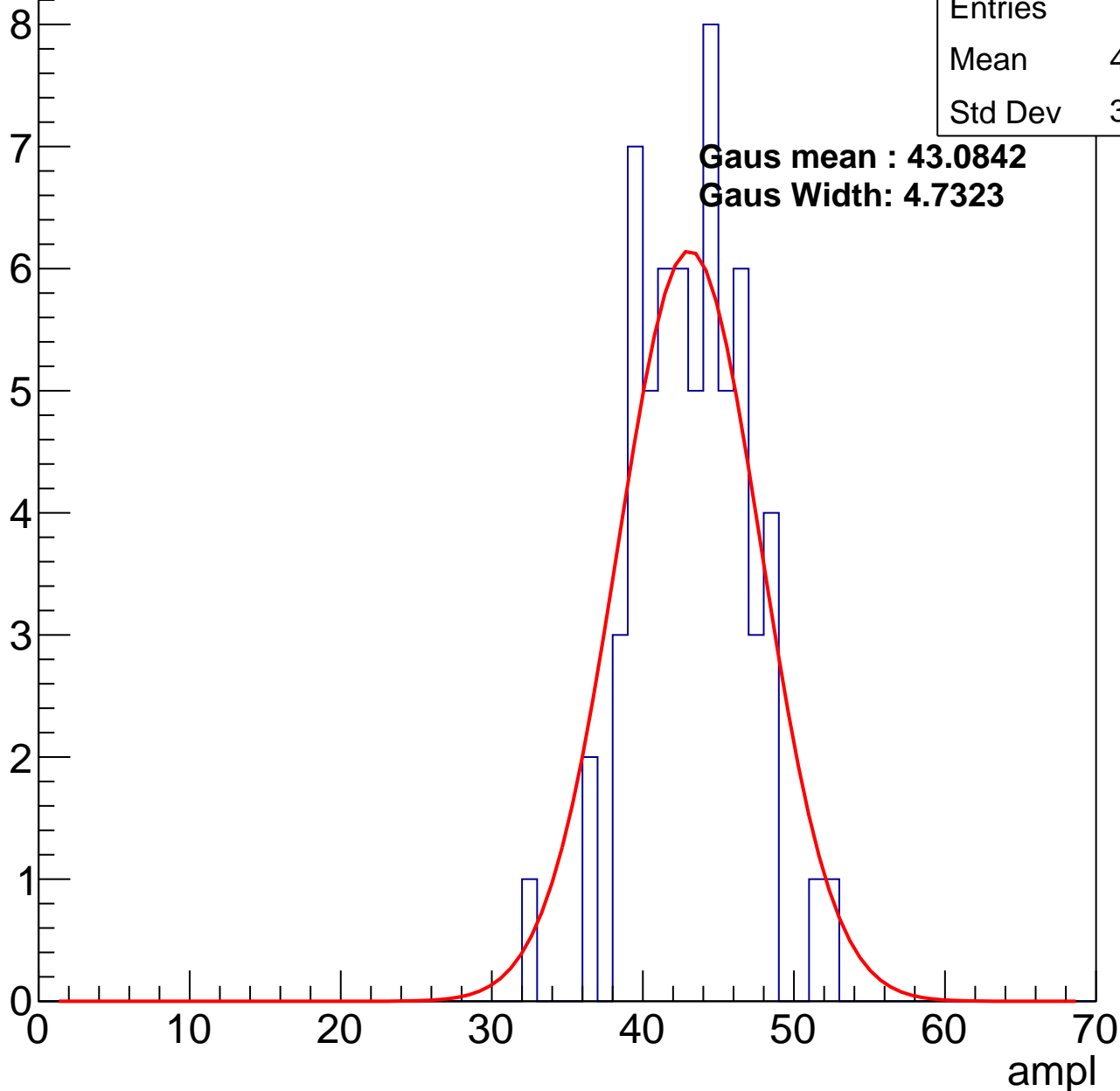
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 42.75 |
| Std Dev | 3.677 |

Gaus mean : 43.0842

Gaus Width: 4.7323

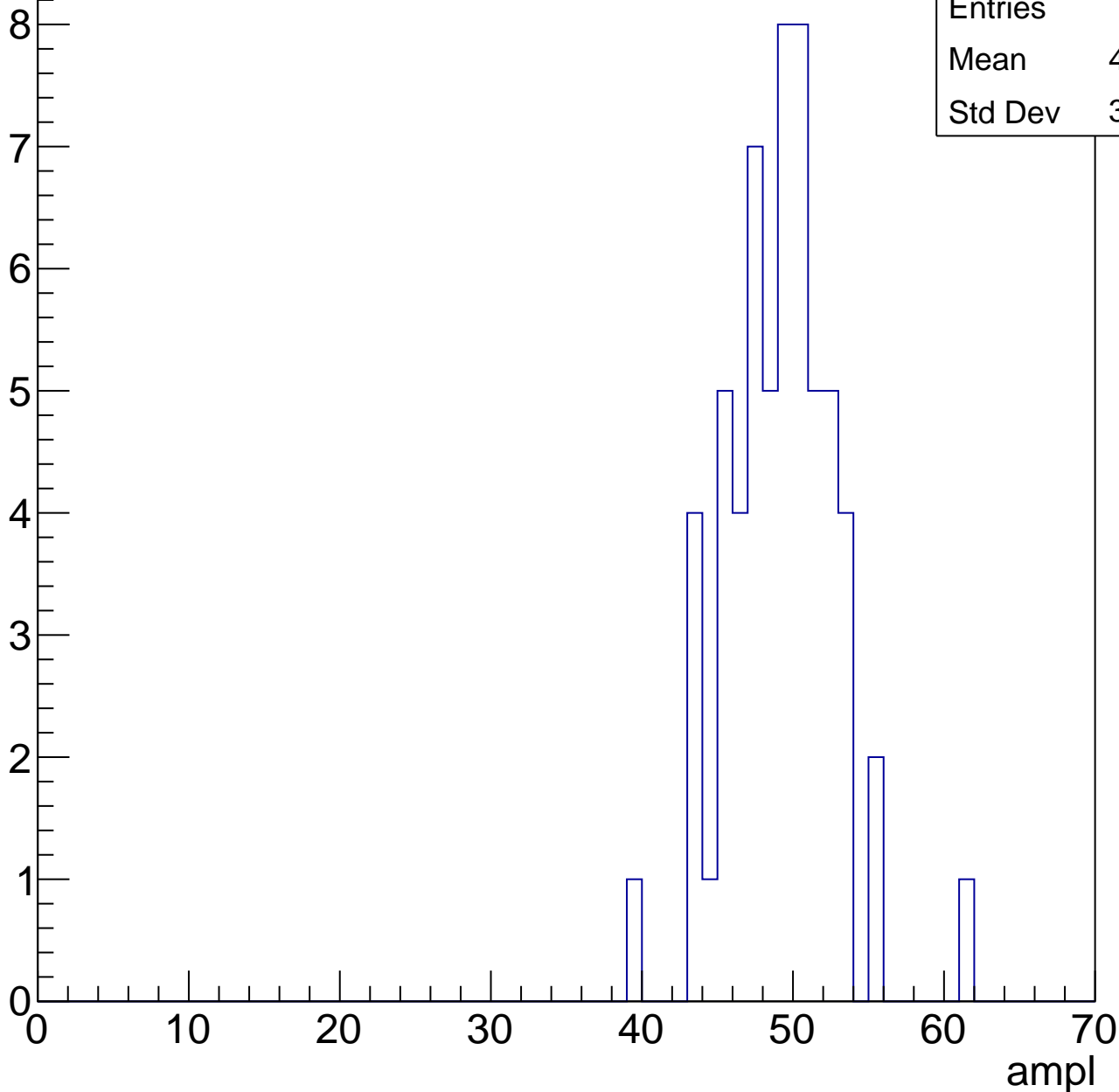


B1L102S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 48.72 |
| Std Dev | 3.573 |

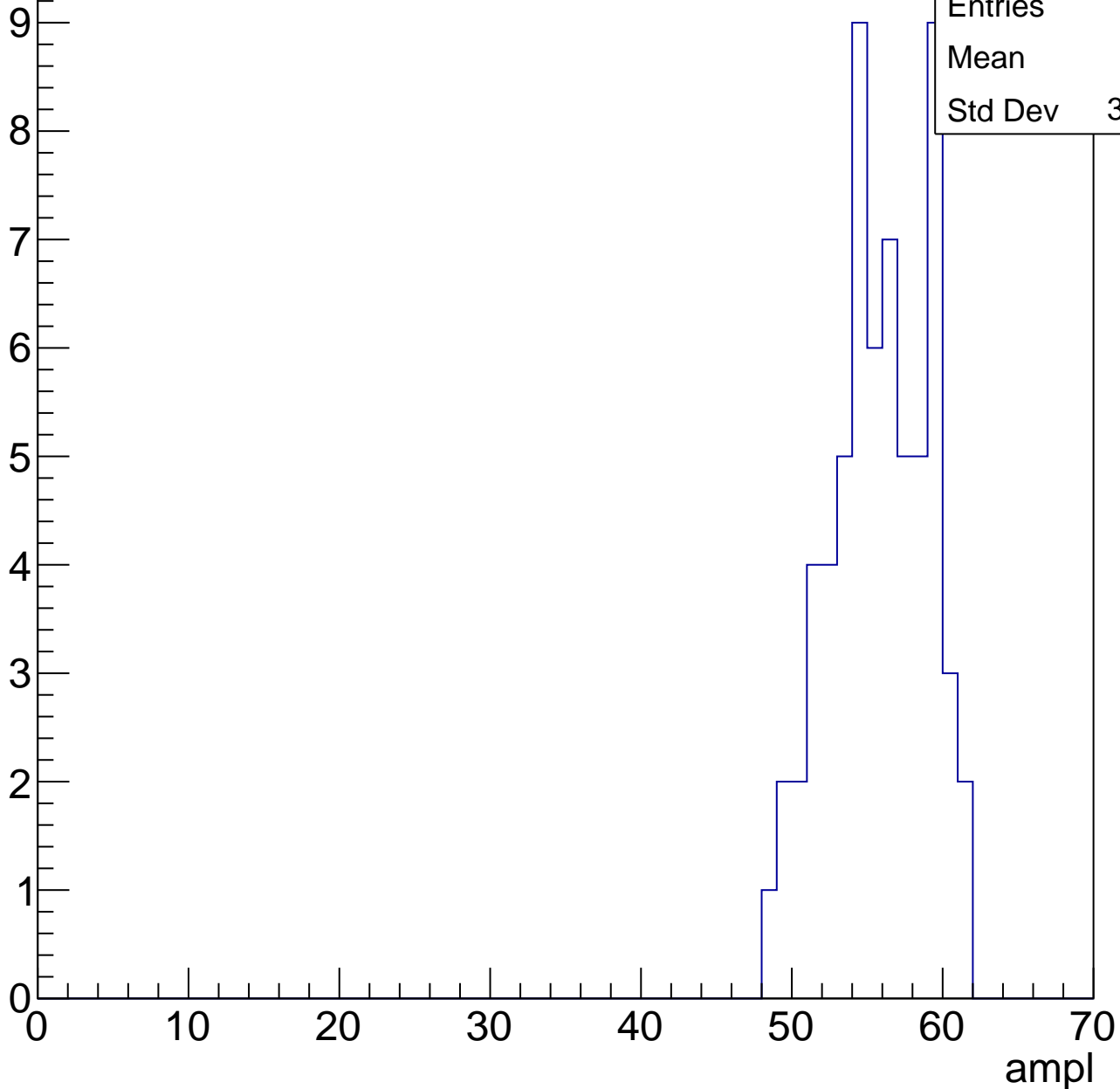


B1L102S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 55.3 |
| Std Dev | 3.205 |

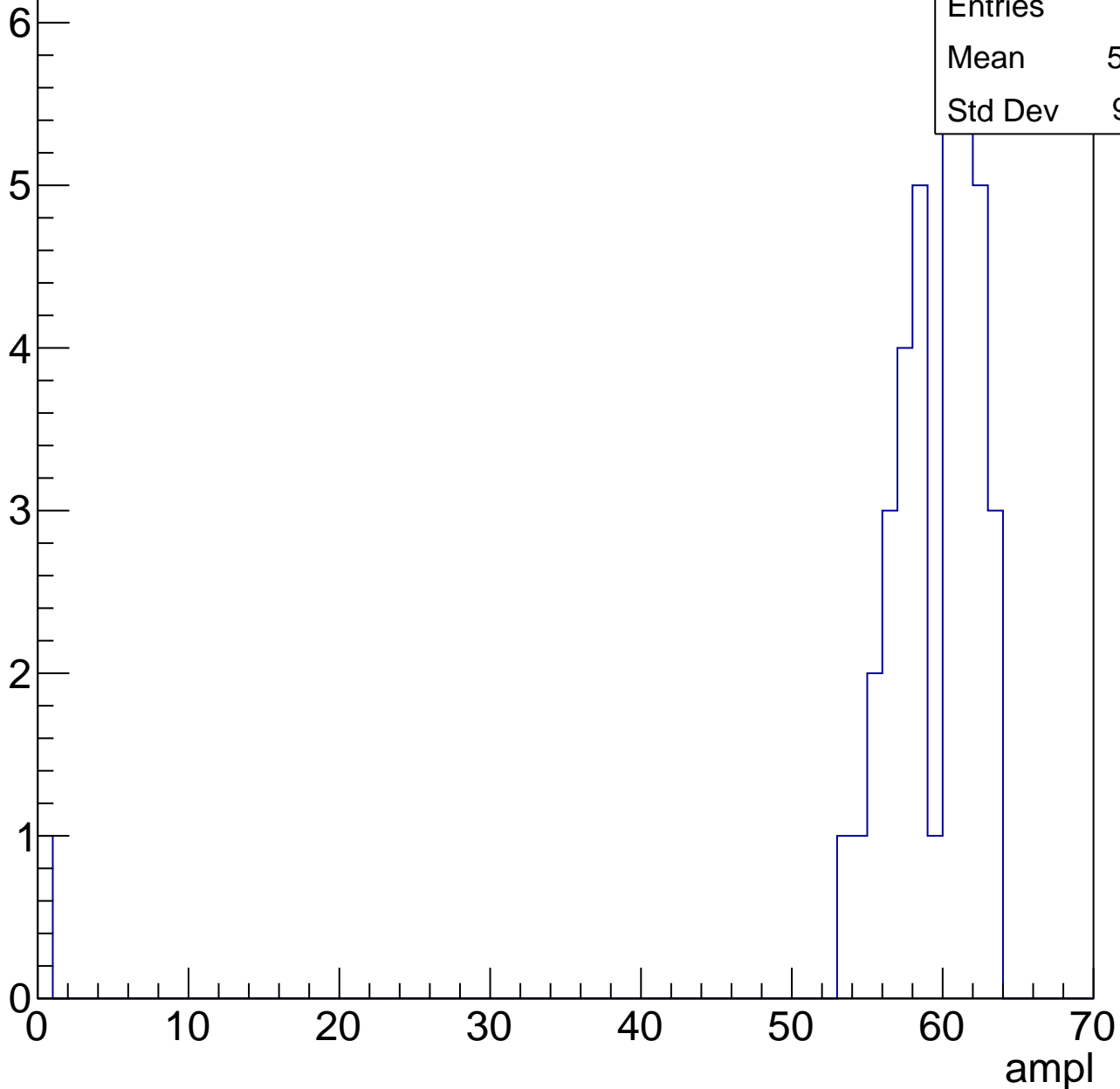


B1L102S, U15-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 38 |
| Mean | 57.55 |
| Std Dev | 9.821 |

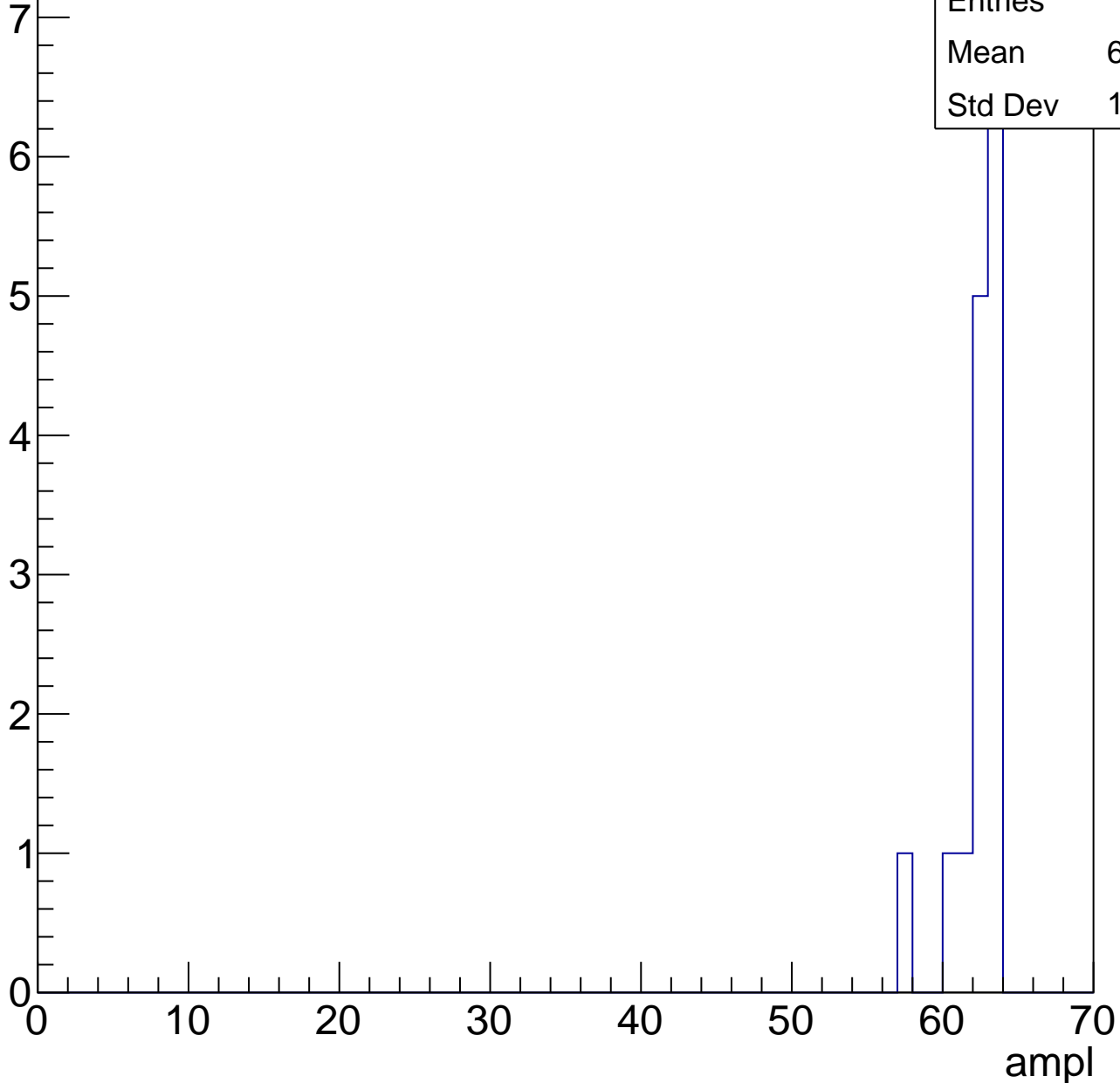


B1L102S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

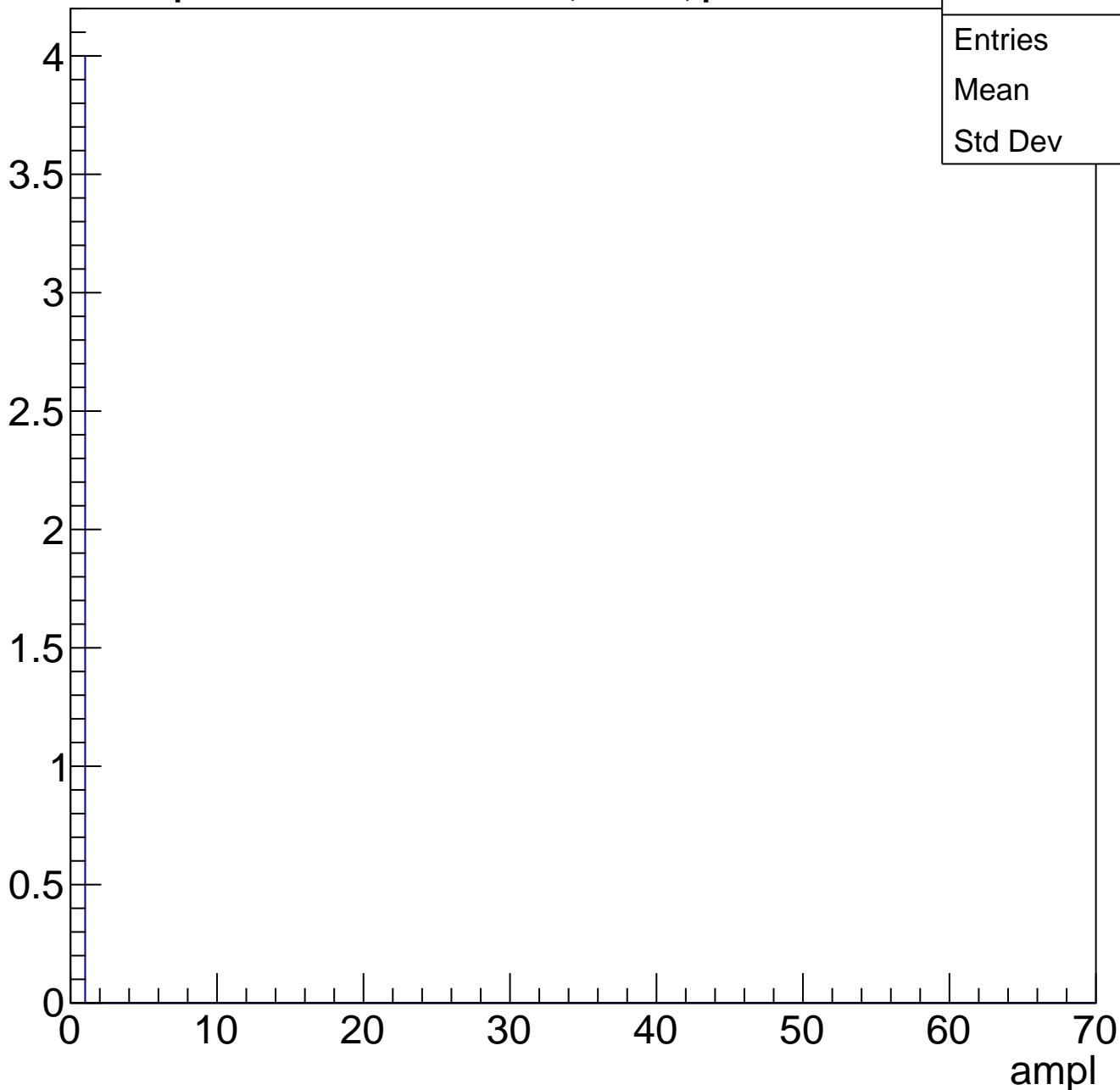
| | |
|---------|-------|
| Entries | 15 |
| Mean | 61.93 |
| Std Dev | 1.569 |



B1L102S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 4 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch94, adc0

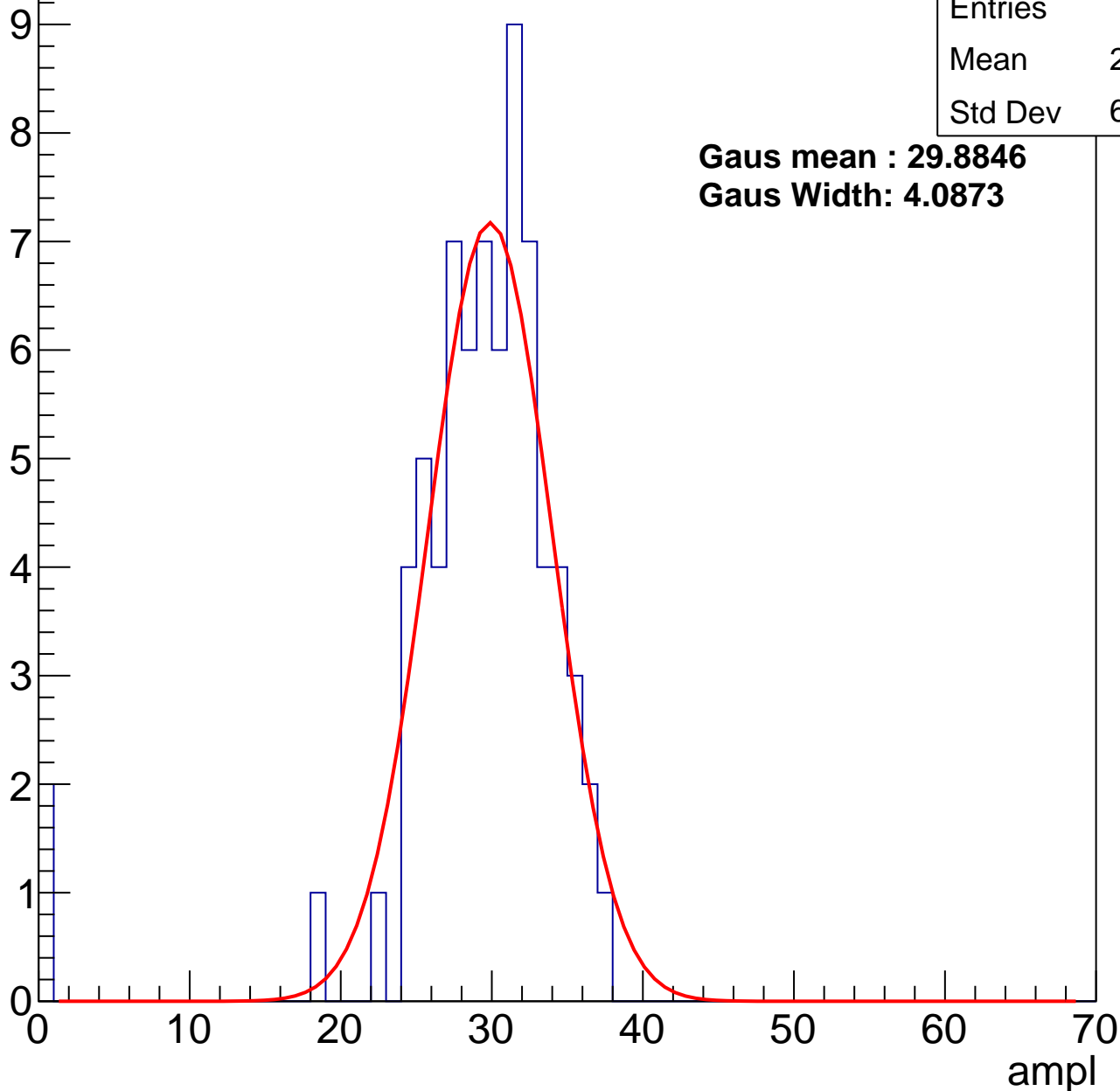
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 28.63 |
| Std Dev | 6.003 |

Gaus mean : 29.8846

Gaus Width: 4.0873



B1L102S, U15-ch94, adc1

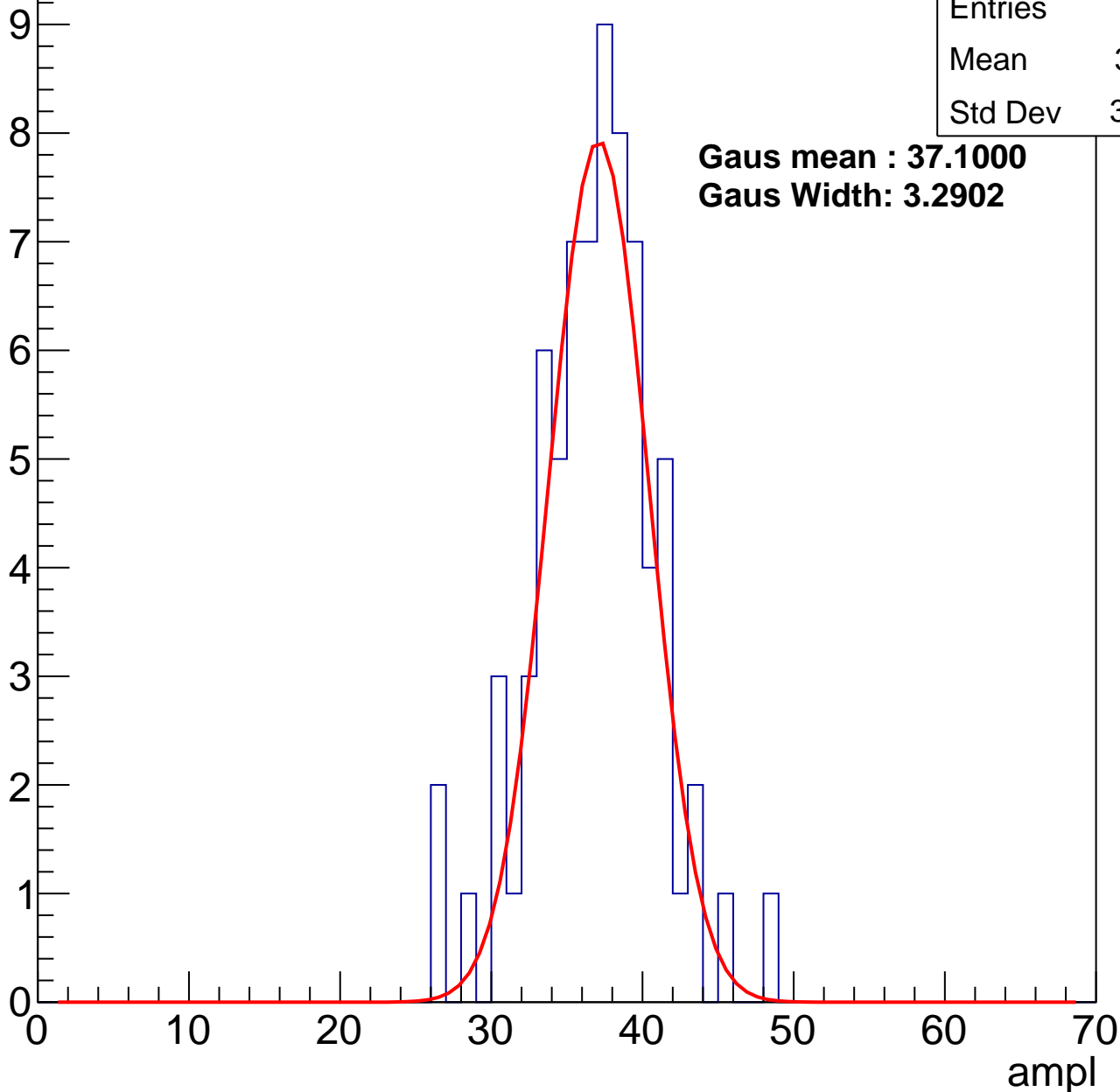
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 36.41 |
| Std Dev | 3.996 |

Gaus mean : 37.1000

Gaus Width: 3.2902



B1L102S, U15-ch94, adc2

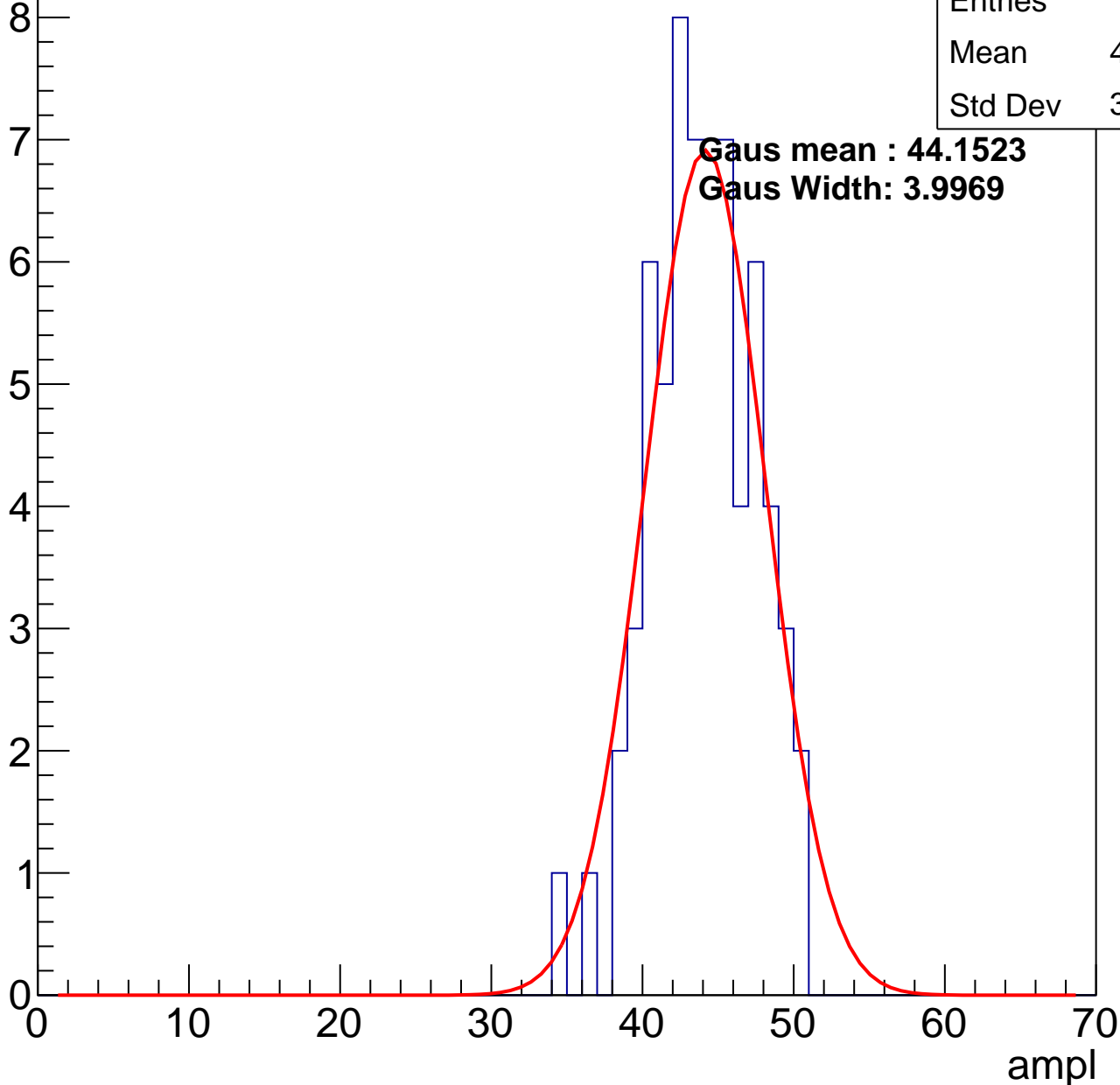
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 43.53 |
| Std Dev | 3.403 |

Gaus mean : 44.1523

Gaus Width: 3.9969

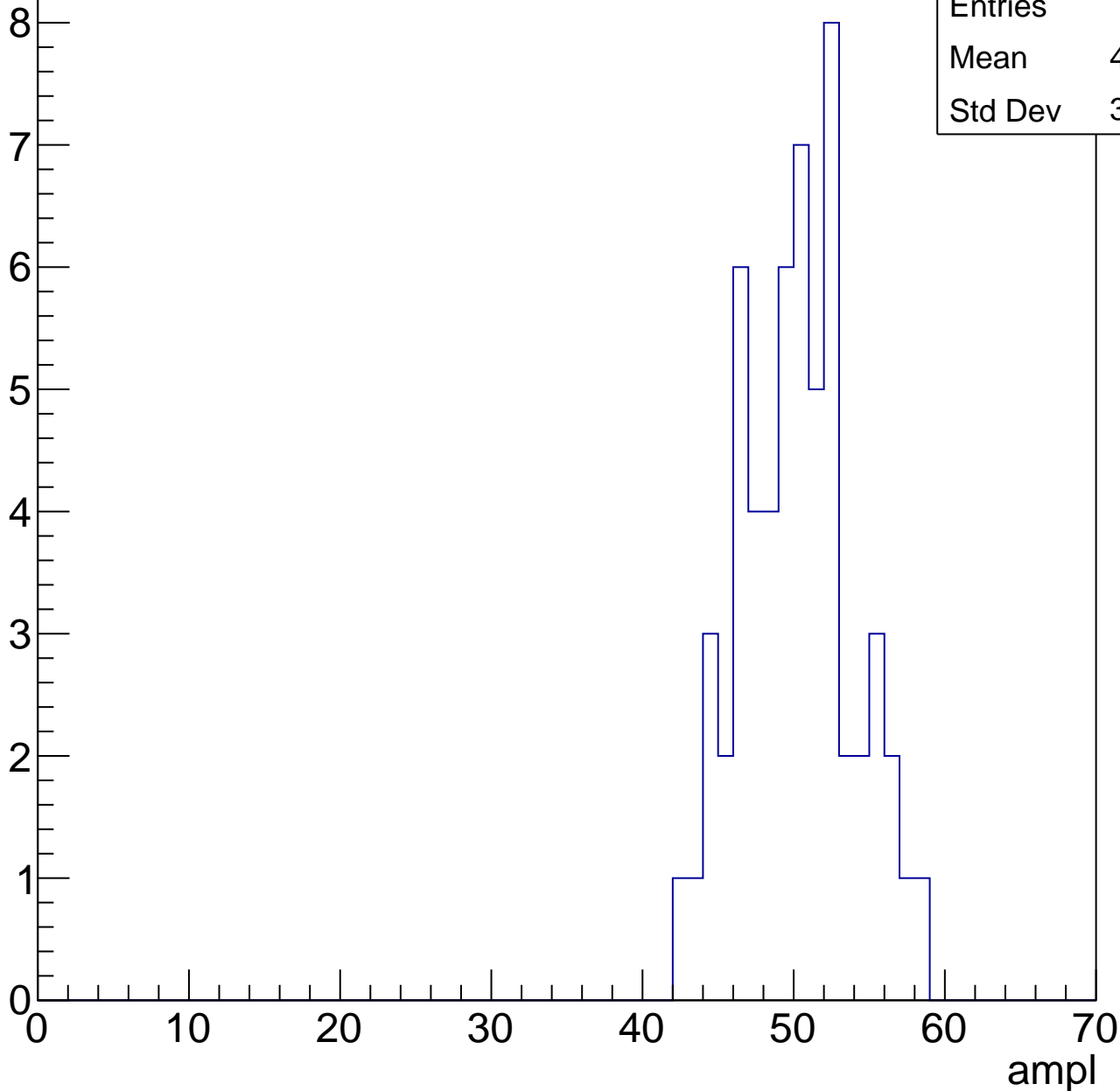


B1L102S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 49.72 |
| Std Dev | 3.638 |

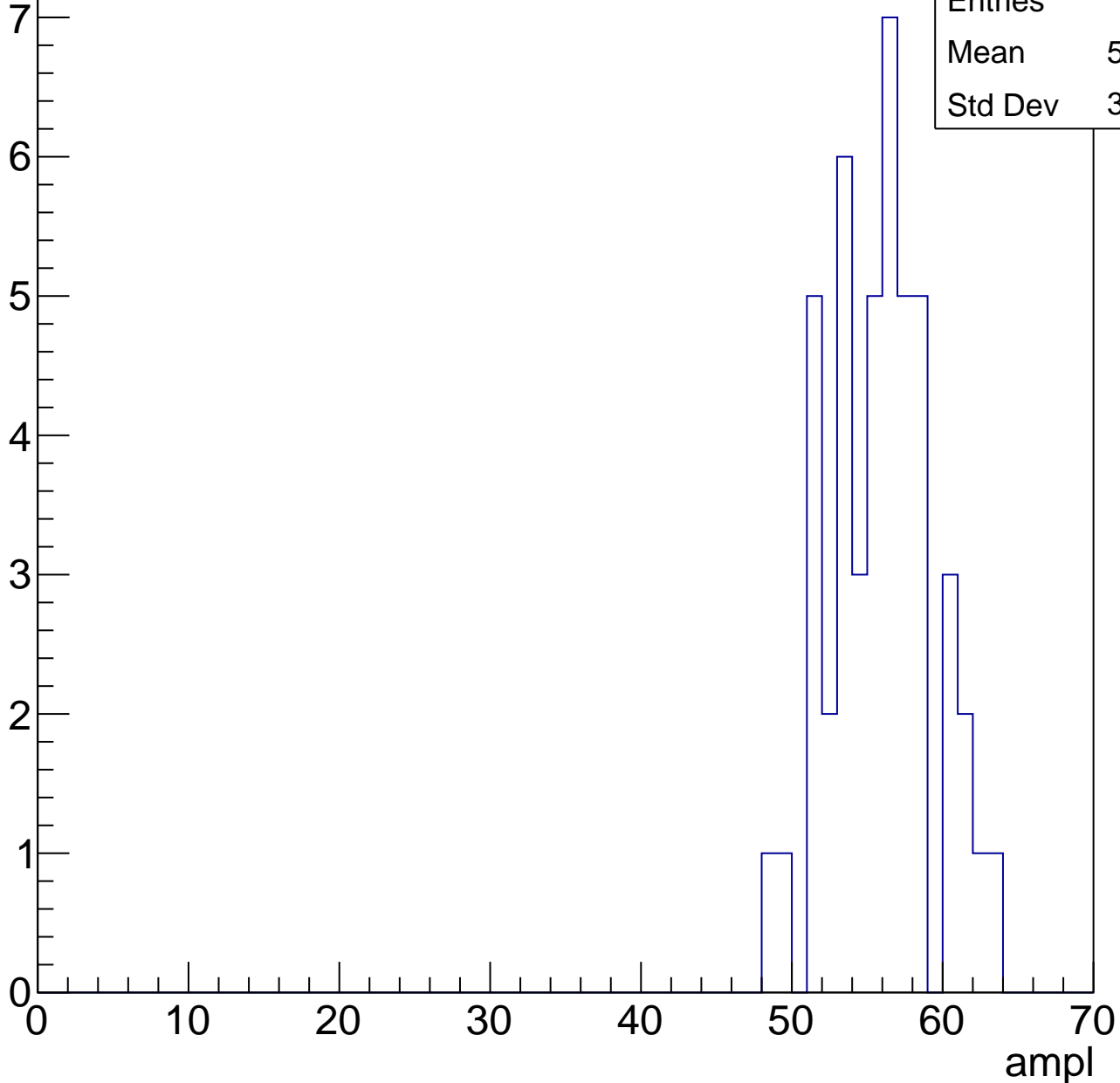


B1L102S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 55.43 |
| Std Dev | 3.369 |

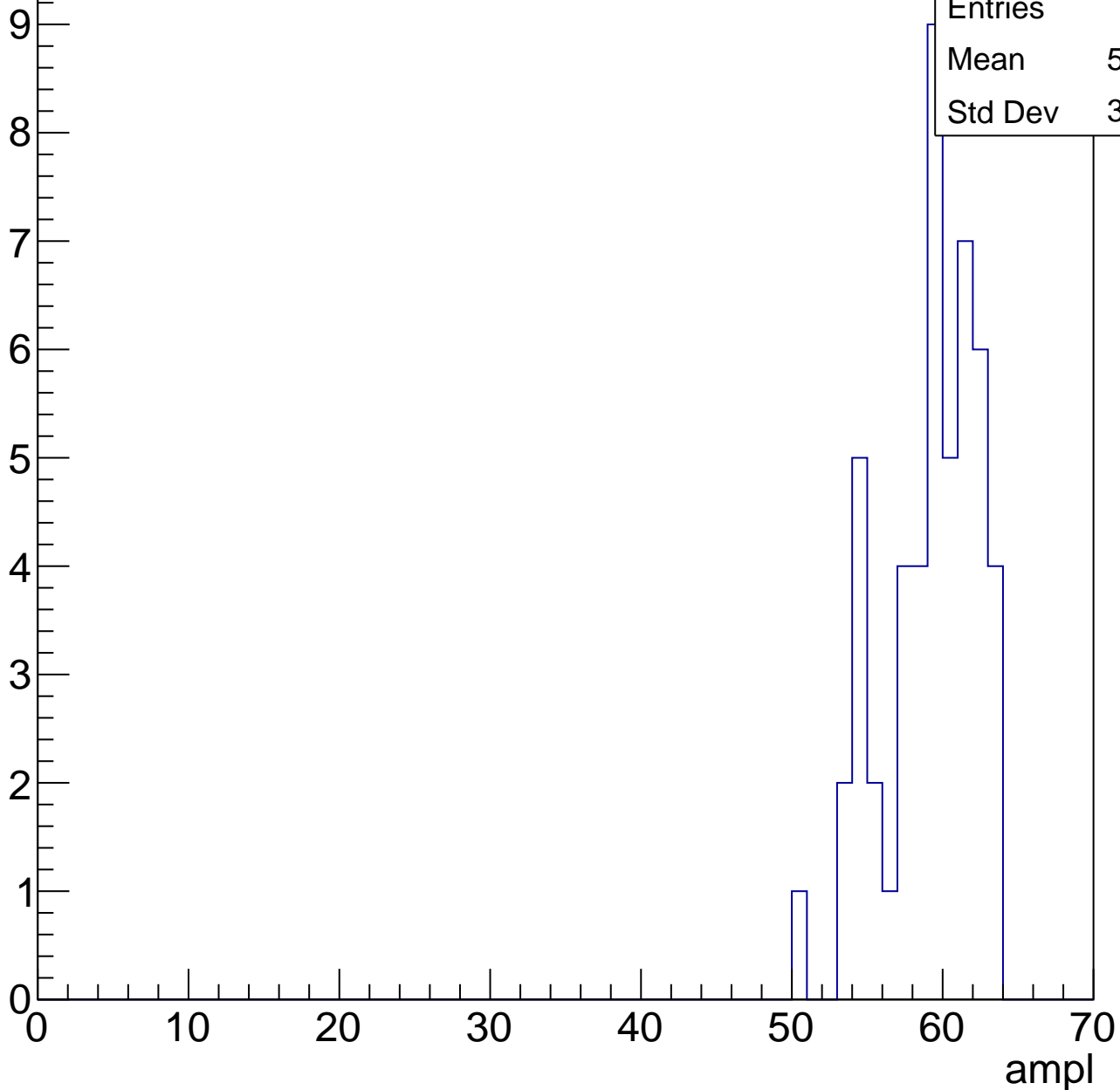


B1L102S, U15-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.68 |
| Std Dev | 3.114 |

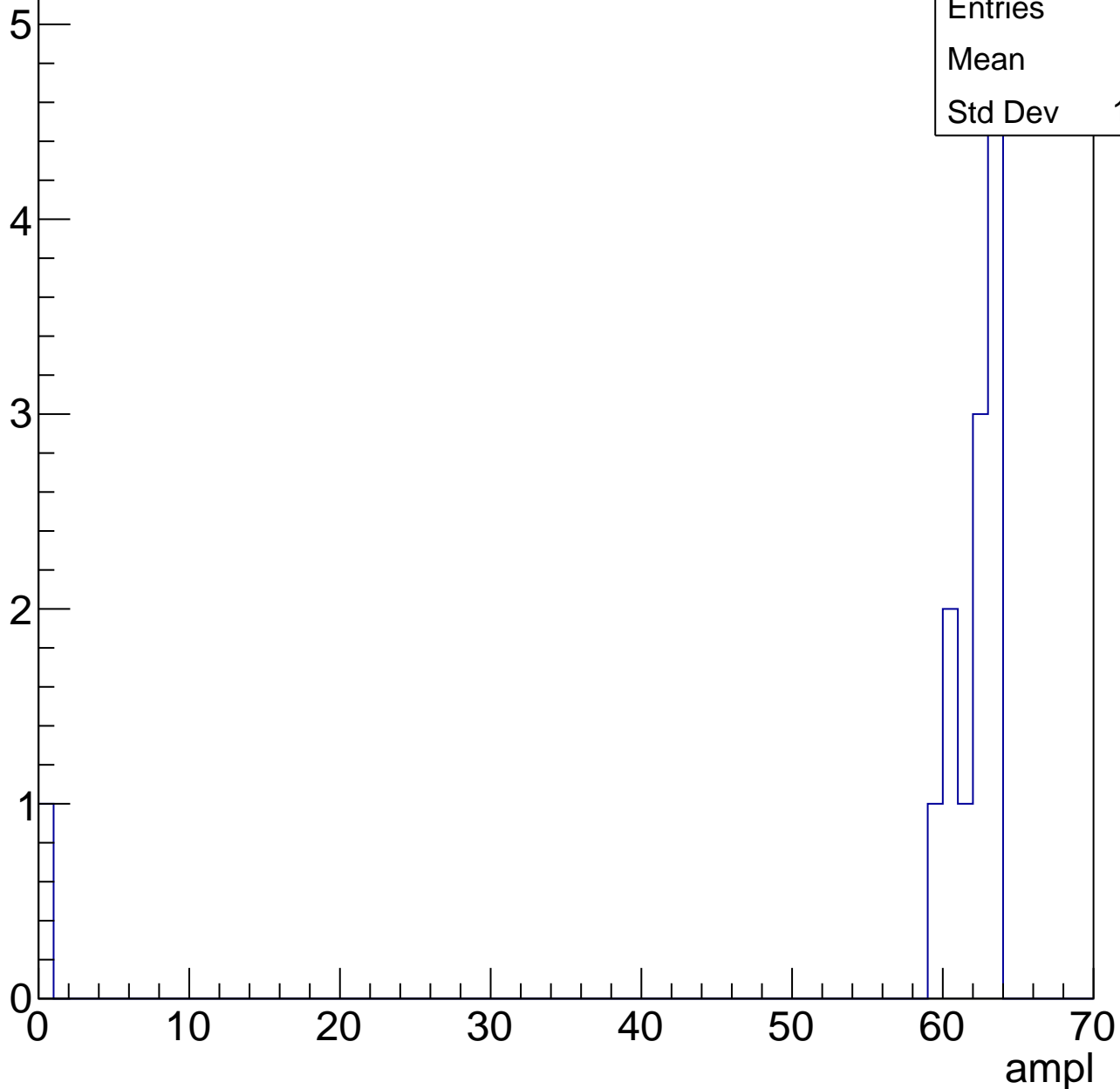


B1L102S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 57 |
| Std Dev | 16.51 |



B1L102S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch95, adc0

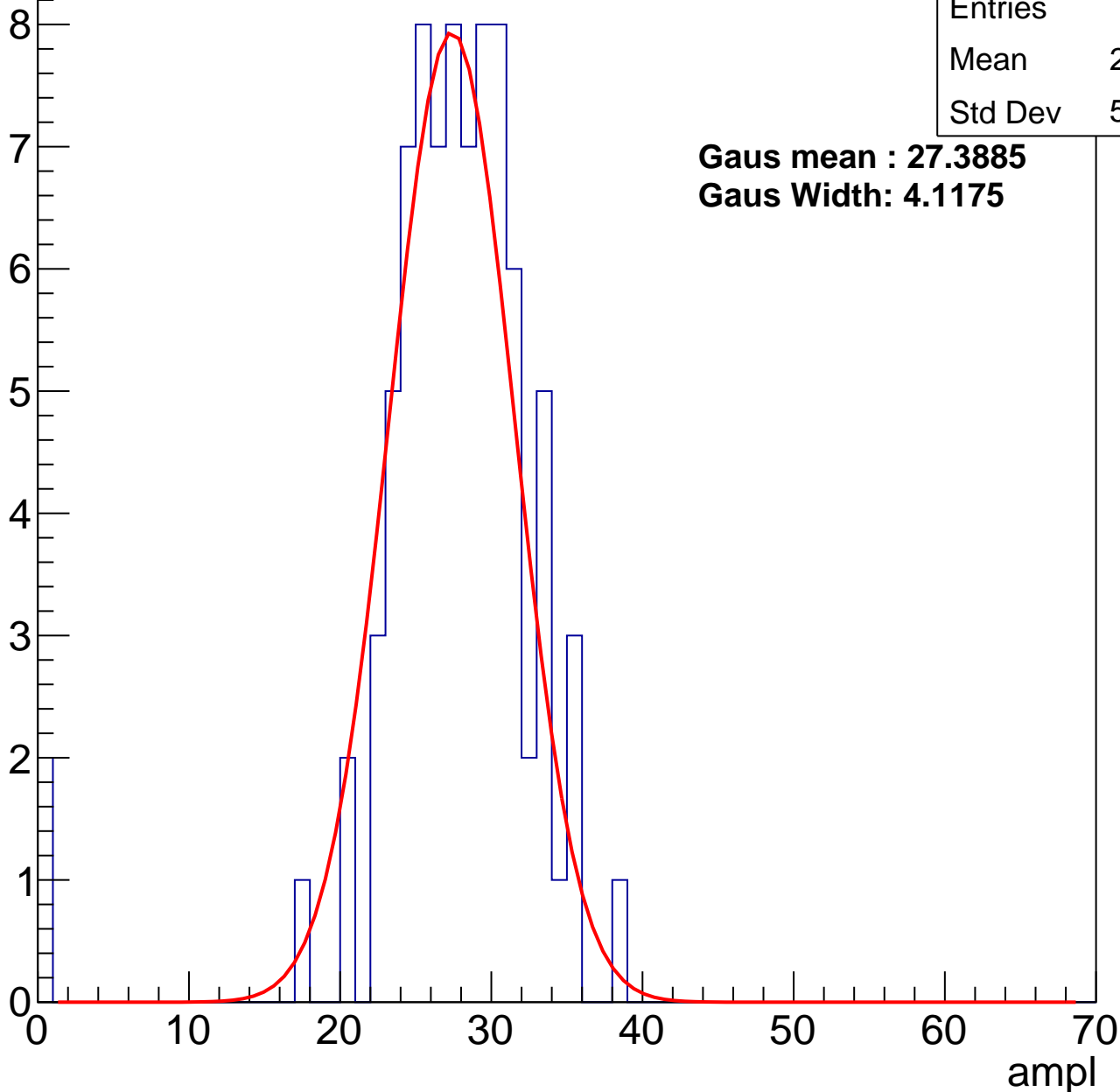
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 84 |
| Mean | 26.95 |
| Std Dev | 5.682 |

Gaus mean : 27.3885

Gaus Width: 4.1175



B1L102S, U15-ch95, adc1

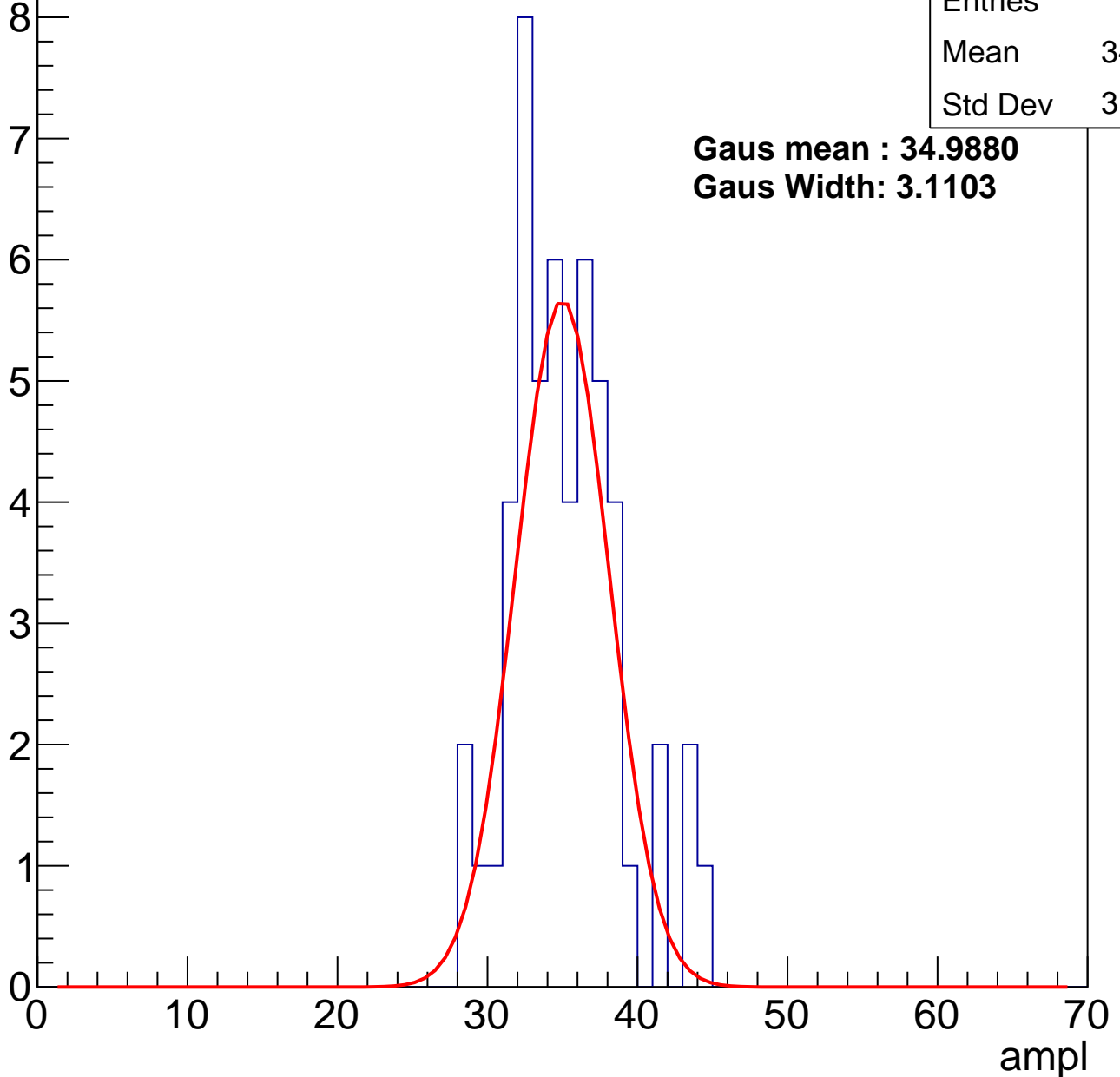
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 34.77 |
| Std Dev | 3.609 |

Gaus mean : 34.9880

Gaus Width: 3.1103



B1L102S, U15-ch95, adc2

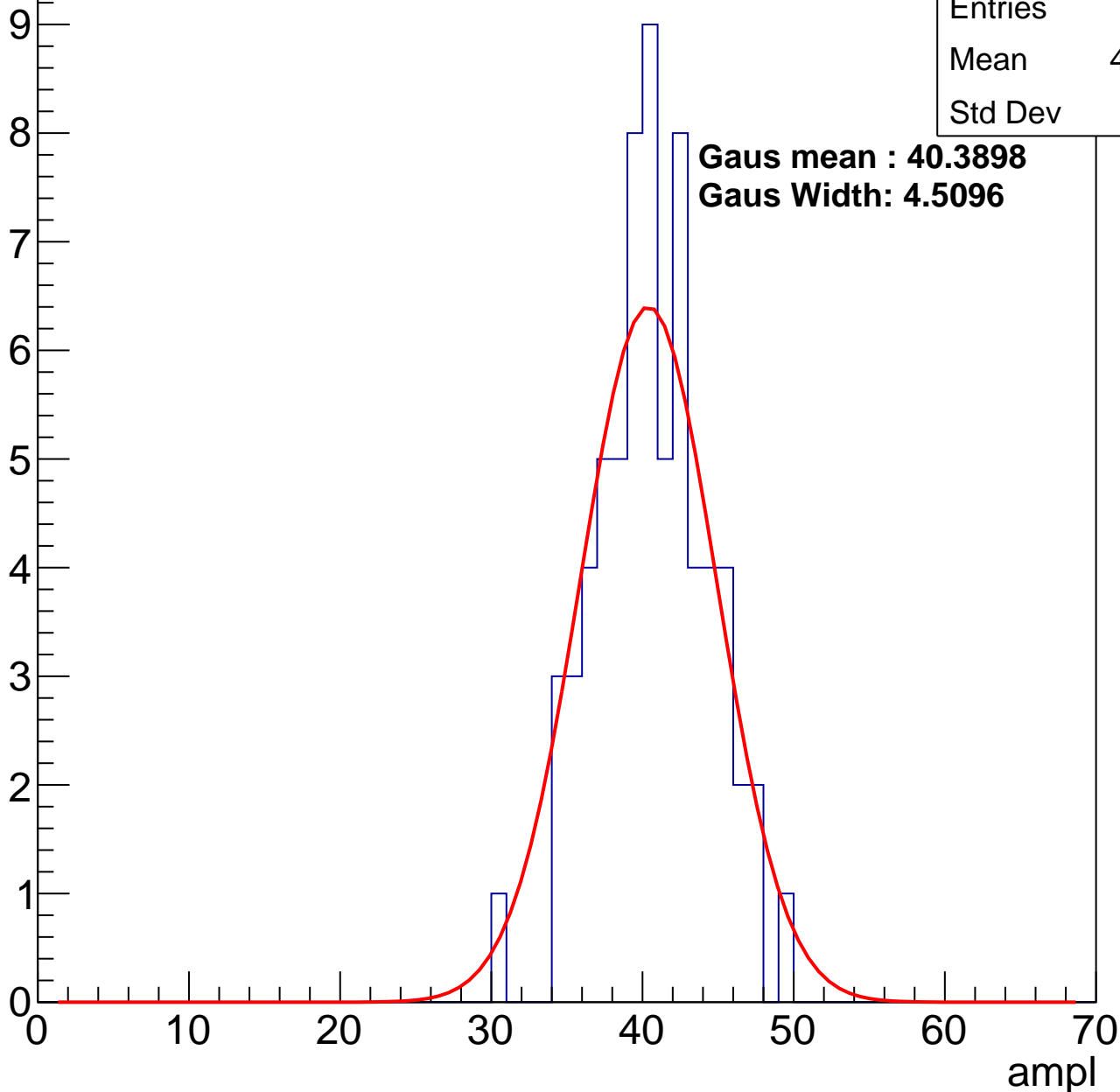
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 40.18 |
| Std Dev | 3.65 |

Gaus mean : 40.3898

Gaus Width: 4.5096

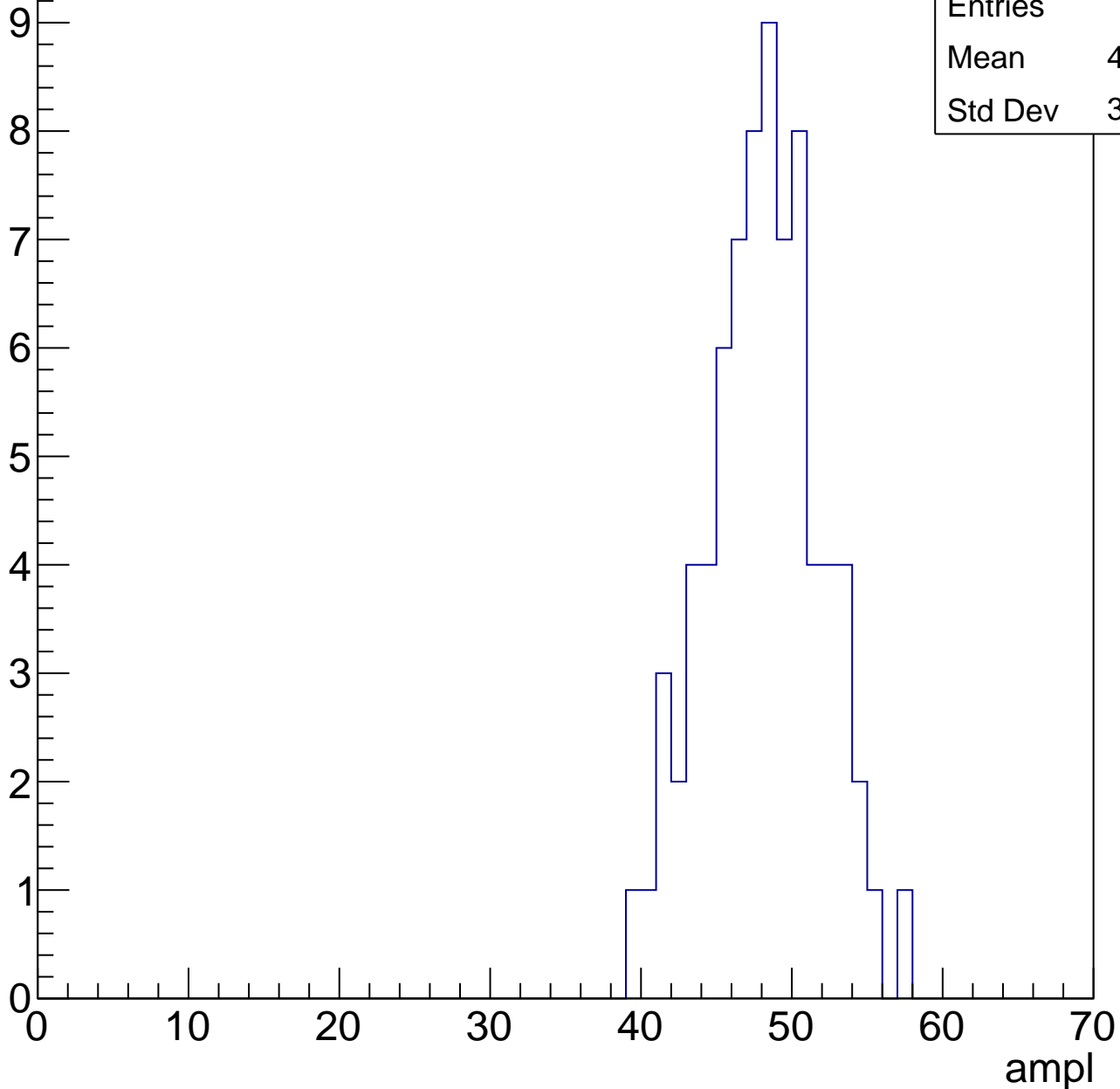


B1L102S, U15-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 47.64 |
| Std Dev | 3.716 |

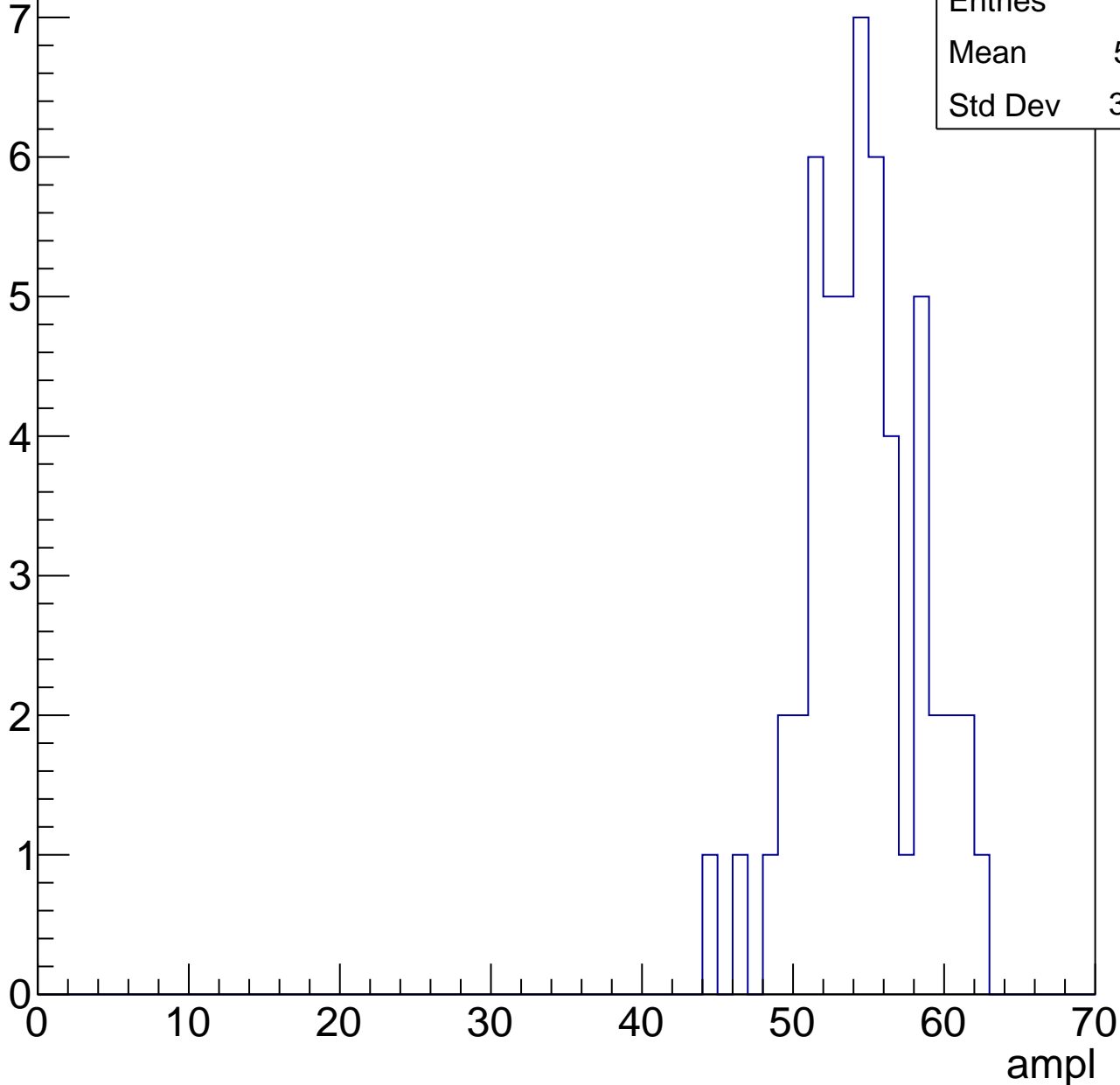


B1L102S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

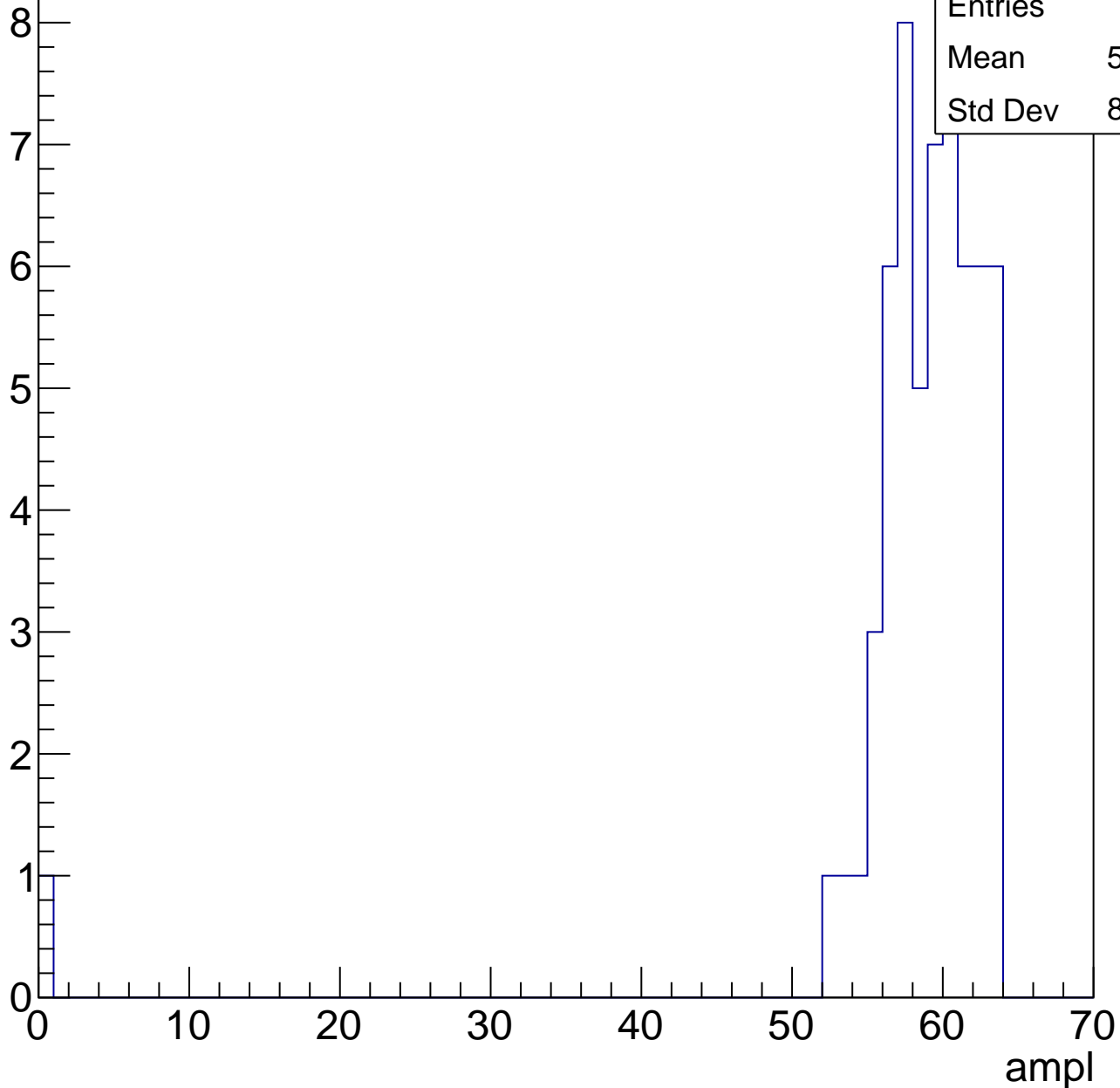
| | |
|---------|-------|
| Entries | 53 |
| Mean | 54.11 |
| Std Dev | 3.775 |



B1L102S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

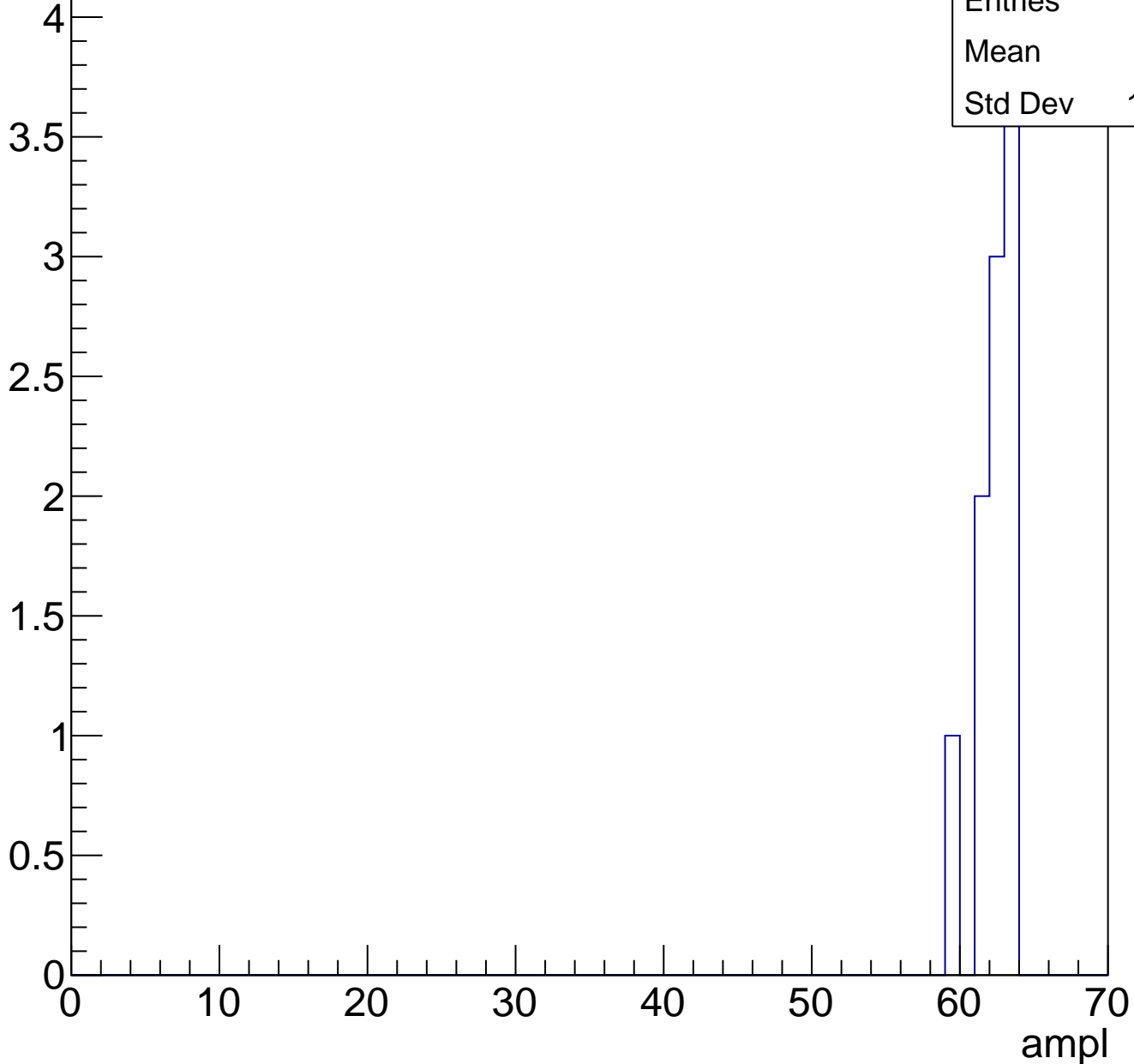
Entry



B1L102S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 10 |
| Mean | 61.9 |
| Std Dev | 1.221 |

B1L102S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch96, adc0

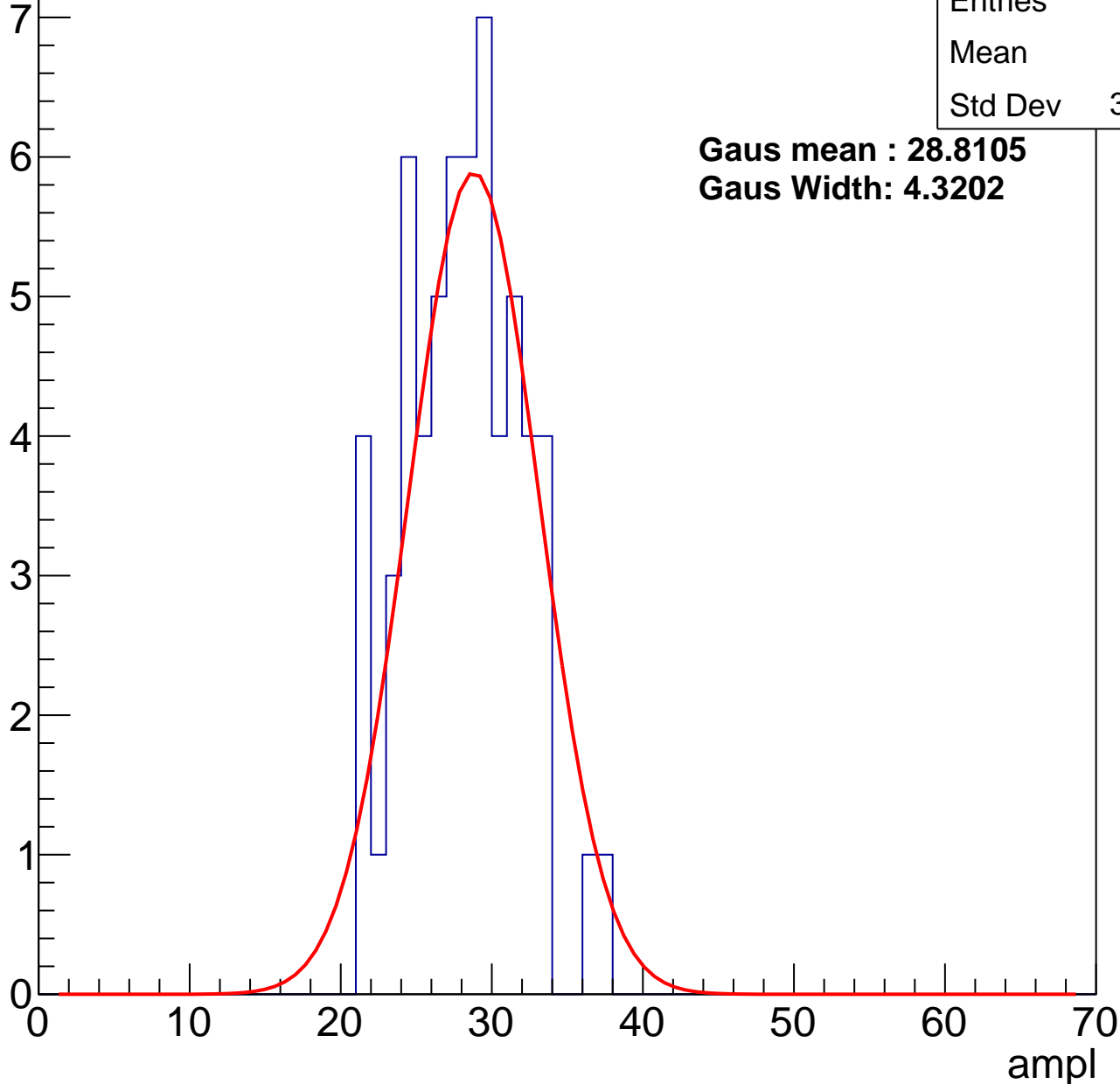
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 27.7 |
| Std Dev | 3.704 |

Gaus mean : 28.8105

Gaus Width: 4.3202



B1L102S, U15-ch96, adc1

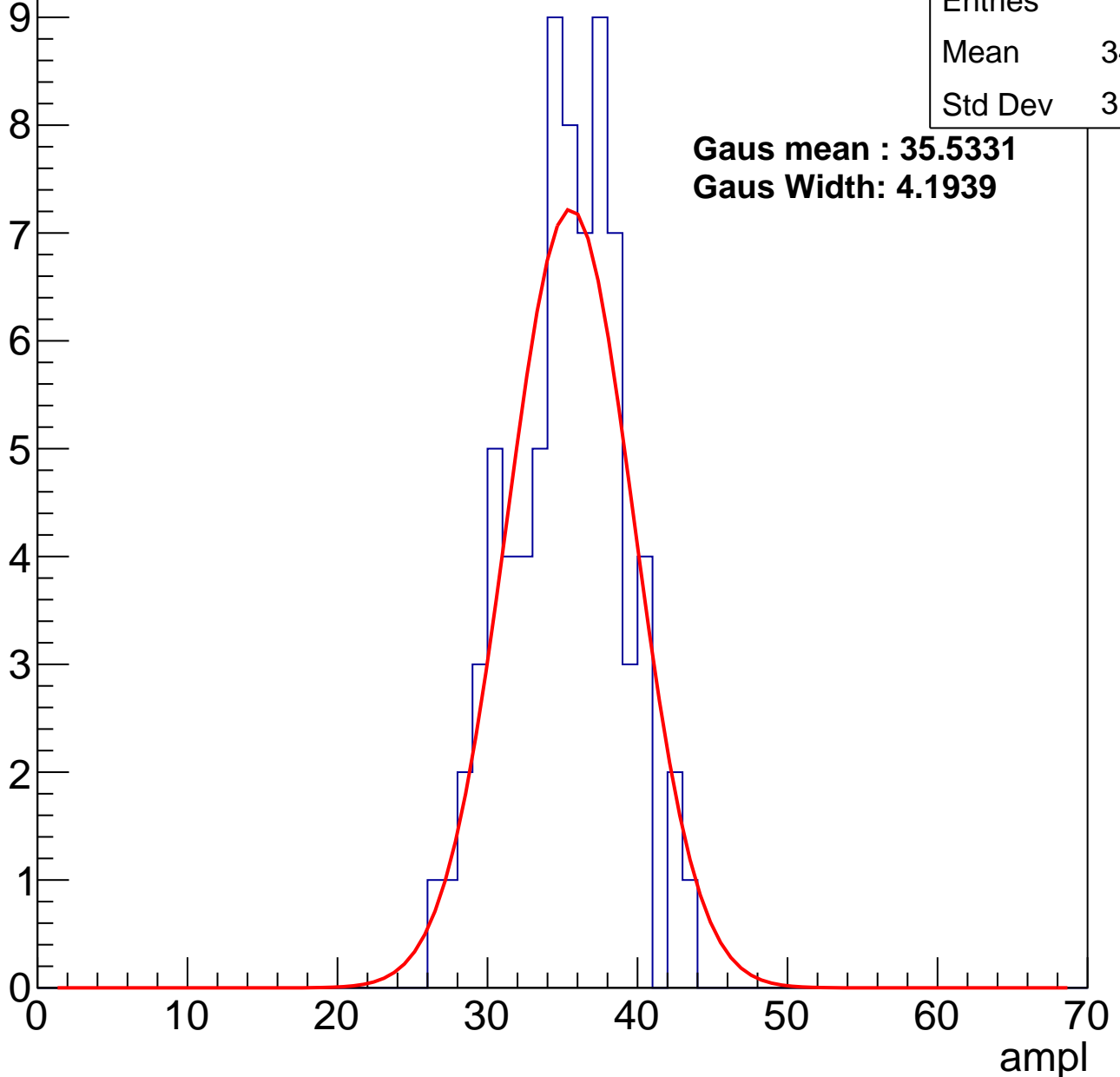
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 34.72 |
| Std Dev | 3.679 |

Gaus mean : 35.5331

Gaus Width: 4.1939



B1L102S, U15-ch96, adc2

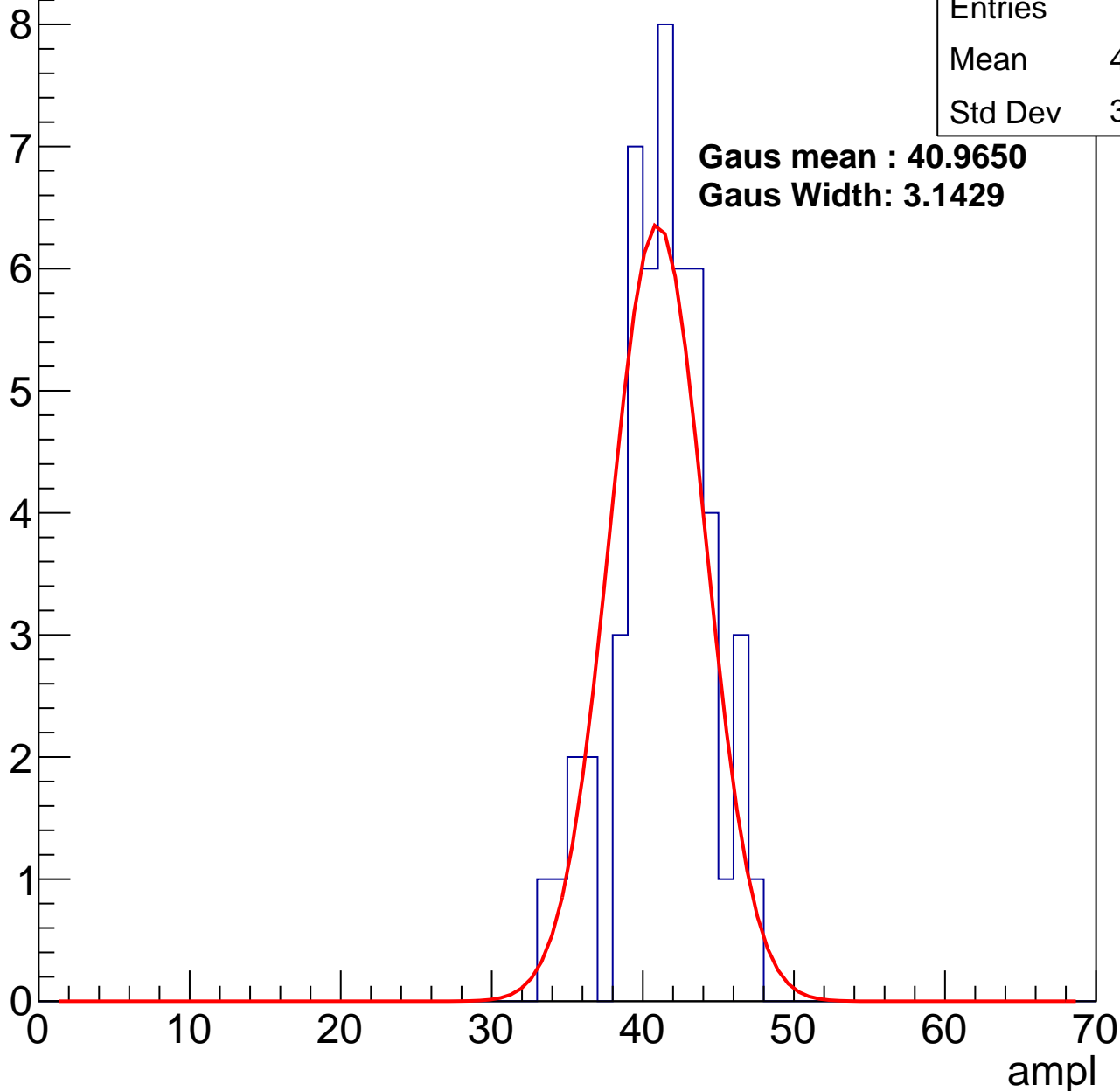
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 40.78 |
| Std Dev | 3.089 |

Gaus mean : 40.9650

Gaus Width: 3.1429



B1L102S, U15-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 47.67 |
| Std Dev | 3.531 |

Entry

10

8

6

4

2

0

0

10

20

30

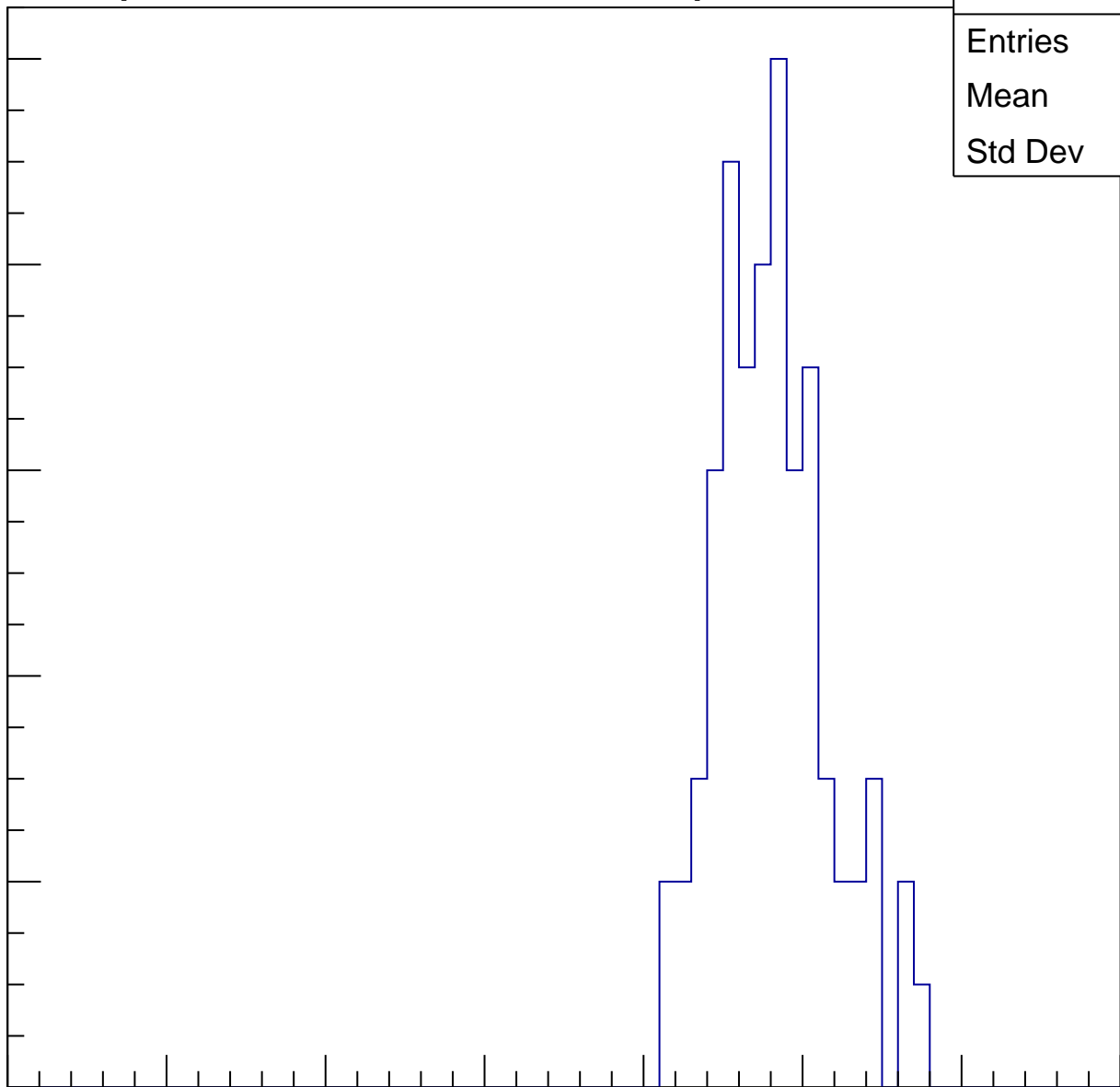
40

50

60

70

ampl

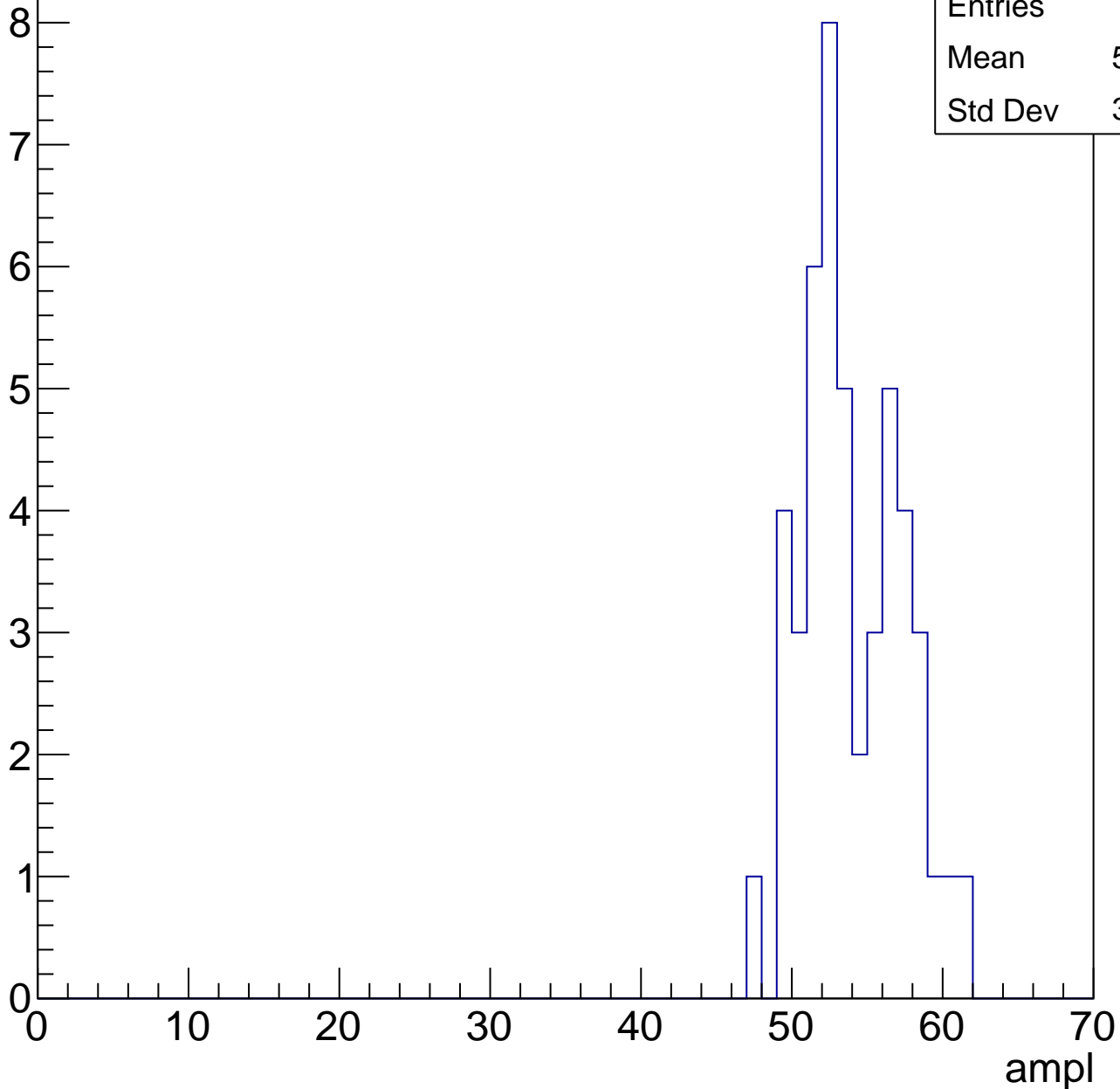


B1L102S, U15-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 53.51 |
| Std Dev | 3.241 |

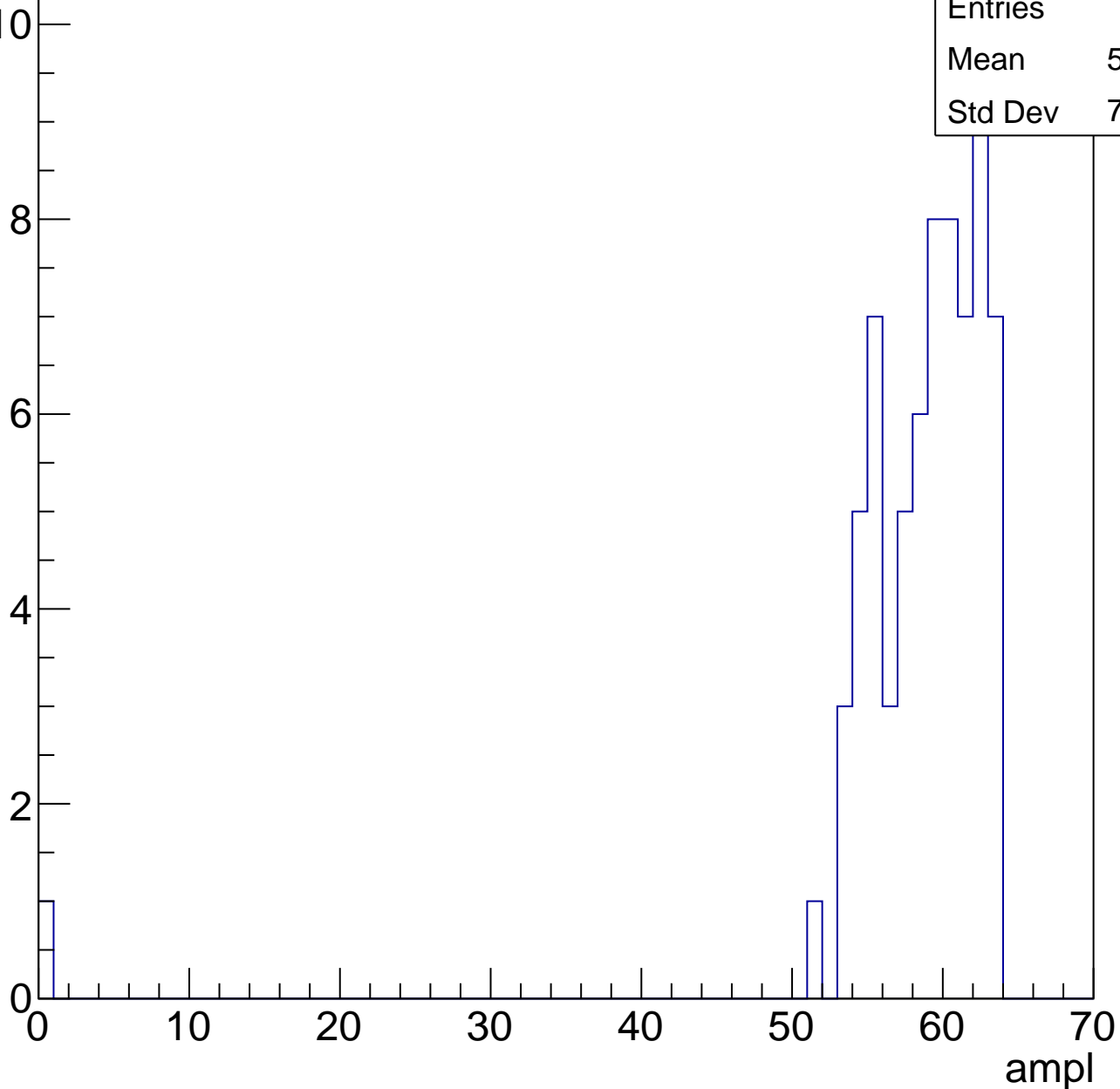


B1L102S, U15-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

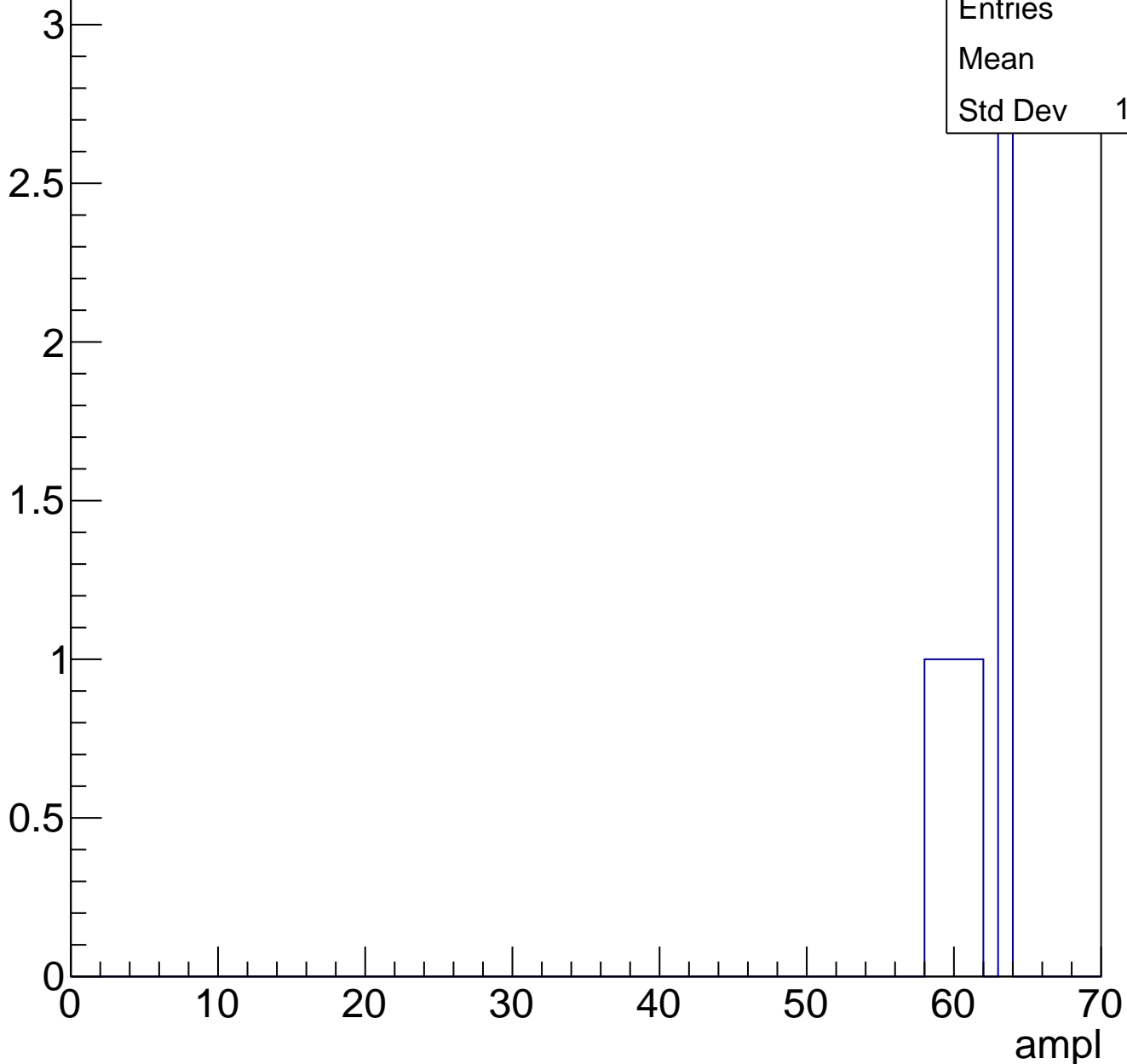
| | |
|---------|-------|
| Entries | 71 |
| Mean | 57.83 |
| Std Dev | 7.584 |



B1L102S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

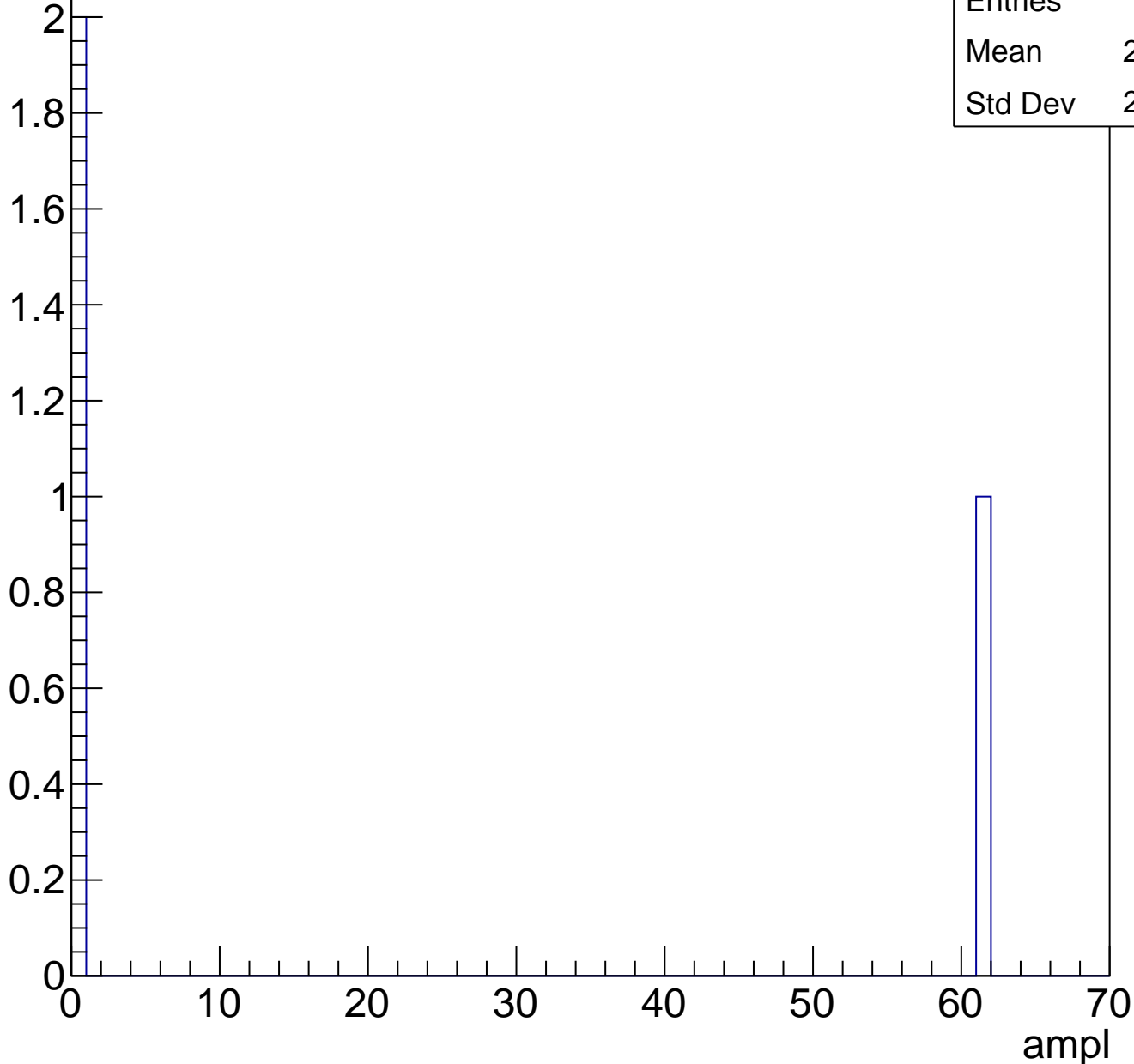
Entry



B1L102S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch97, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 67 |
| Mean | 25.76 |
| Std Dev | 7.935 |

Gaus mean : 28.3920

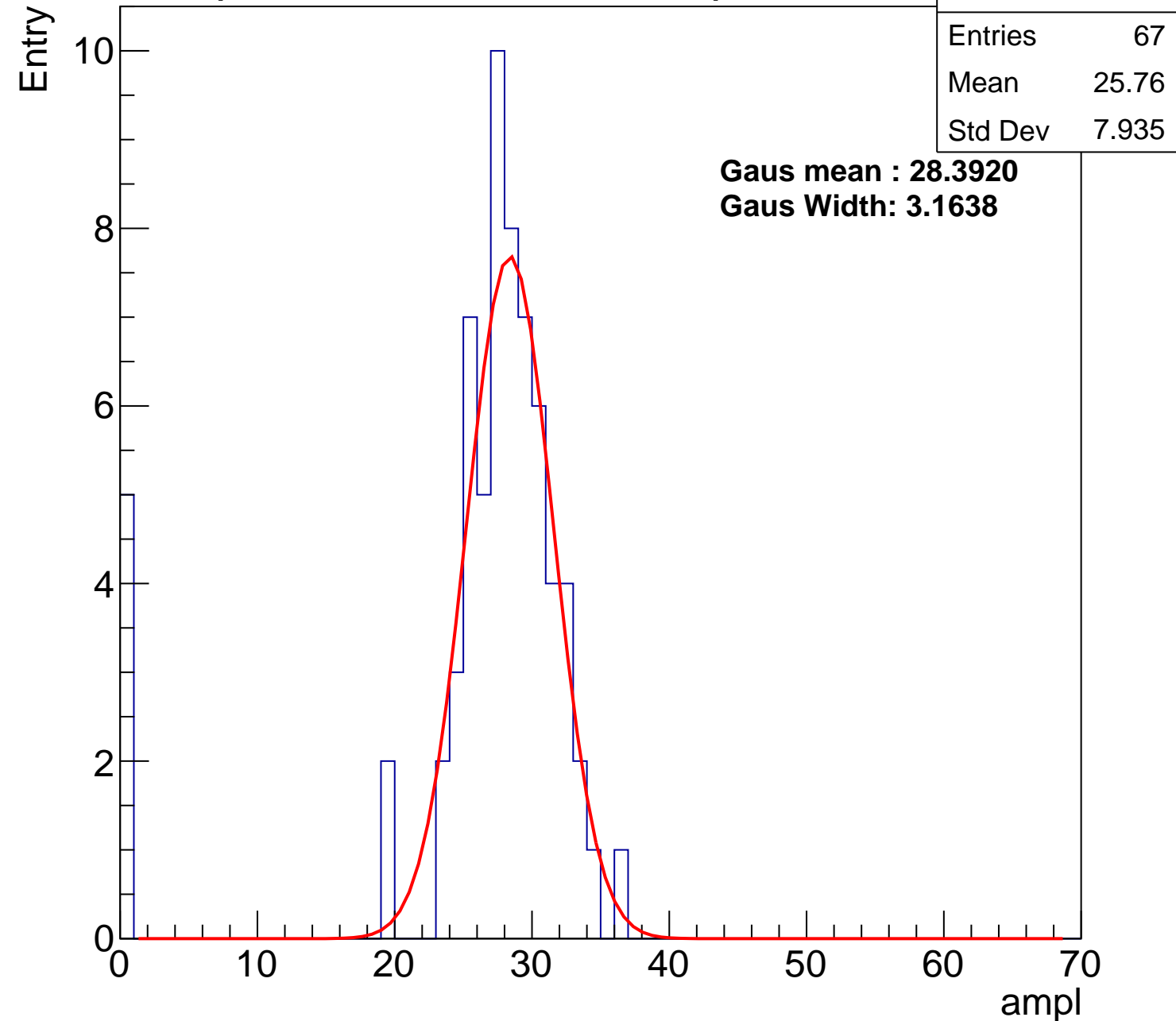
Gaus Width: 3.1638

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch97, adc1

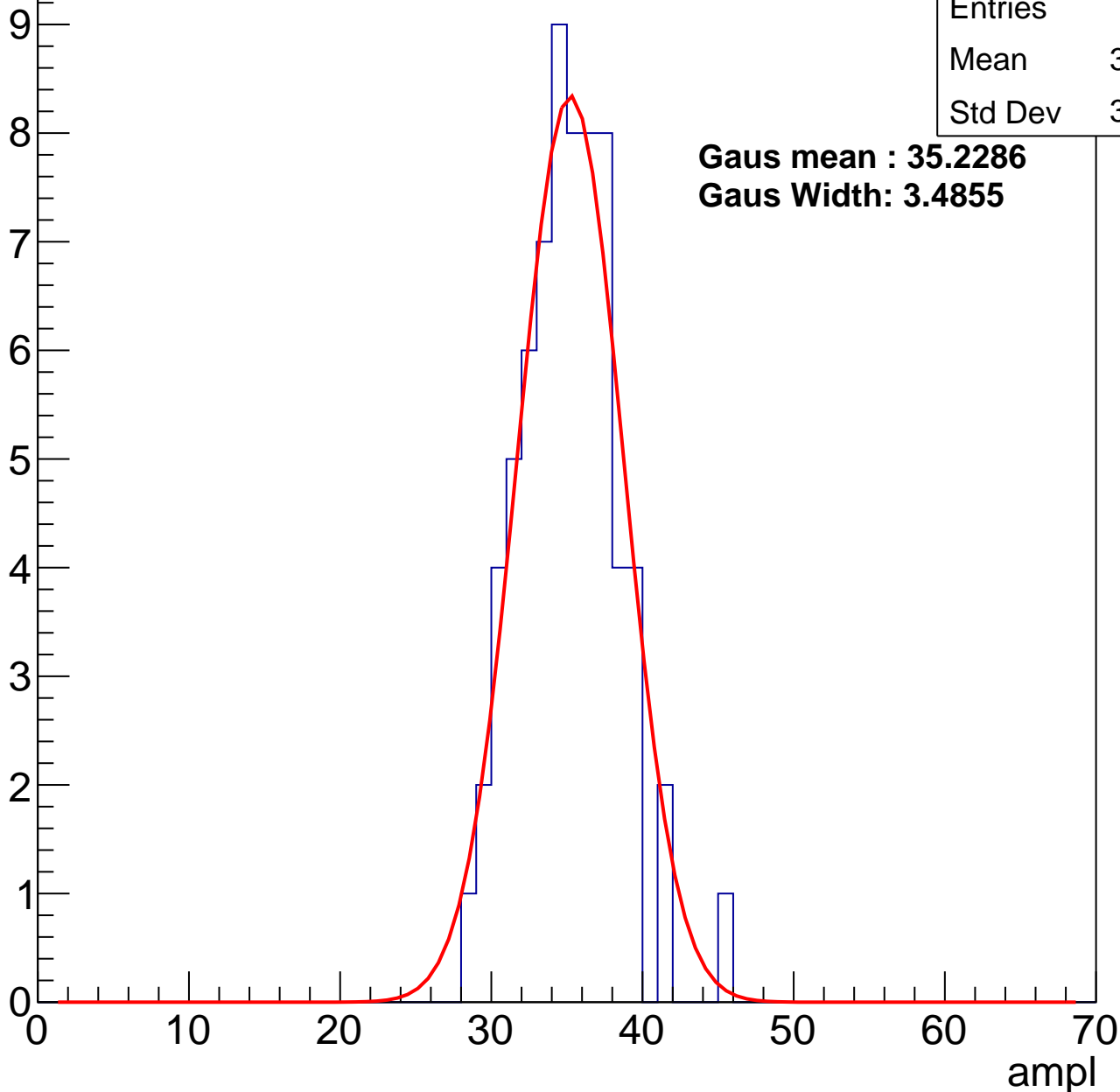
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 34.62 |
| Std Dev | 3.172 |

Gaus mean : 35.2286

Gaus Width: 3.4855



B1L102S, U15-ch97, adc2

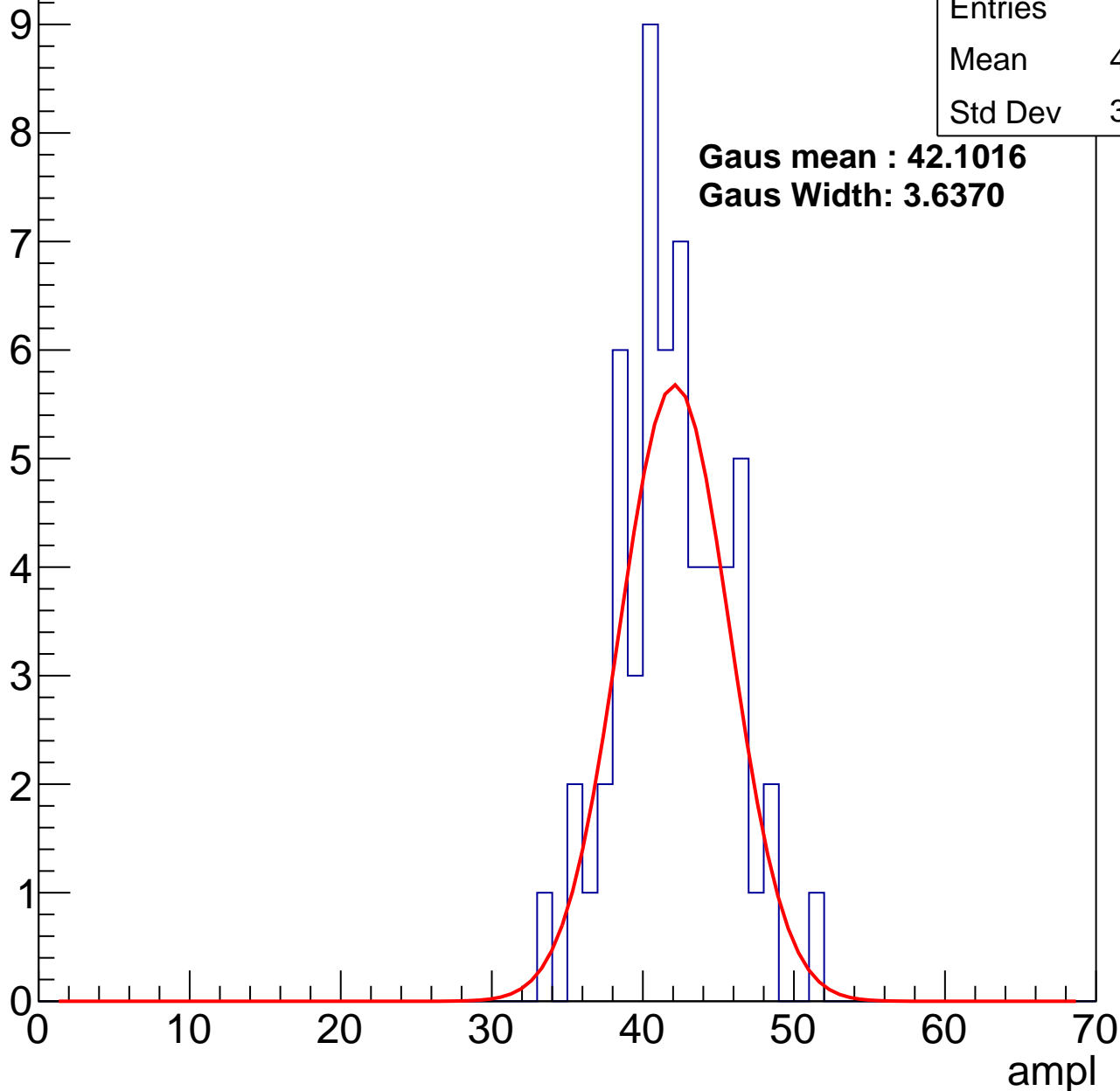
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 41.55 |
| Std Dev | 3.553 |

Gaus mean : 42.1016

Gaus Width: 3.6370

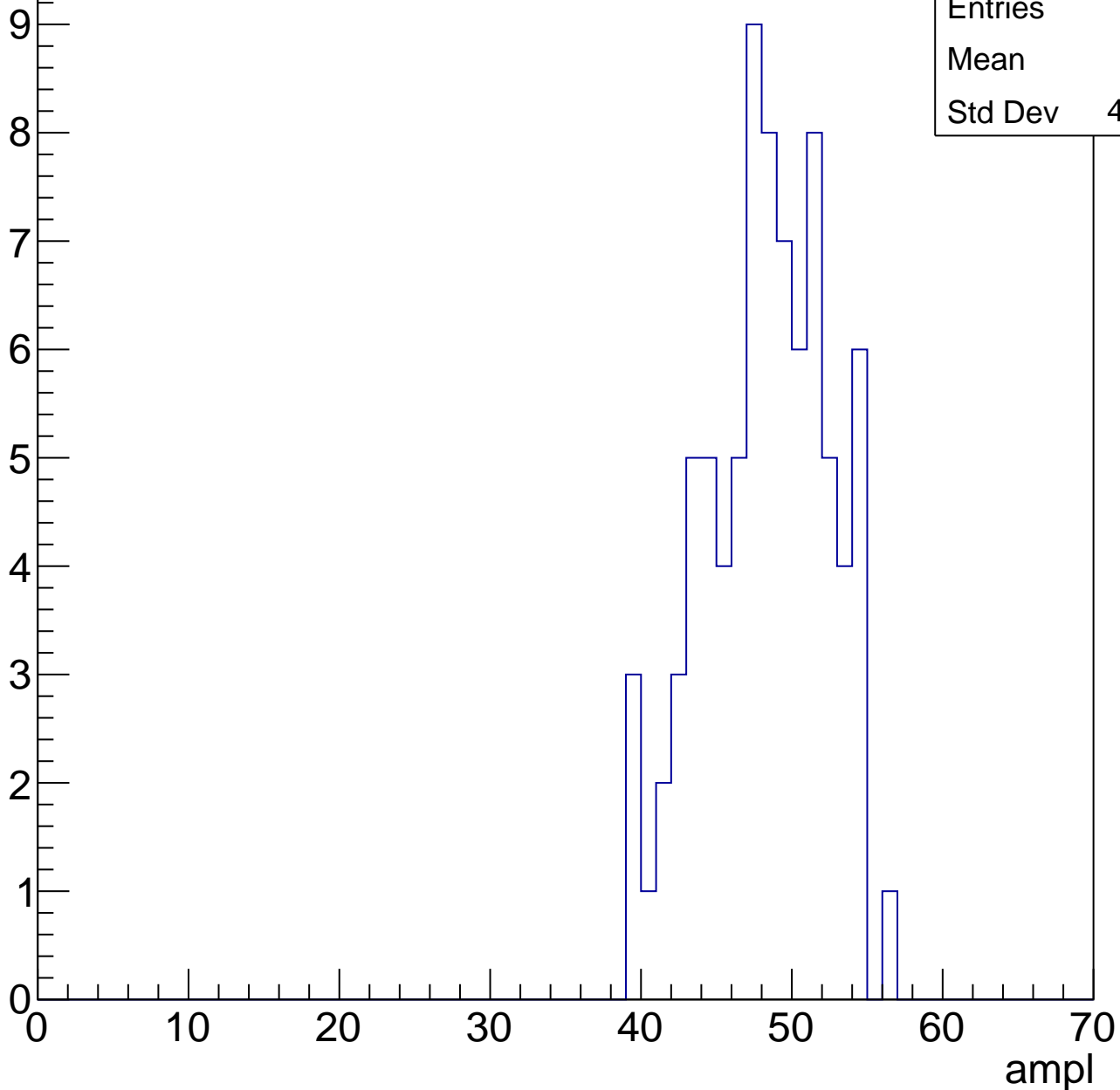


B1L102S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 82 |
| Mean | 47.8 |
| Std Dev | 4.047 |

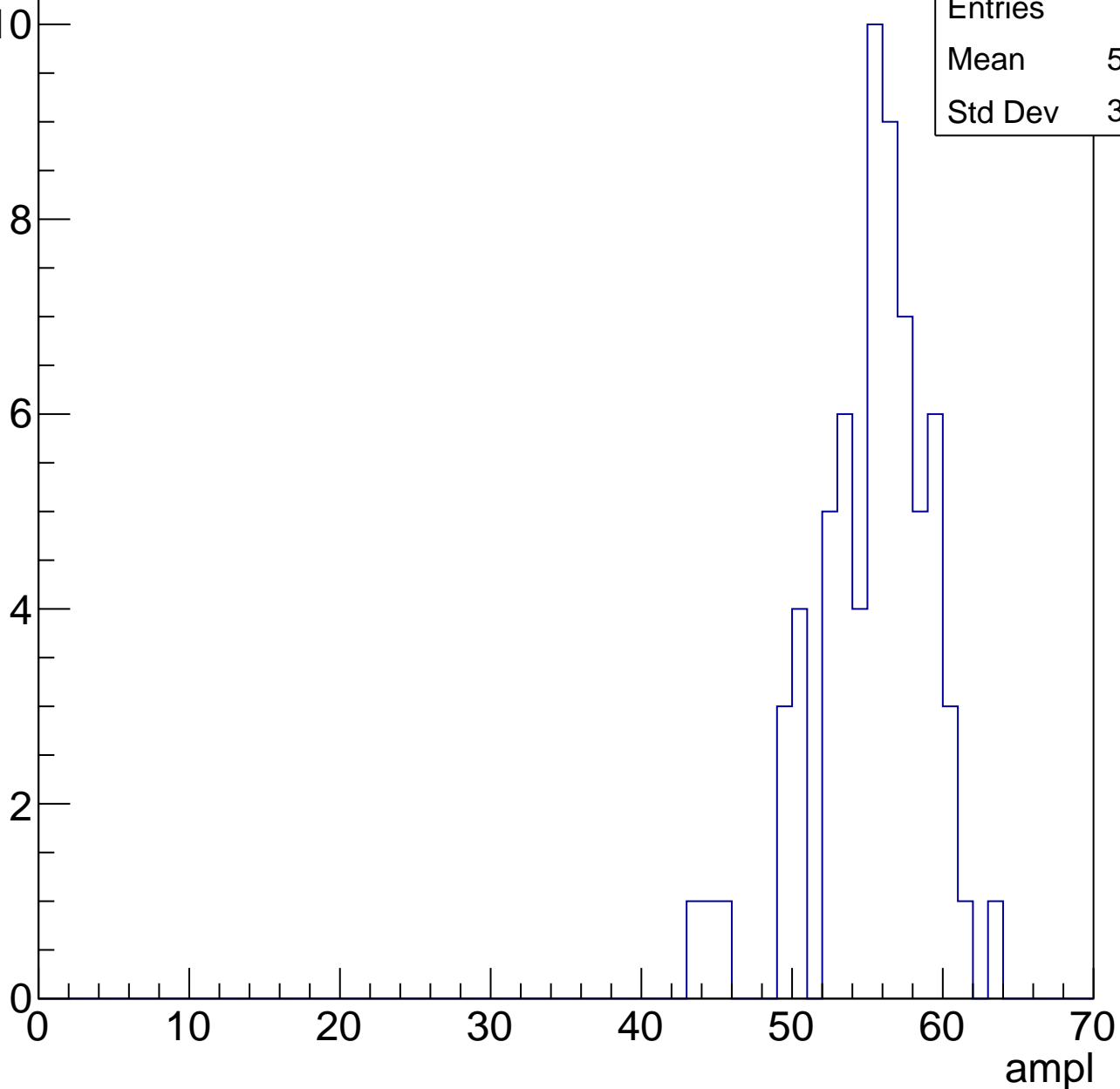


B1L102S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 54.84 |
| Std Dev | 3.858 |

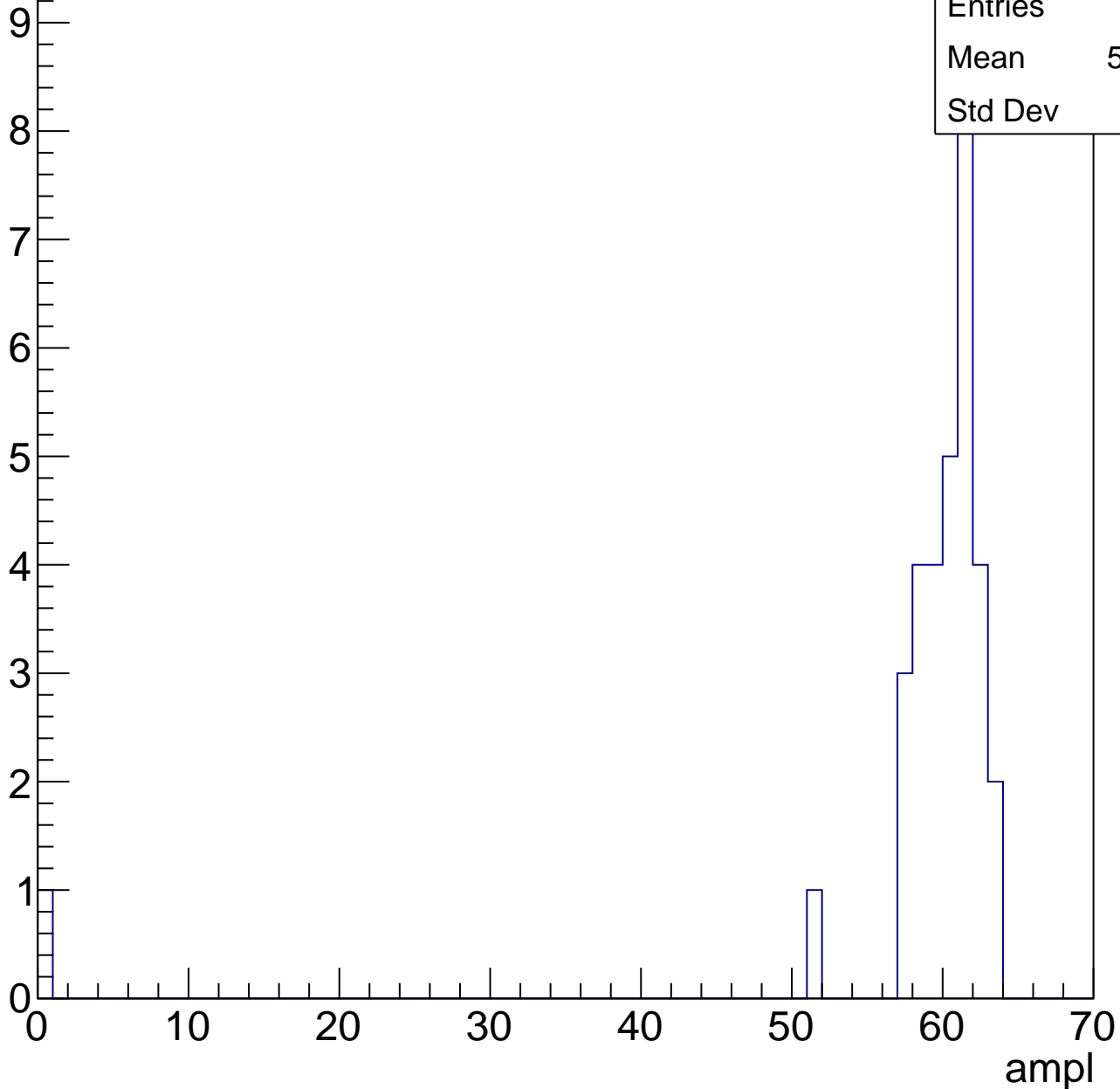


B1L102S, U15-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 33 |
| Mean | 57.97 |
| Std Dev | 10.5 |

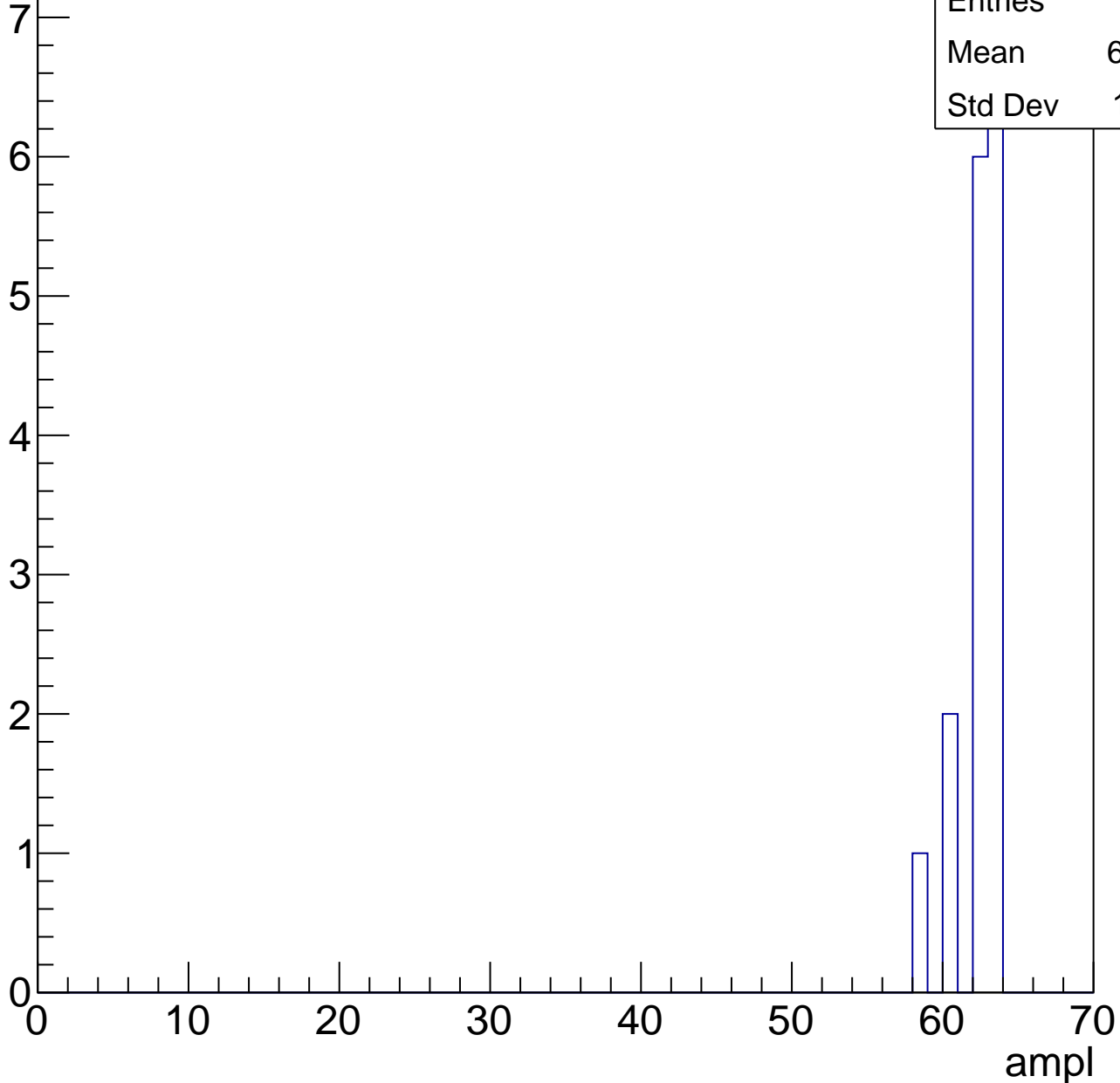


B1L102S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 16 |
| Mean | 61.94 |
| Std Dev | 1.391 |



B1L102S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch98, adc0

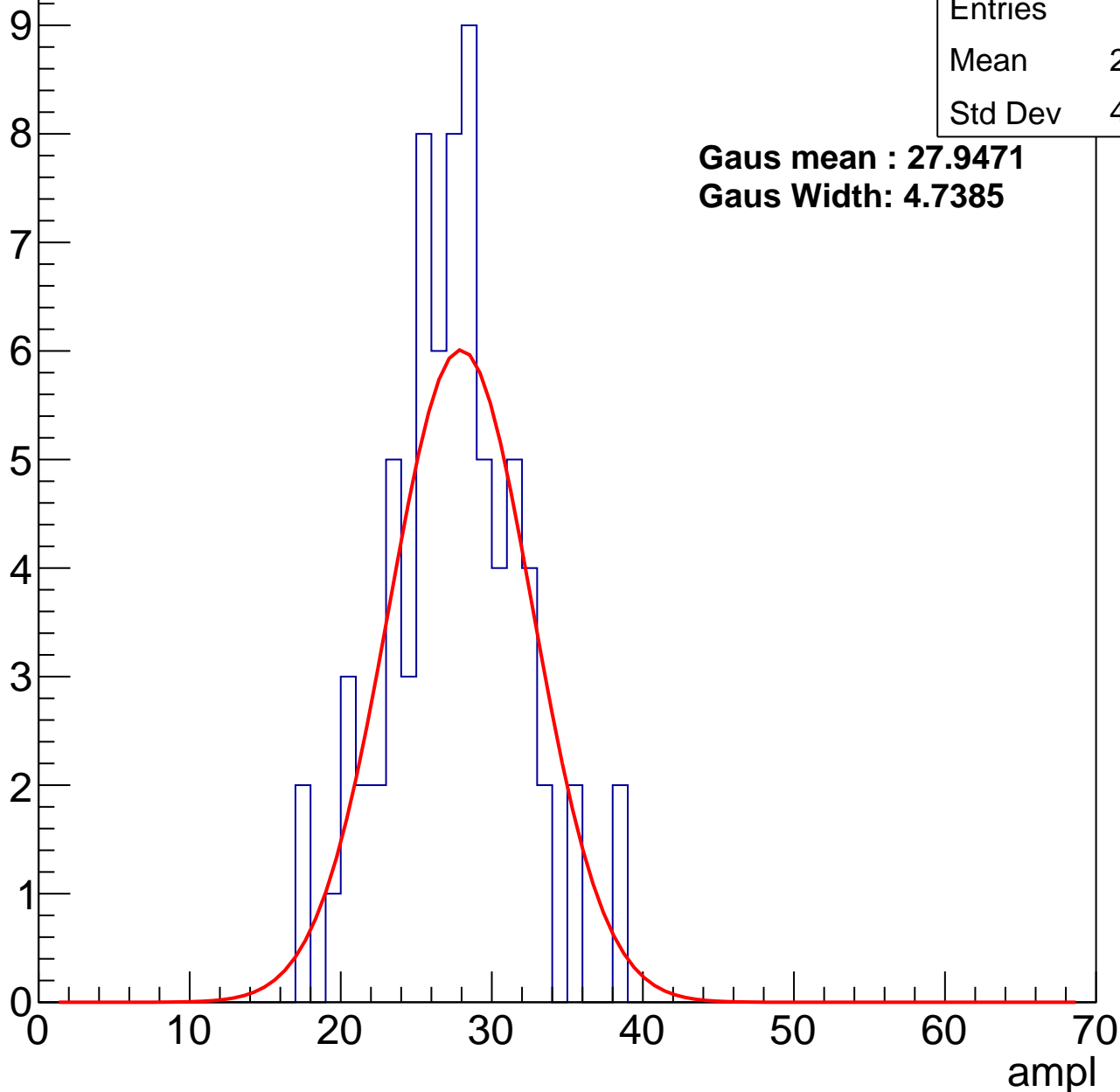
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 26.99 |
| Std Dev | 4.324 |

Gaus mean : 27.9471

Gaus Width: 4.7385



B1L102S, U15-ch98, adc1

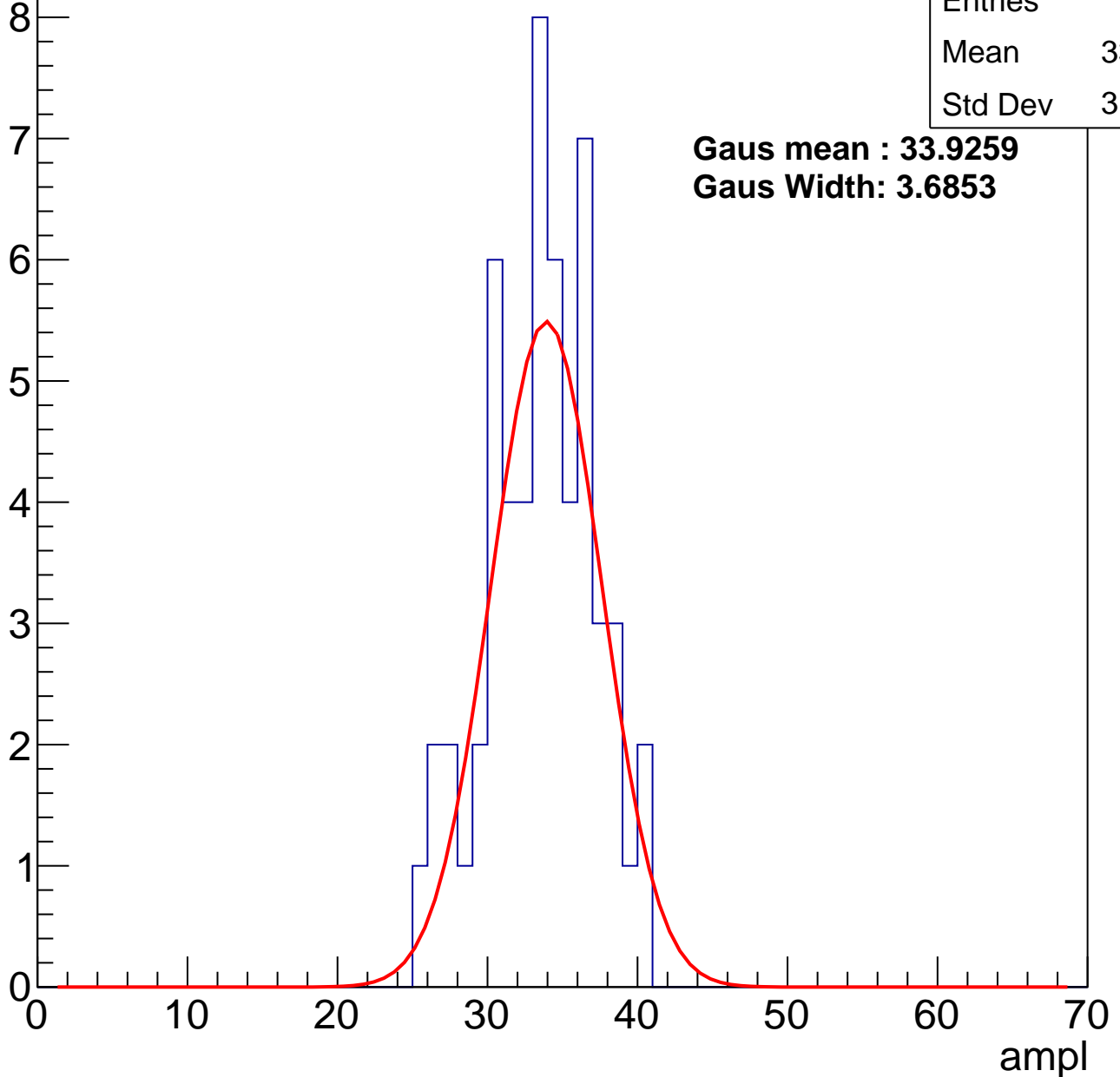
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 33.09 |
| Std Dev | 3.552 |

Gaus mean : 33.9259

Gaus Width: 3.6853



B1L102S, U15-ch98, adc2

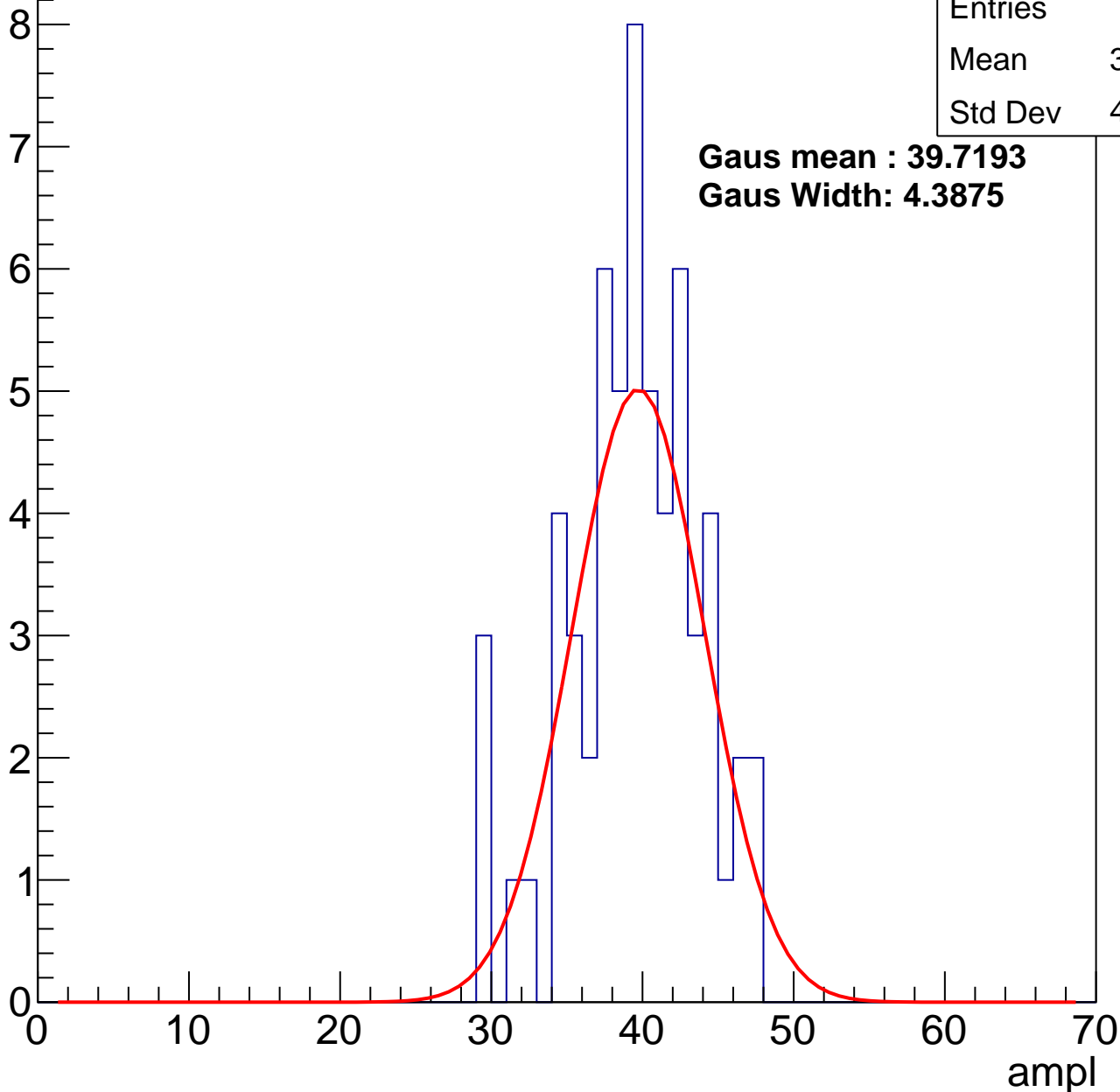
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 38.98 |
| Std Dev | 4.268 |

Gaus mean : 39.7193

Gaus Width: 4.3875

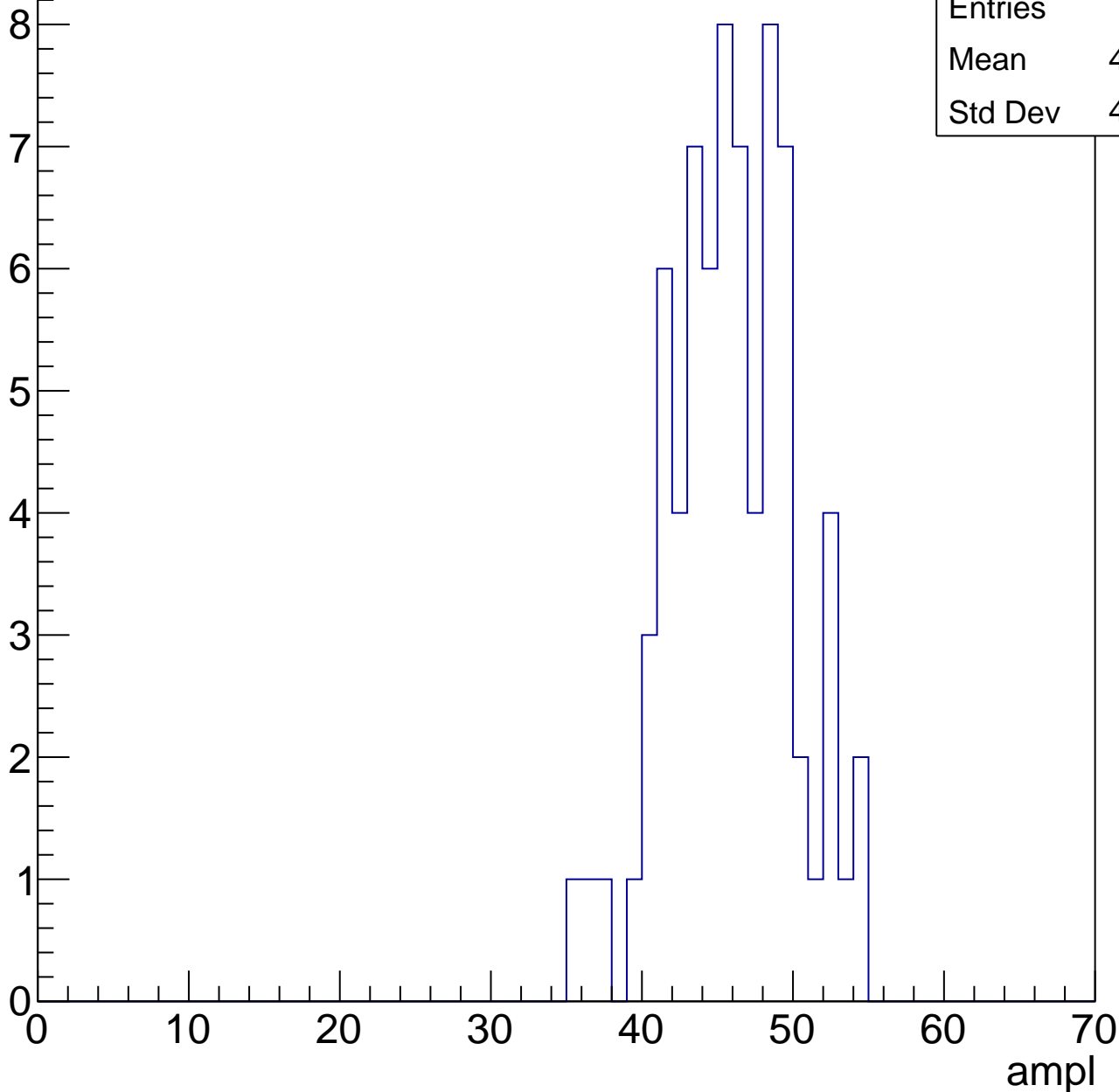


B1L102S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 45.45 |
| Std Dev | 4.064 |

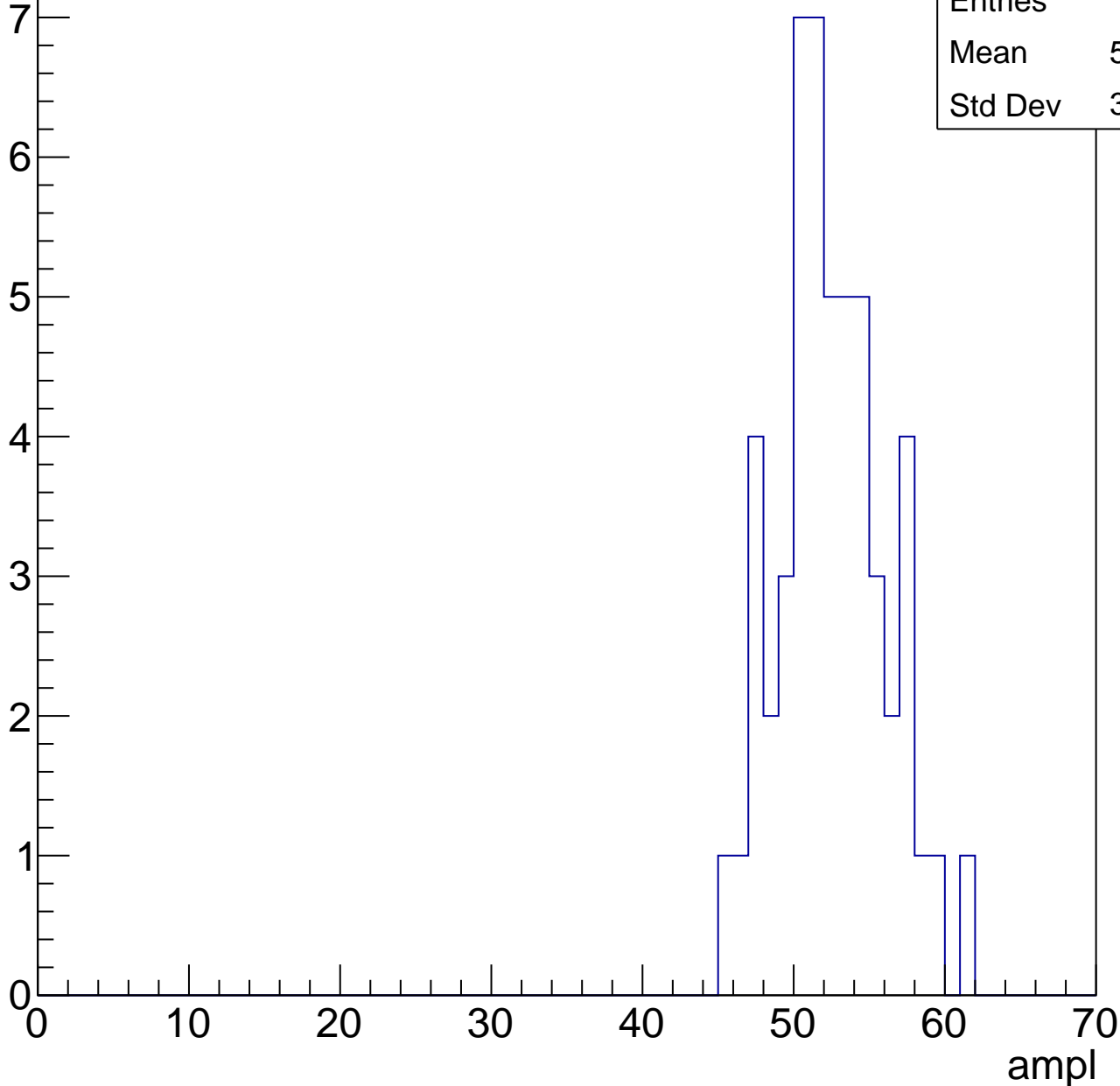


B1L102S, U15-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 52.06 |
| Std Dev | 3.477 |

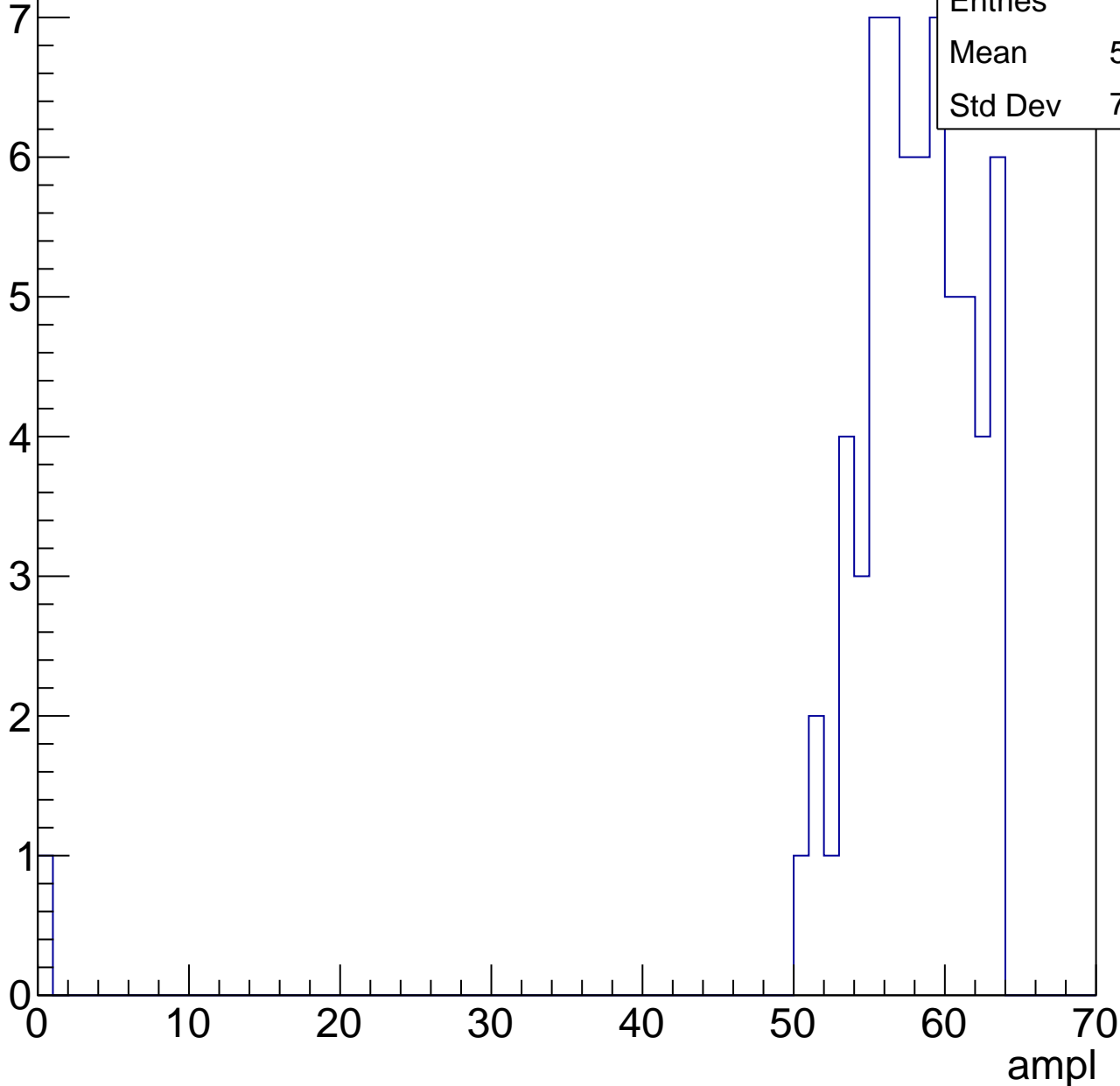


B1L102S, U15-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 56.75 |
| Std Dev | 7.837 |

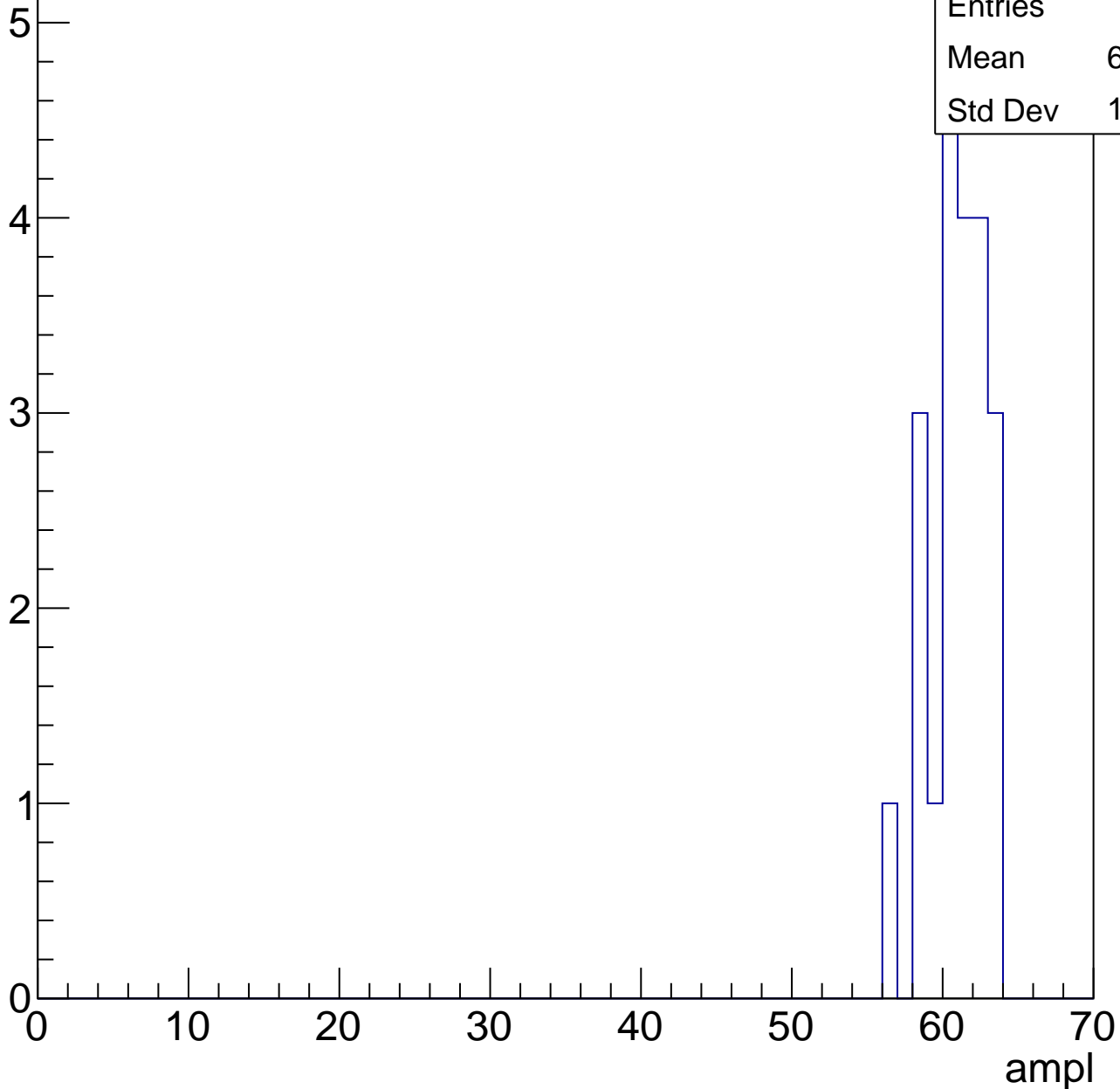


B1L102S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

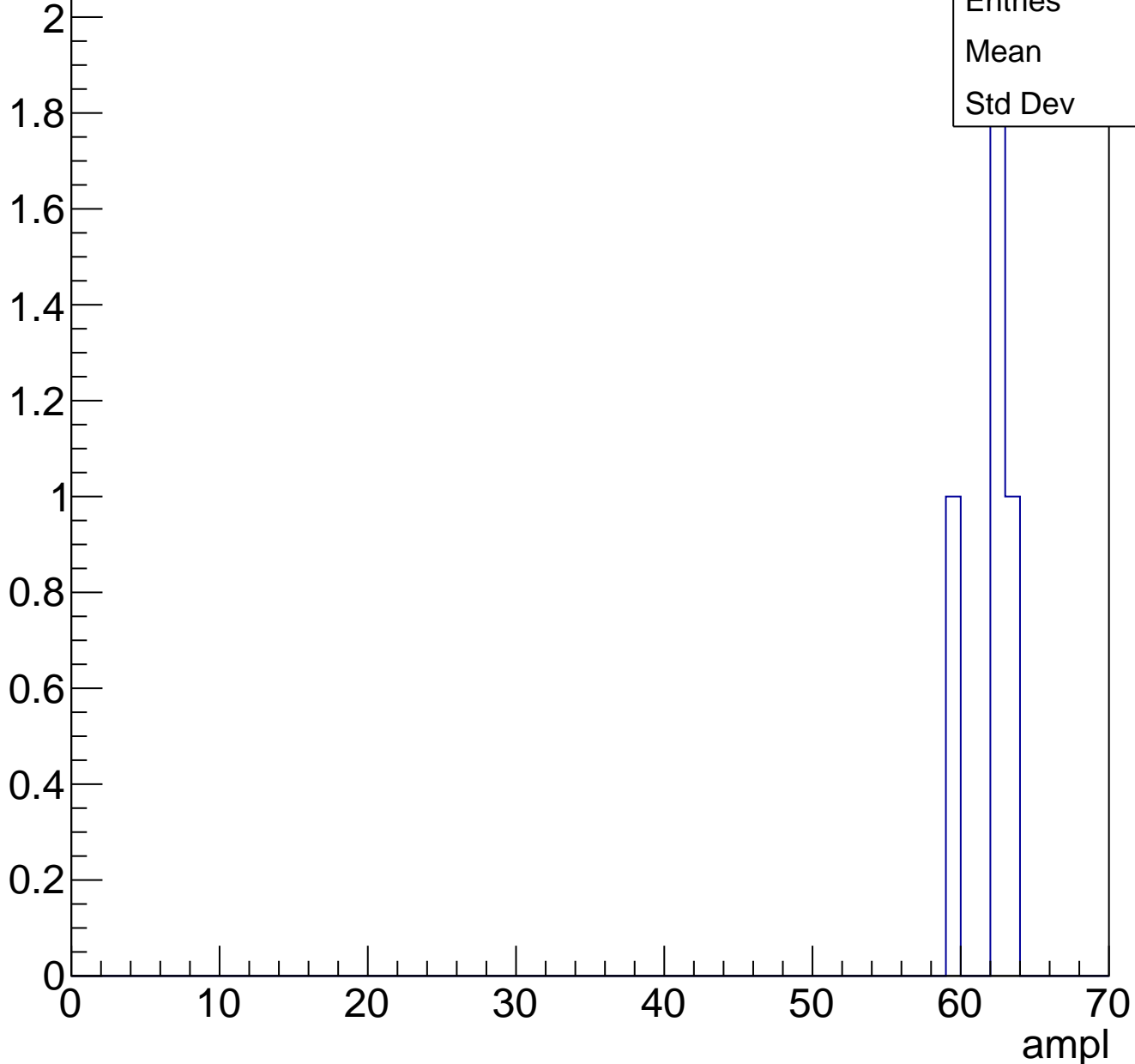
| | |
|---------|-------|
| Entries | 21 |
| Mean | 60.48 |
| Std Dev | 1.842 |



B1L102S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch99, adc0

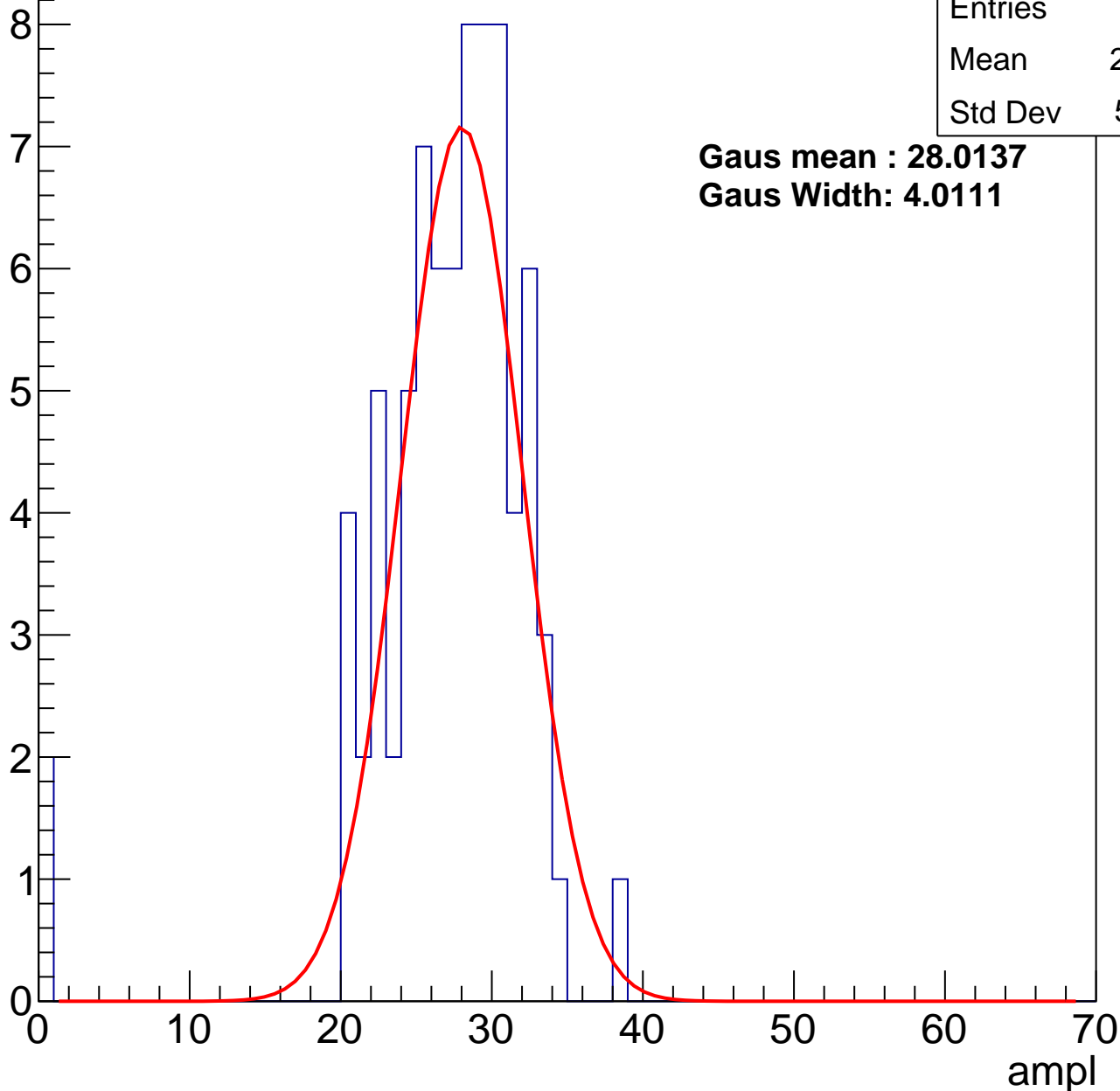
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 26.59 |
| Std Dev | 5.721 |

Gaus mean : 28.0137

Gaus Width: 4.0111



B1L102S, U15-ch99, adc1

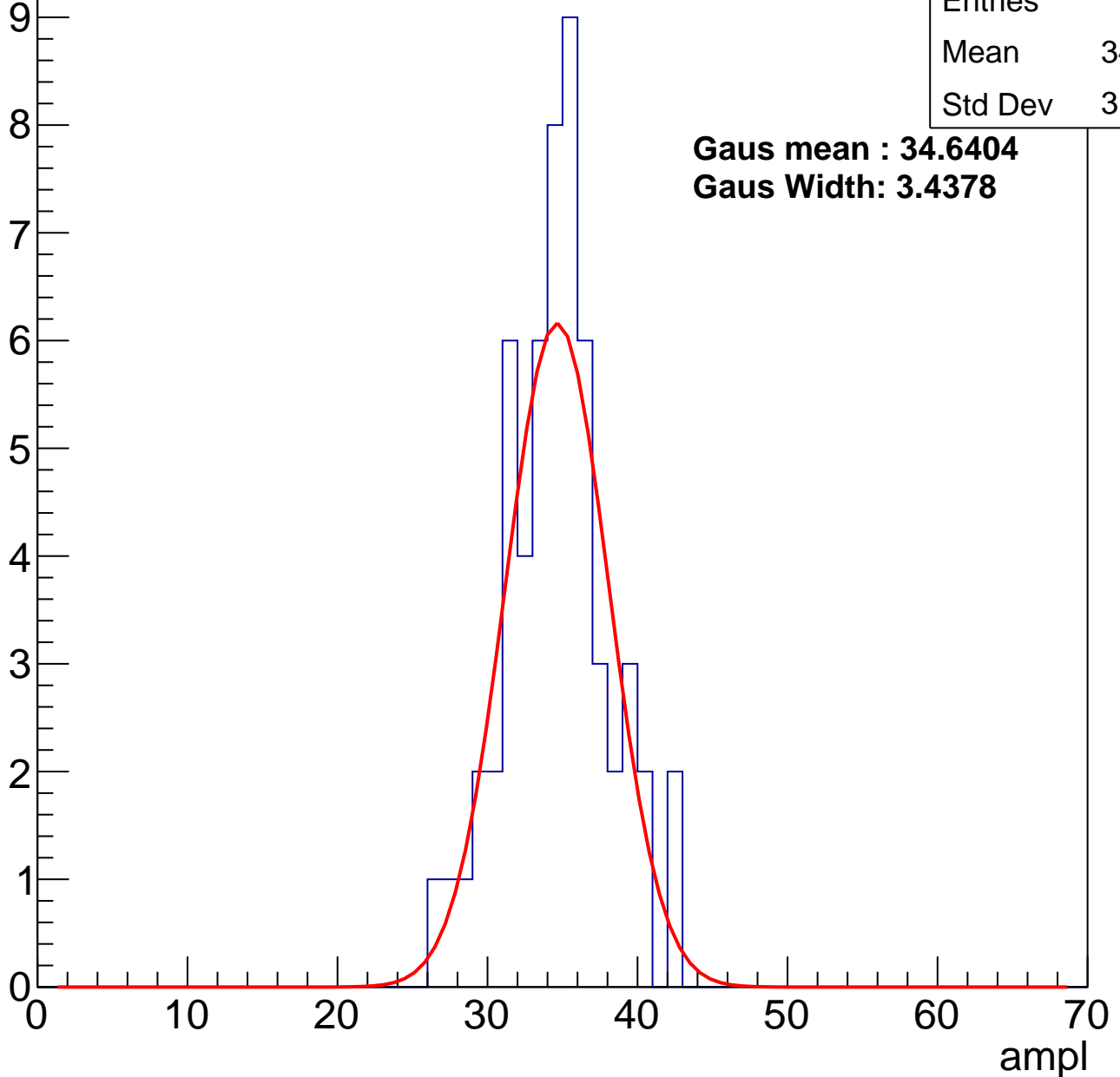
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 34.17 |
| Std Dev | 3.405 |

Gaus mean : 34.6404

Gaus Width: 3.4378



B1L102S, U15-ch99, adc2

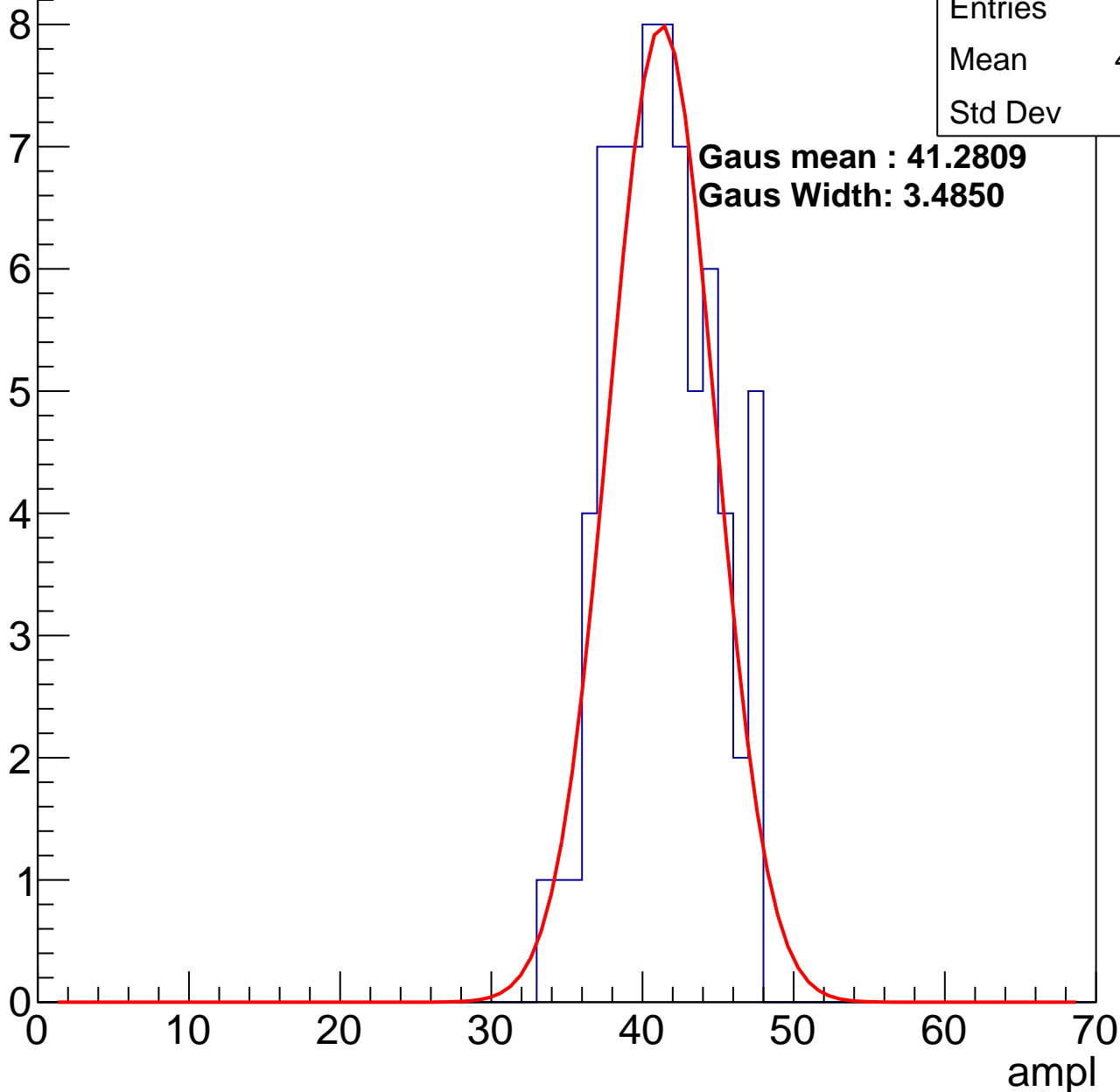
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 73 |
| Mean | 40.71 |
| Std Dev | 3.37 |

Gaus mean : 41.2809

Gaus Width: 3.4850

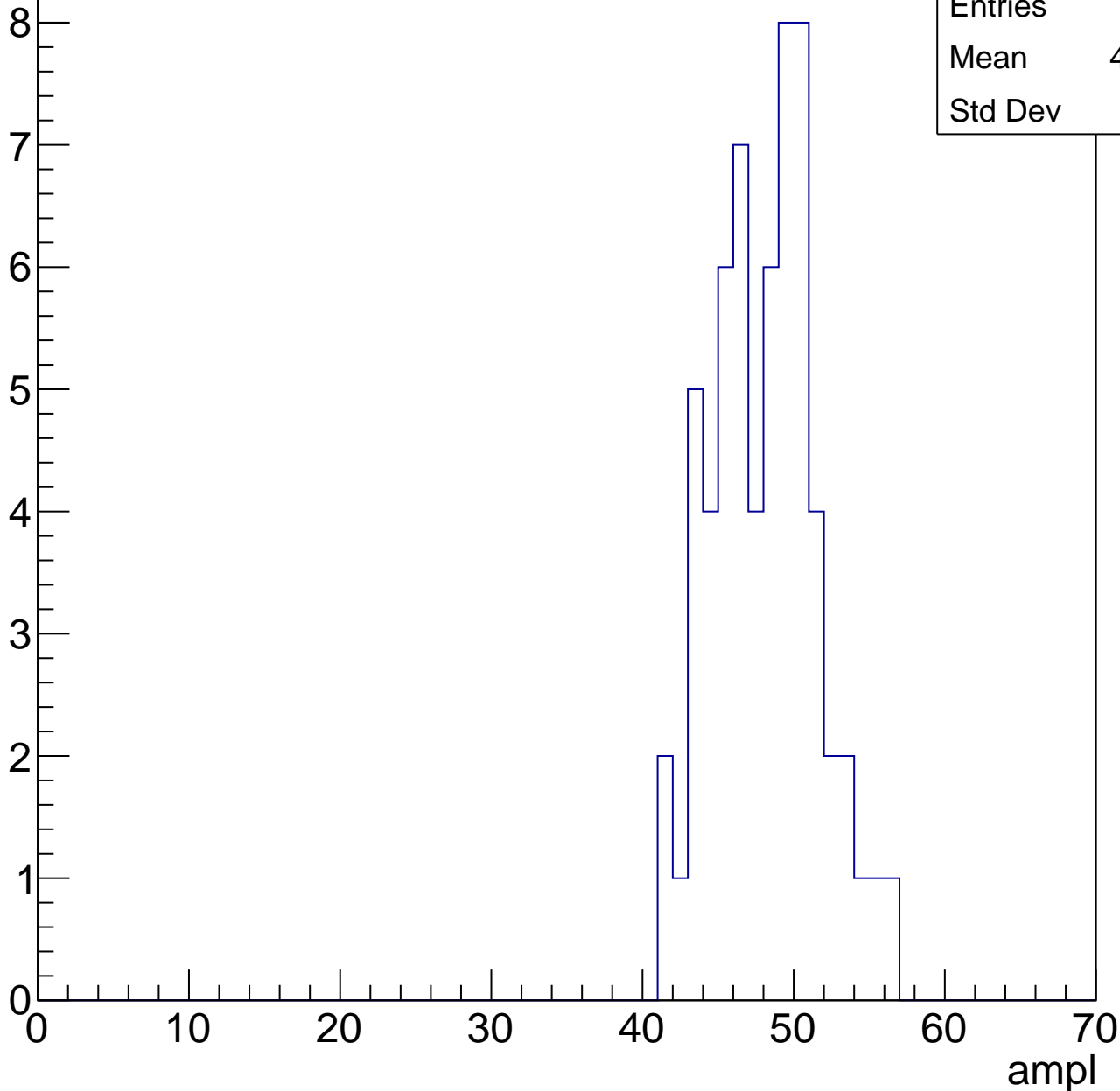


B1L102S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 47.65 |
| Std Dev | 3.38 |

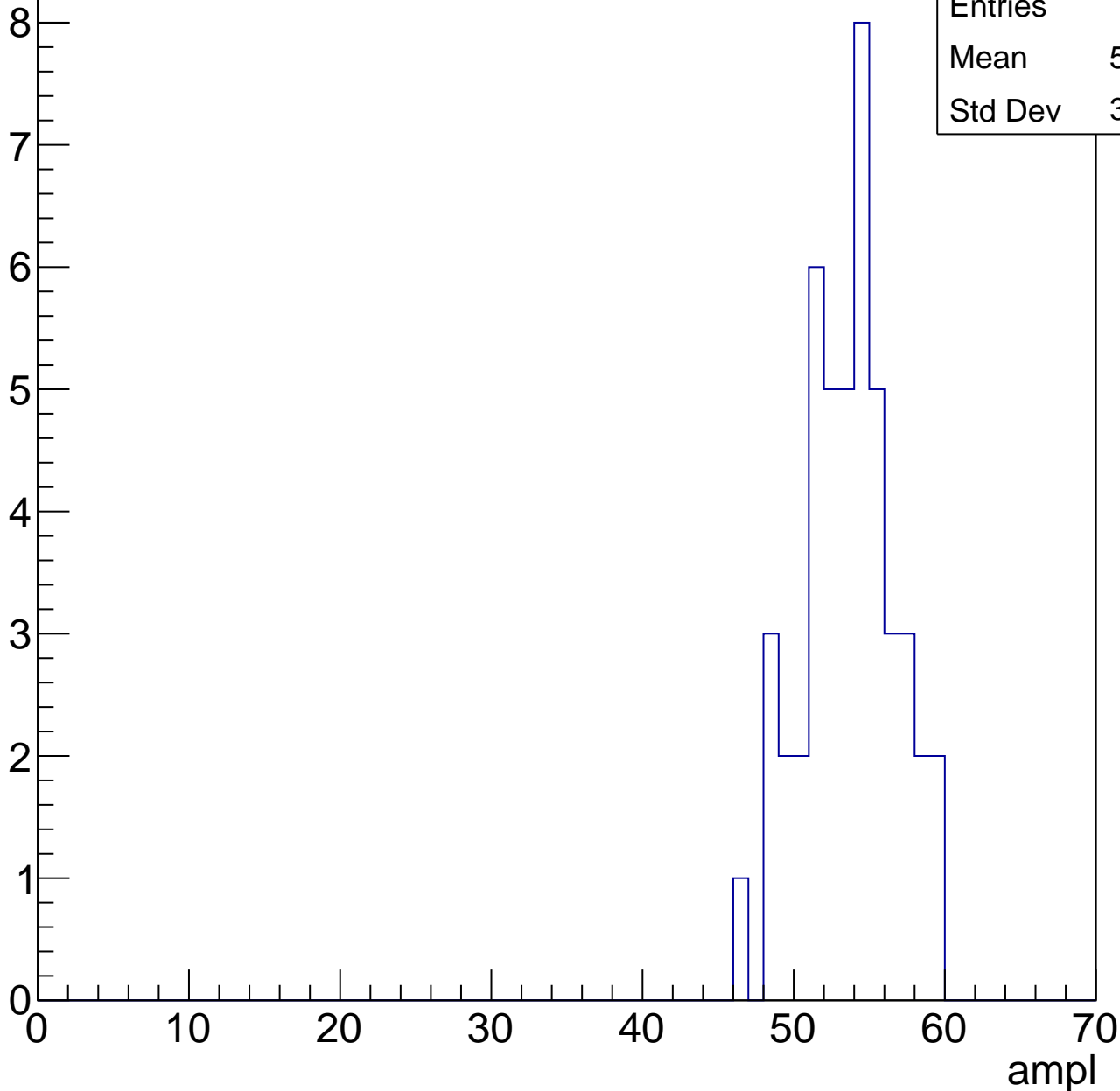


B1L102S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 53.17 |
| Std Dev | 3.013 |

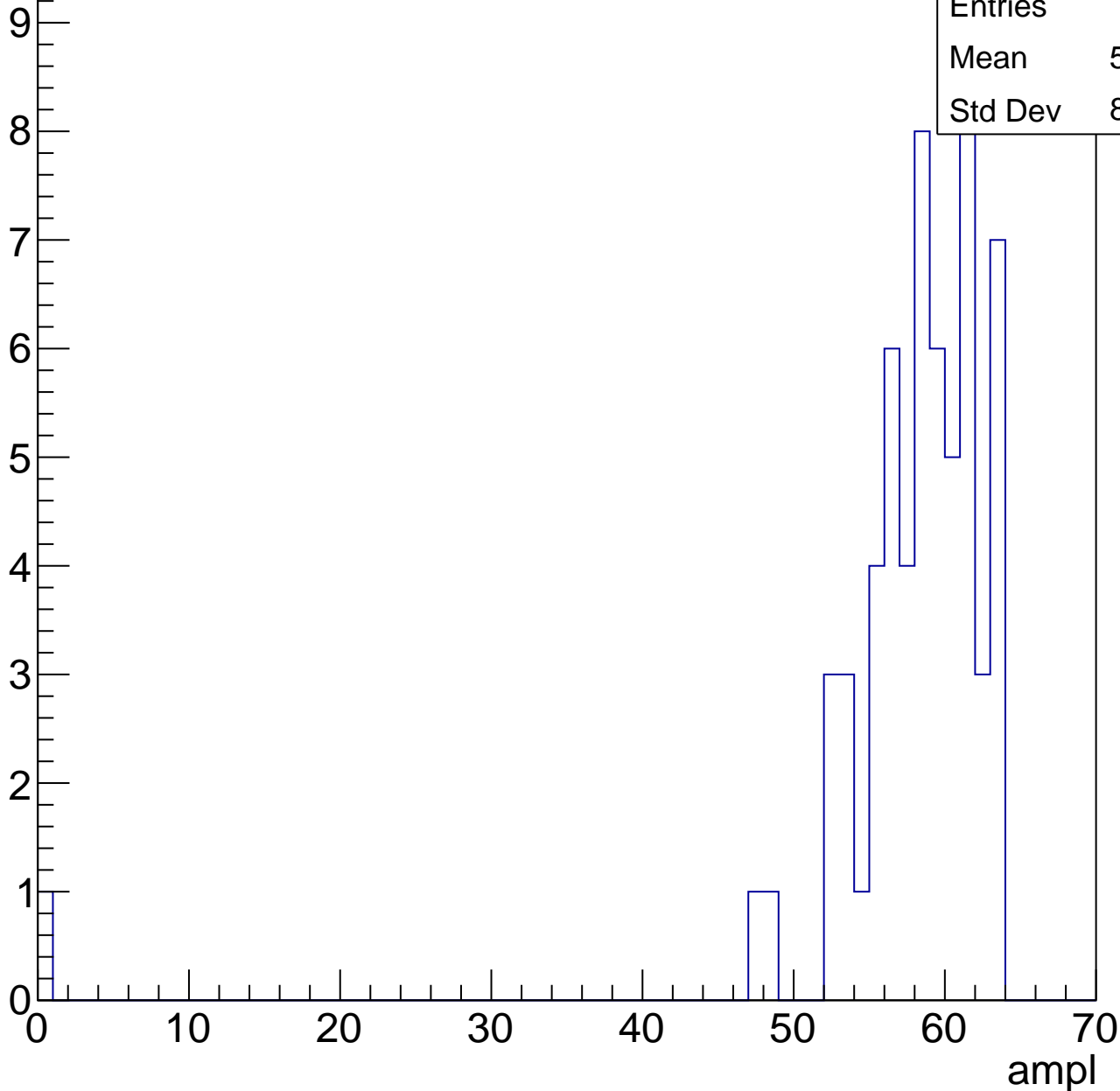


B1L102S, U15-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.13 |
| Std Dev | 8.163 |

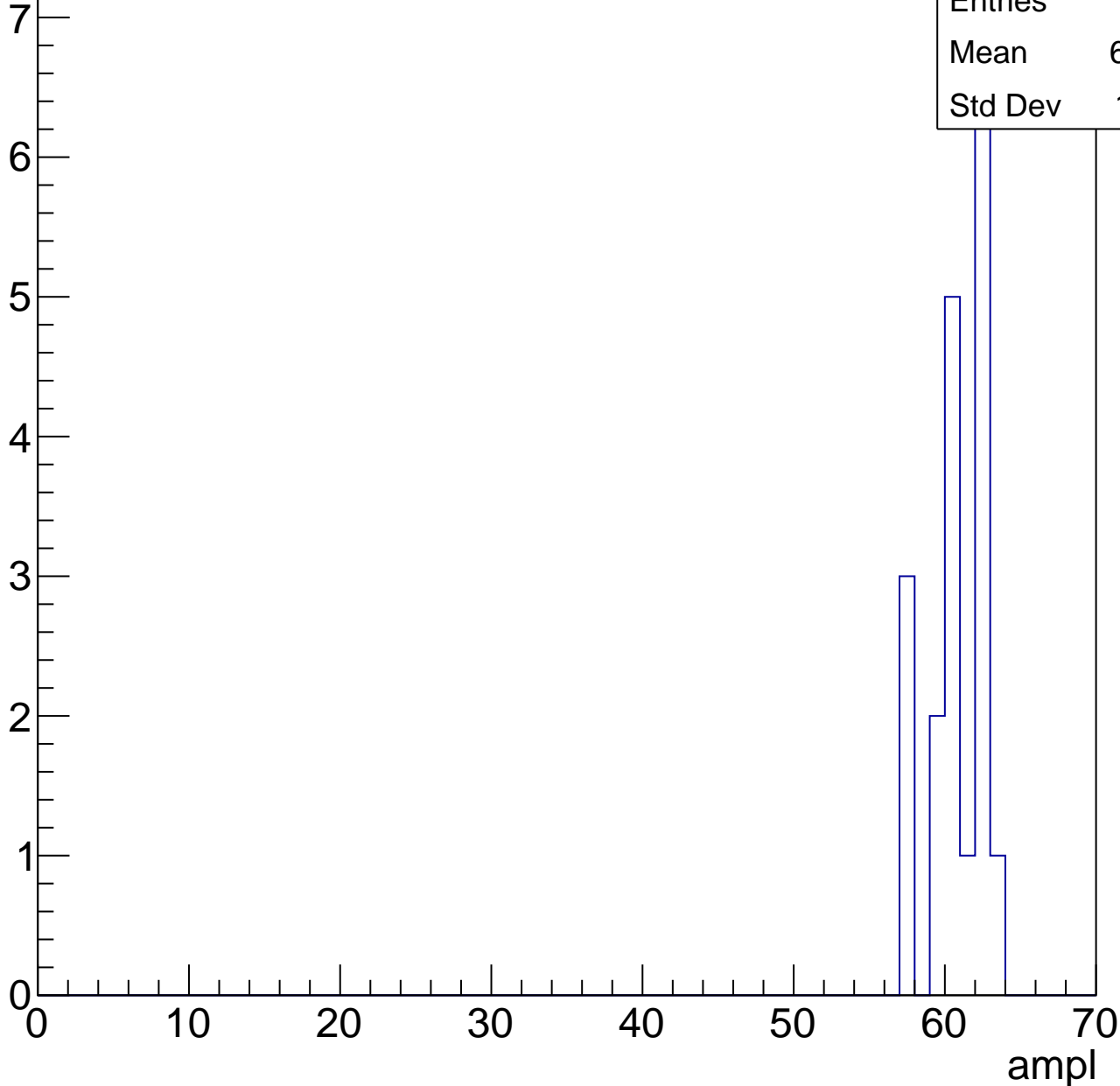


B1L102S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 19 |
| Mean | 60.37 |
| Std Dev | 1.841 |



B1L102S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch100, adc0

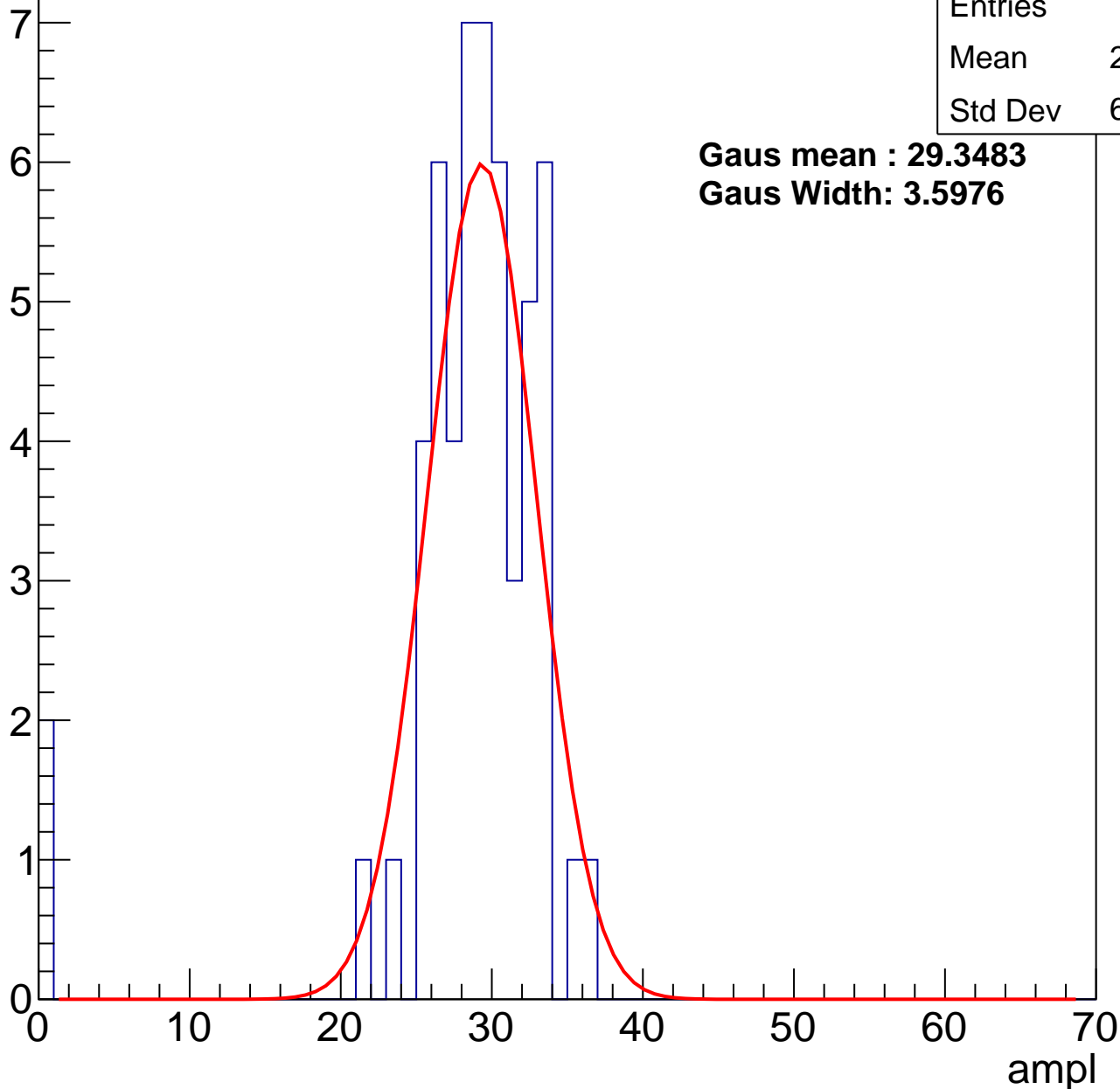
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 27.94 |
| Std Dev | 6.246 |

Gaus mean : 29.3483

Gaus Width: 3.5976



B1L102S, U15-ch100, adc1

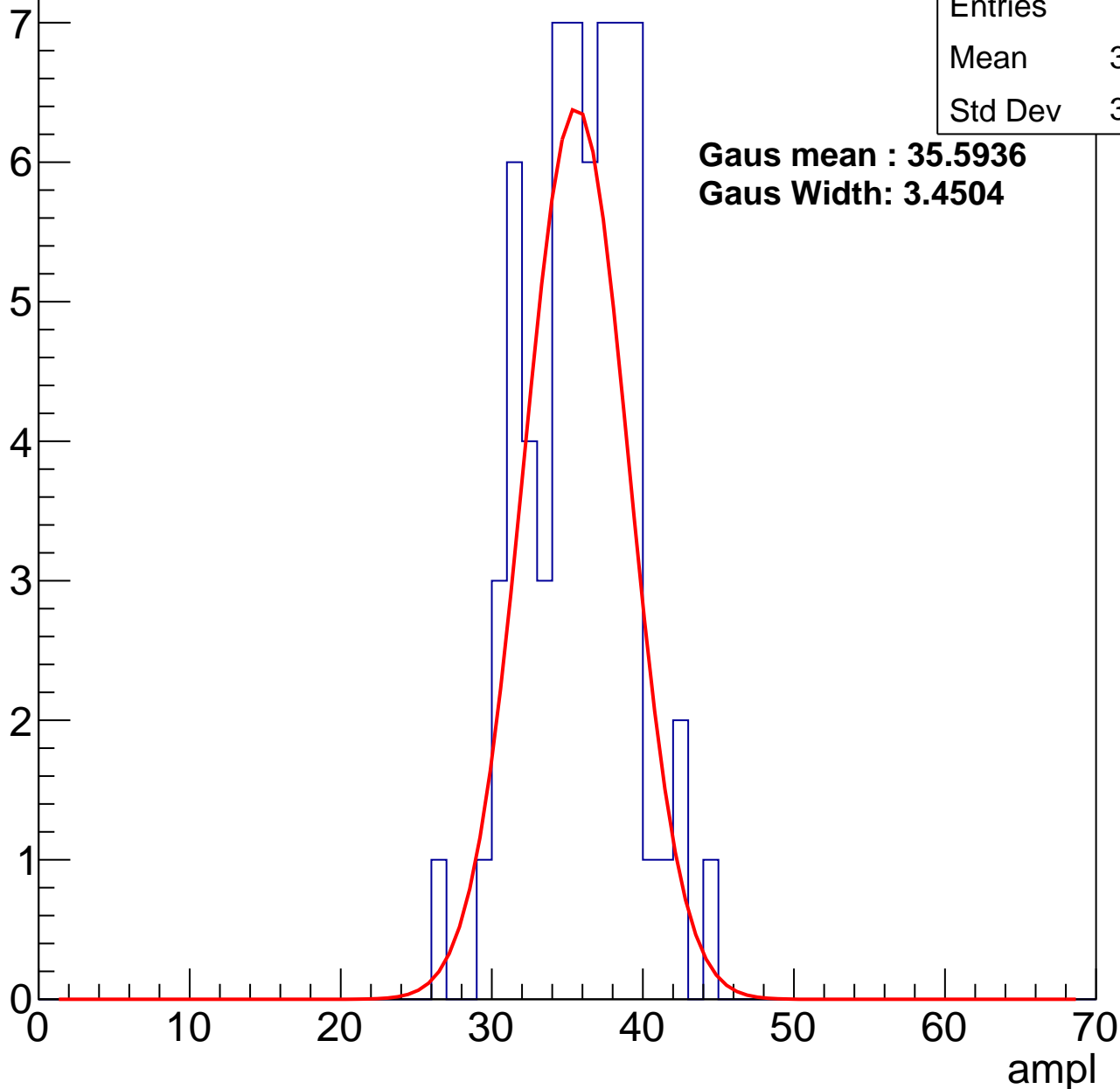
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 35.38 |
| Std Dev | 3.498 |

Gaus mean : 35.5936

Gaus Width: 3.4504



B1L102S, U15-ch100, adc2

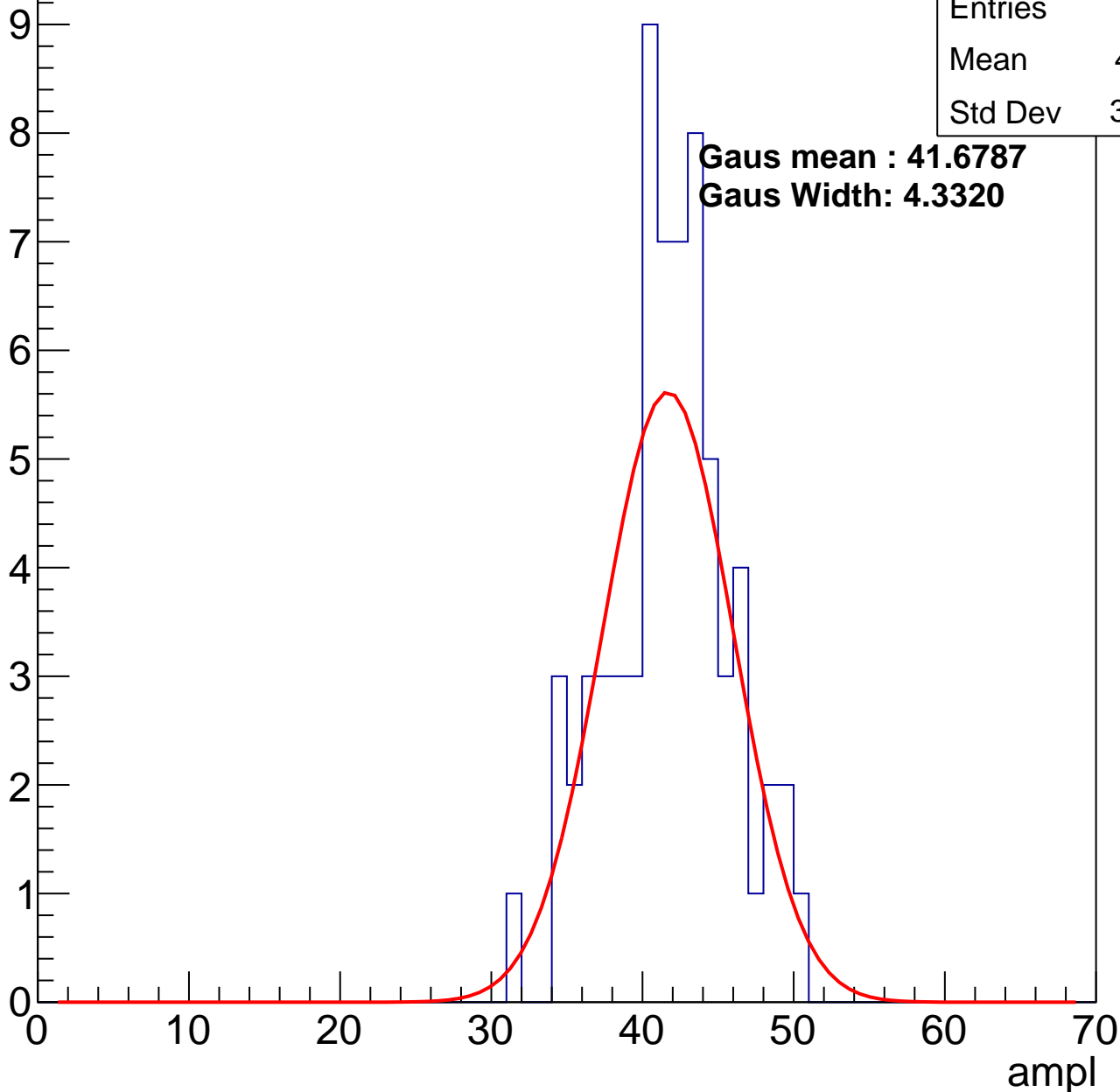
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 41.31 |
| Std Dev | 3.975 |

Gaus mean : 41.6787

Gaus Width: 4.3320

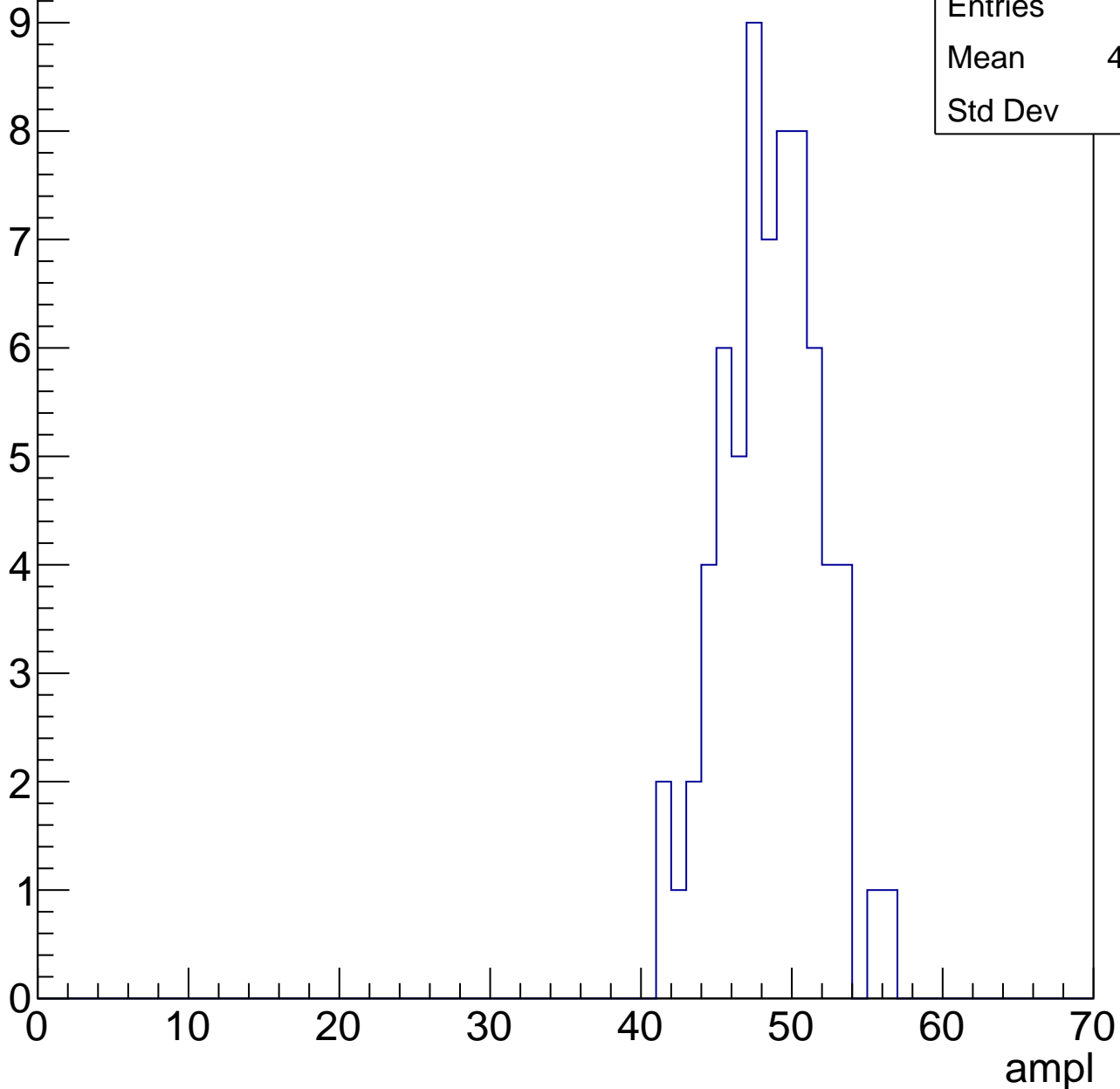


B1L102S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 48.15 |
| Std Dev | 3.21 |

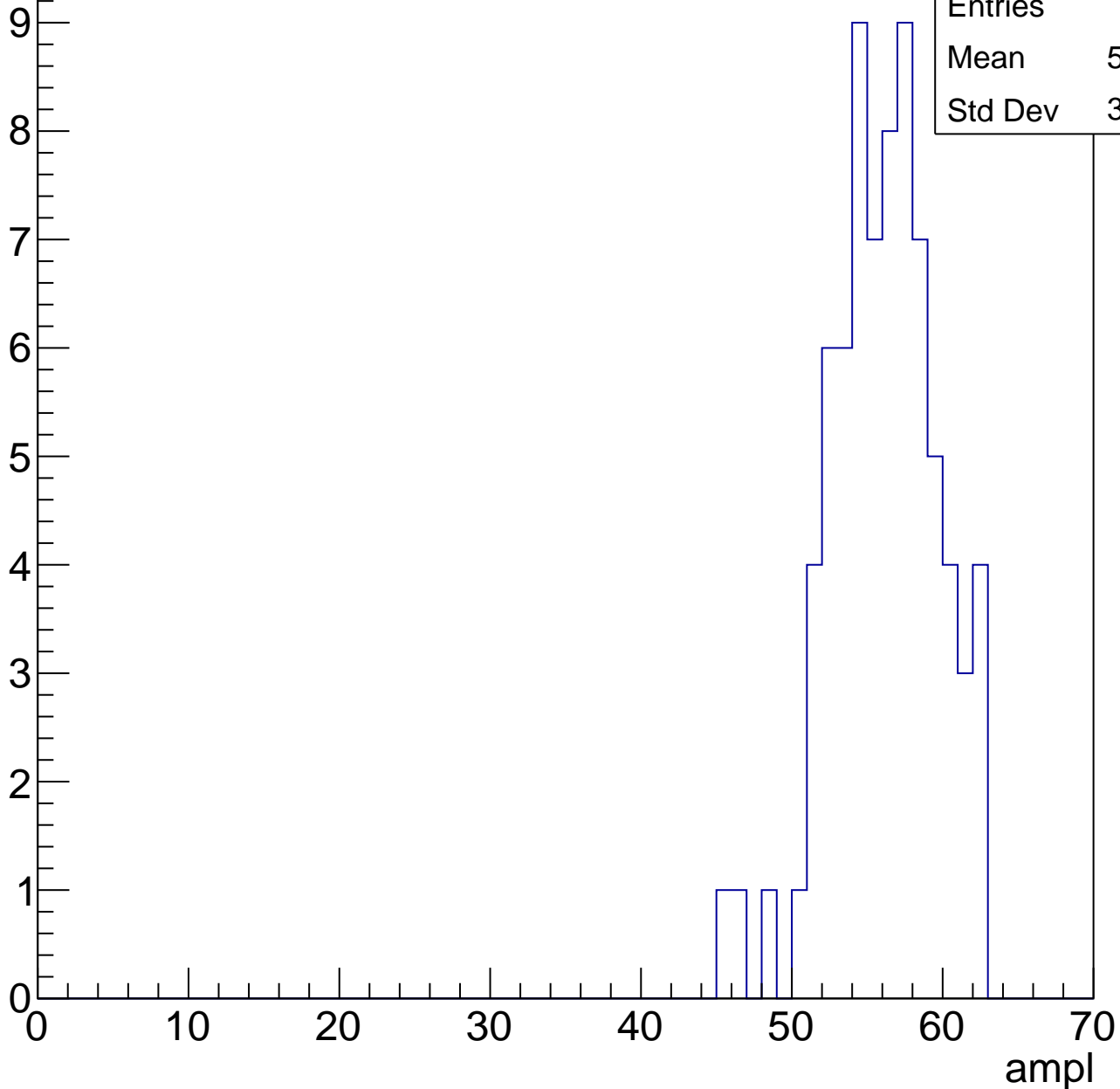


B1L102S, U15-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

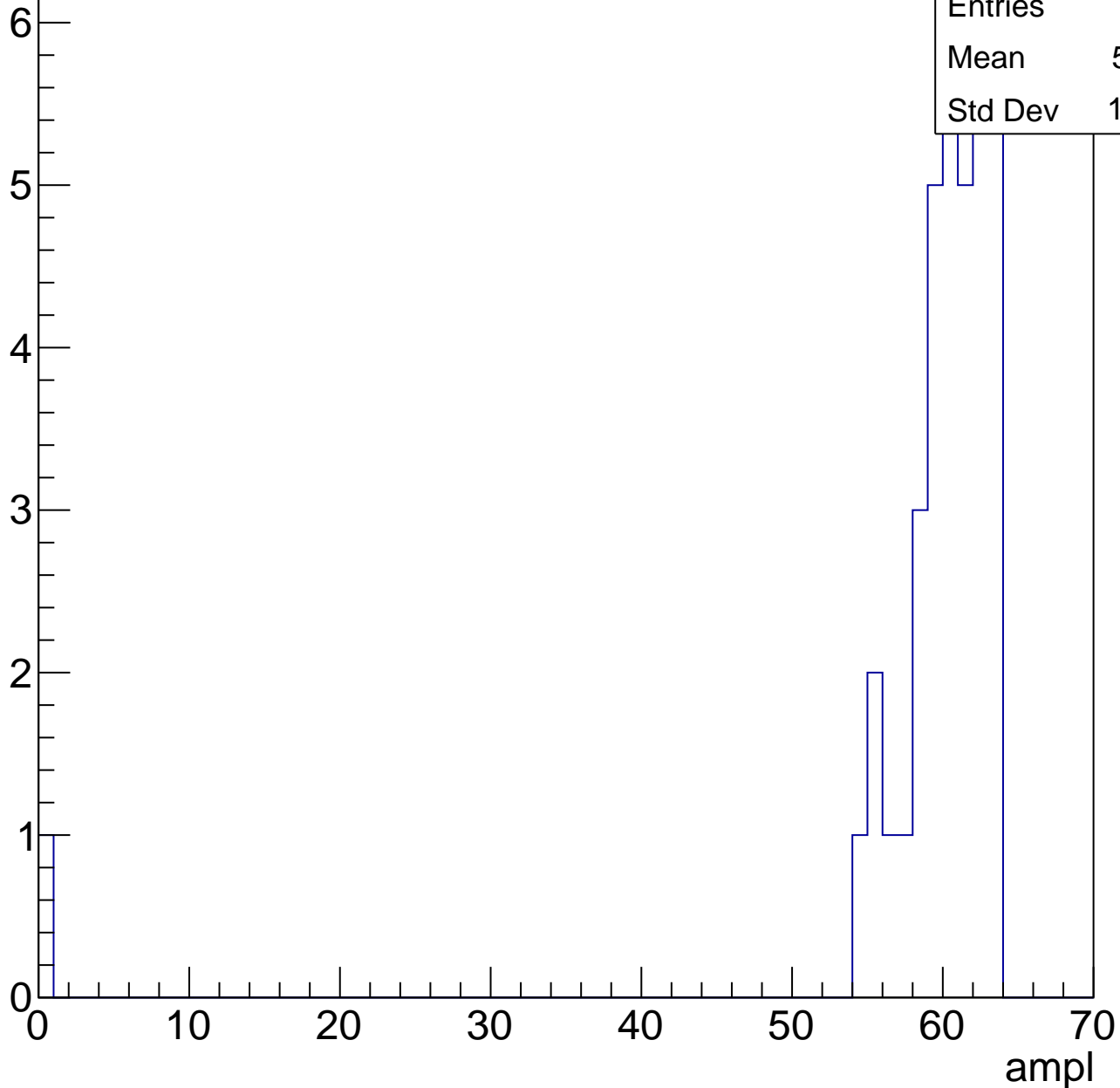
| | |
|---------|-------|
| Entries | 76 |
| Mean | 55.62 |
| Std Dev | 3.569 |



B1L102S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch101, adc0

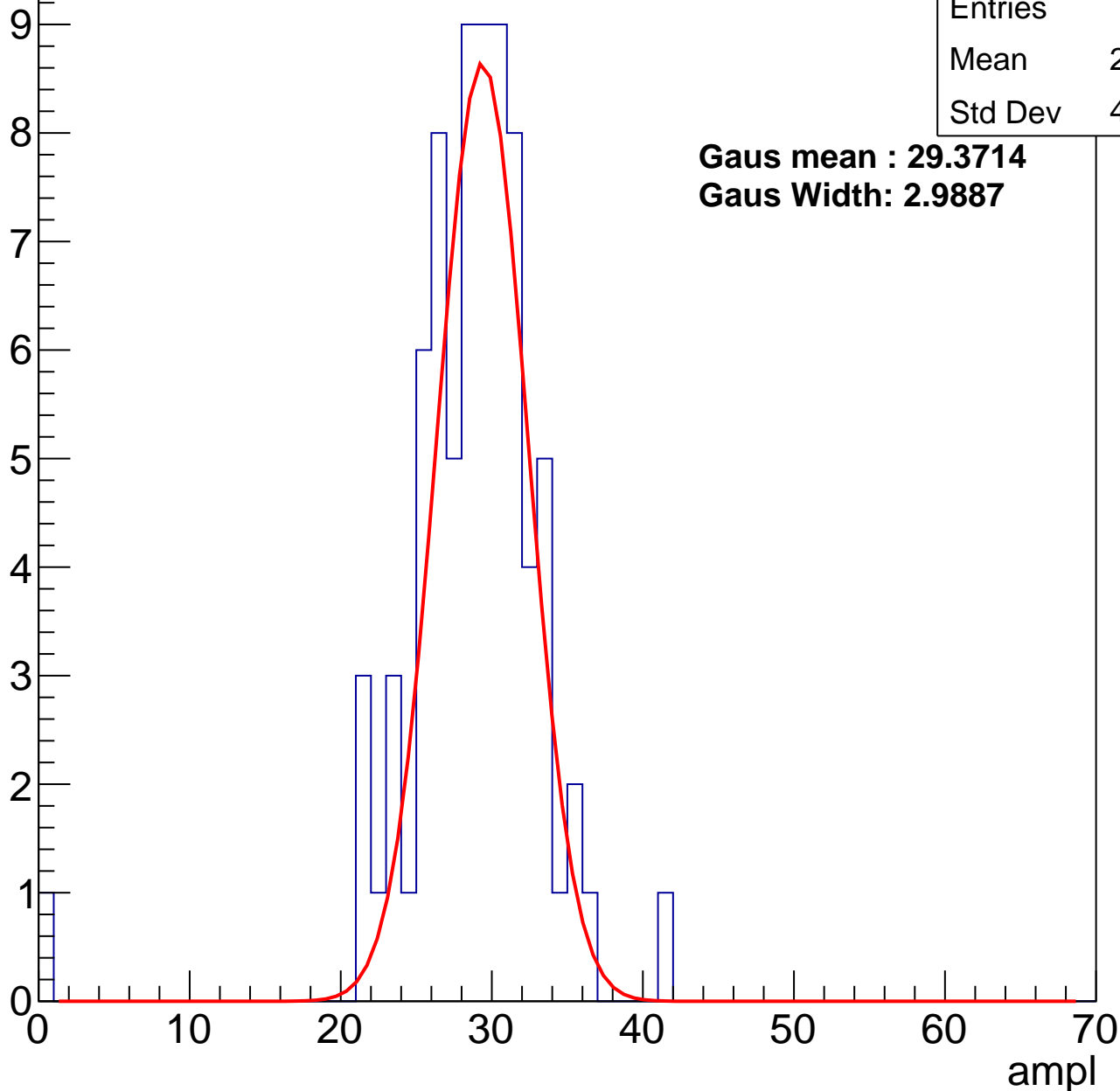
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 28.26 |
| Std Dev | 4.857 |

Gaus mean : 29.3714

Gaus Width: 2.9887



B1L102S, U15-ch101, adc1

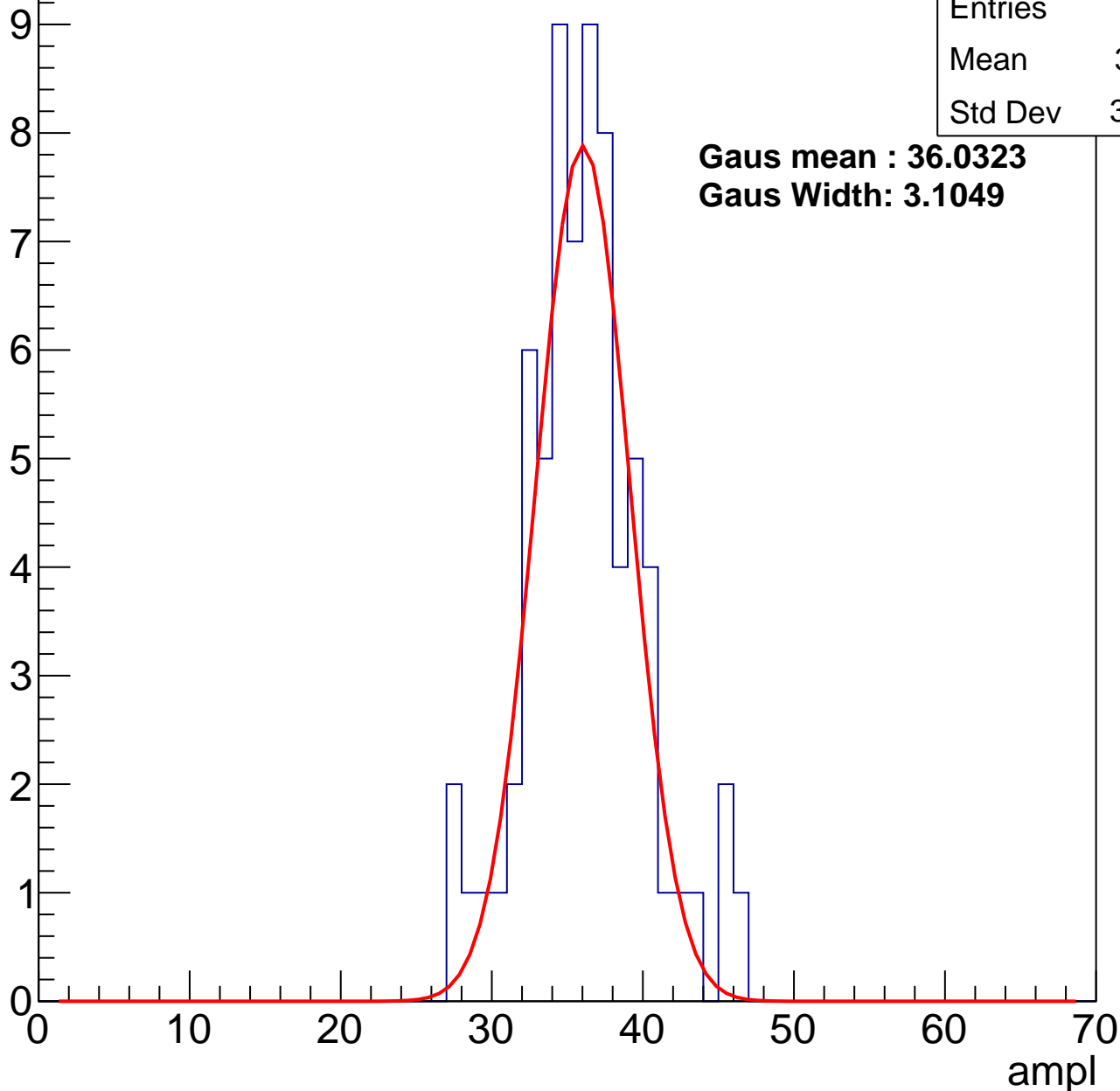
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 35.71 |
| Std Dev | 3.862 |

Gaus mean : 36.0323

Gaus Width: 3.1049



B1L102S, U15-ch101, adc2

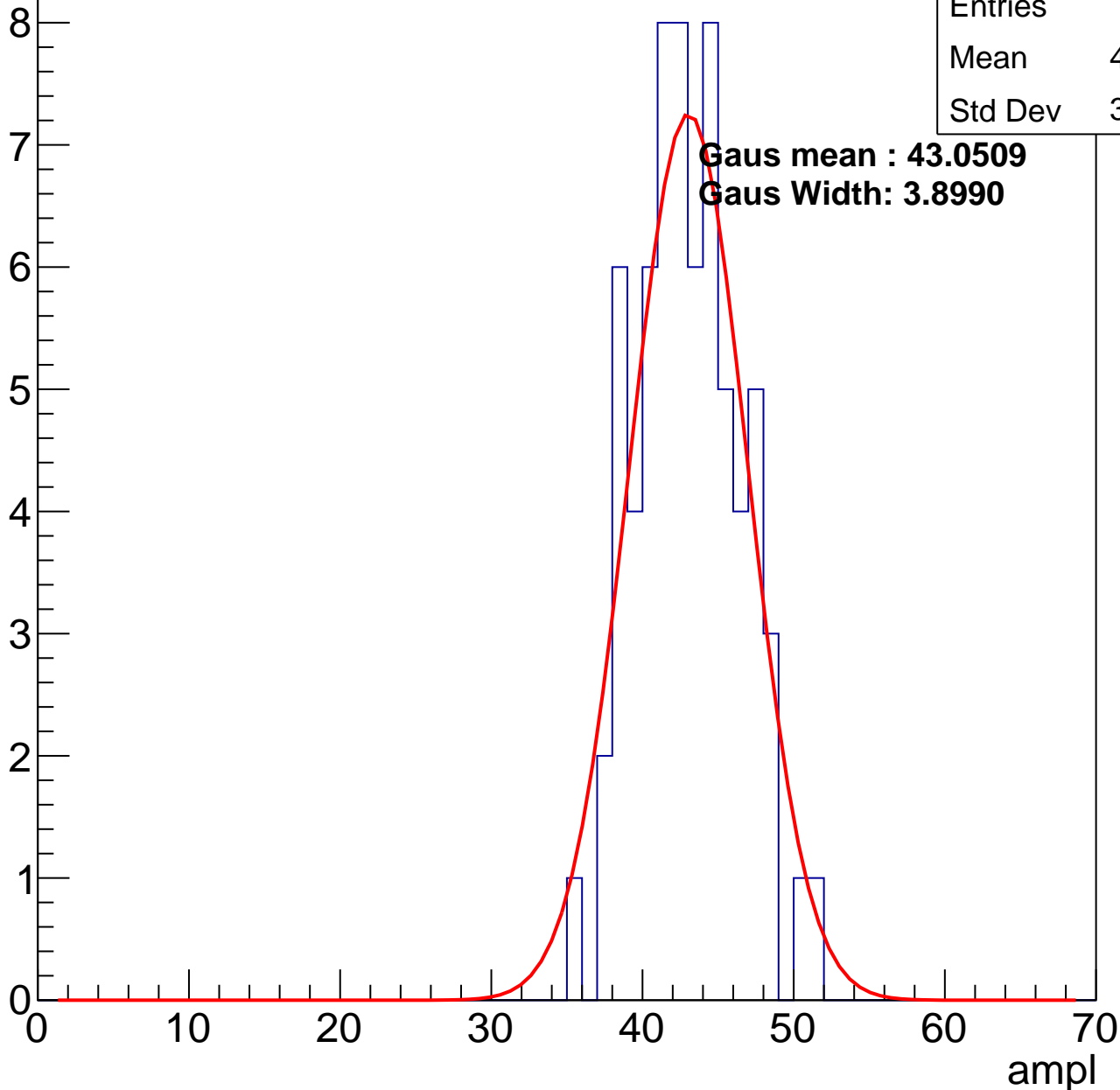
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 42.59 |
| Std Dev | 3.353 |

Gaus mean : 43.0509

Gaus Width: 3.8990

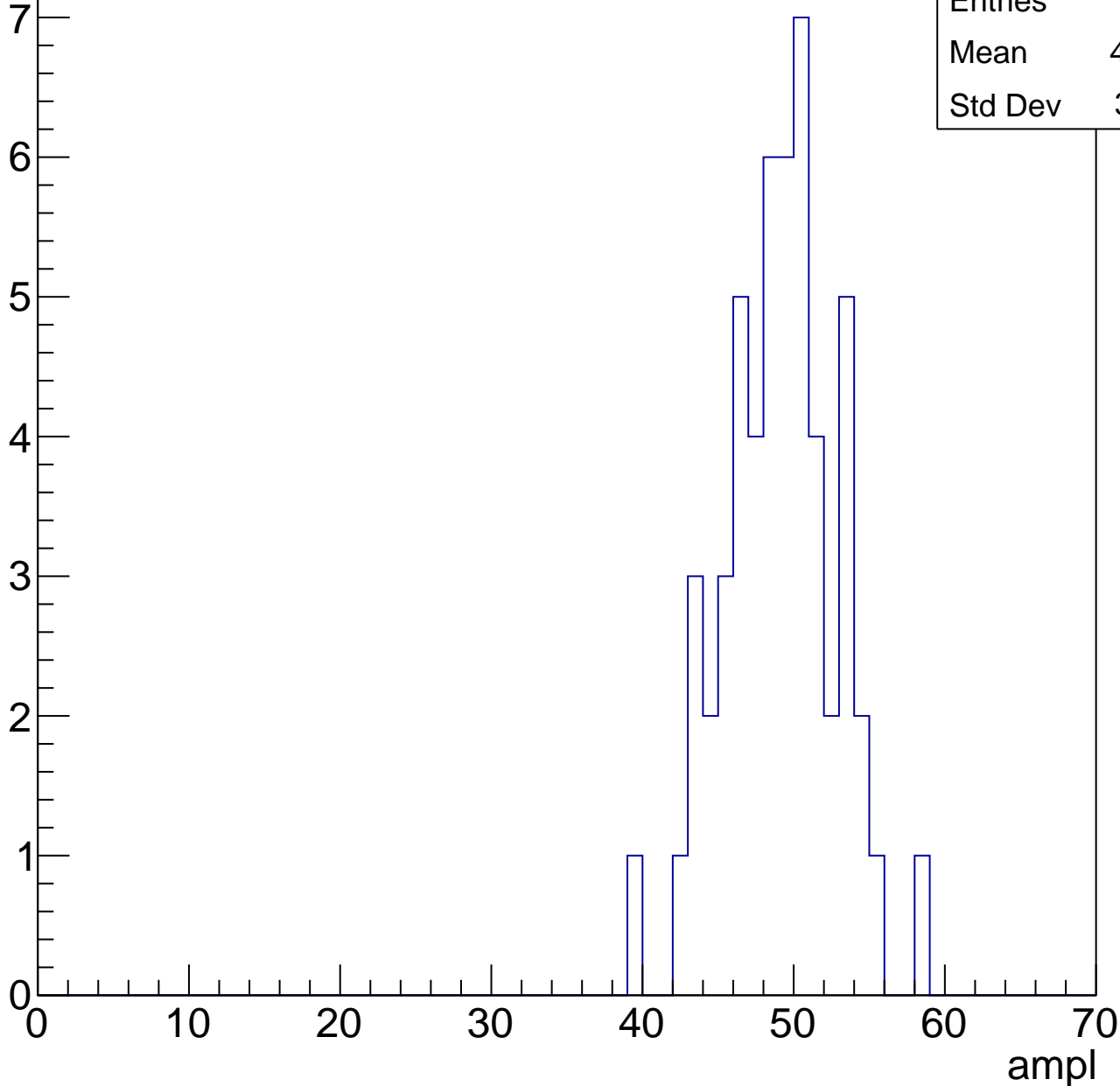


B1L102S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 48.62 |
| Std Dev | 3.641 |

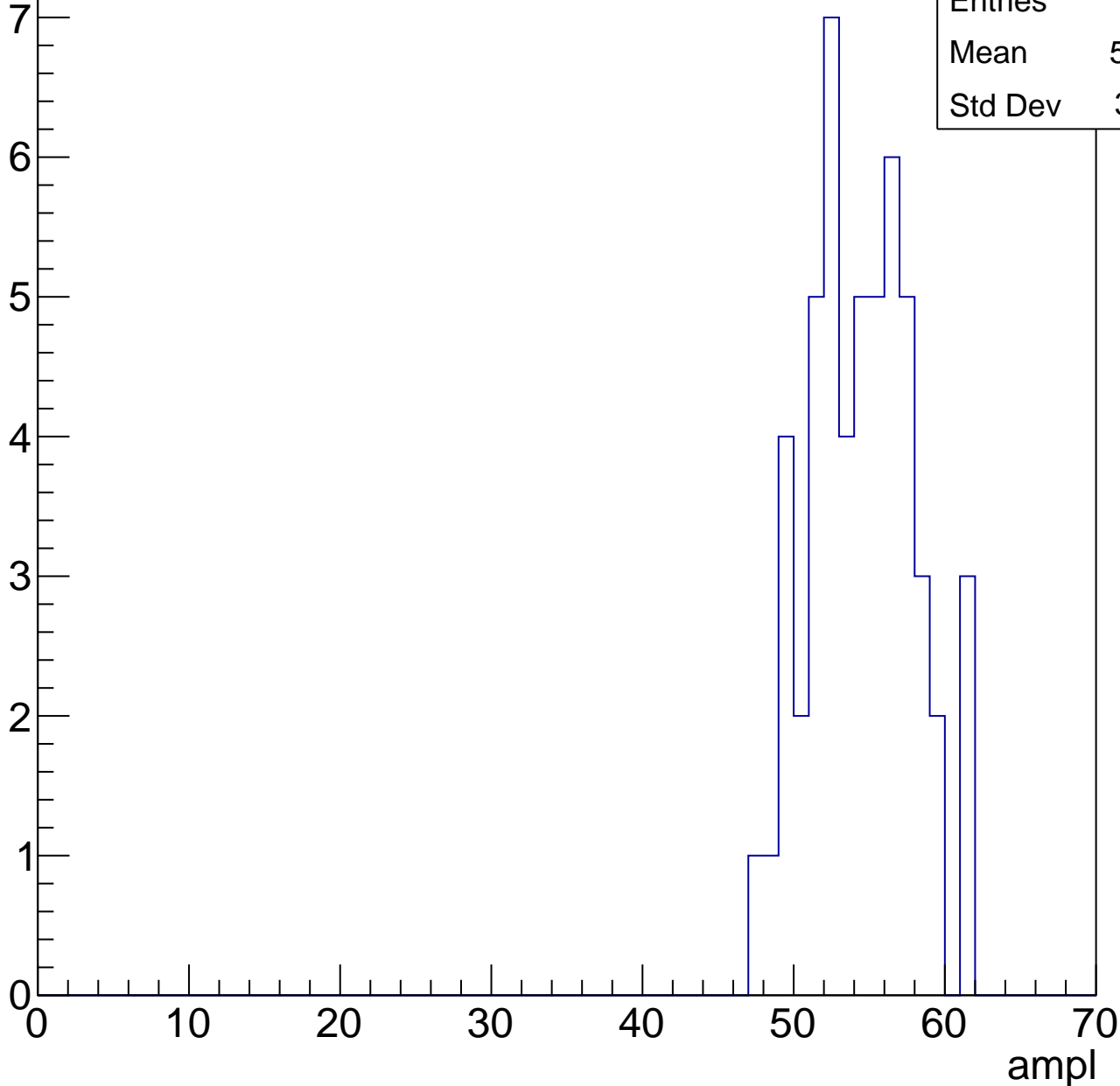


B1L102S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 54.02 |
| Std Dev | 3.401 |

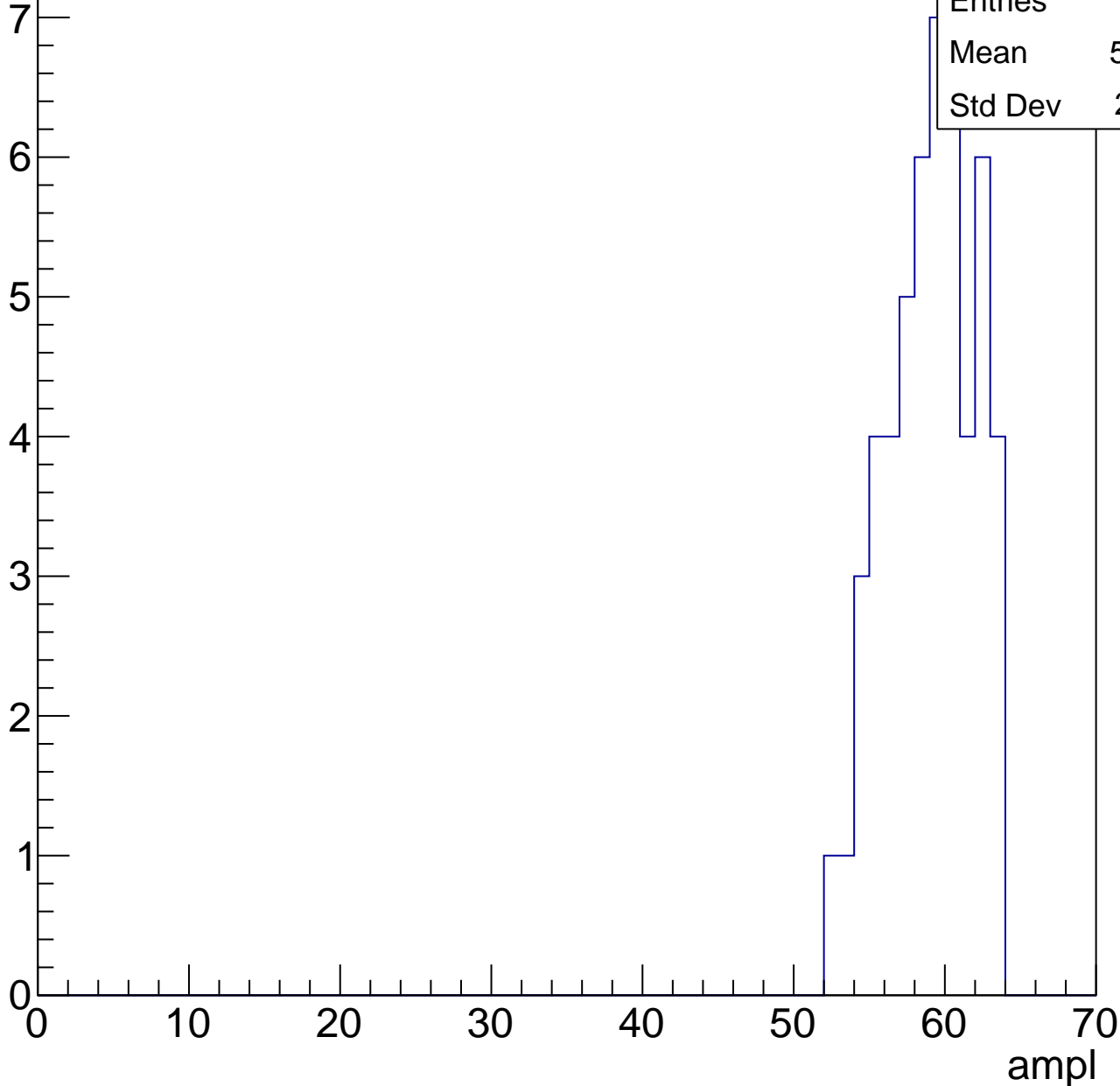


B1L102S, U15-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.56 |
| Std Dev | 2.831 |

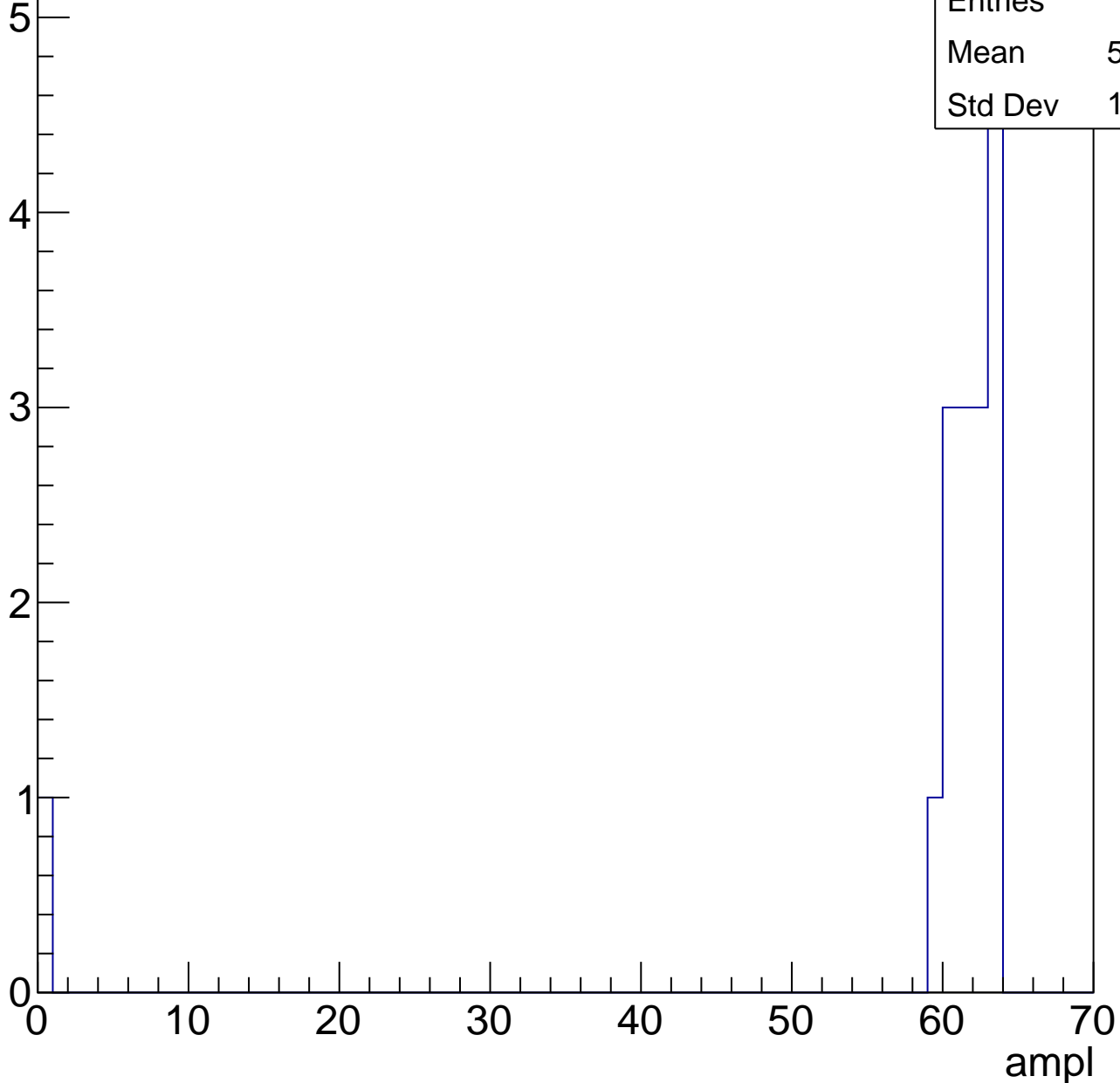


B1L102S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

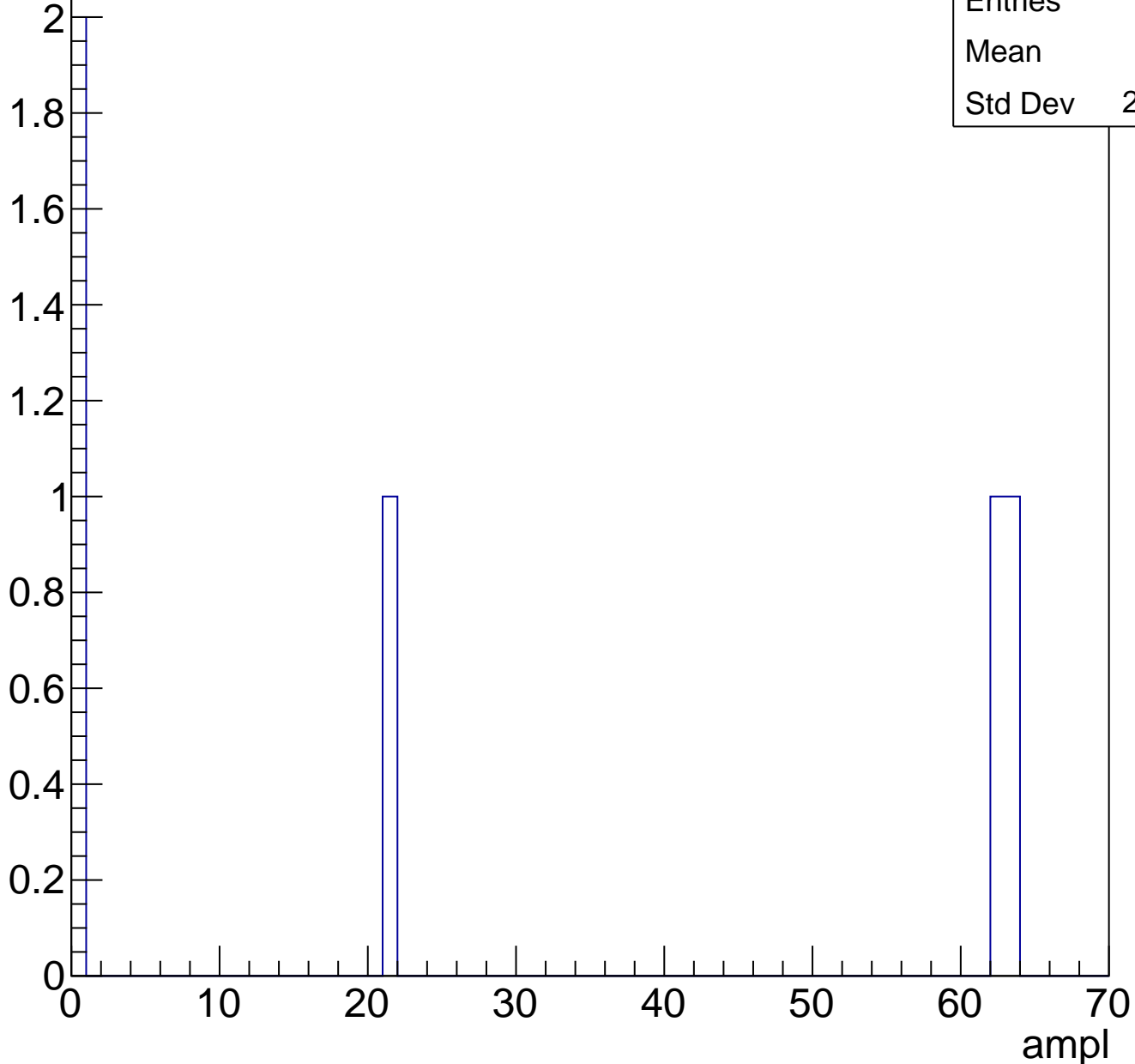
| | |
|---------|-------|
| Entries | 16 |
| Mean | 57.69 |
| Std Dev | 14.95 |



B1L102S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 5 |
| Mean | 29.2 |
| Std Dev | 28.25 |

B1L102S, U15-ch102, adc0

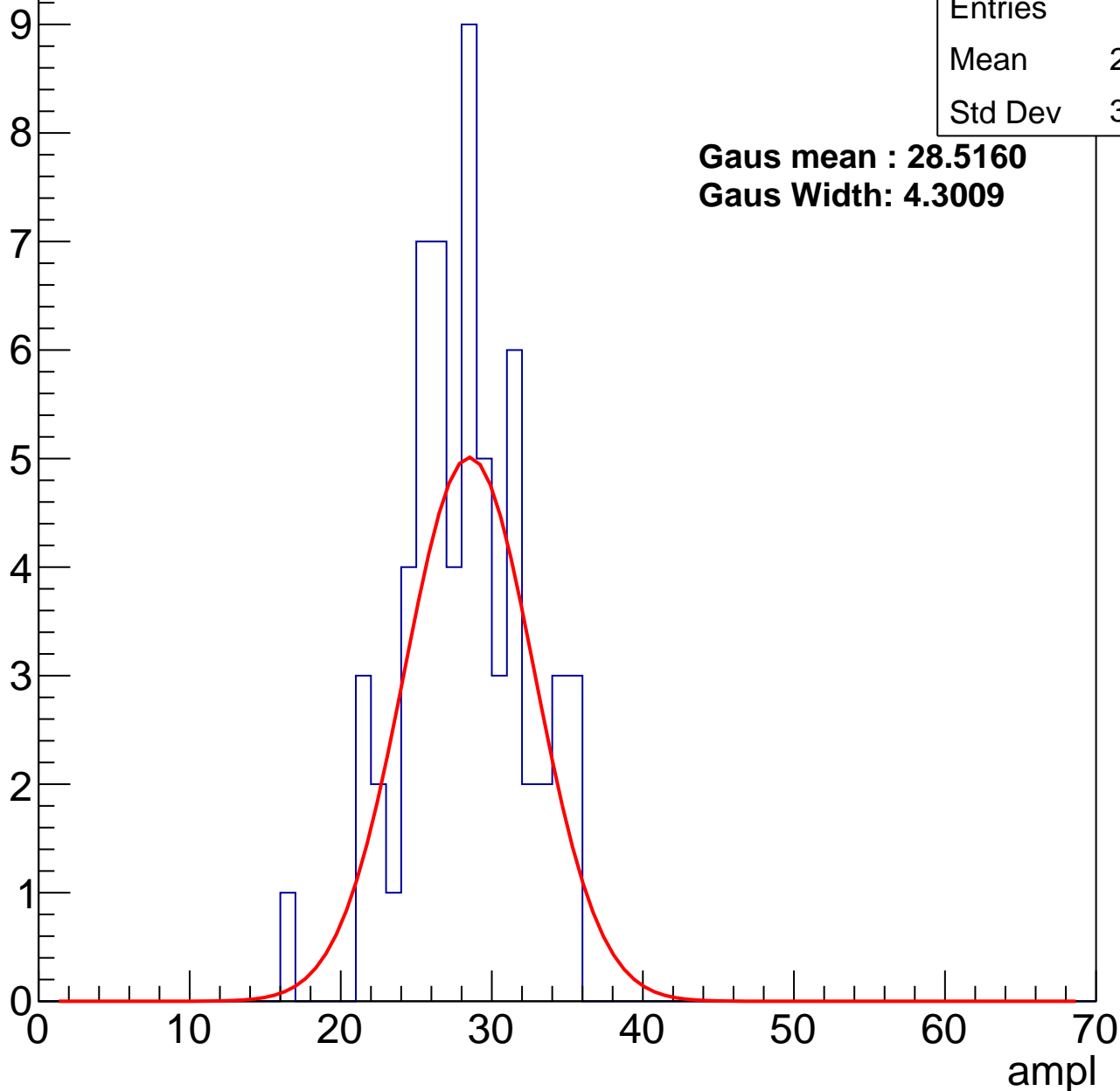
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 27.69 |
| Std Dev | 3.913 |

Gaus mean : 28.5160

Gaus Width: 4.3009



B1L102S, U15-ch102, adc1

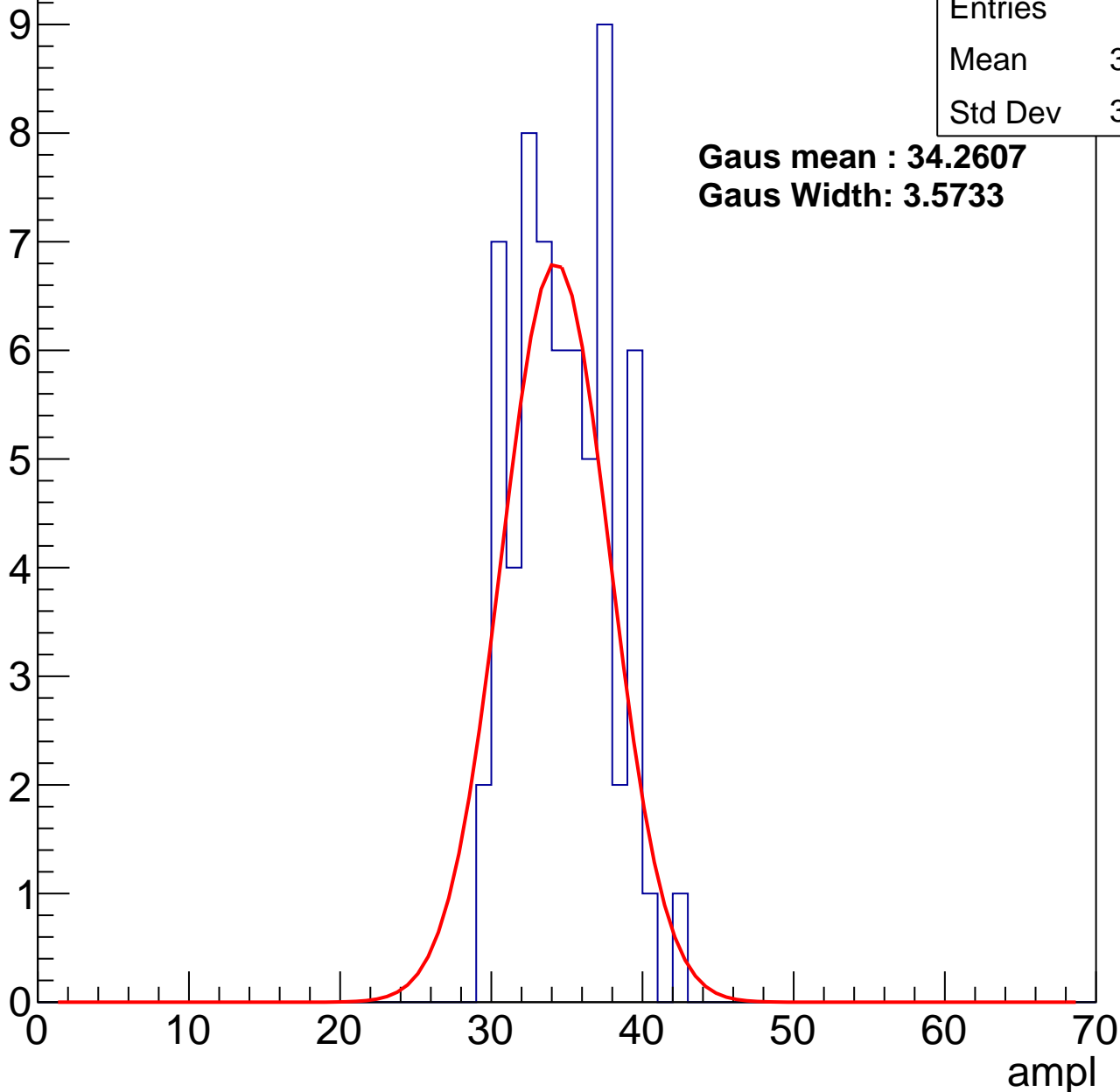
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 34.34 |
| Std Dev | 3.109 |

Gaus mean : 34.2607

Gaus Width: 3.5733



B1L102S, U15-ch102, adc2

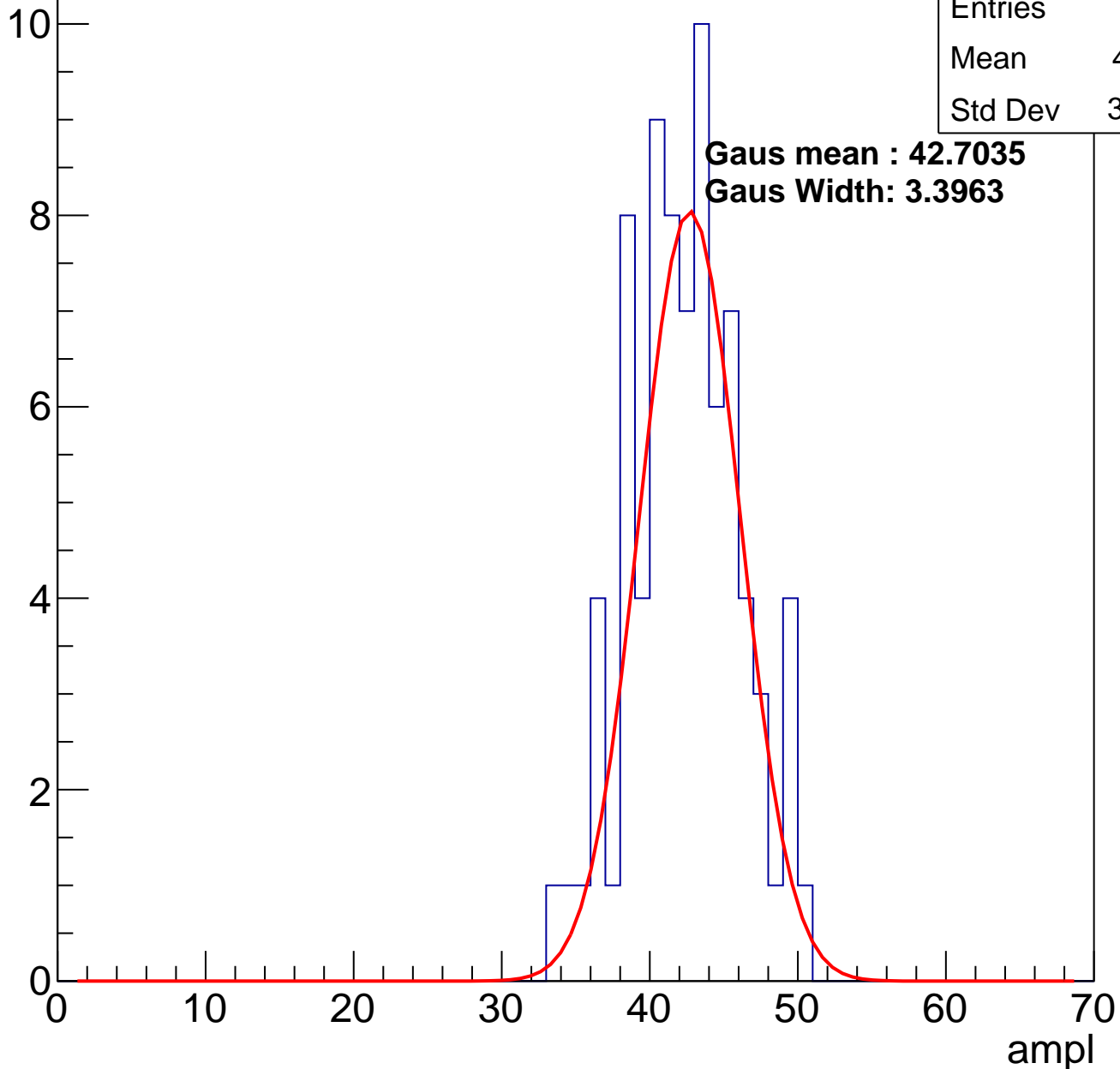
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 80 |
| Mean | 41.91 |
| Std Dev | 3.712 |

Gaus mean : 42.7035

Gaus Width: 3.3963

Entry

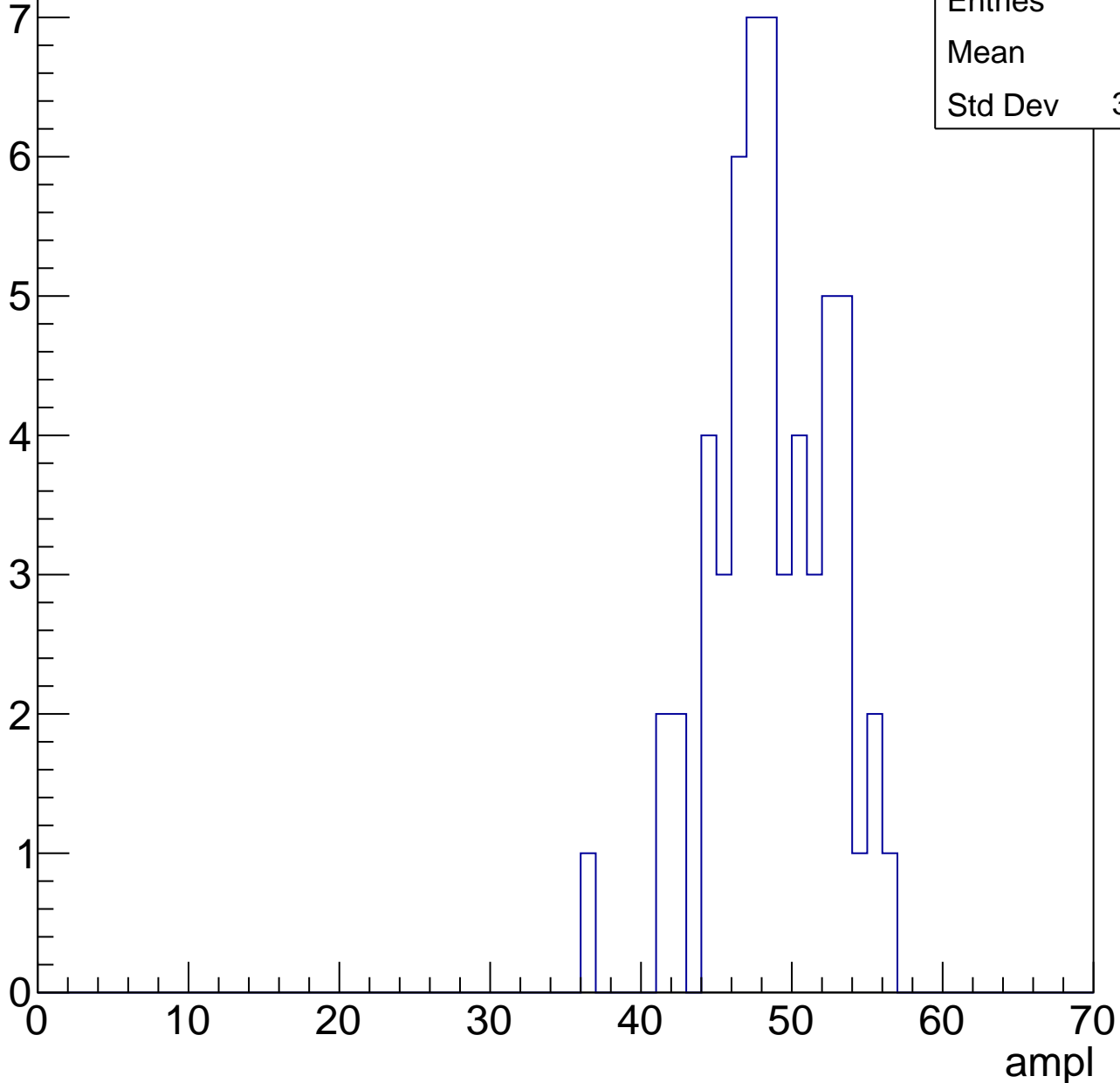


B1L102S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 48.2 |
| Std Dev | 3.971 |

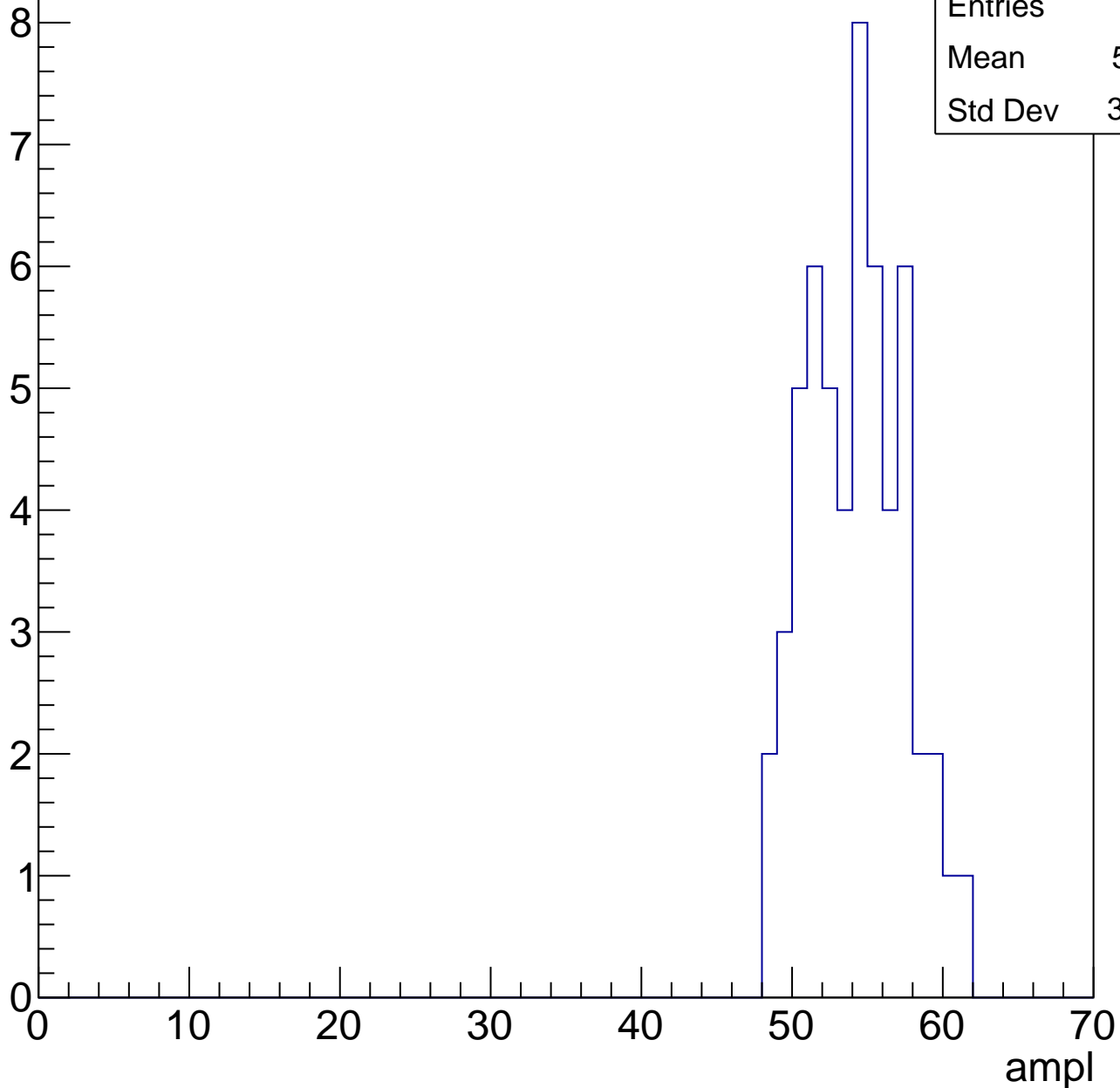


B1L102S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 53.71 |
| Std Dev | 3.143 |

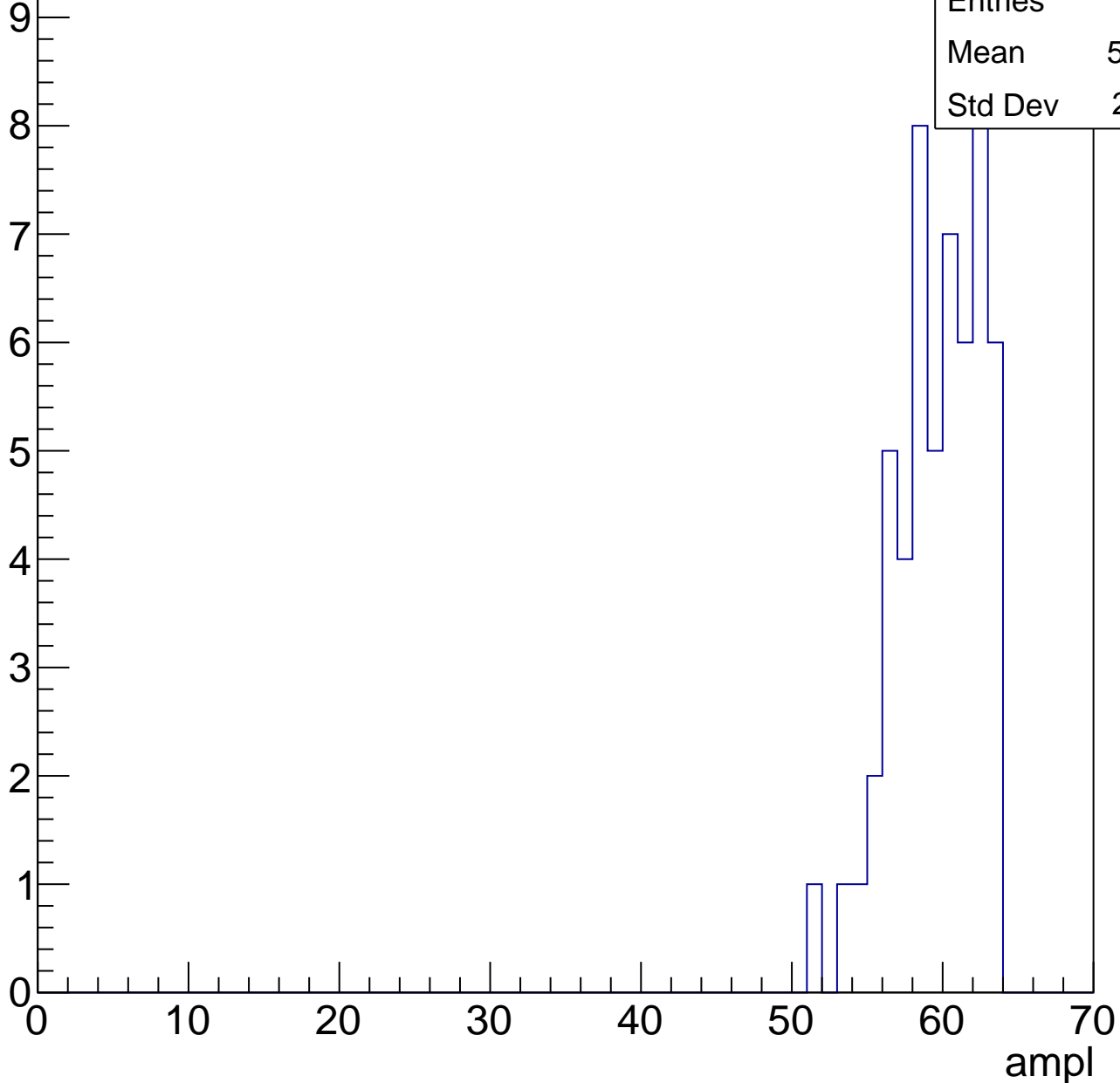


B1L102S, U15-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

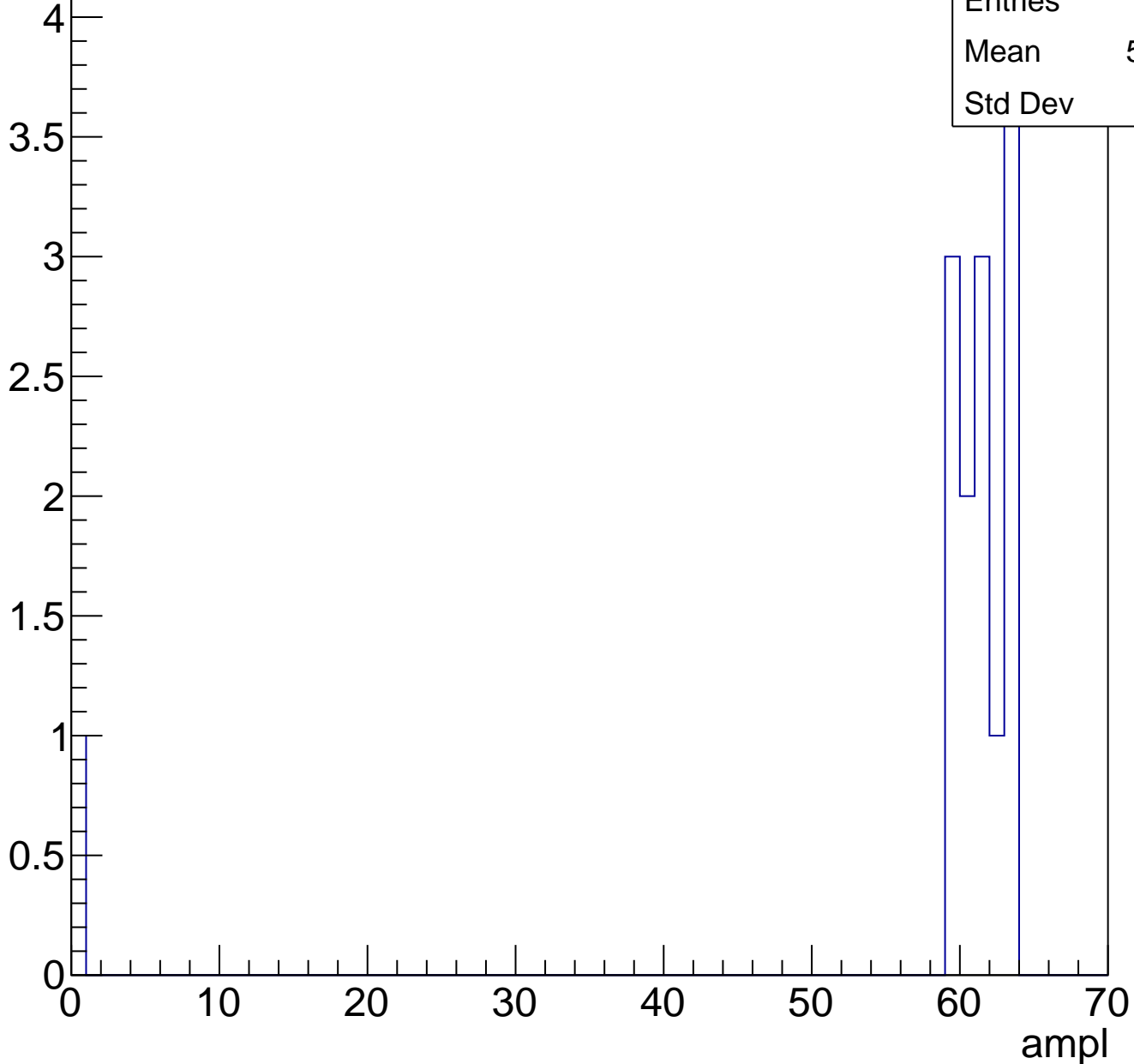
| | |
|---------|-------|
| Entries | 55 |
| Mean | 59.22 |
| Std Dev | 2.801 |



B1L102S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch103, adc0

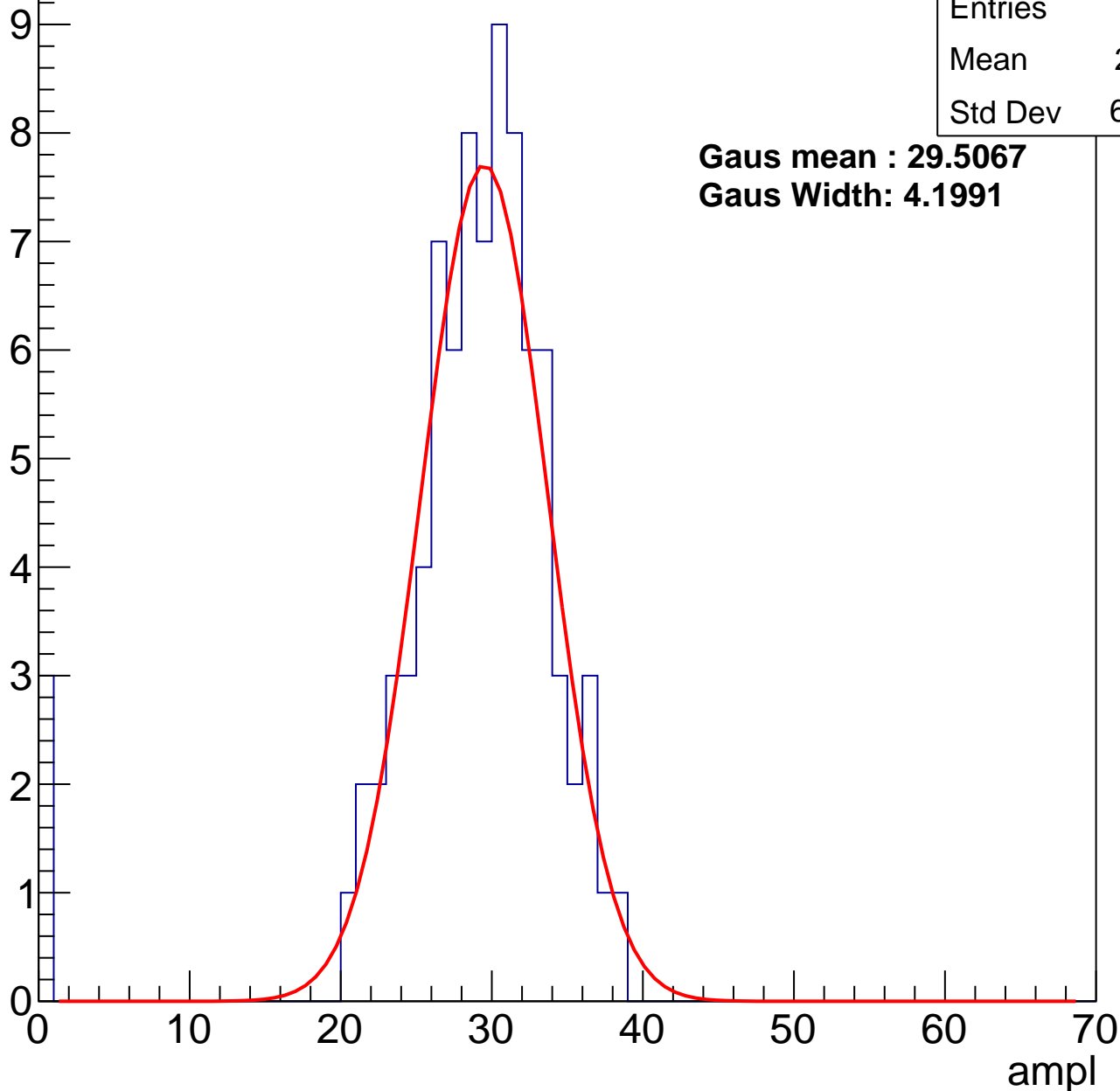
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 28.01 |
| Std Dev | 6.615 |

Gaus mean : 29.5067

Gaus Width: 4.1991



B1L102S, U15-ch103, adc1

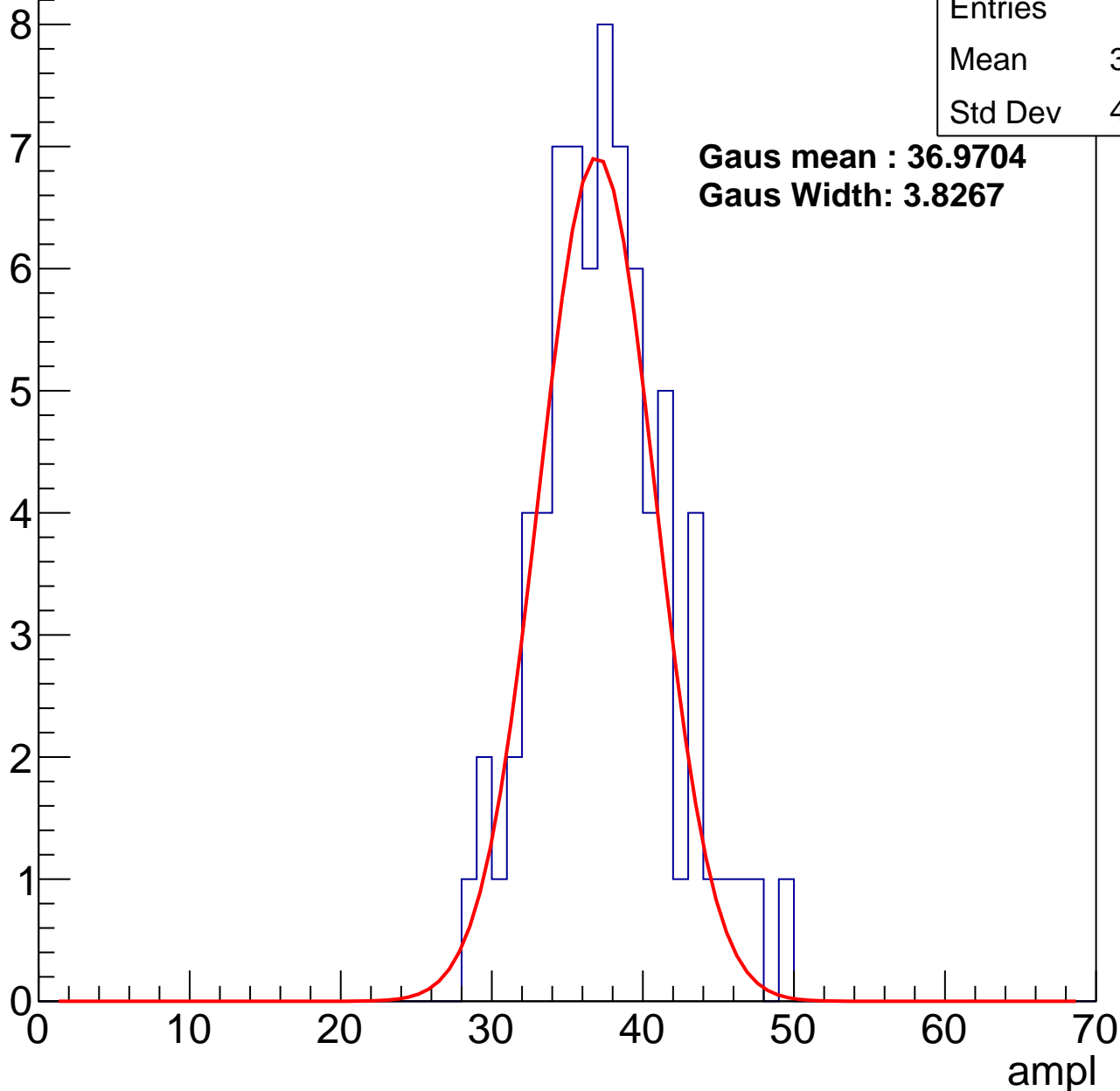
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 37.07 |
| Std Dev | 4.263 |

Gaus mean : 36.9704

Gaus Width: 3.8267



B1L102S, U15-ch103, adc2

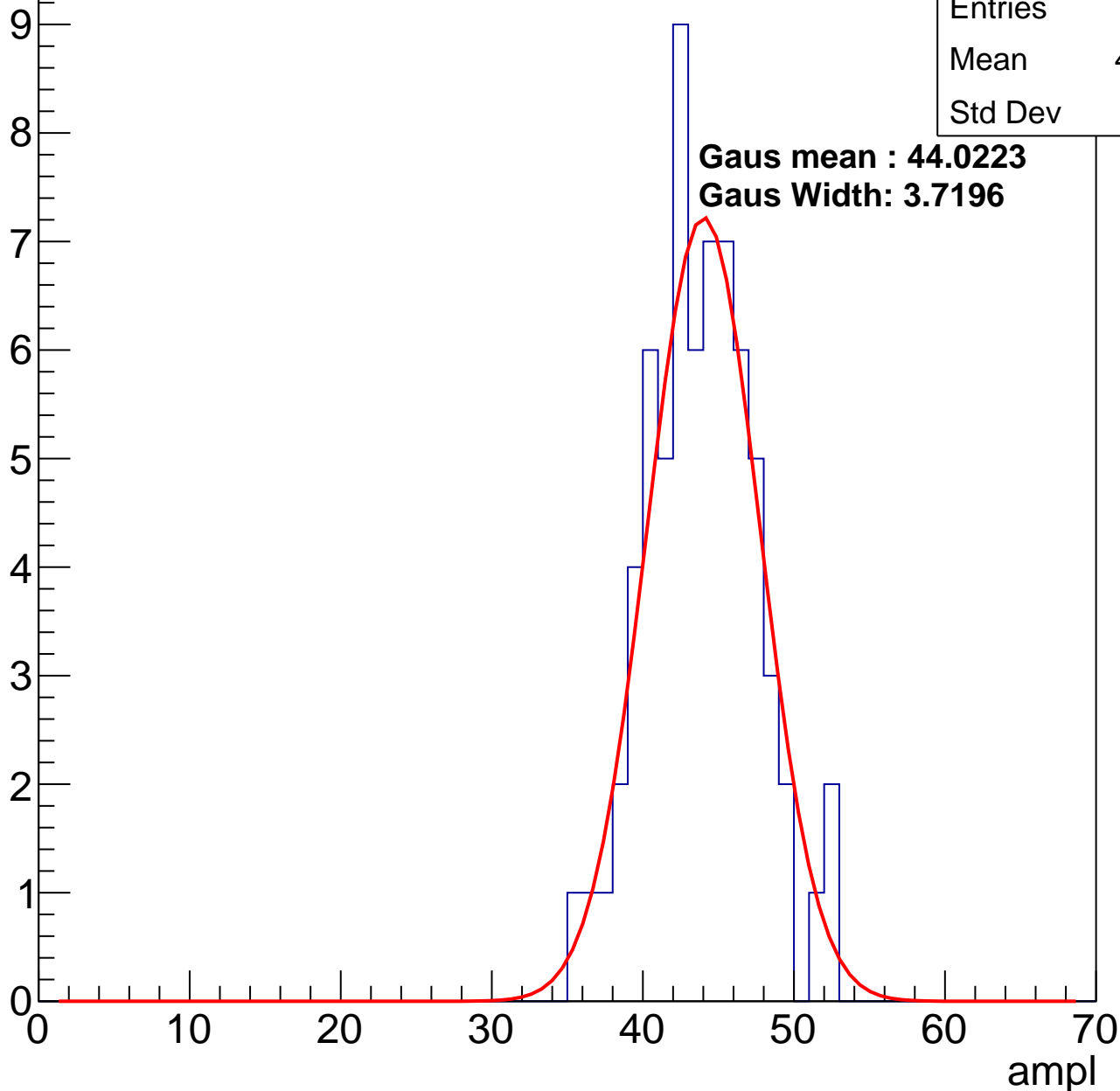
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 43.41 |
| Std Dev | 3.59 |

Gaus mean : 44.0223

Gaus Width: 3.7196

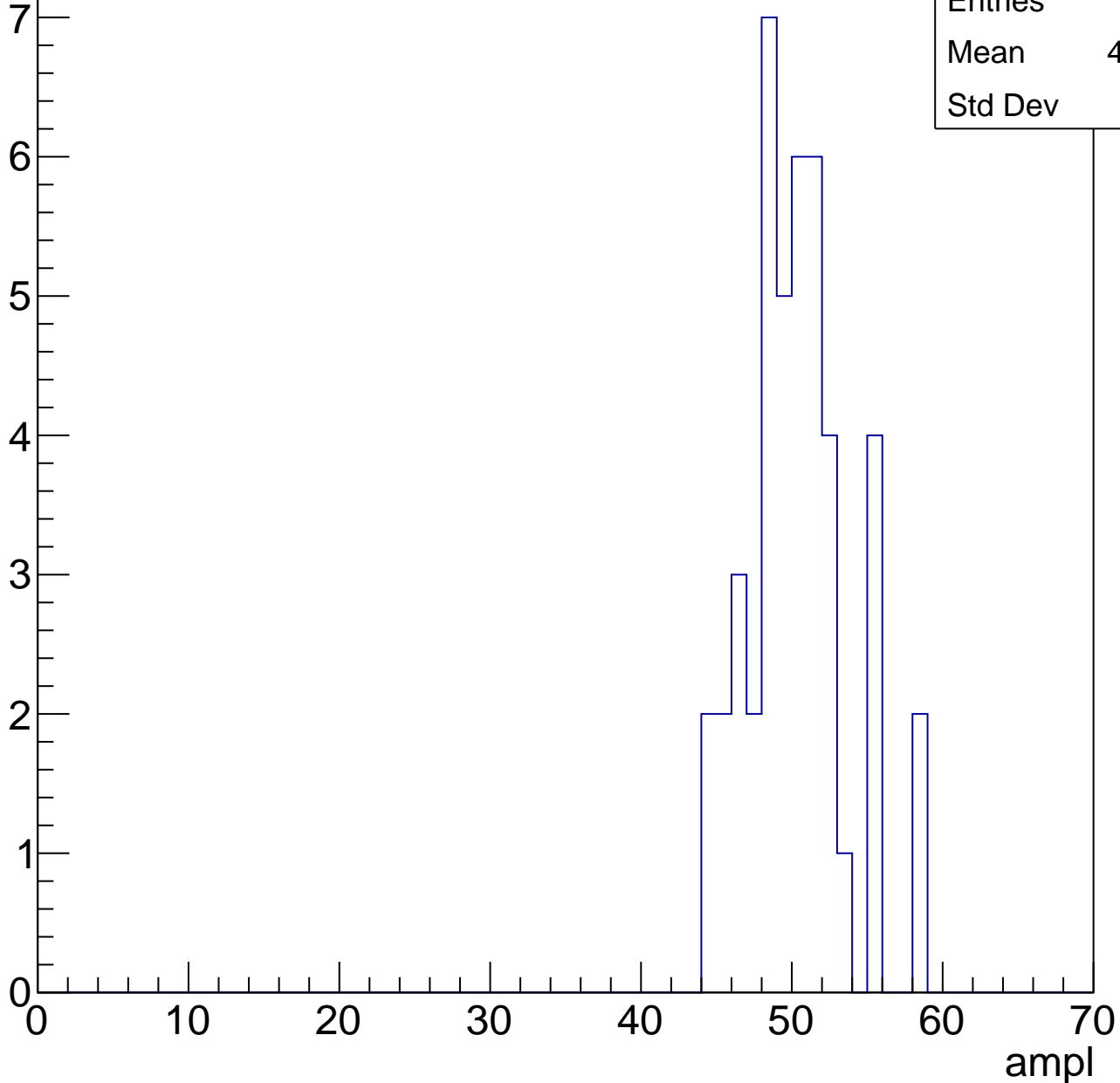


B1L102S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 44 |
| Mean | 49.86 |
| Std Dev | 3.3 |

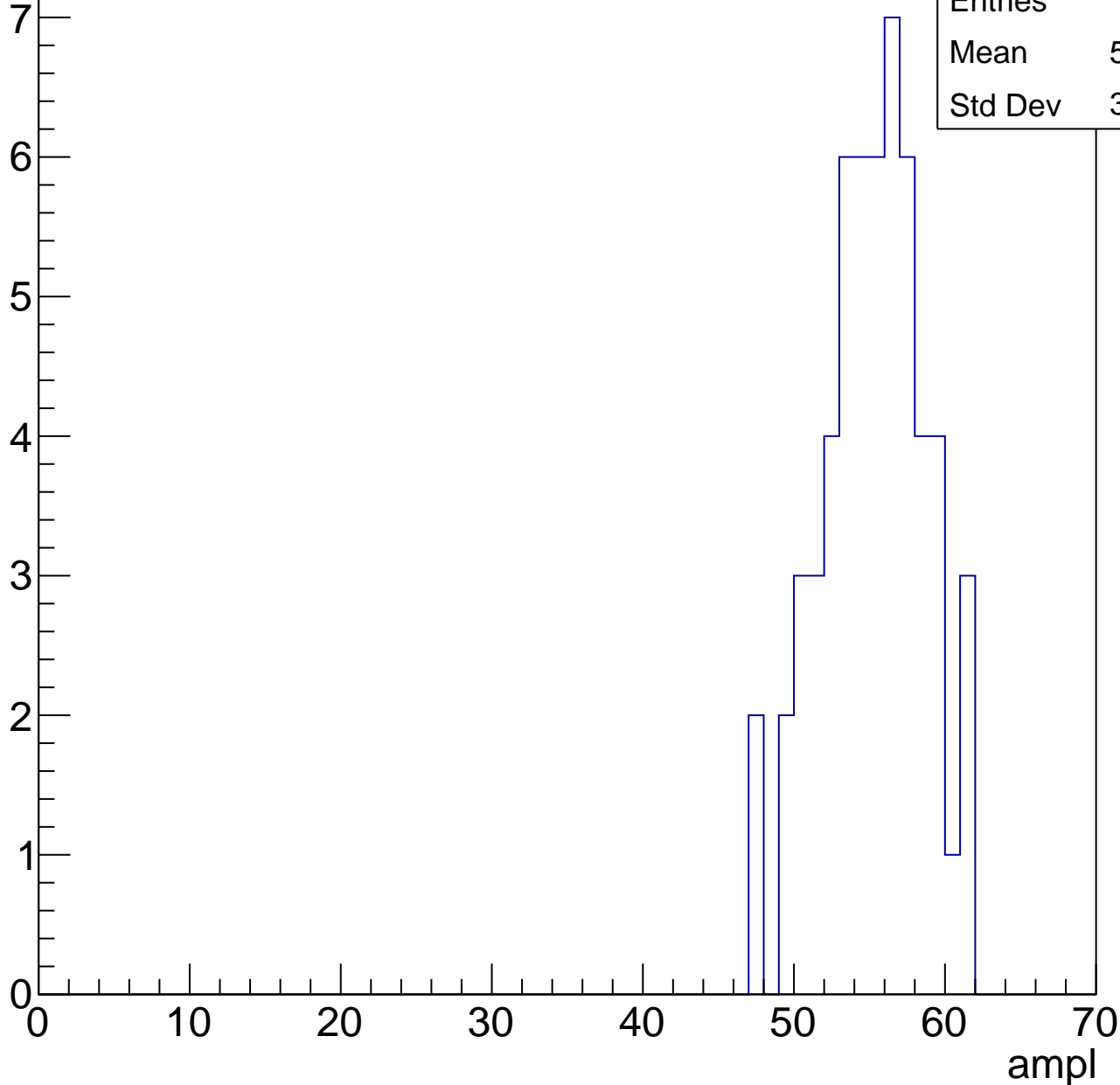


B1L102S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 54.74 |
| Std Dev | 3.374 |

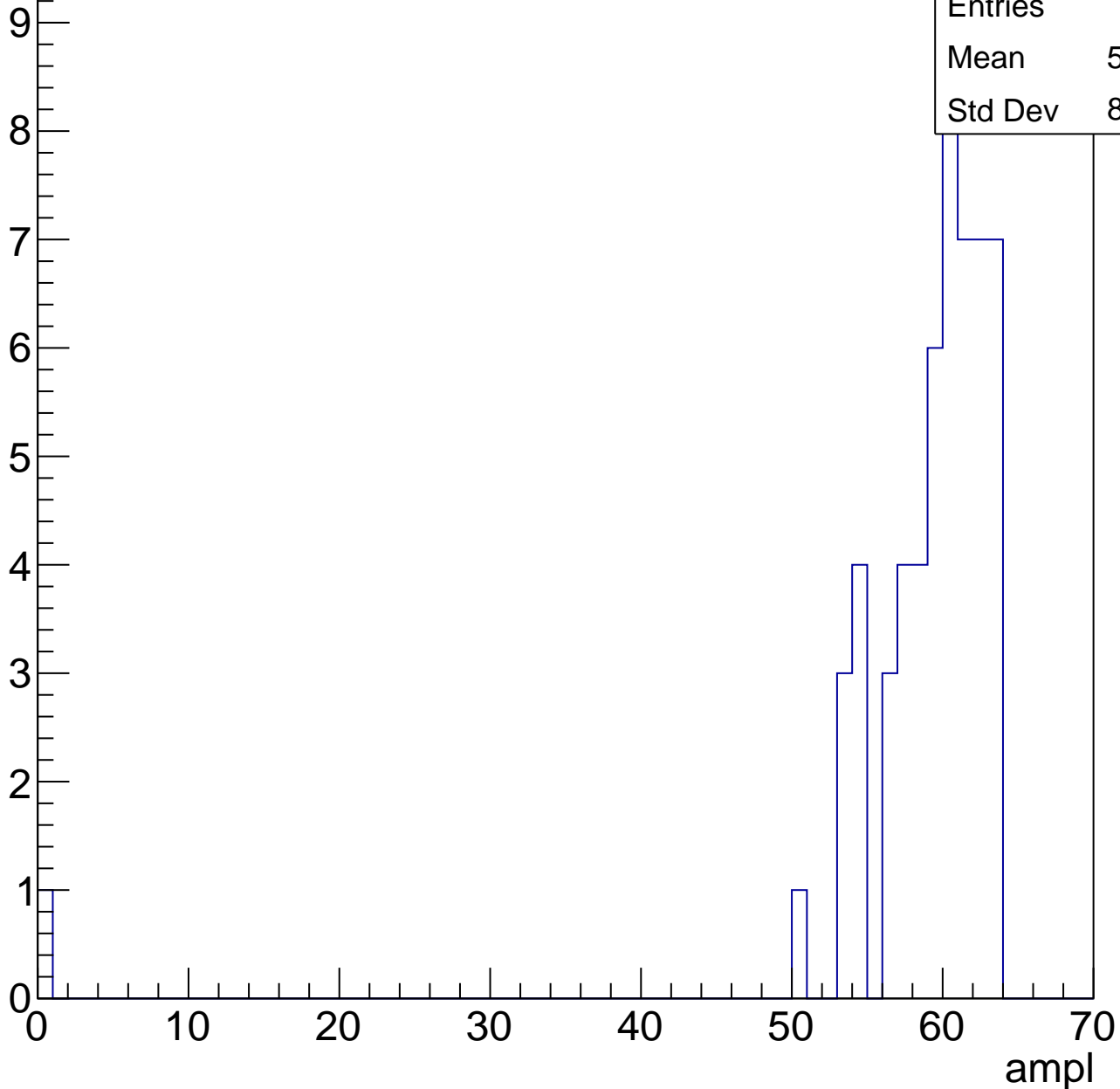


B1L102S, U15-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 58.02 |
| Std Dev | 8.425 |



B1L102S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 2 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch104, adc0

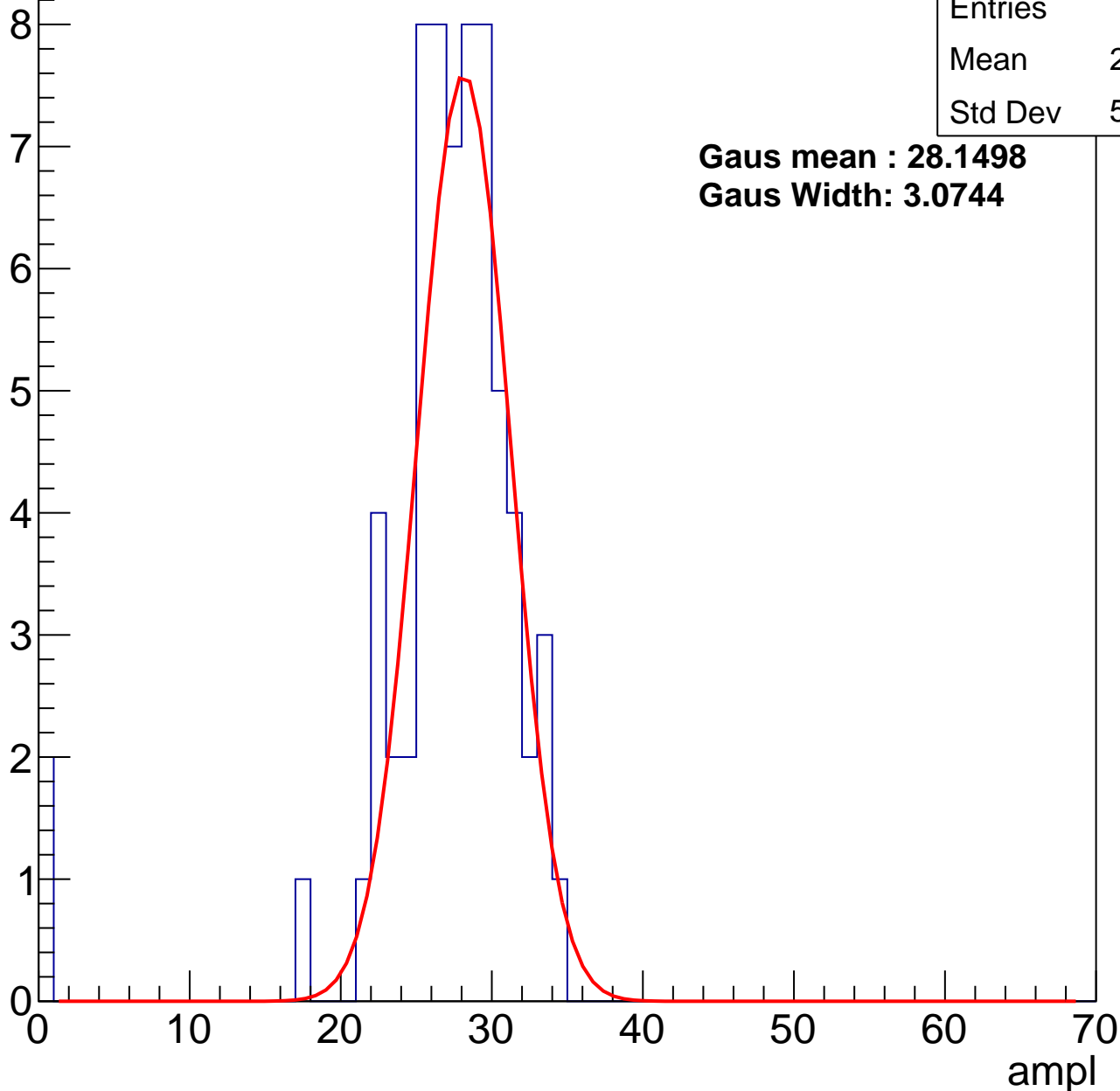
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 26.42 |
| Std Dev | 5.665 |

Gaus mean : 28.1498

Gaus Width: 3.0744



B1L102S, U15-ch104, adc1

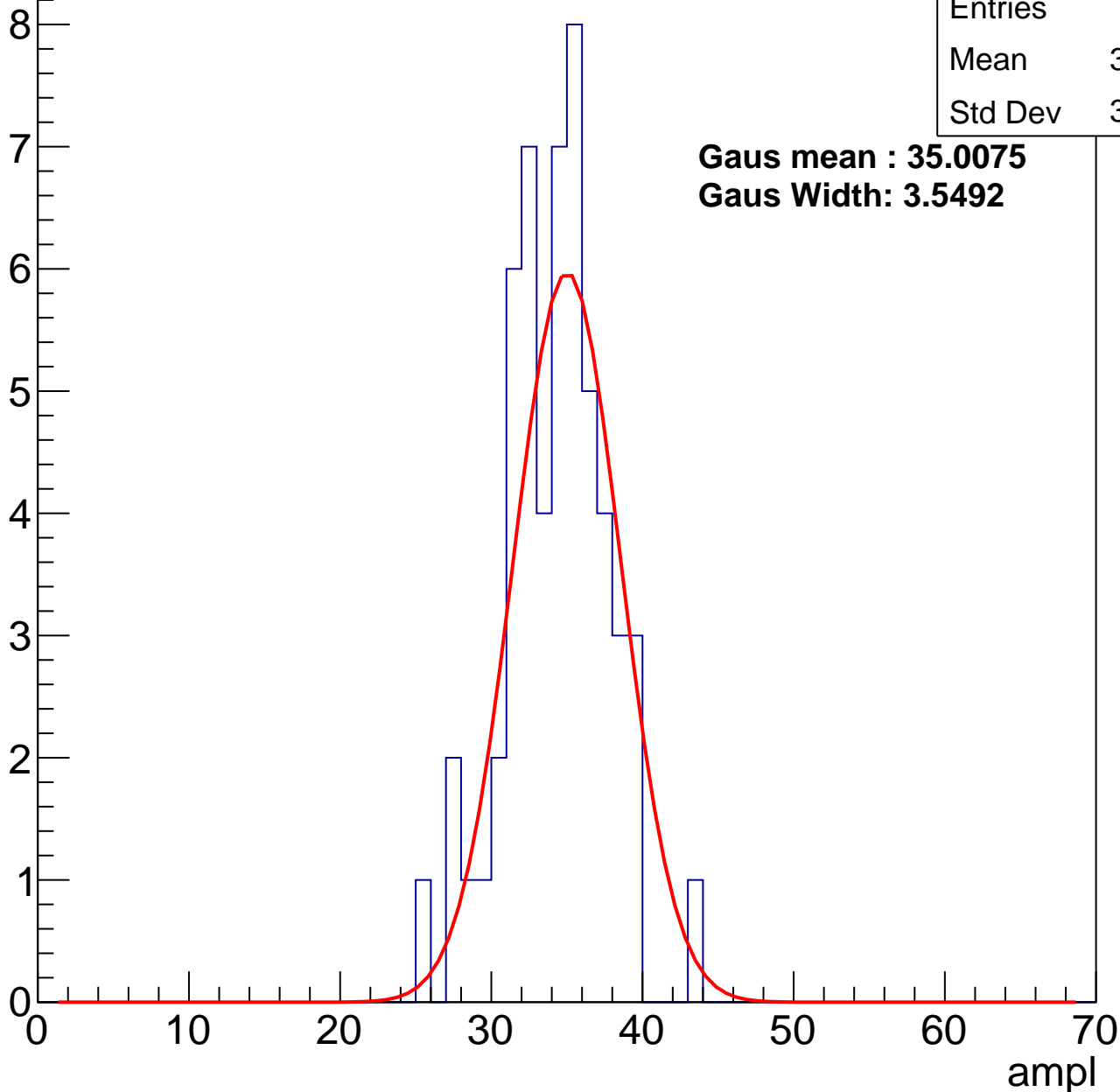
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 33.78 |
| Std Dev | 3.367 |

Gaus mean : 35.0075

Gaus Width: 3.5492



B1L102S, U15-ch104, adc2

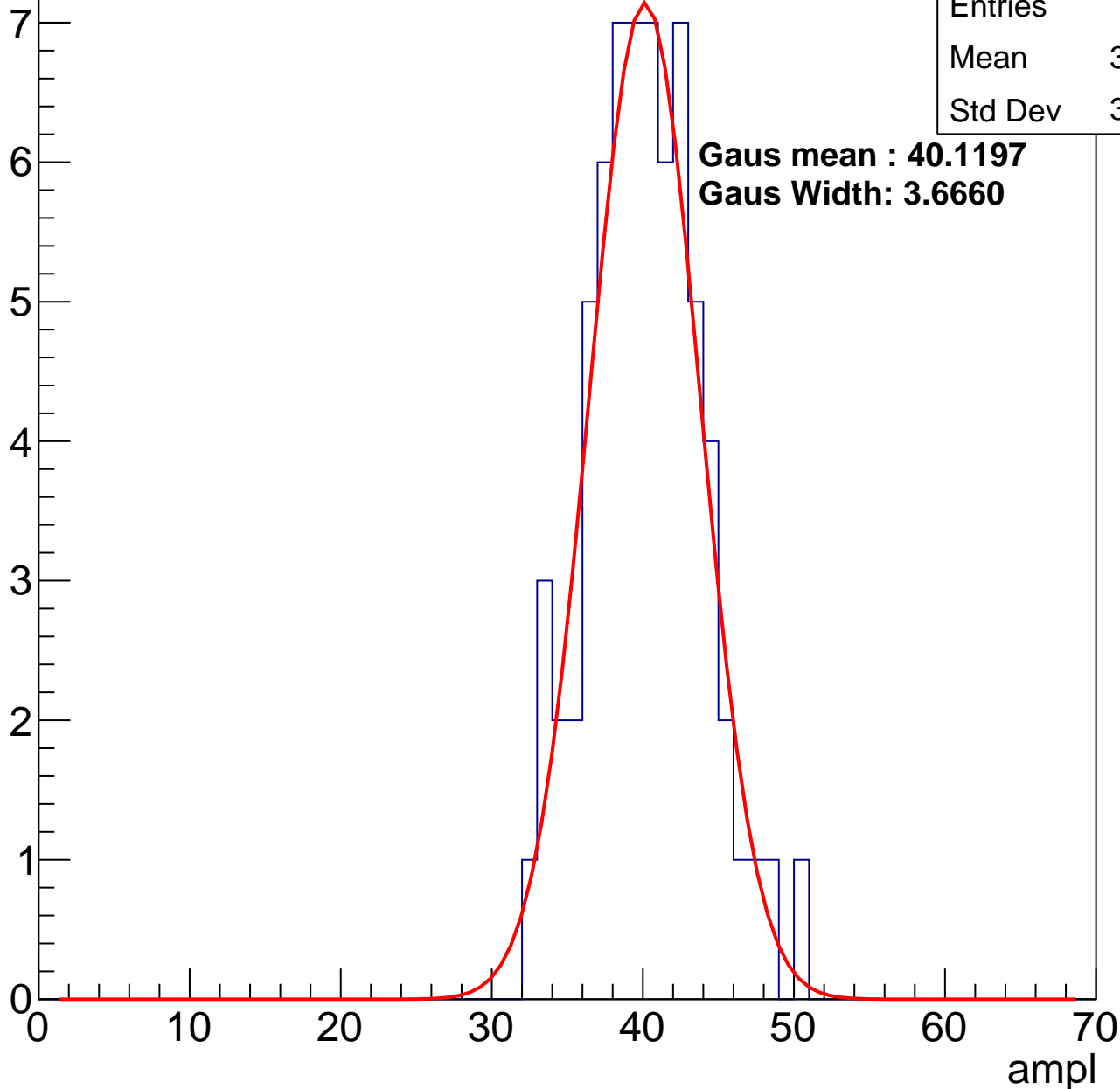
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 39.74 |
| Std Dev | 3.716 |

Gaus mean : 40.1197

Gaus Width: 3.6660

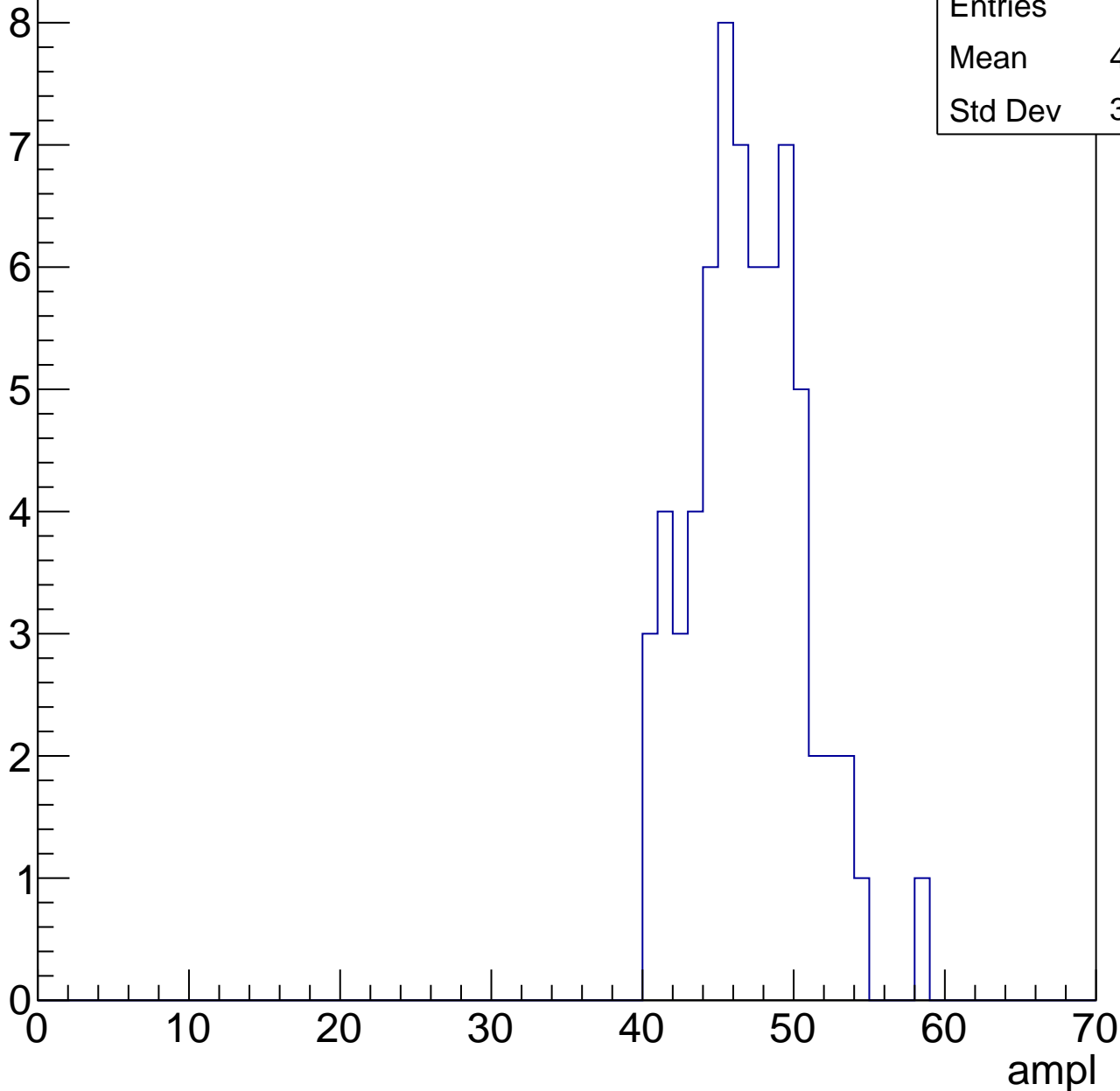


B1L102S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 46.49 |
| Std Dev | 3.683 |

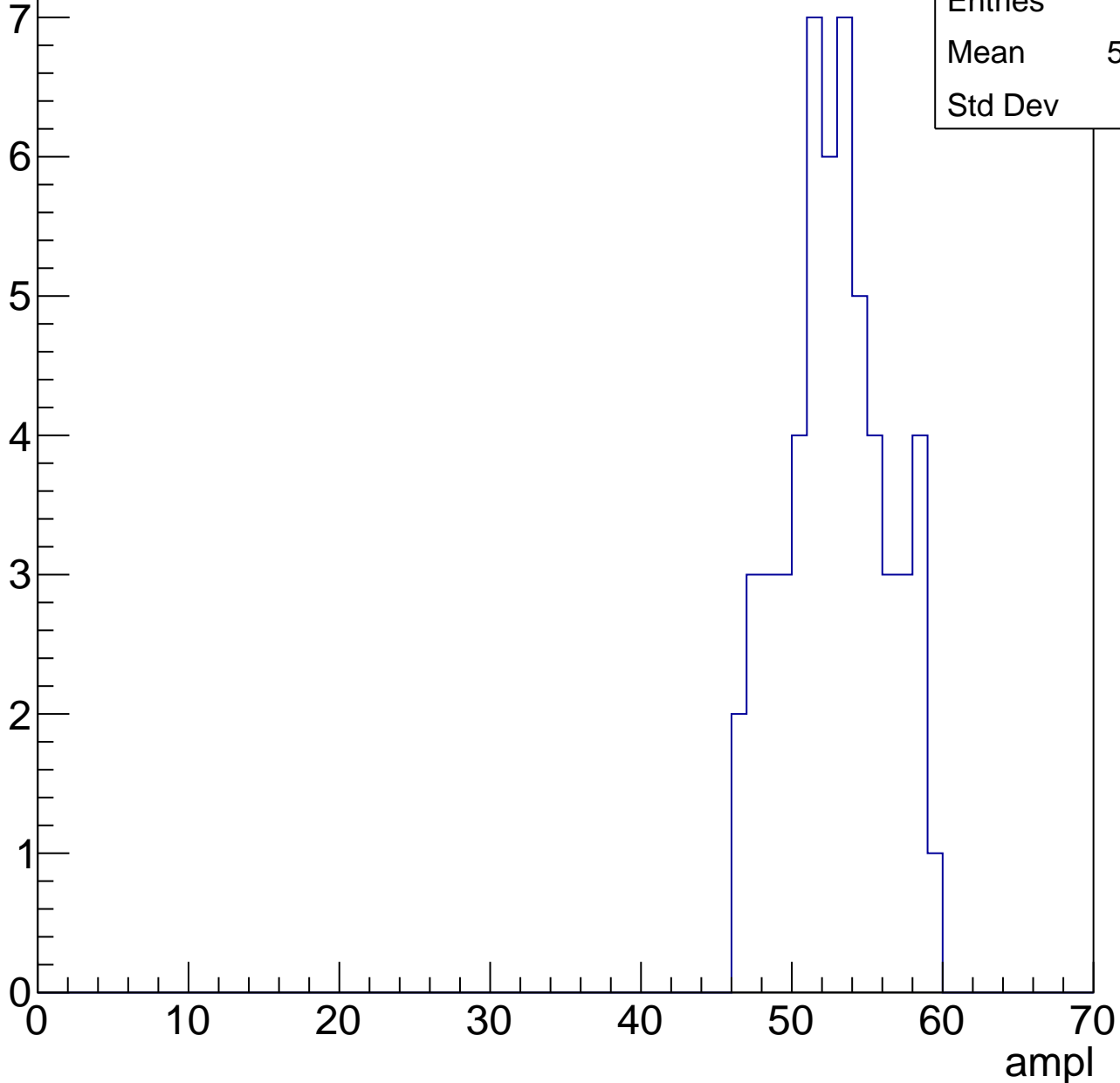


B1L102S, U15-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 52.44 |
| Std Dev | 3.34 |

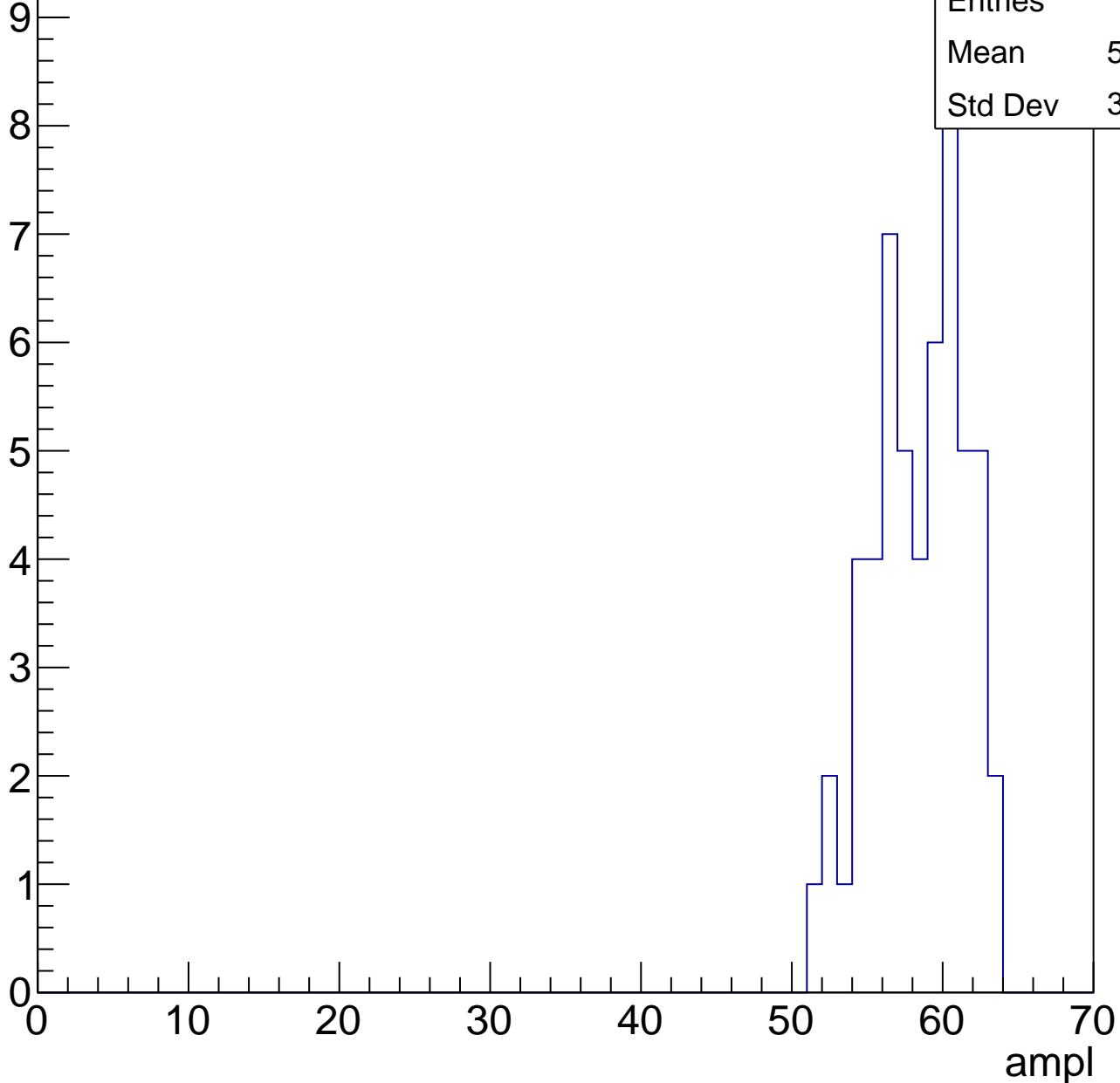


B1L102S, U15-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 57.96 |
| Std Dev | 3.003 |

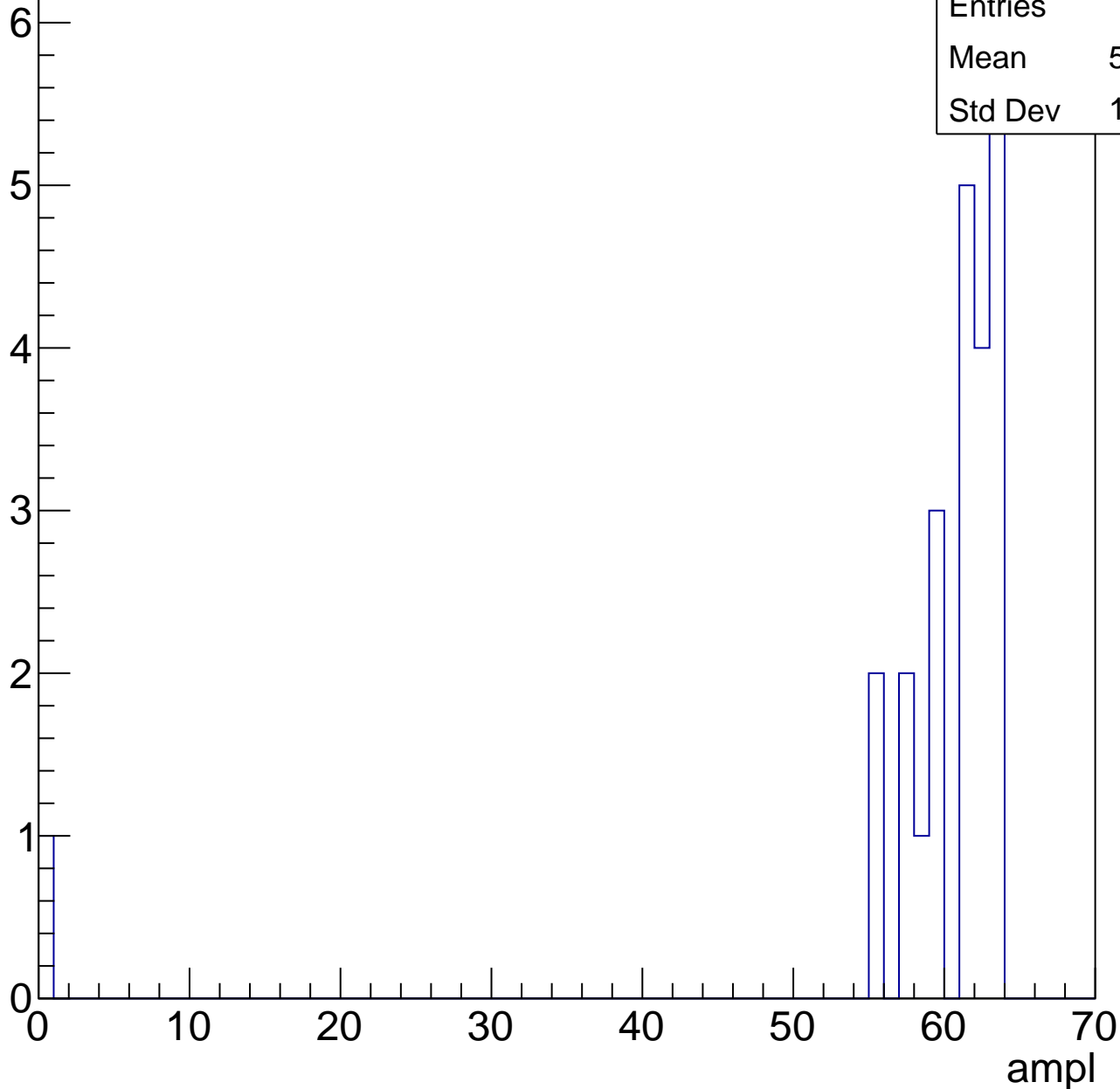


B1L102S, U15-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

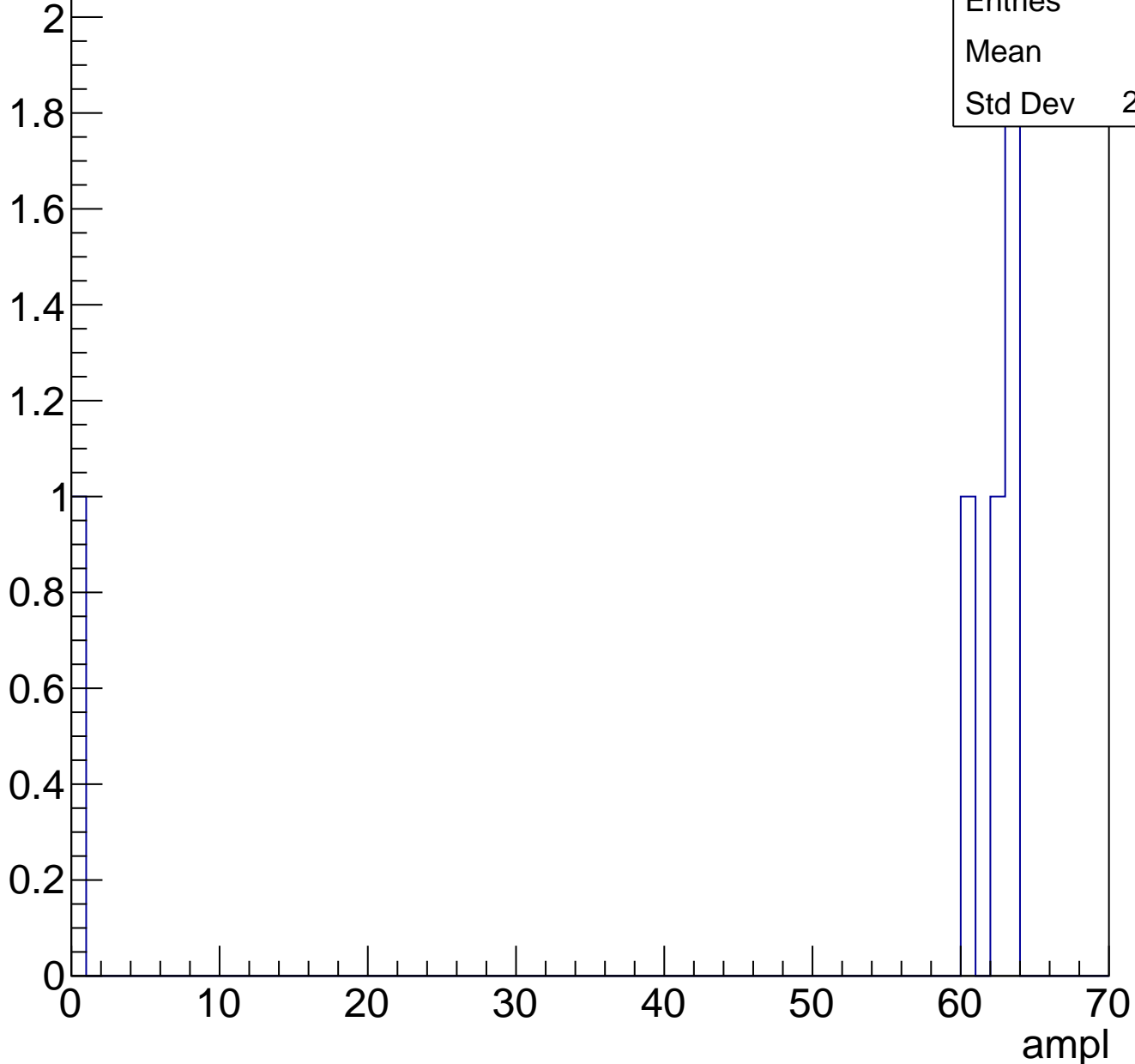
| | |
|---------|-------|
| Entries | 24 |
| Mean | 57.92 |
| Std Dev | 12.33 |



B1L102S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch105, adc0

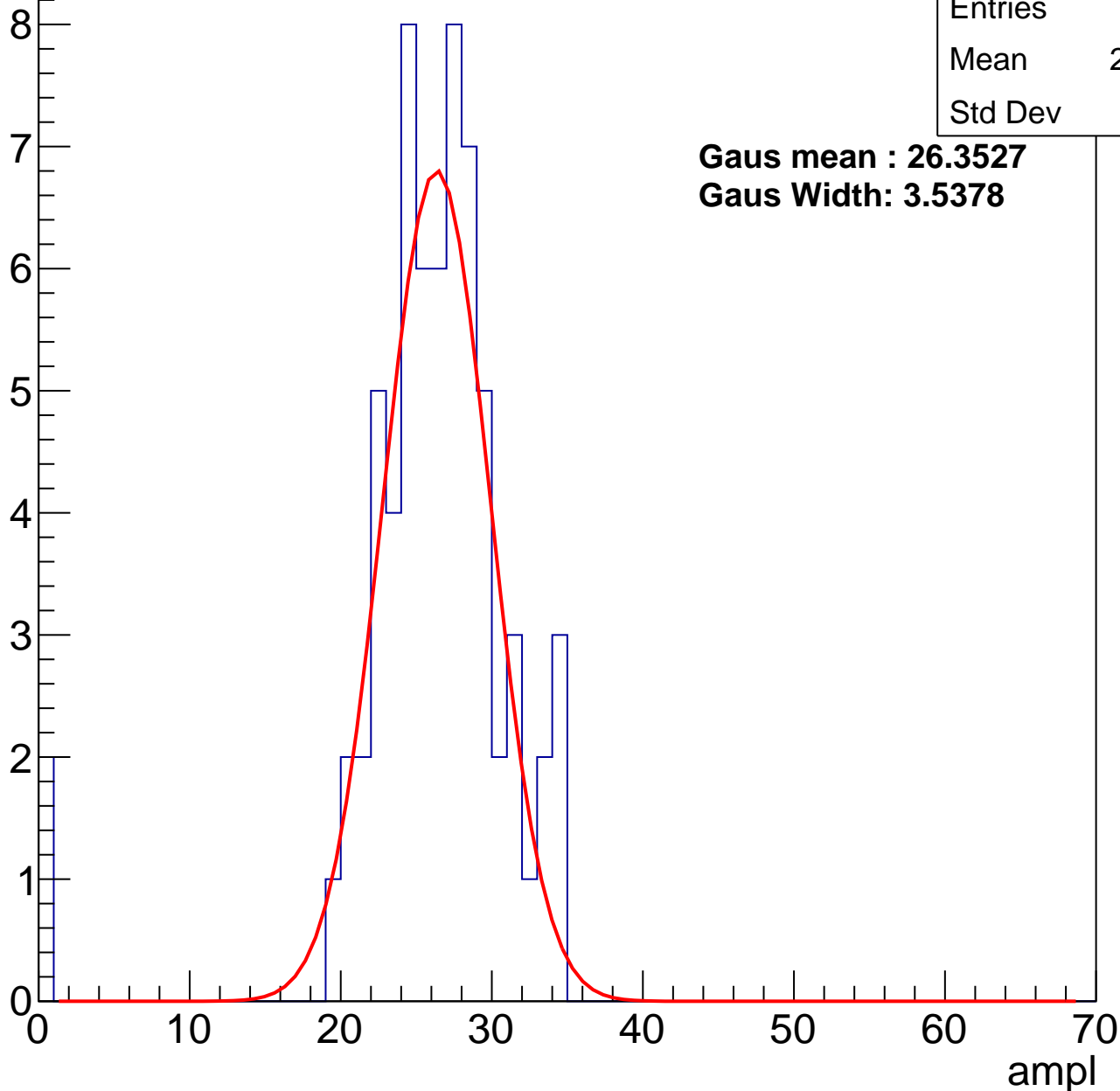
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 25.54 |
| Std Dev | 5.7 |

Gaus mean : 26.3527

Gaus Width: 3.5378



B1L102S, U15-ch105, adc1

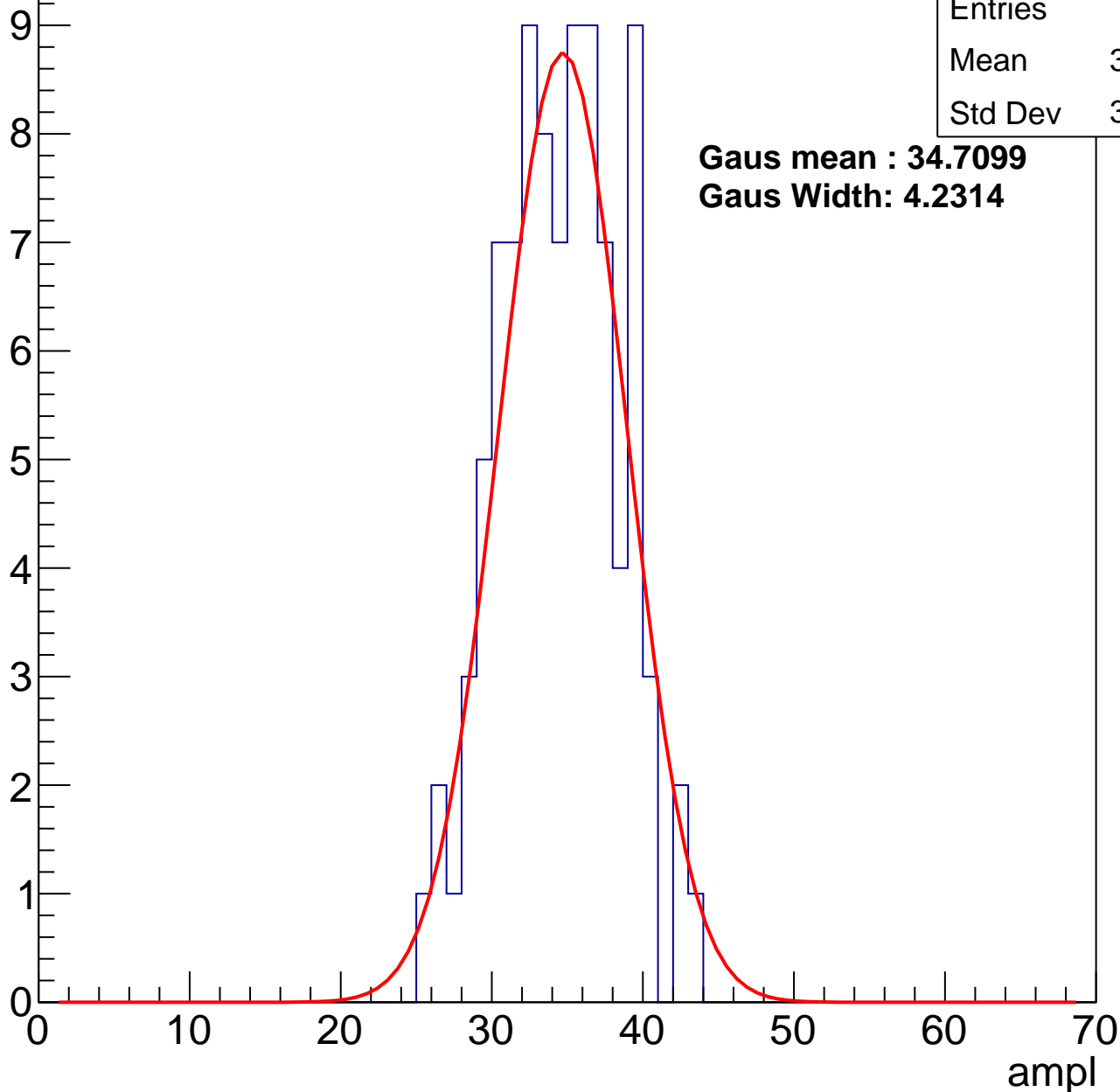
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 94 |
| Mean | 34.02 |
| Std Dev | 3.892 |

Gaus mean : 34.7099

Gaus Width: 4.2314



B1L102S, U15-ch105, adc2

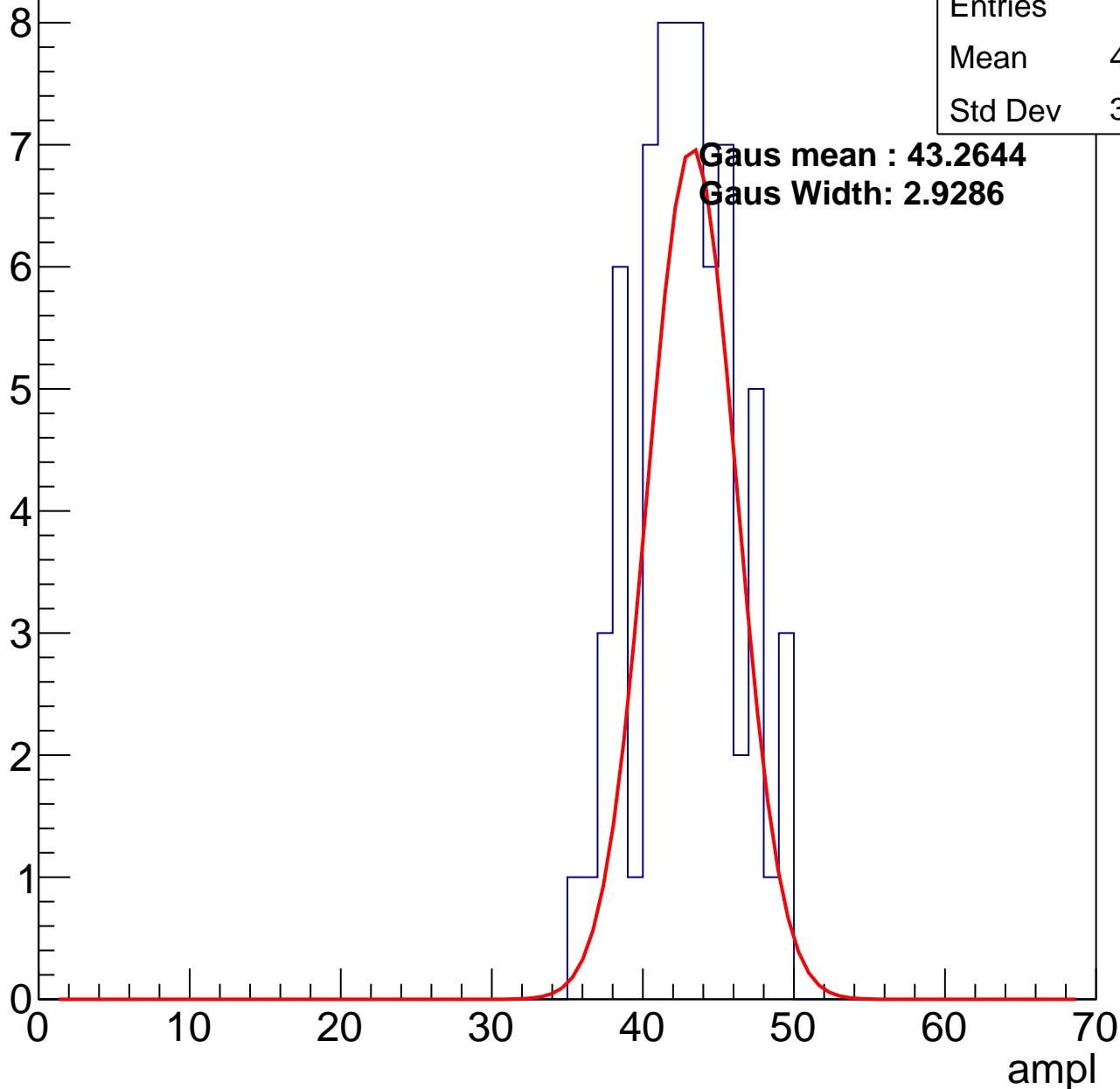
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 42.36 |
| Std Dev | 3.295 |

Gaus mean : 43.2644

Gaus Width: 2.9286

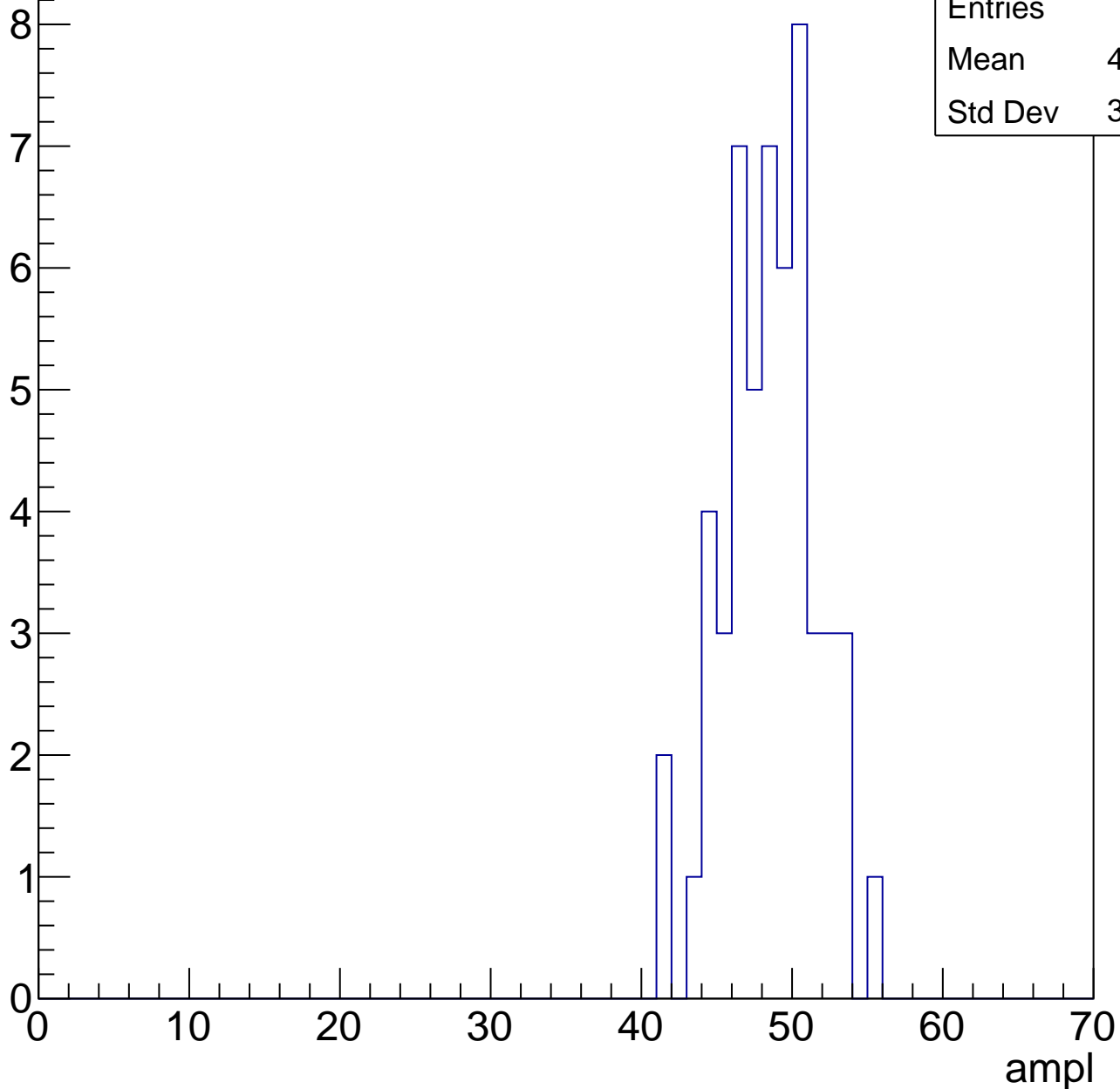


B1L102S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 48.04 |
| Std Dev | 3.022 |

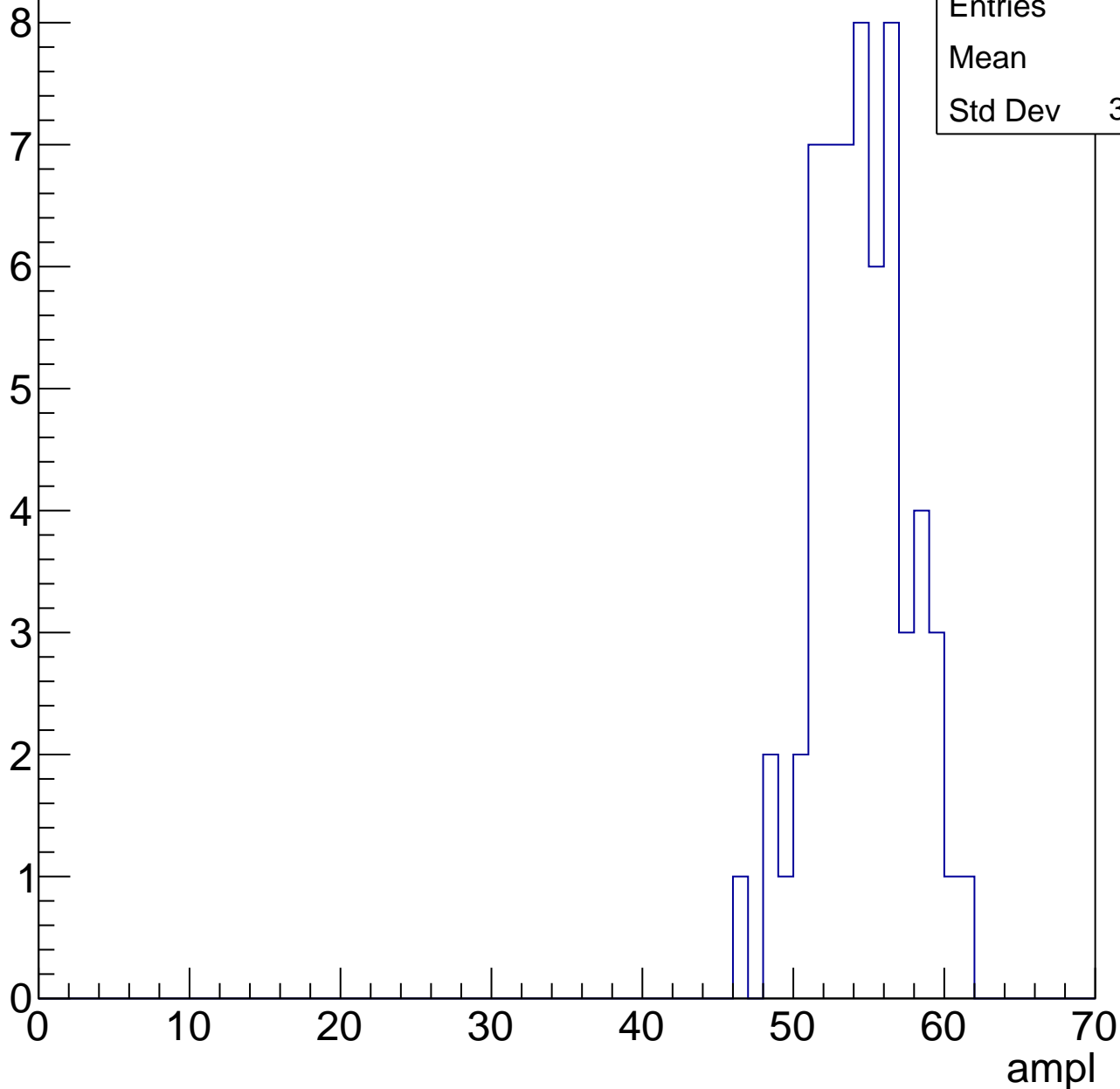


B1L102S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 54 |
| Std Dev | 3.084 |



B1L102S, U15-ch105, adc5

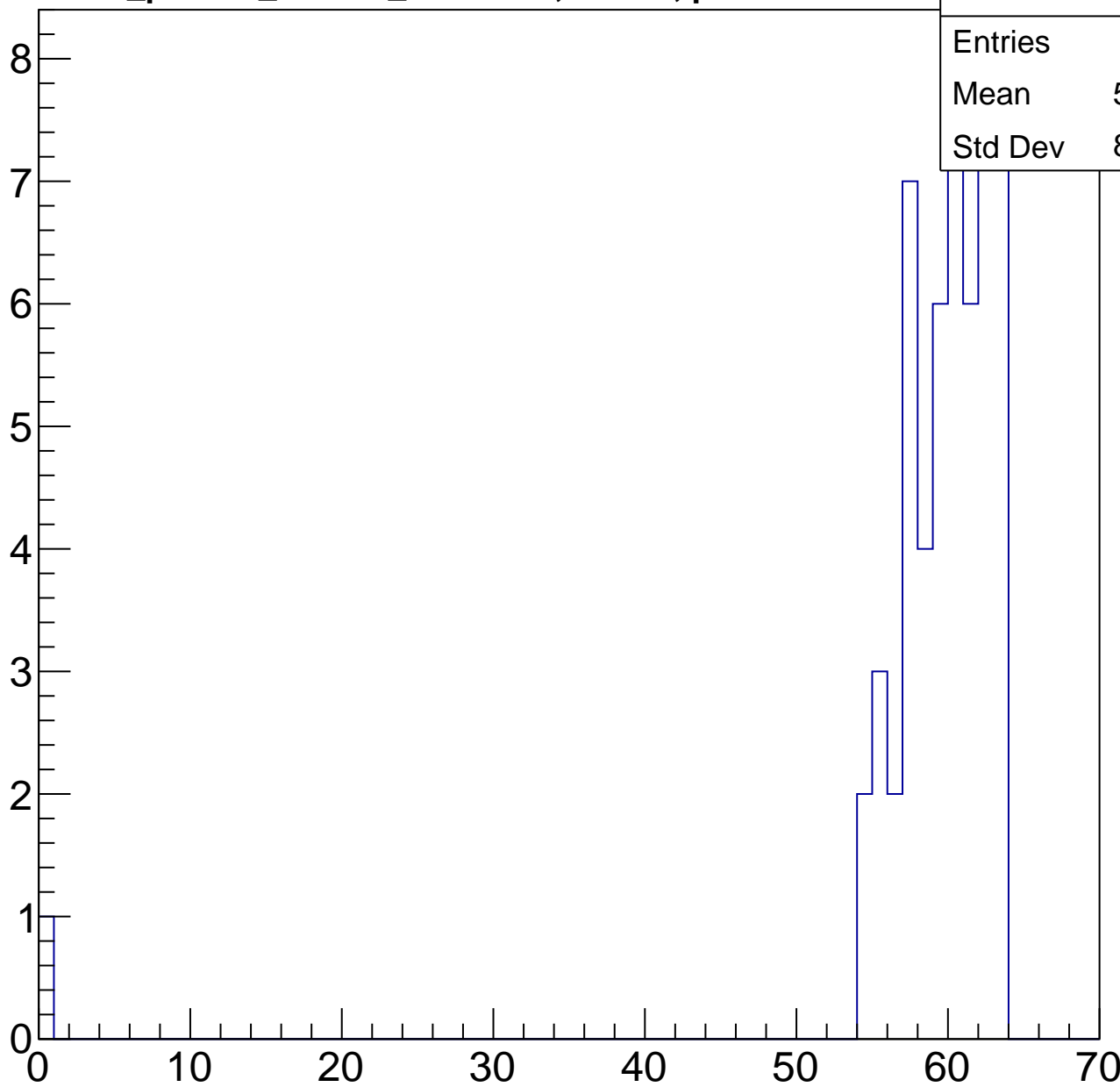
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 55 |
| Mean | 58.47 |
| Std Dev | 8.362 |

ampl



B1L102S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

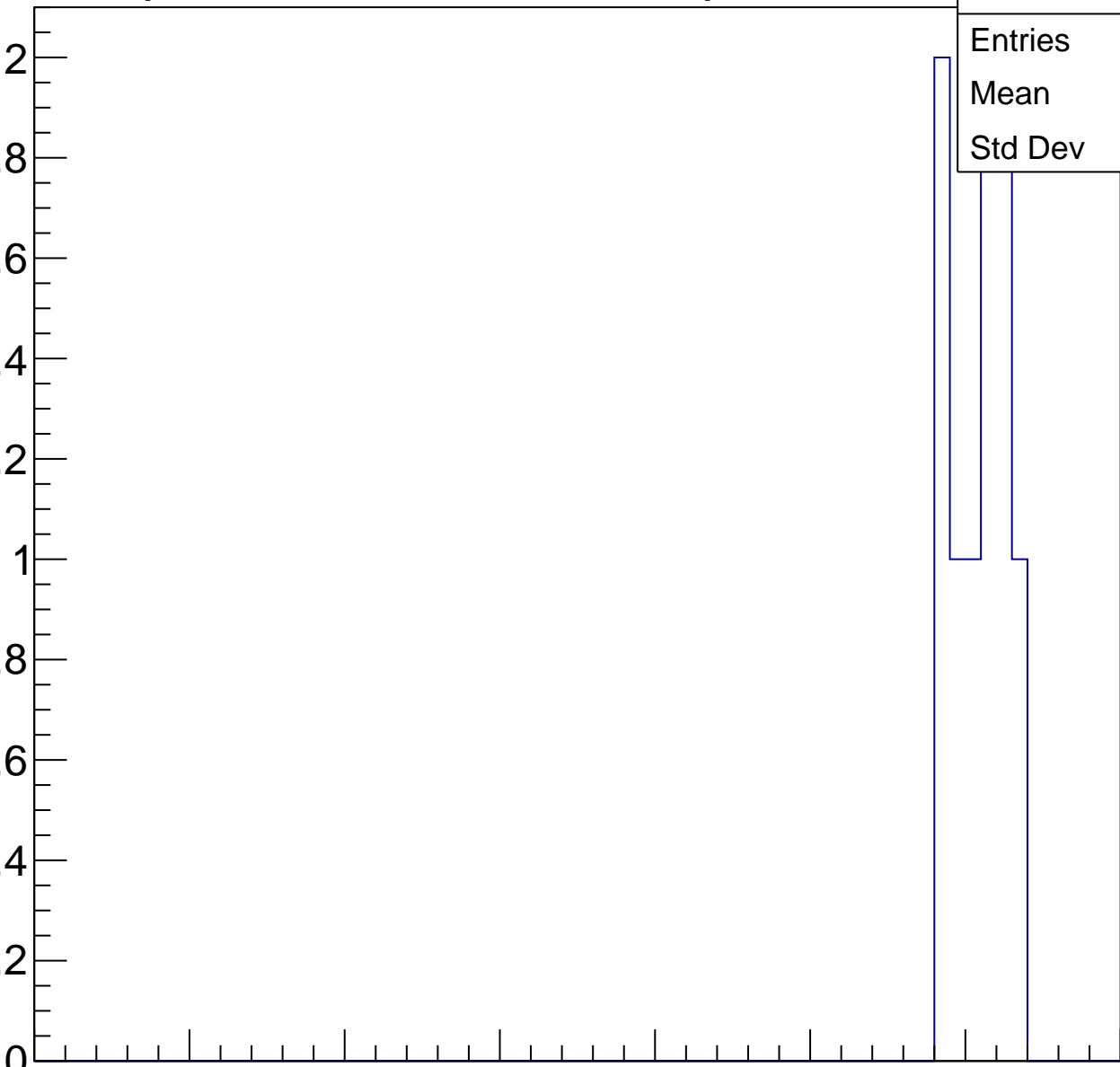
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 9 |
| Mean | 60.44 |
| Std Dev | 1.707 |

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries

2

Mean

62

Std Dev

1

B1L102S, U15-ch106, adc0

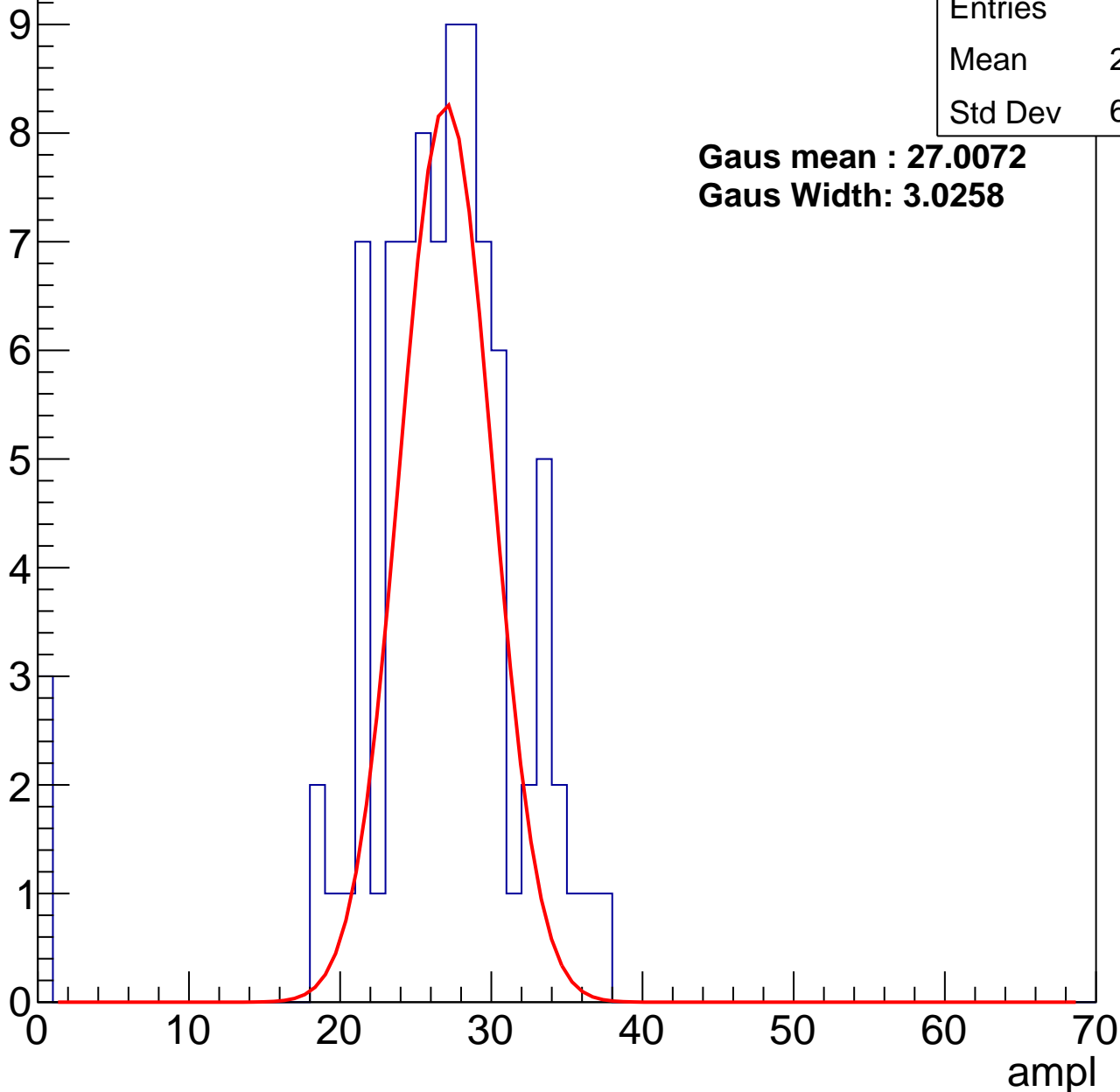
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 25.78 |
| Std Dev | 6.309 |

Gaus mean : 27.0072

Gaus Width: 3.0258



B1L102S, U15-ch106, adc1

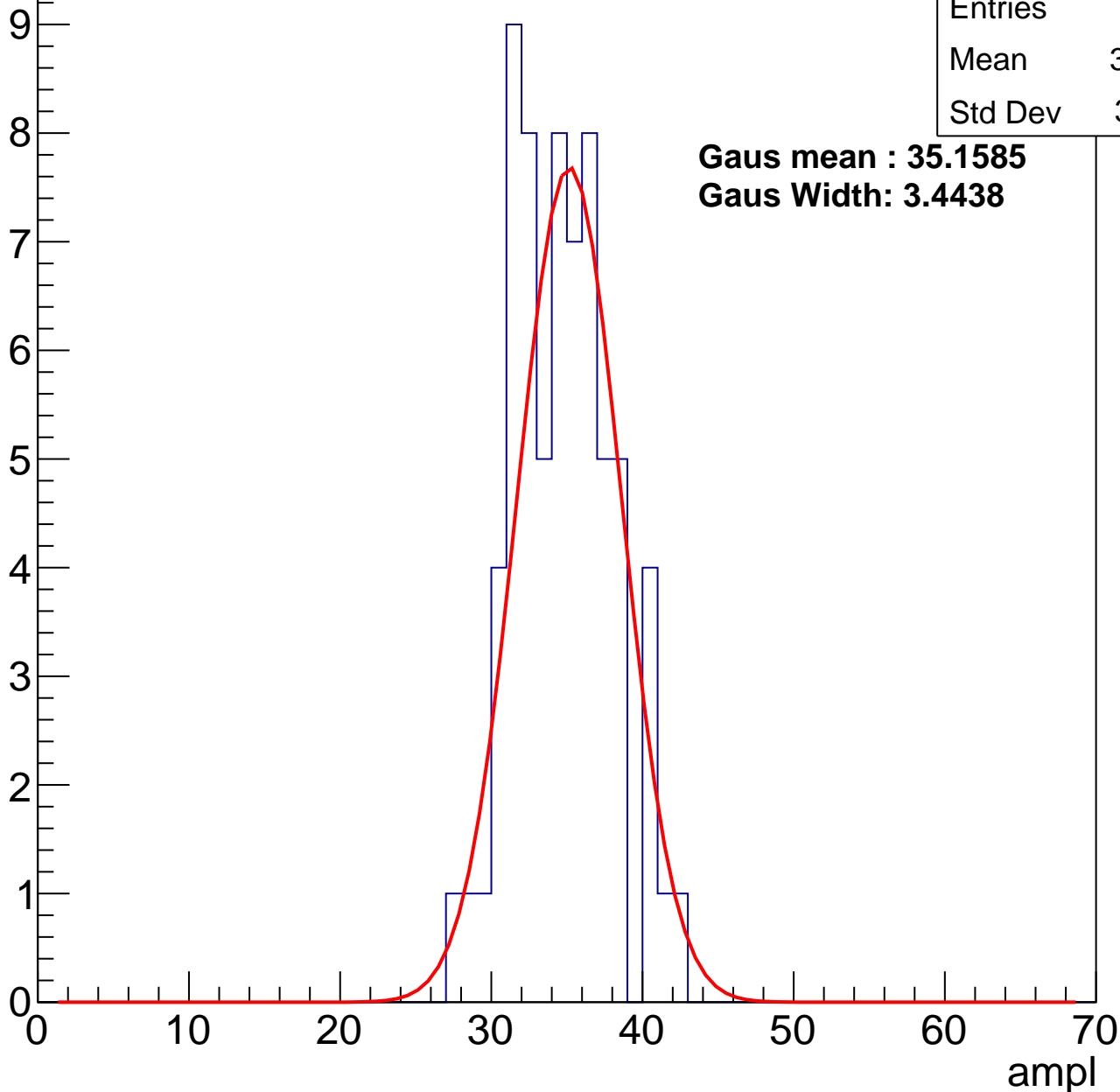
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 34.22 |
| Std Dev | 3.231 |

Gaus mean : 35.1585

Gaus Width: 3.4438



B1L102S, U15-ch106, adc2

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 74 |
| Mean | 41.96 |
| Std Dev | 3.454 |

Gaus mean : 42.6927

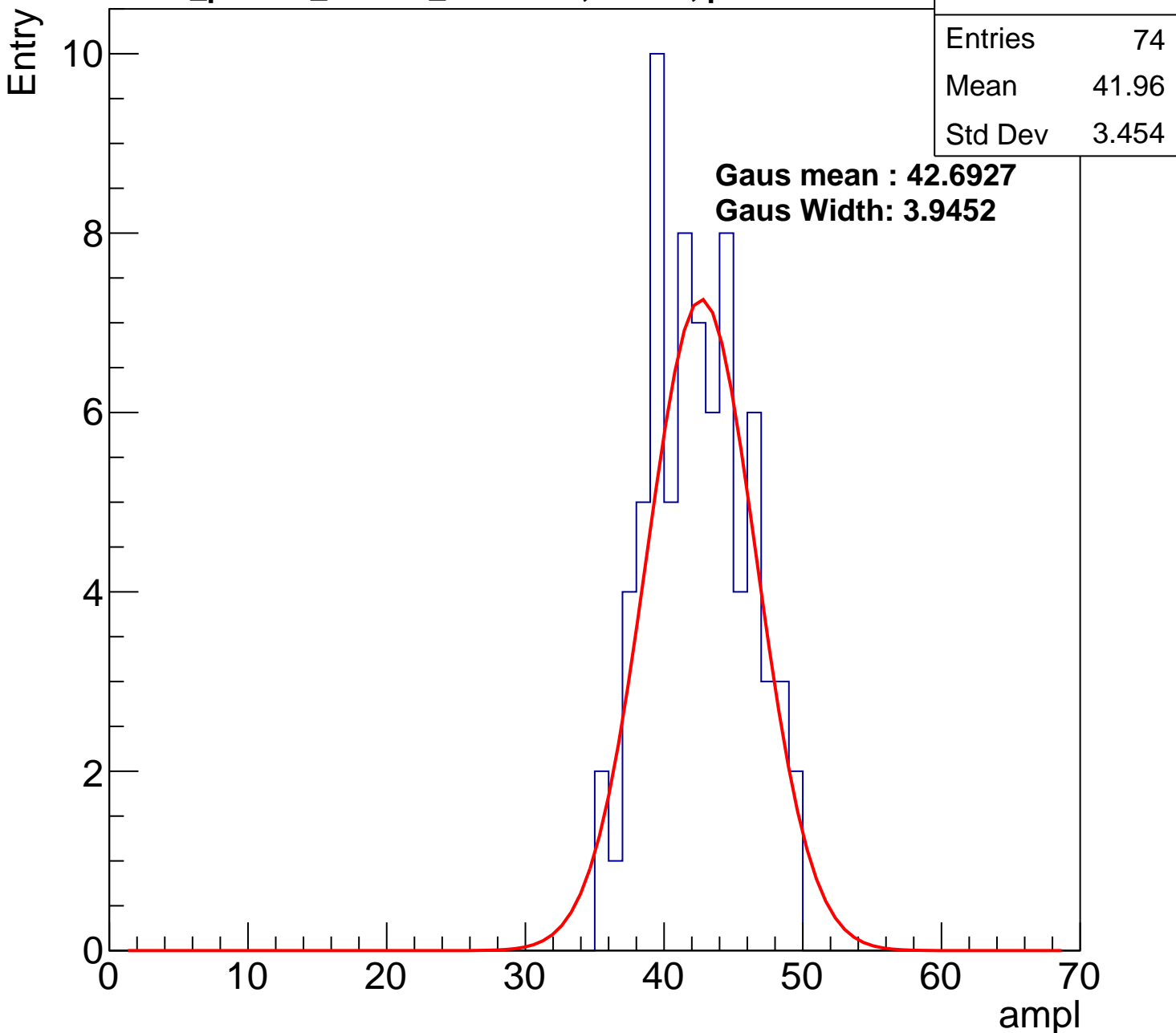
Gaus Width: 3.9452

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

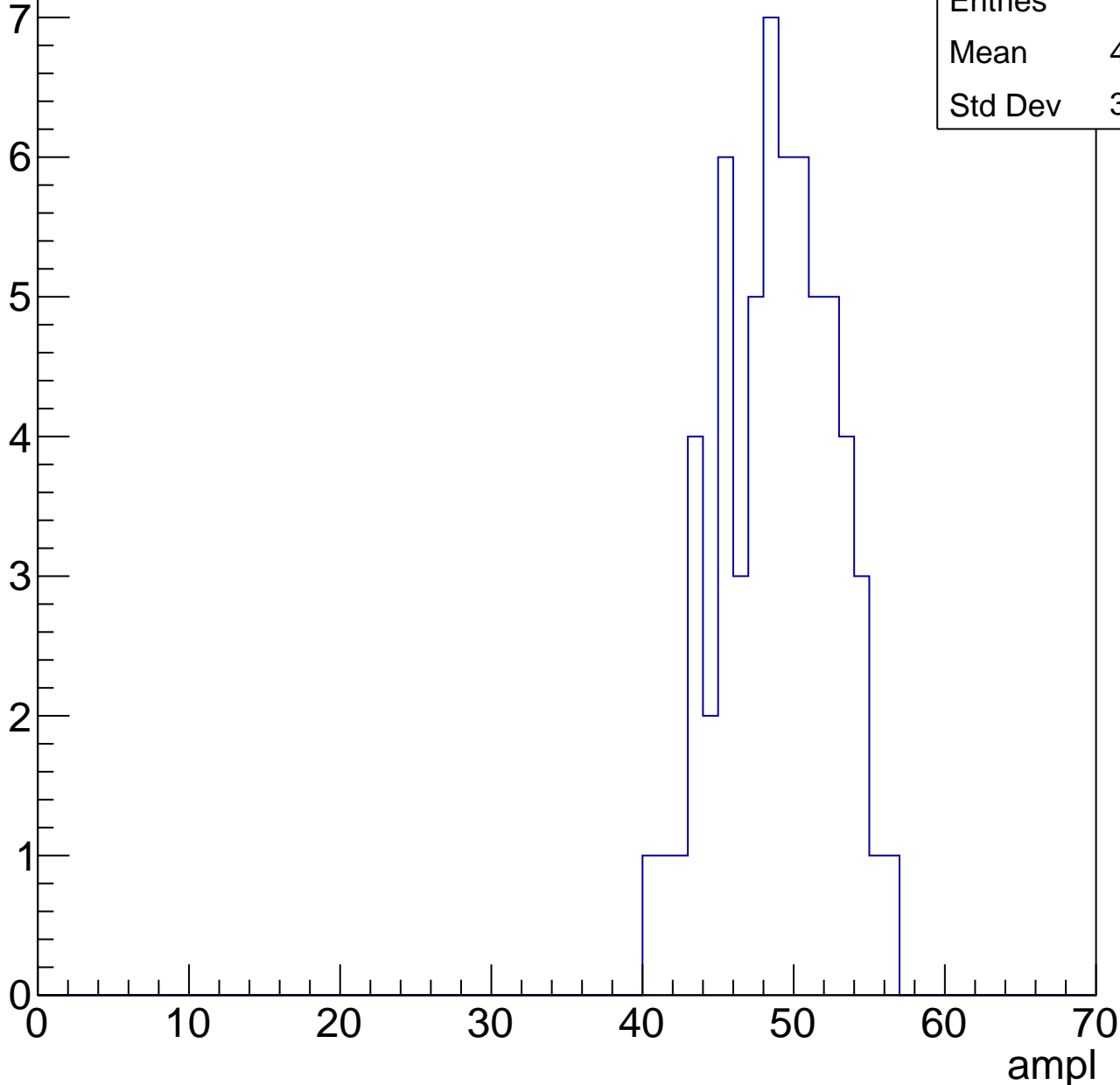


B1L102S, U15-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 48.46 |
| Std Dev | 3.656 |

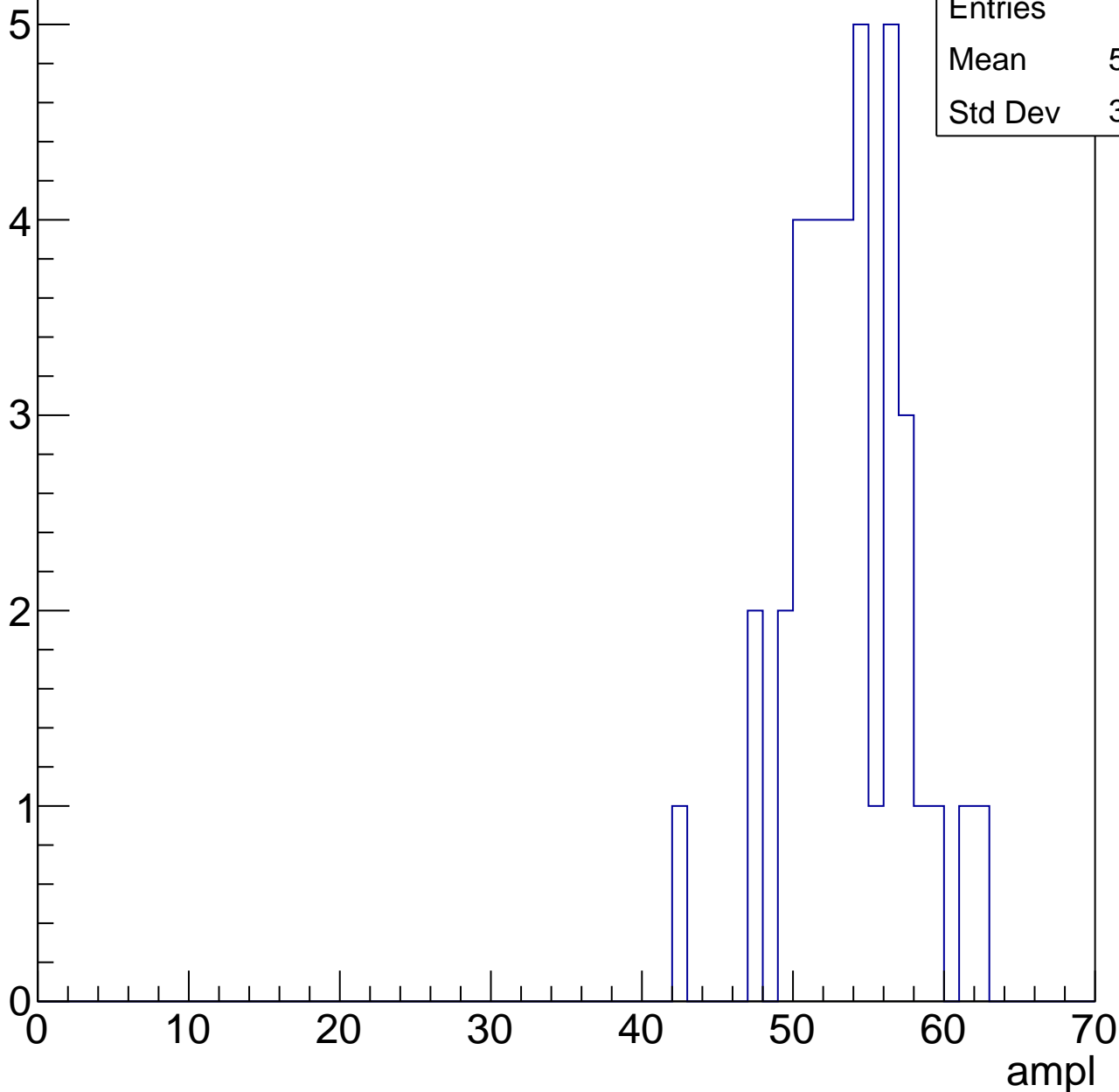


B1L102S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 39 |
| Mean | 53.18 |
| Std Dev | 3.882 |

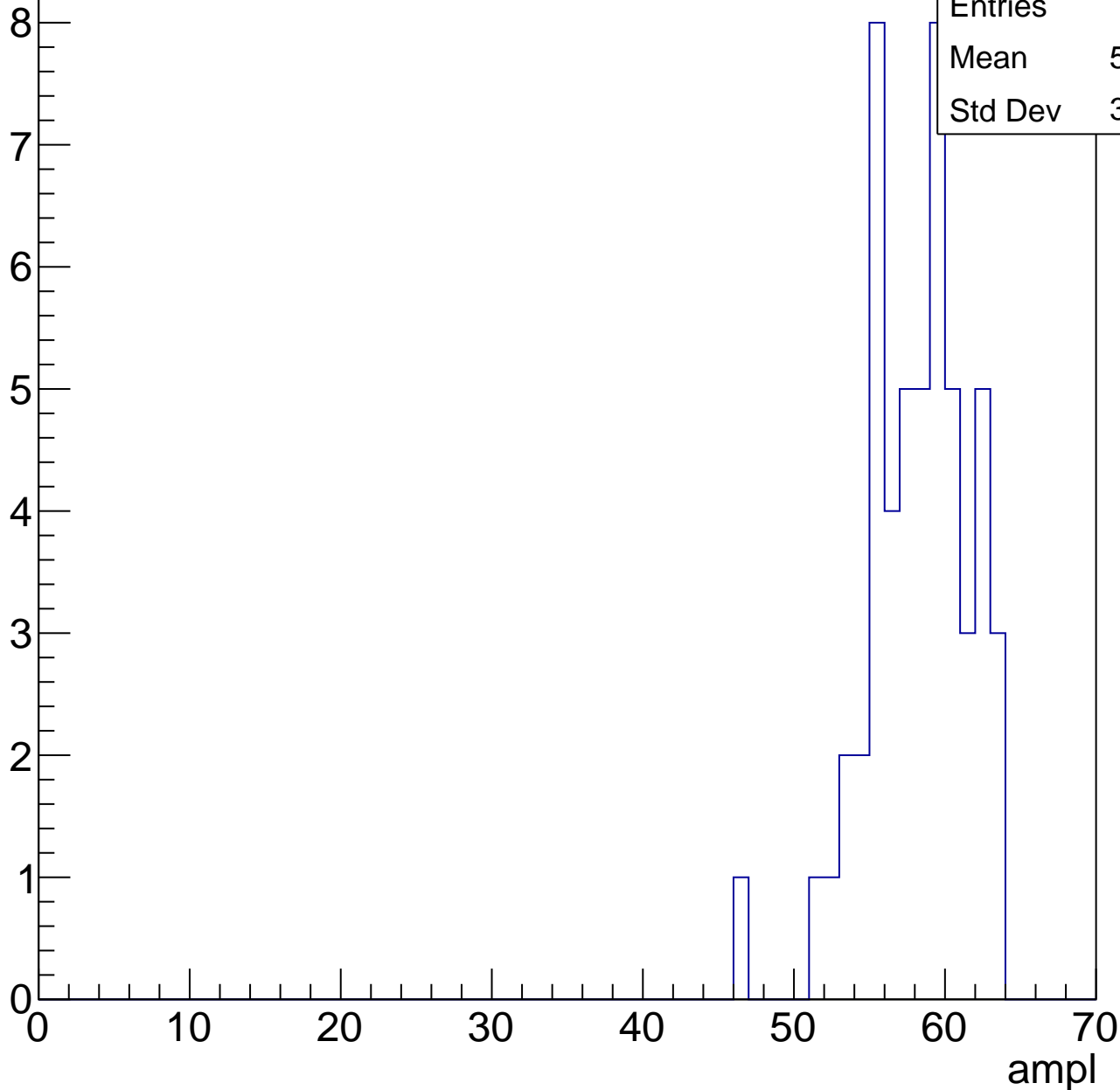


B1L102S, U15-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 57.66 |
| Std Dev | 3.387 |

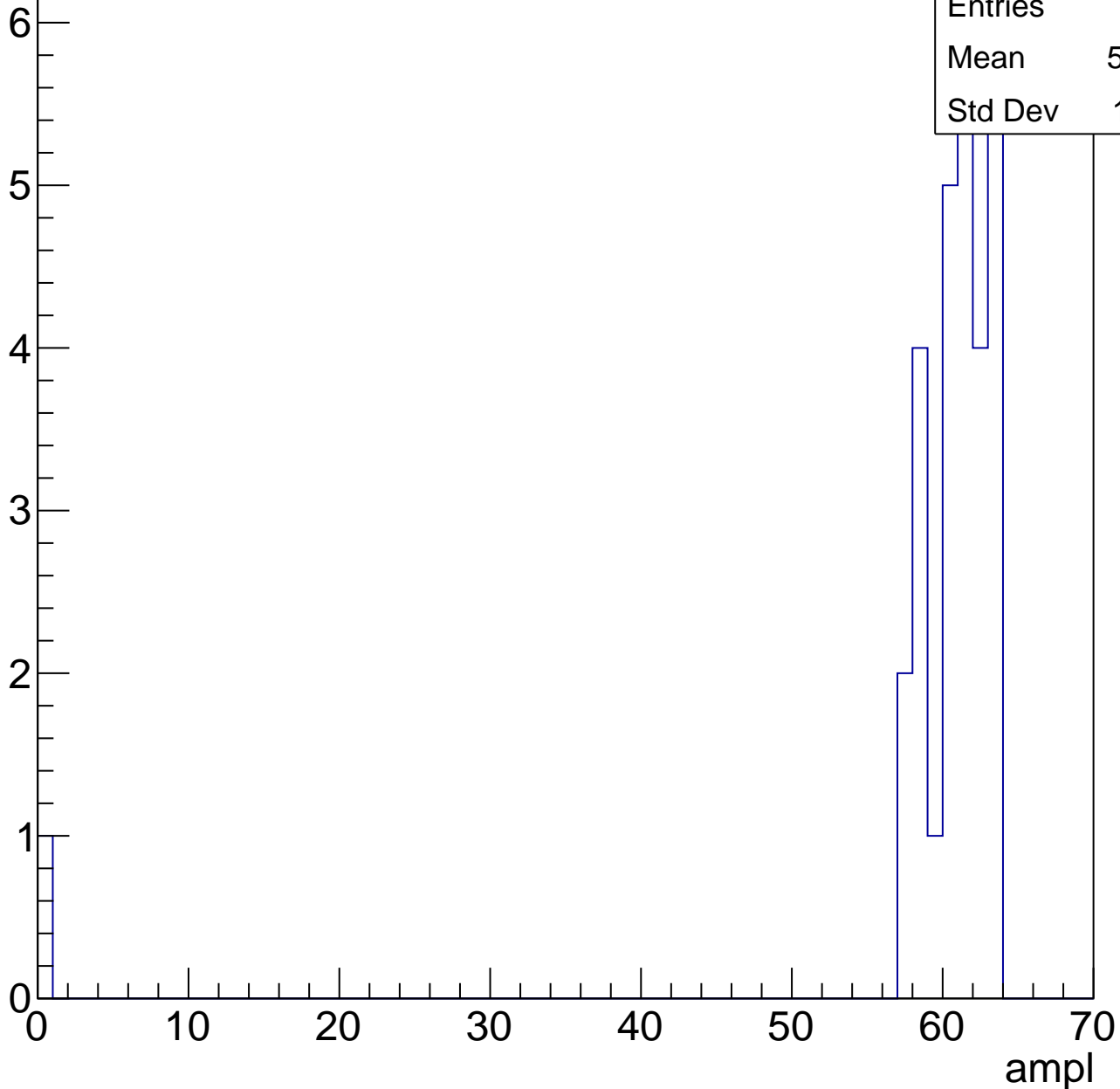


B1L102S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 29 |
| Mean | 58.52 |
| Std Dev | 11.21 |



B1L102S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|------|
| Entries | 3 |
| Mean | 21 |
| Std Dev | 29.7 |

B1L102S, U15-ch107, adc0

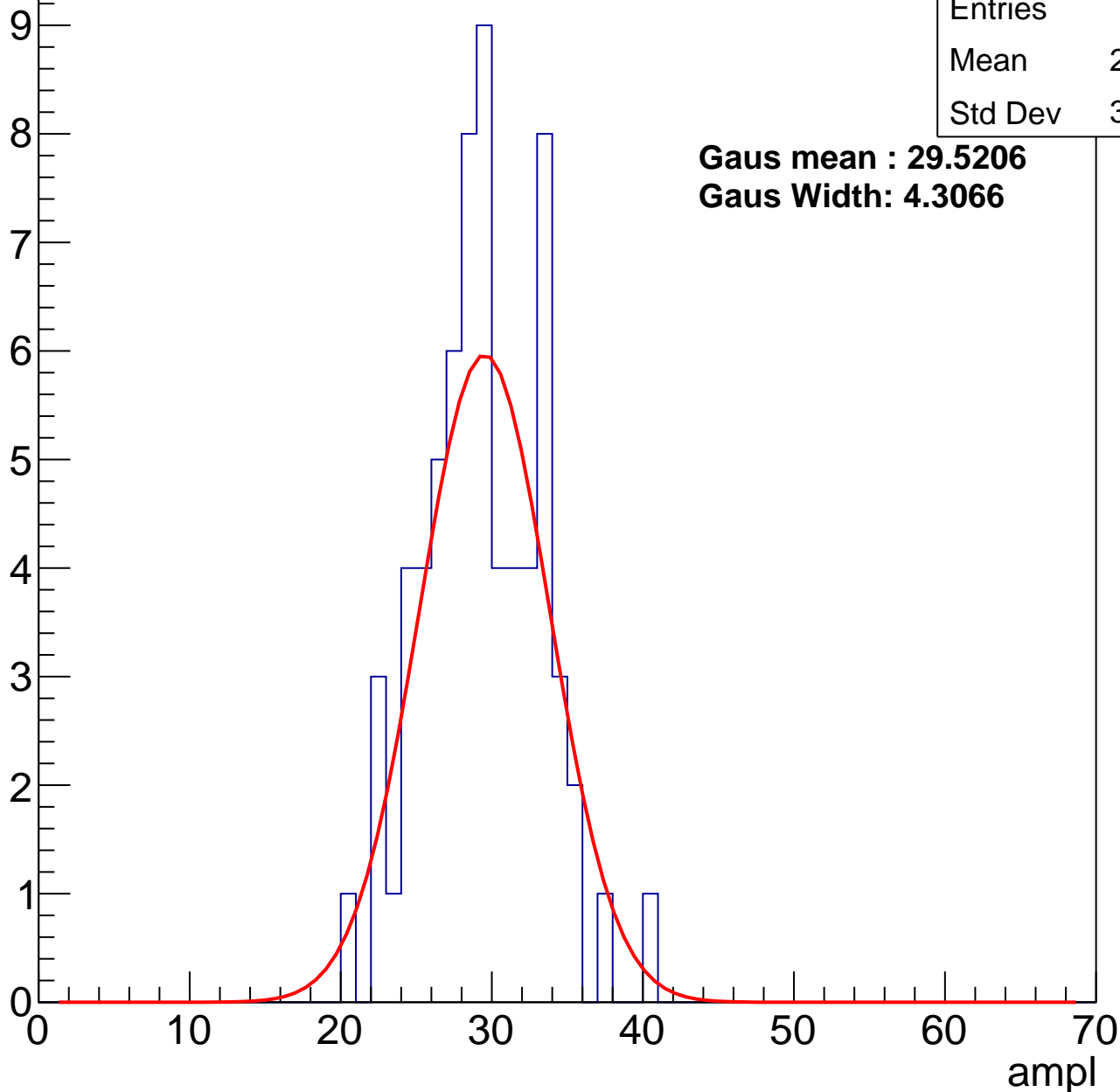
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 28.93 |
| Std Dev | 3.874 |

Gaus mean : 29.5206

Gaus Width: 4.3066



B1L102S, U15-ch107, adc1

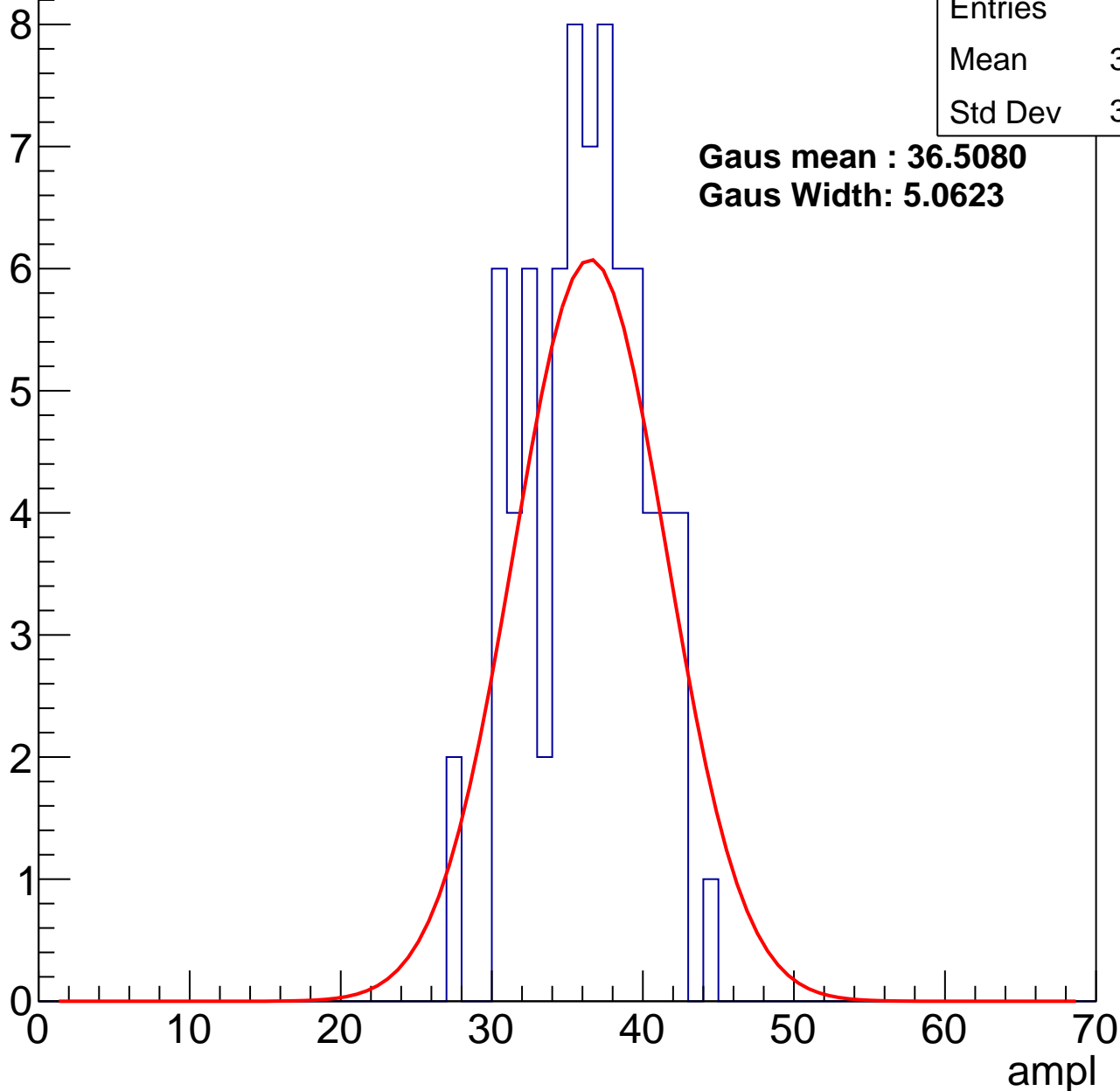
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 35.76 |
| Std Dev | 3.816 |

Gaus mean : 36.5080

Gaus Width: 5.0623



B1L102S, U15-ch107, adc2

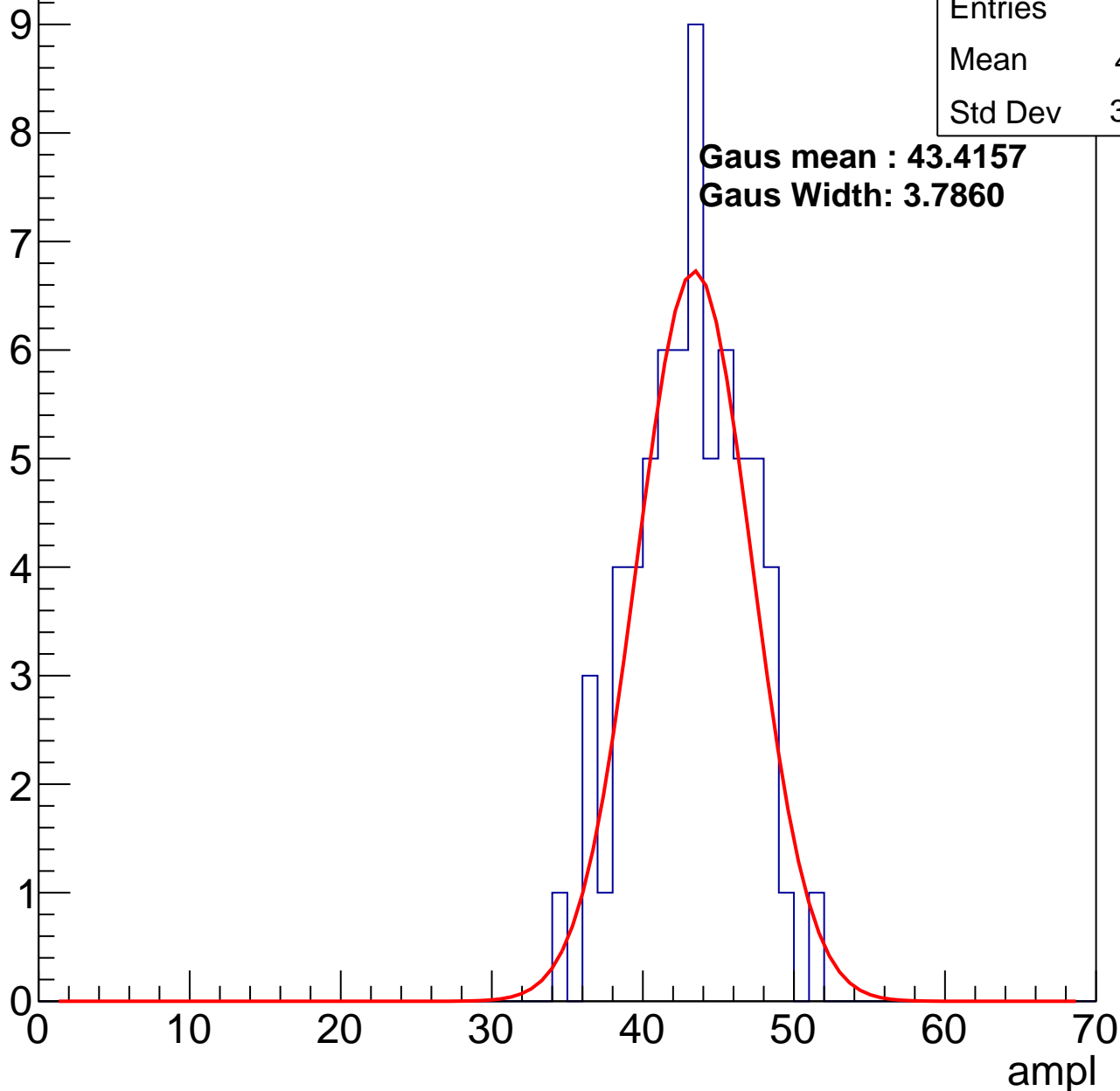
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 42.71 |
| Std Dev | 3.592 |

Gaus mean : 43.4157

Gaus Width: 3.7860

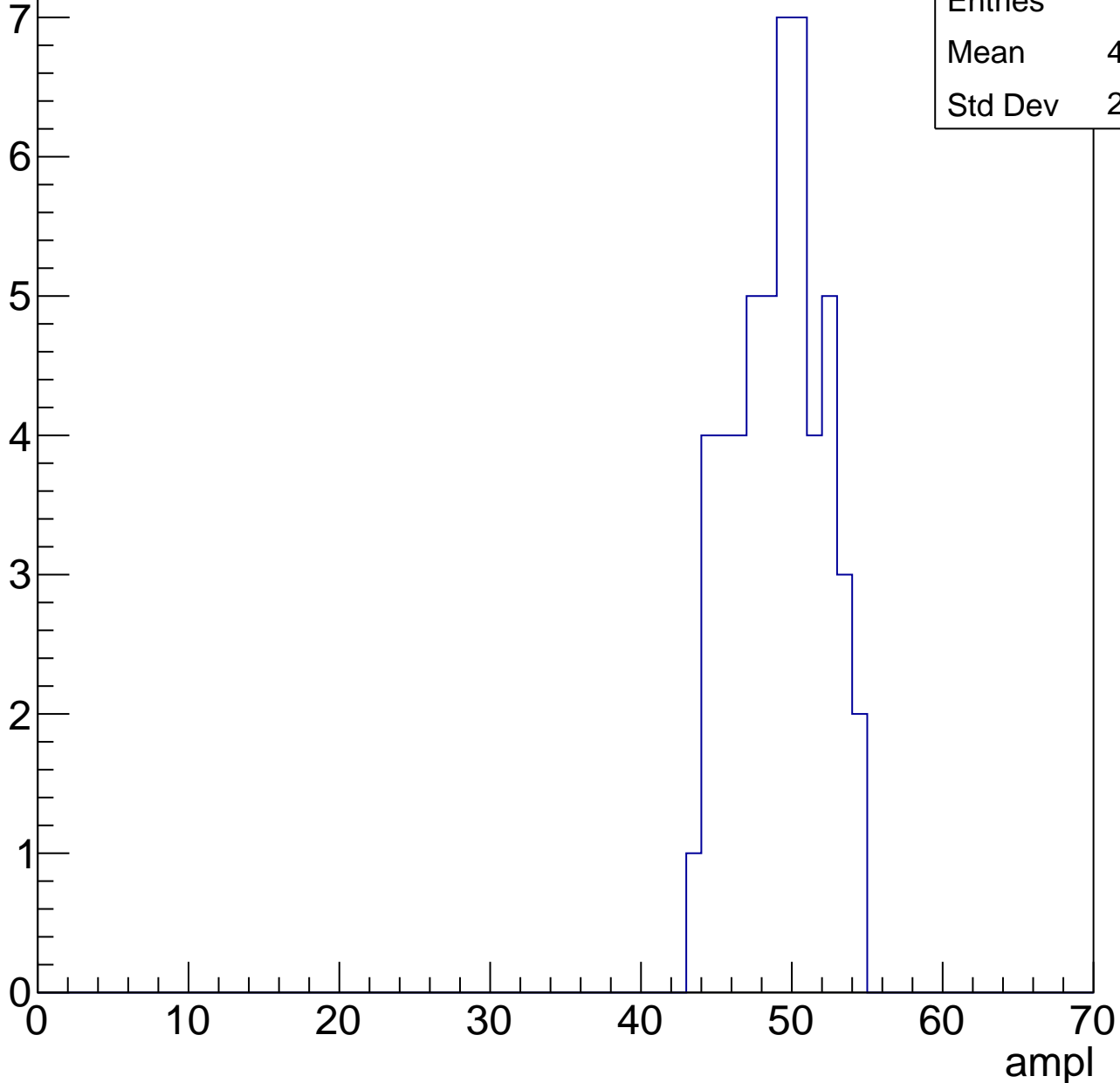


B1L102S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 48.67 |
| Std Dev | 2.874 |

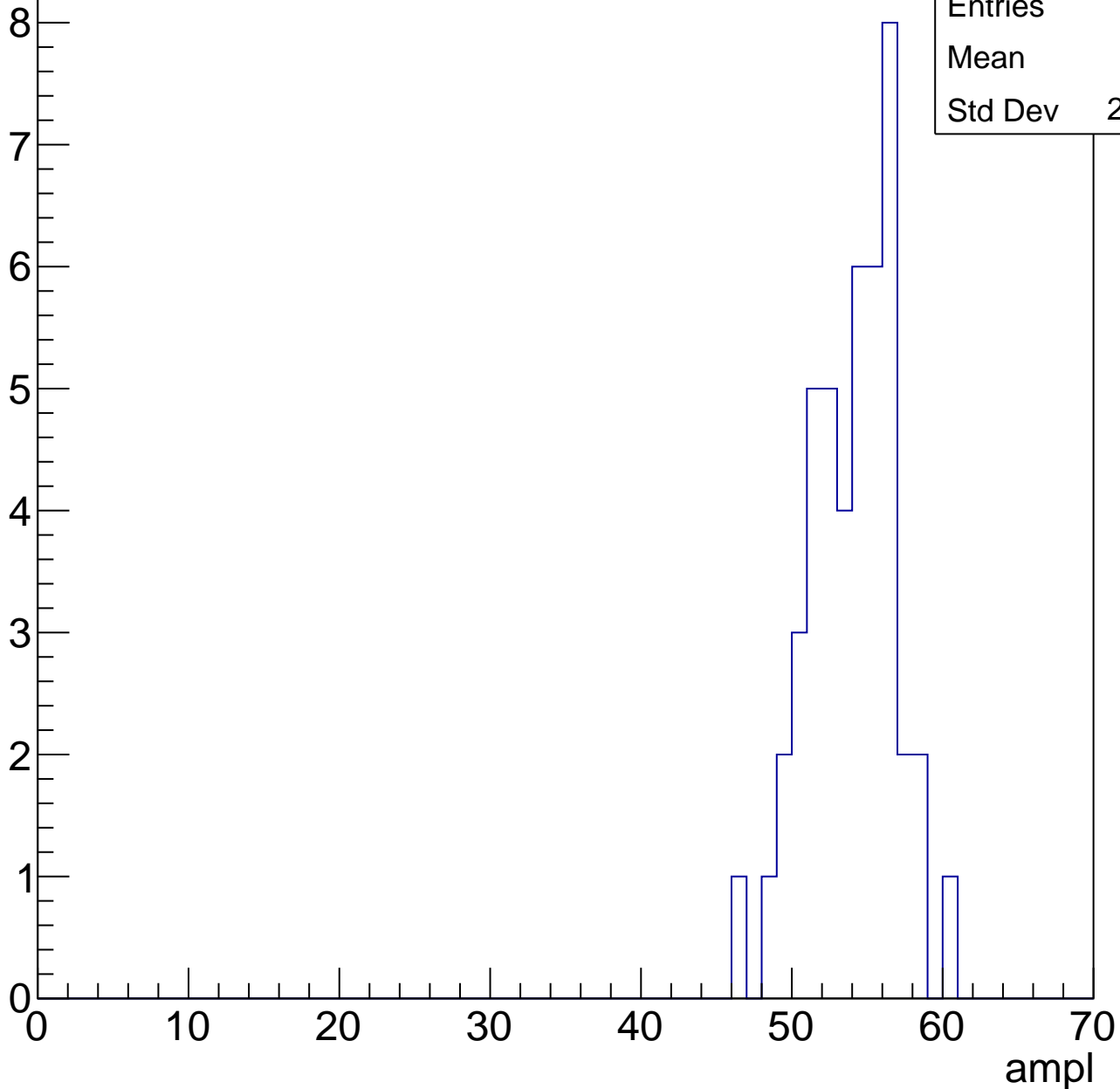


B1L102S, U15-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 53.5 |
| Std Dev | 2.872 |

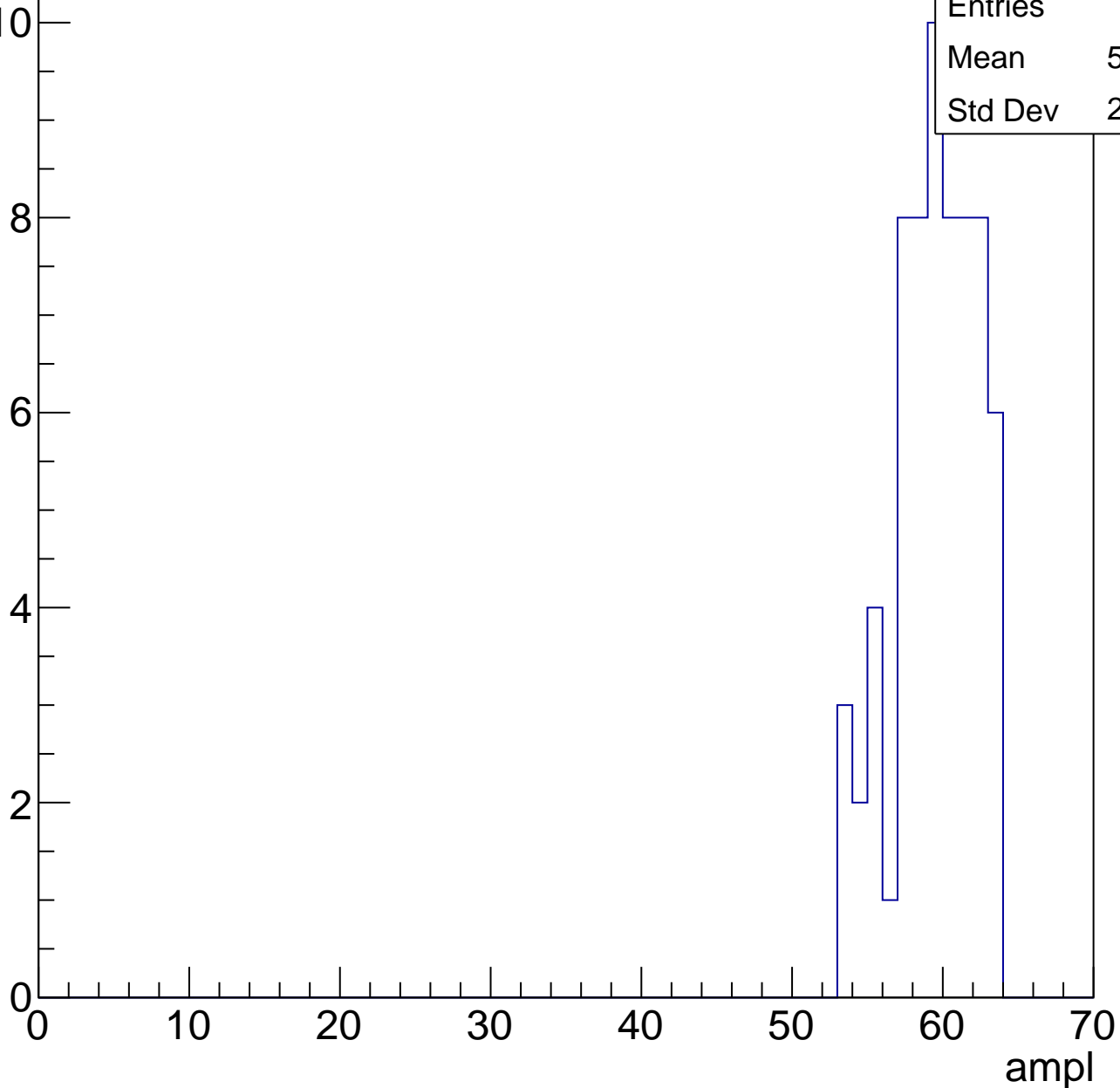


B1L102S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 59.02 |
| Std Dev | 2.694 |



B1L102S, U15-ch107, adc6

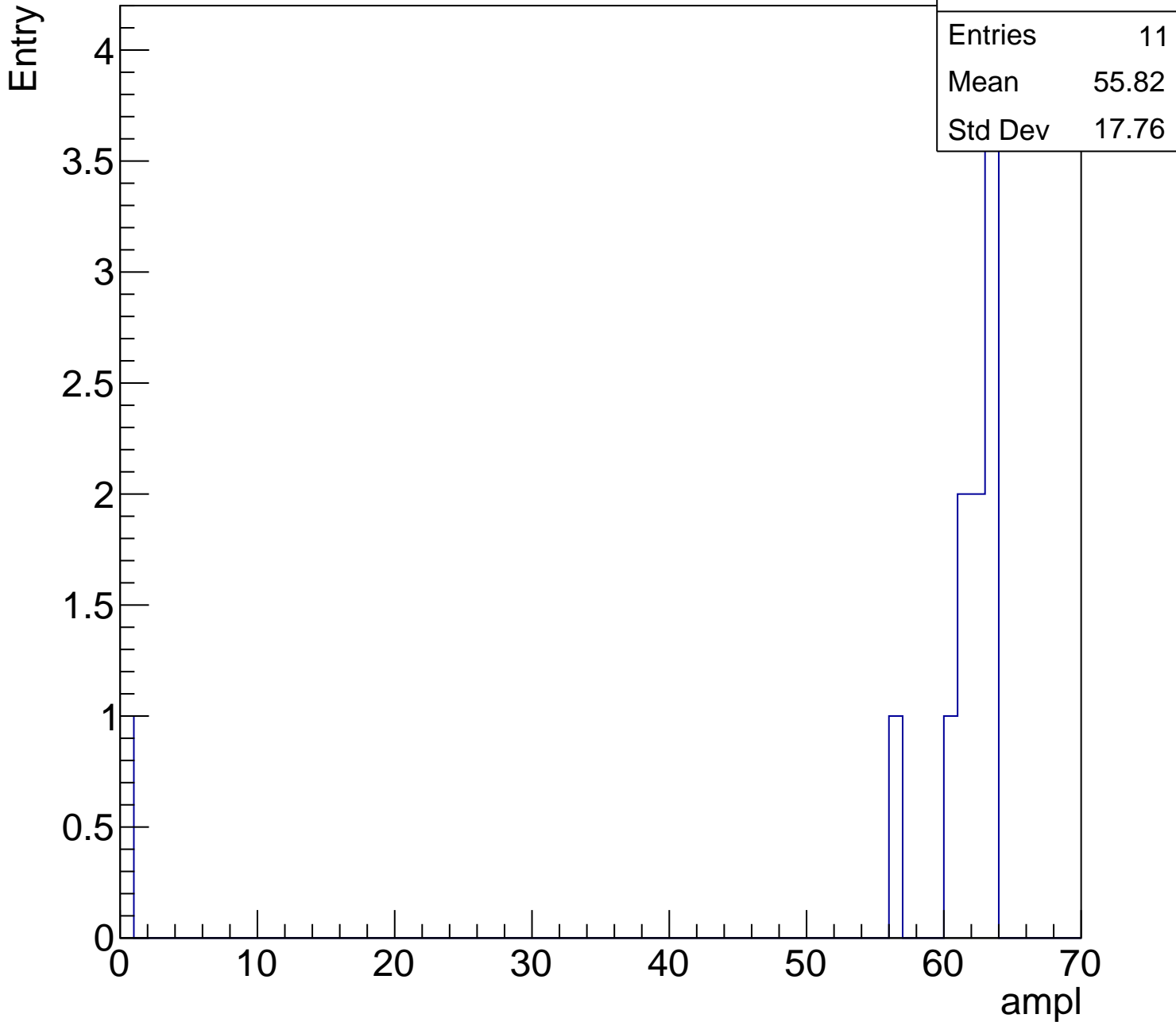
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

| | |
|---------|-------|
| Entries | 11 |
| Mean | 55.82 |
| Std Dev | 17.76 |

ampl



B1L102S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch108, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 77 |
| Mean | 27.45 |
| Std Dev | 3.982 |

Gaus mean : 28.2376

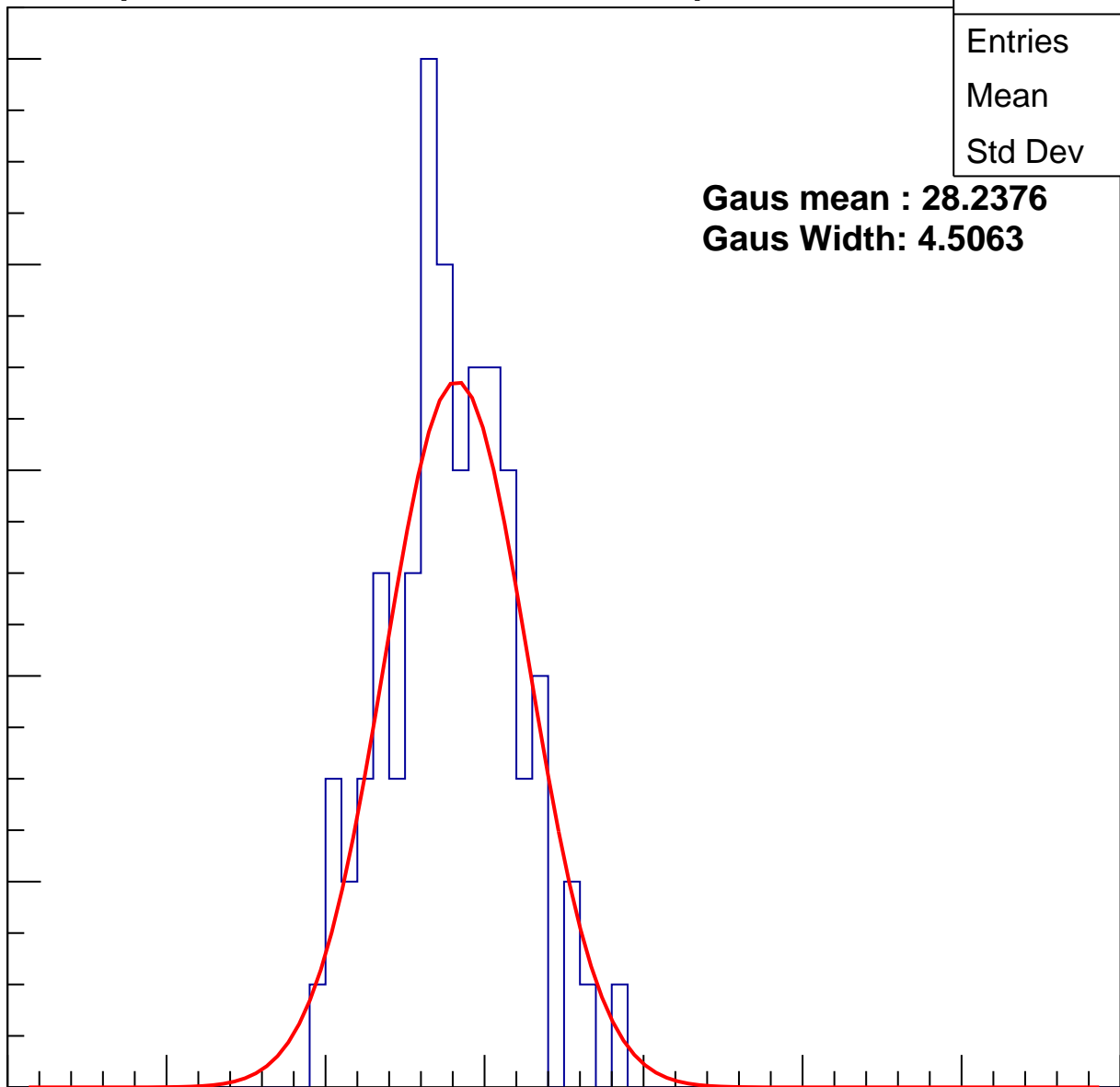
Gaus Width: 4.5063

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch108, adc1

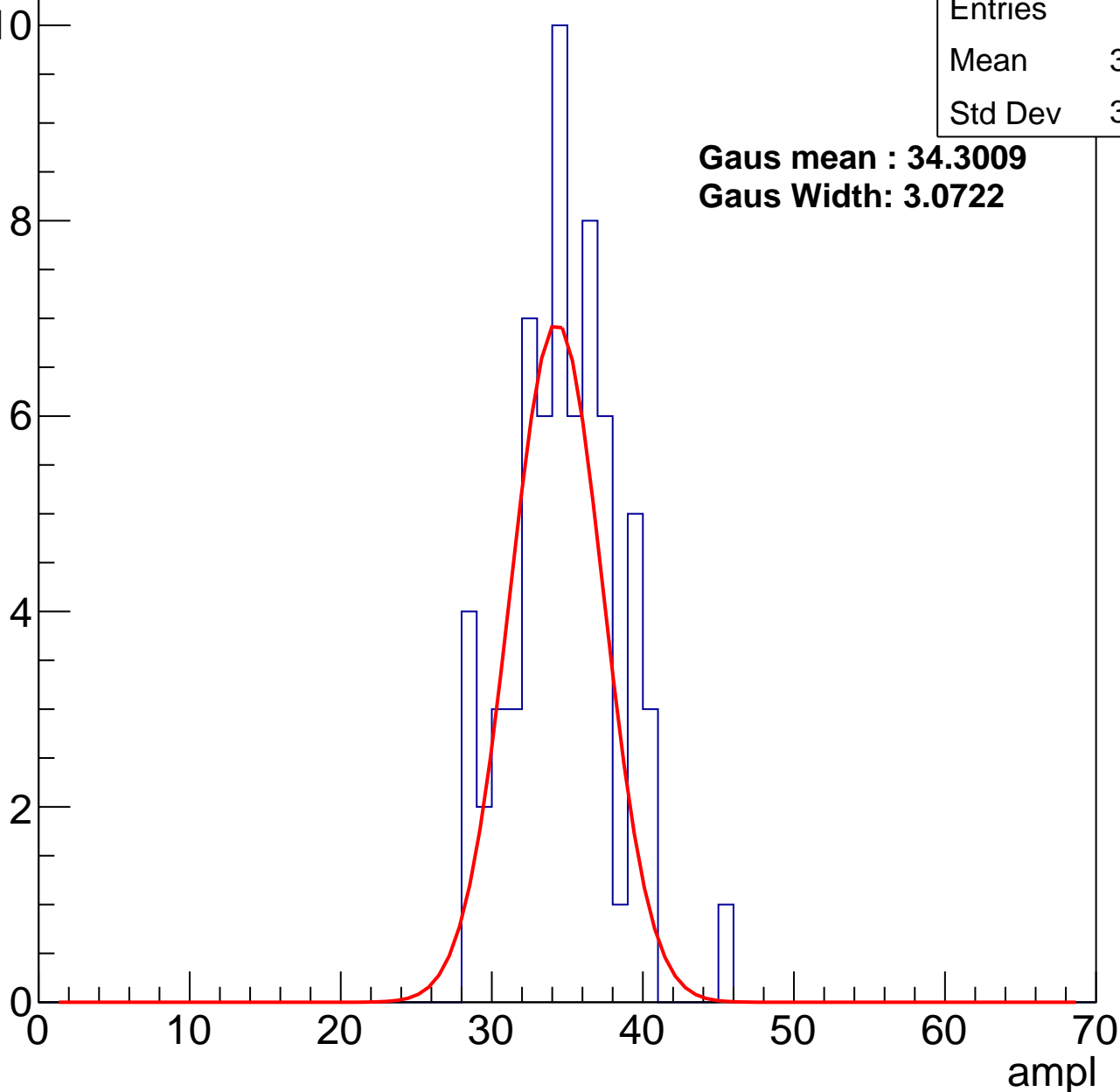
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 34.35 |
| Std Dev | 3.412 |

Gaus mean : 34.3009

Gaus Width: 3.0722



B1L102S, U15-ch108, adc2

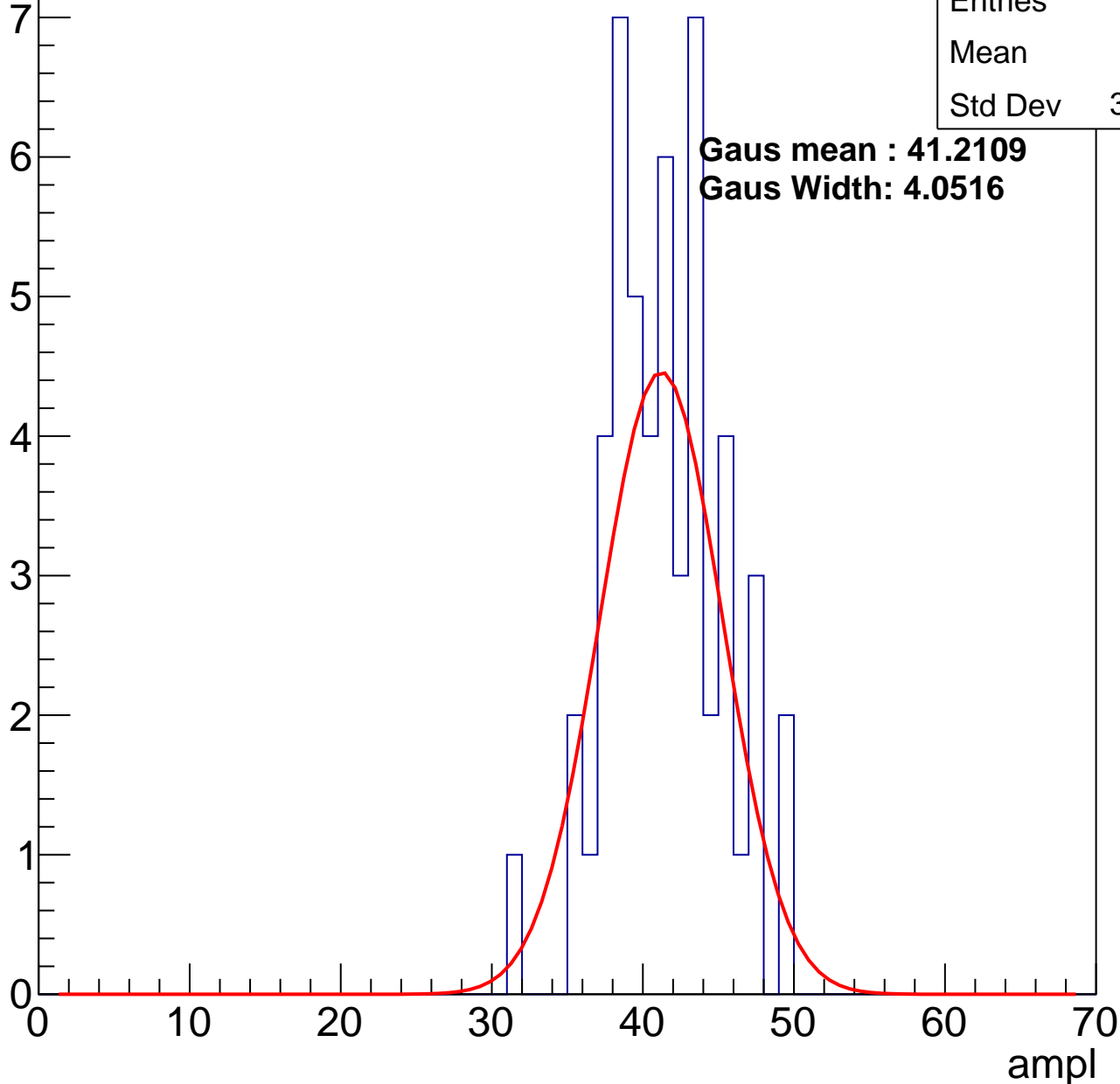
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 41 |
| Std Dev | 3.726 |

Gaus mean : 41.2109

Gaus Width: 4.0516

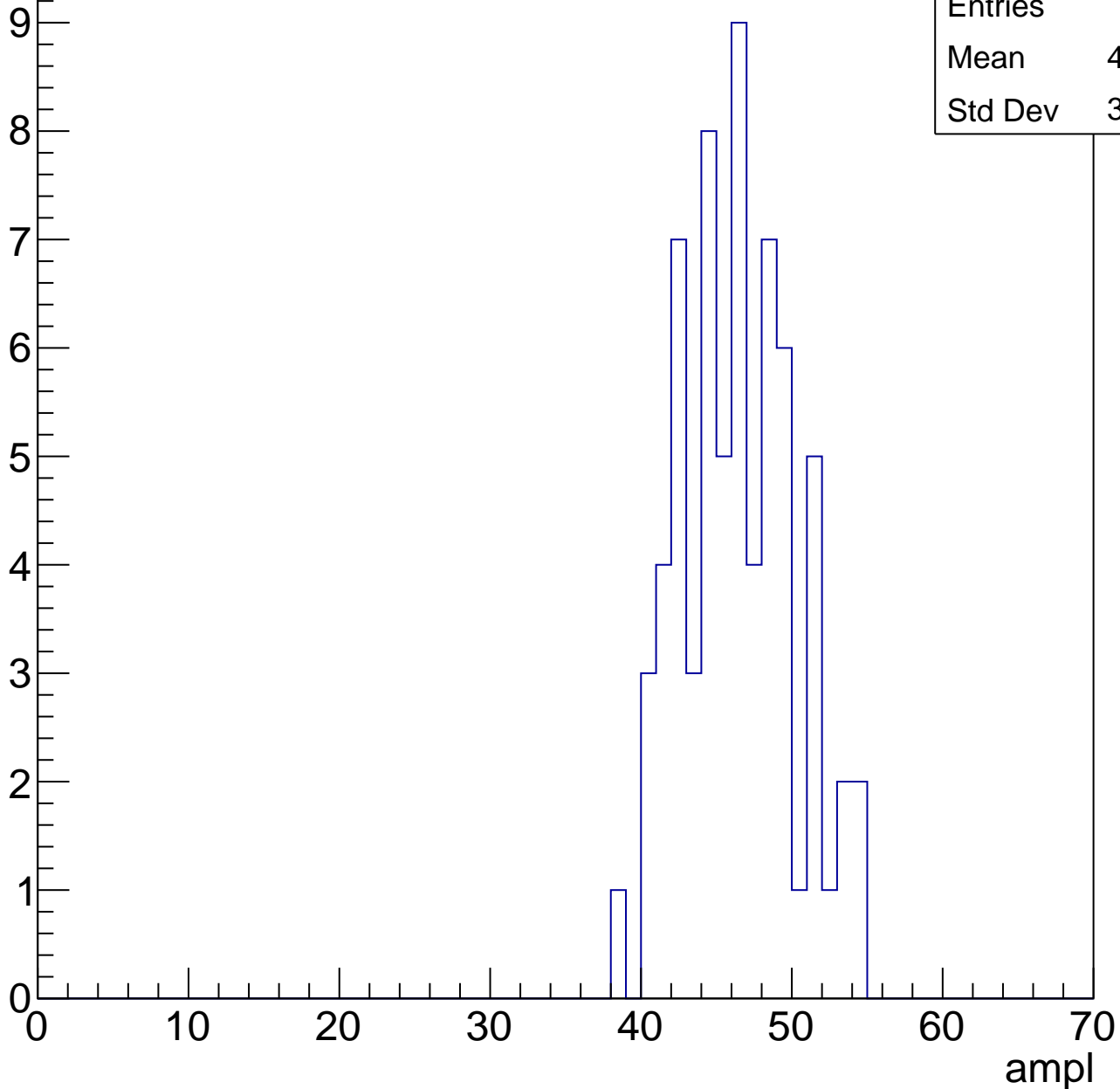


B1L102S, U15-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 45.96 |
| Std Dev | 3.712 |

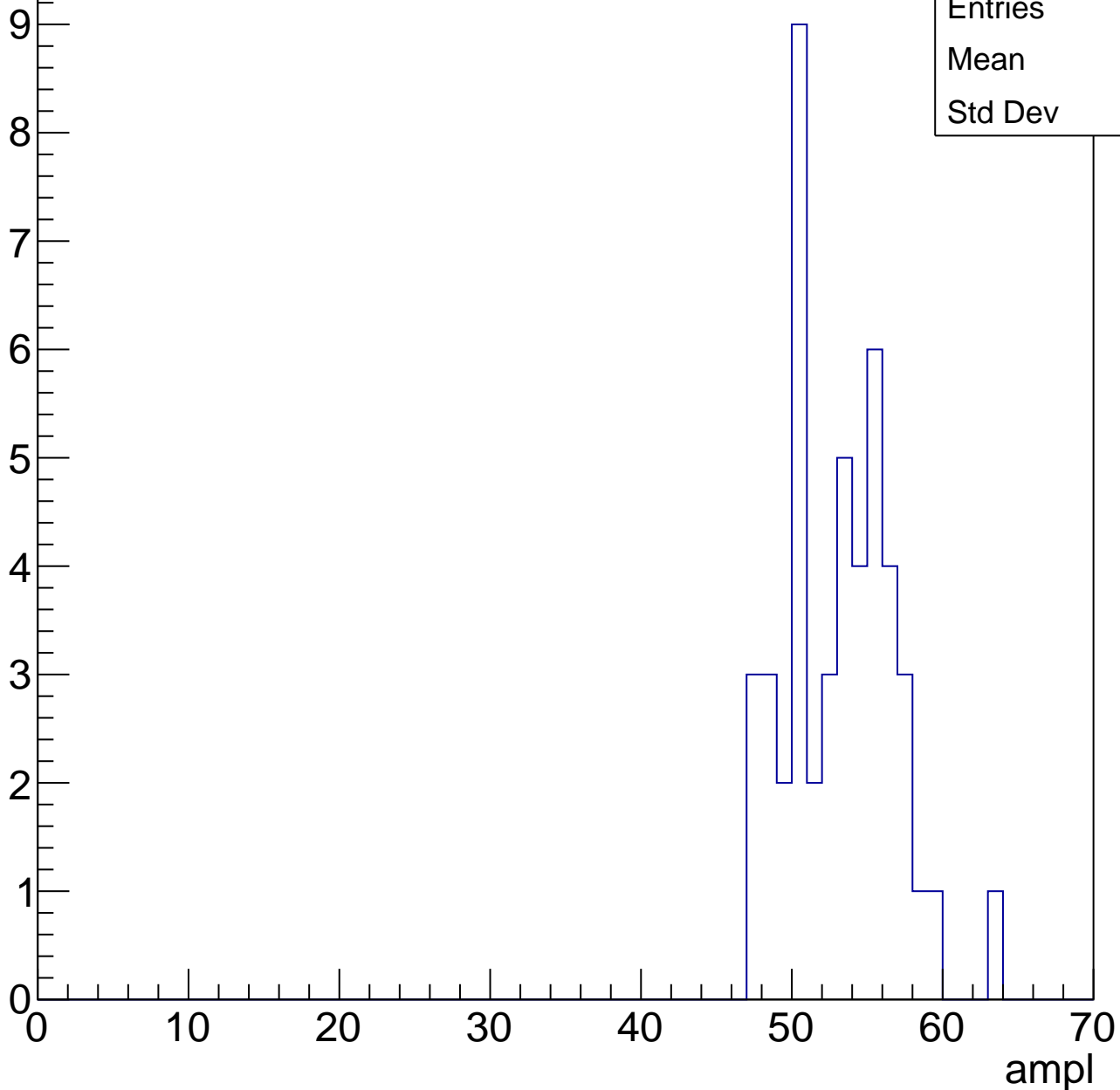


B1L102S, U15-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|------|
| Entries | 47 |
| Mean | 52.7 |
| Std Dev | 3.5 |



B1L102S, U15-ch108, adc5

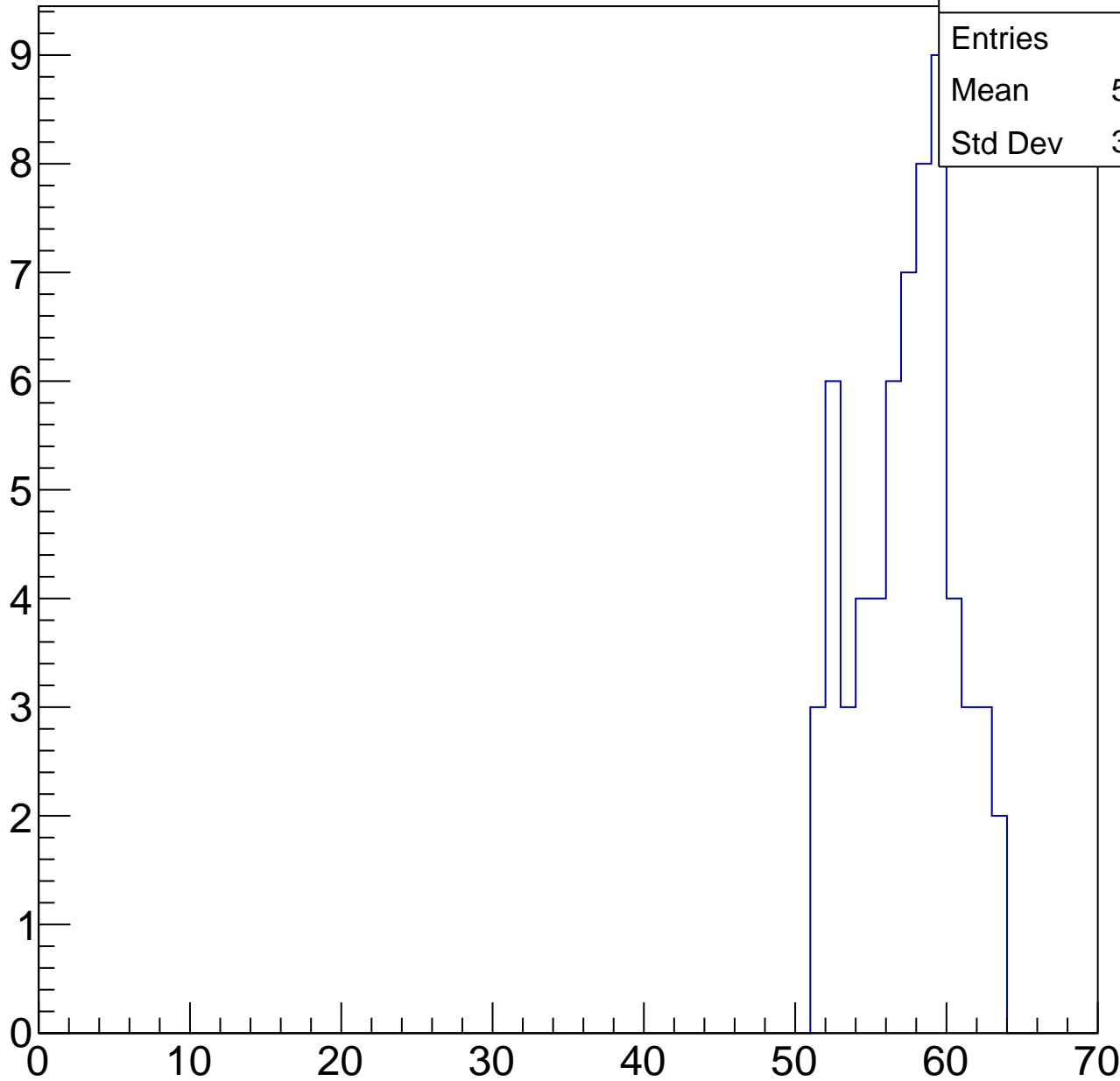
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 62 |
| Mean | 56.85 |
| Std Dev | 3.207 |

ampl

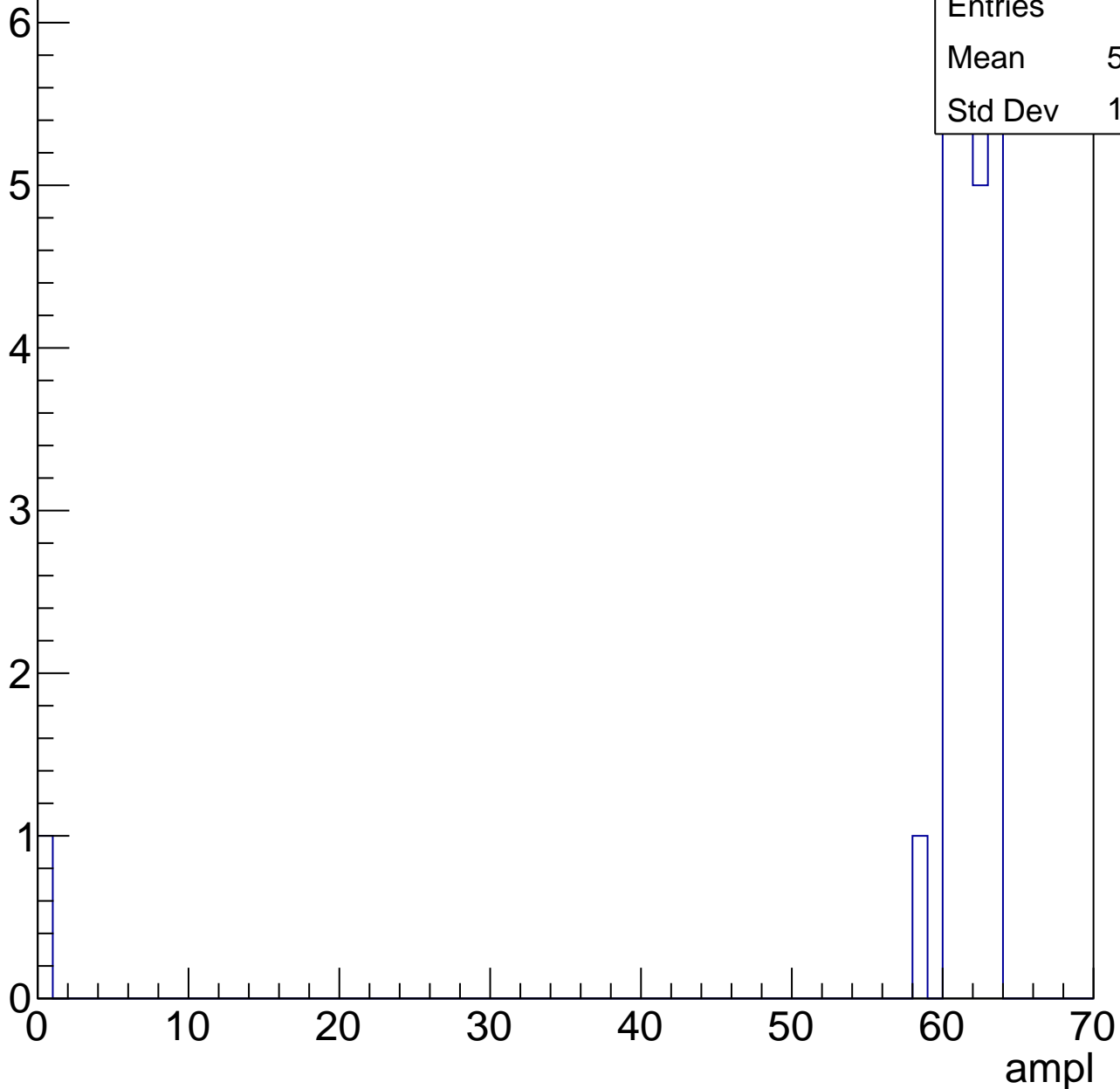


B1L102S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

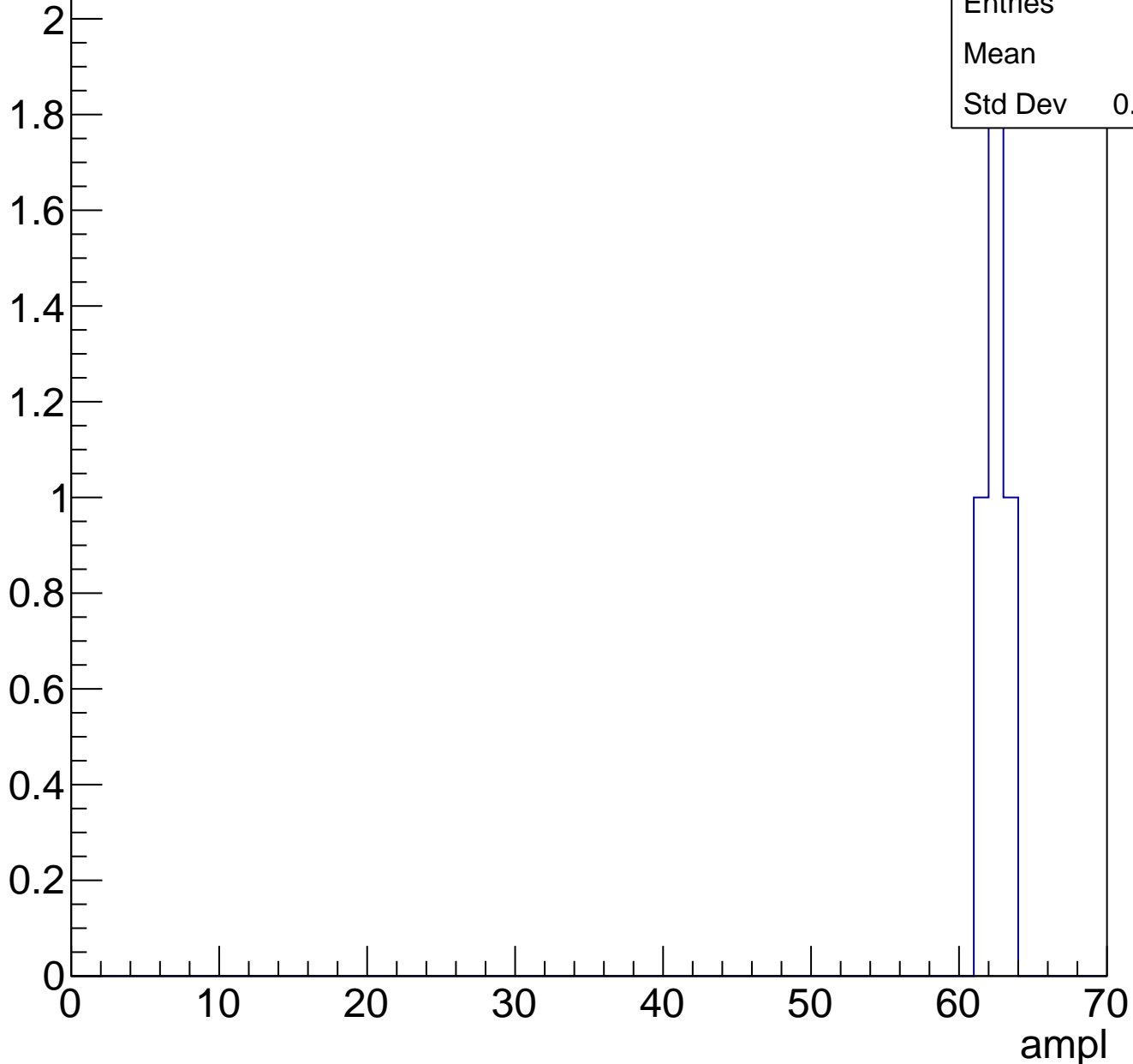
| | |
|---------|-------|
| Entries | 25 |
| Mean | 58.88 |
| Std Dev | 12.09 |



B1L102S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch109, adc0

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 81 |
| Mean | 28.38 |
| Std Dev | 4.916 |

Gaus mean : 29.4441

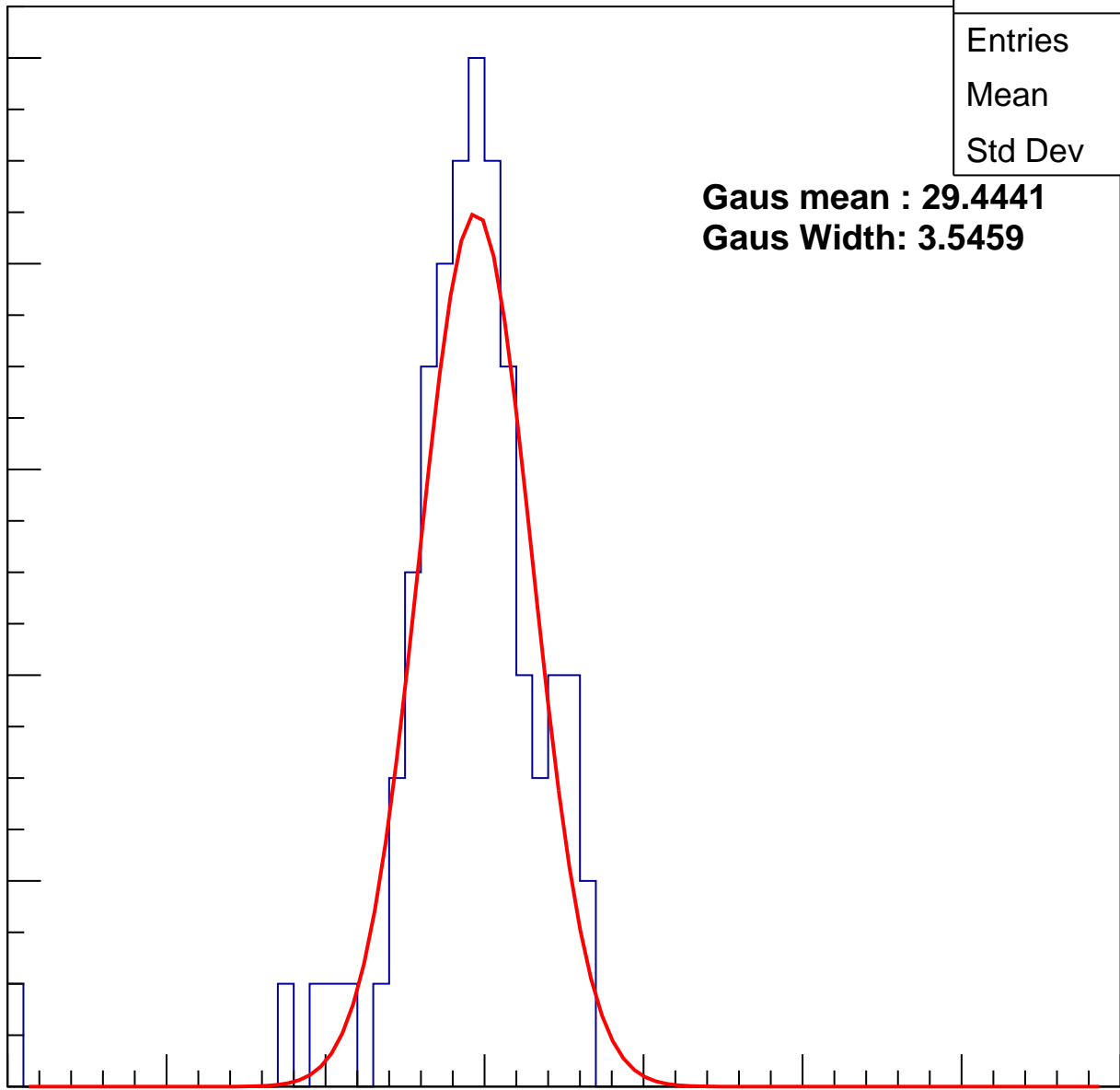
Gaus Width: 3.5459

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch109, adc1

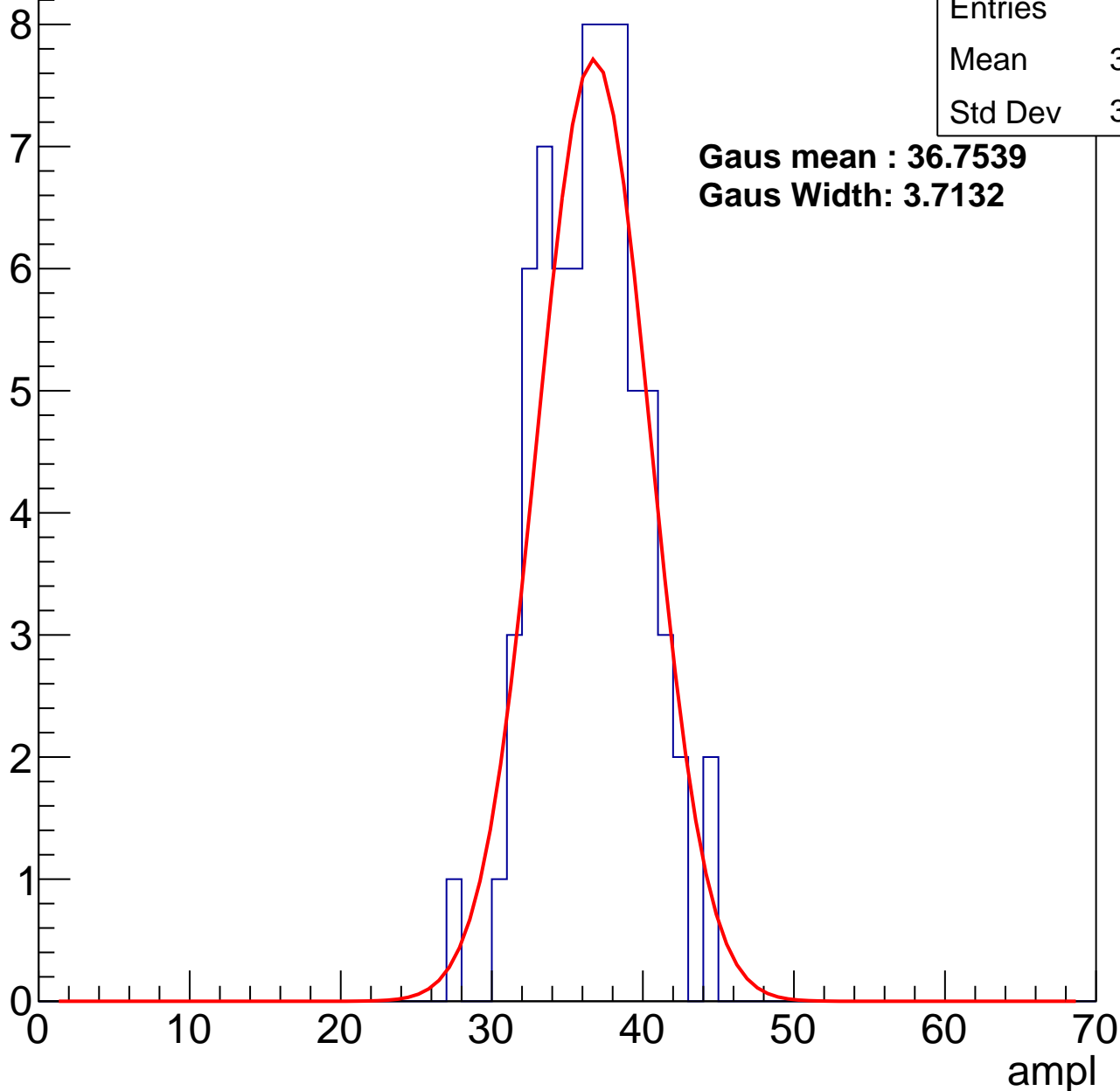
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 36.13 |
| Std Dev | 3.398 |

Gaus mean : 36.7539

Gaus Width: 3.7132



B1L102S, U15-ch109, adc2

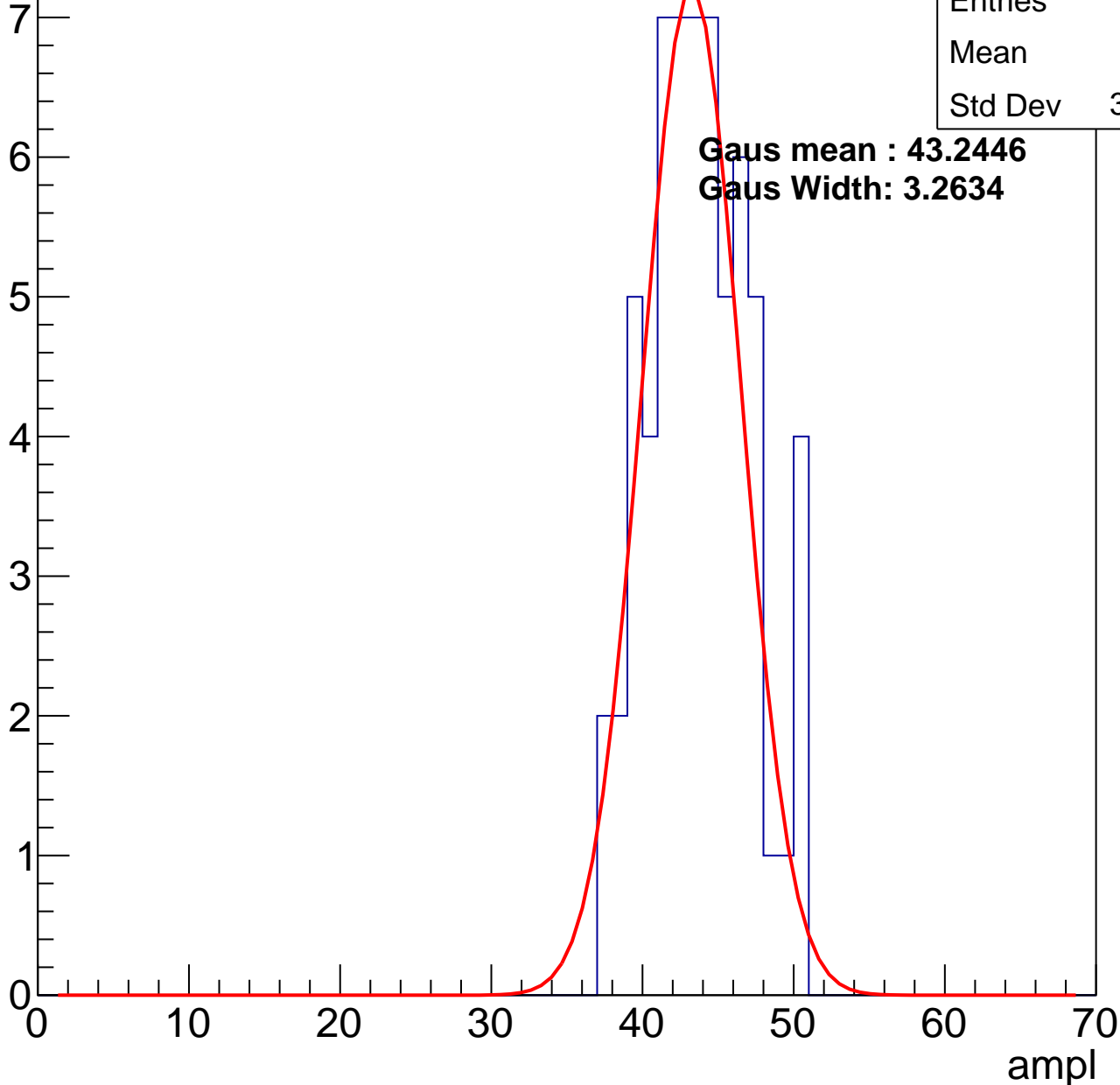
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 43.3 |
| Std Dev | 3.298 |

Gaus mean : 43.2446

Gaus Width: 3.2634

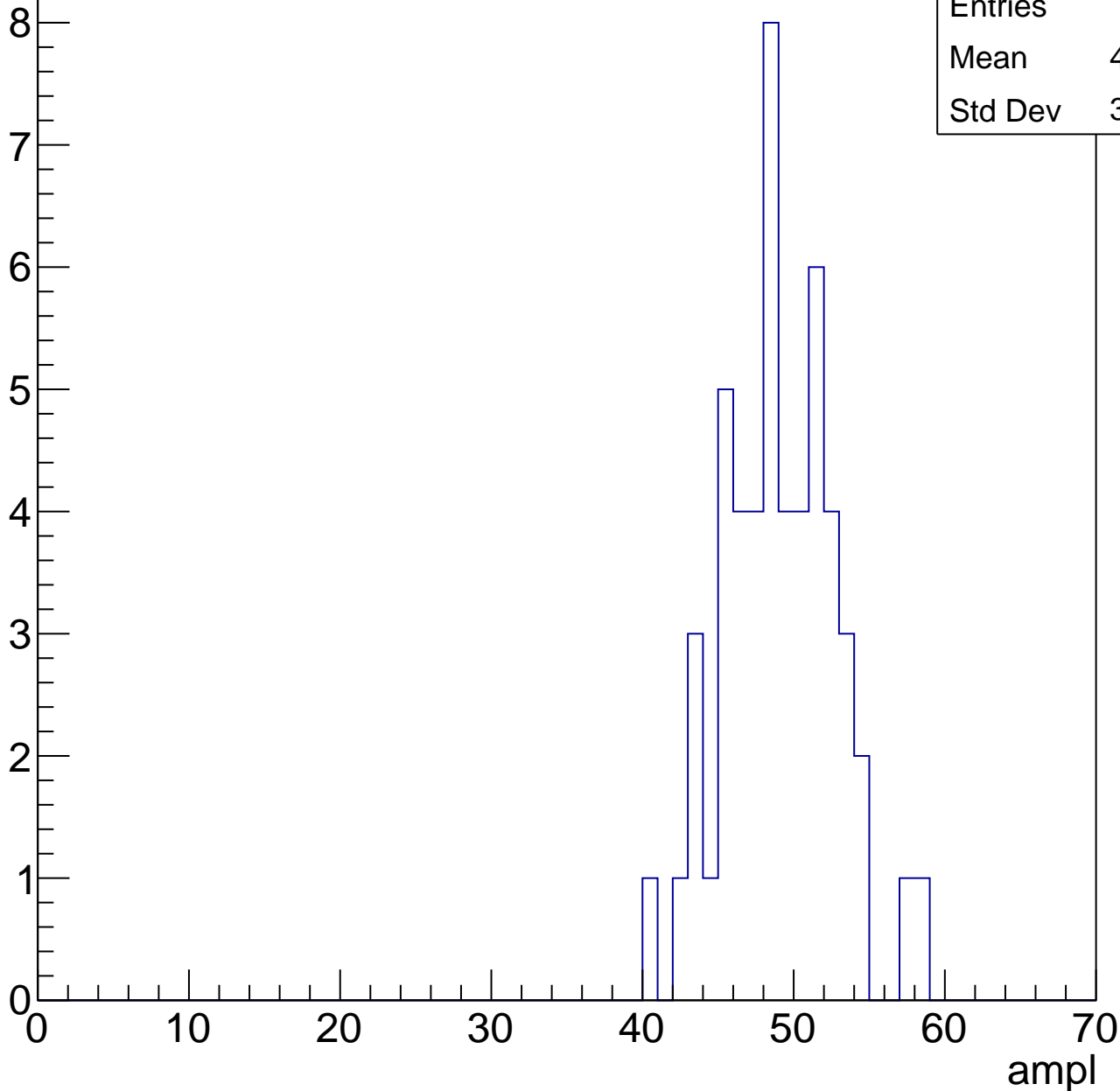


B1L102S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

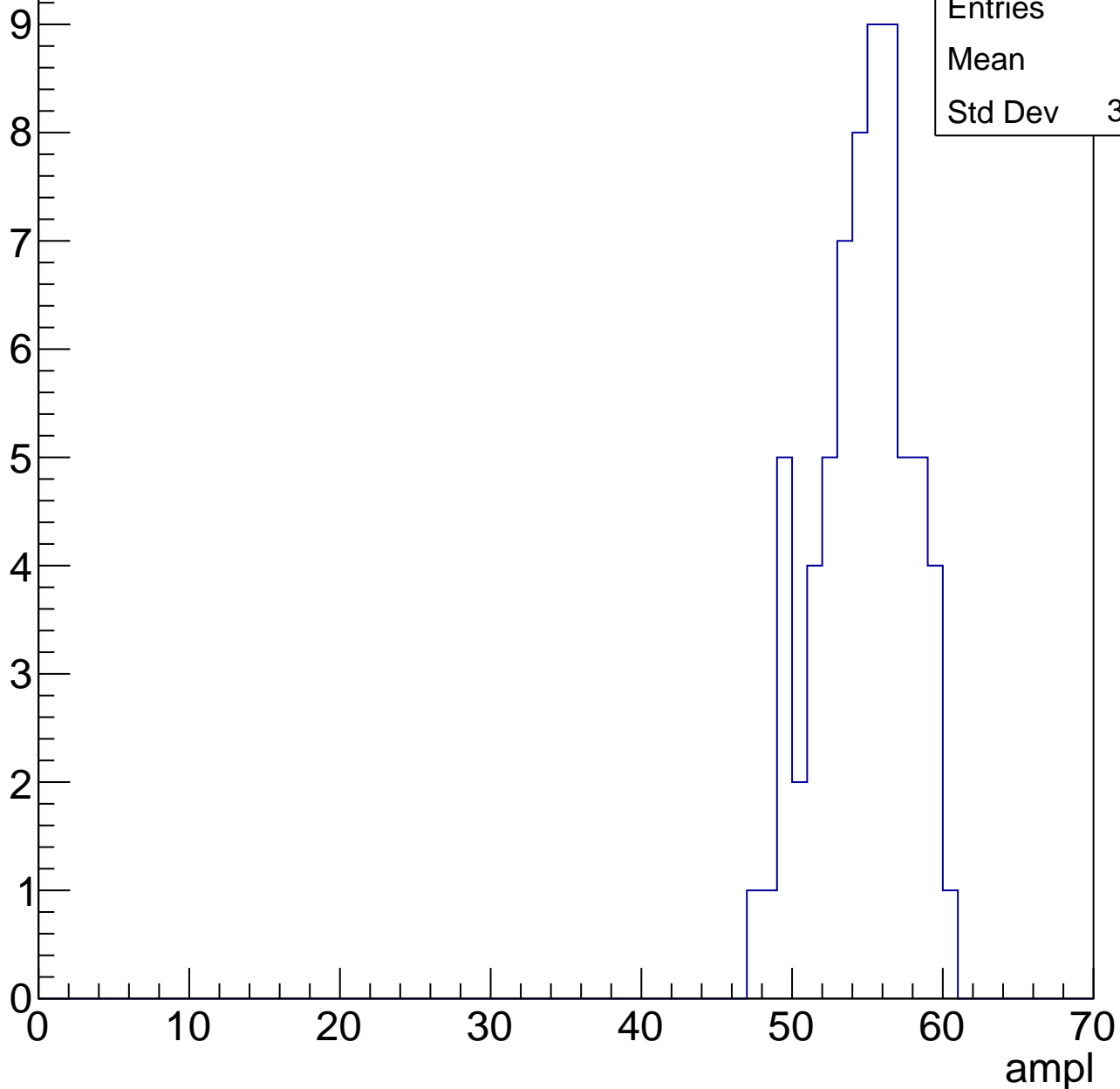
| | |
|---------|-------|
| Entries | 52 |
| Mean | 48.62 |
| Std Dev | 3.675 |



B1L102S, U15-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

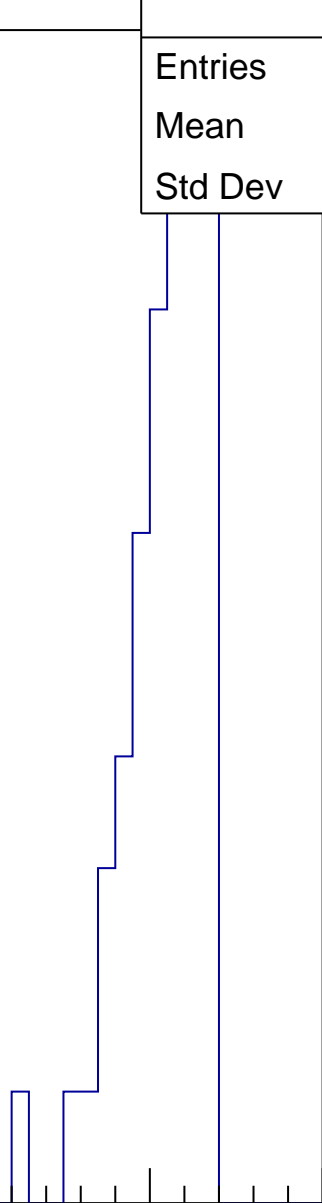
| | |
|---------|-------|
| Entries | 52 |
| Mean | 60.31 |
| Std Dev | 2.317 |

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

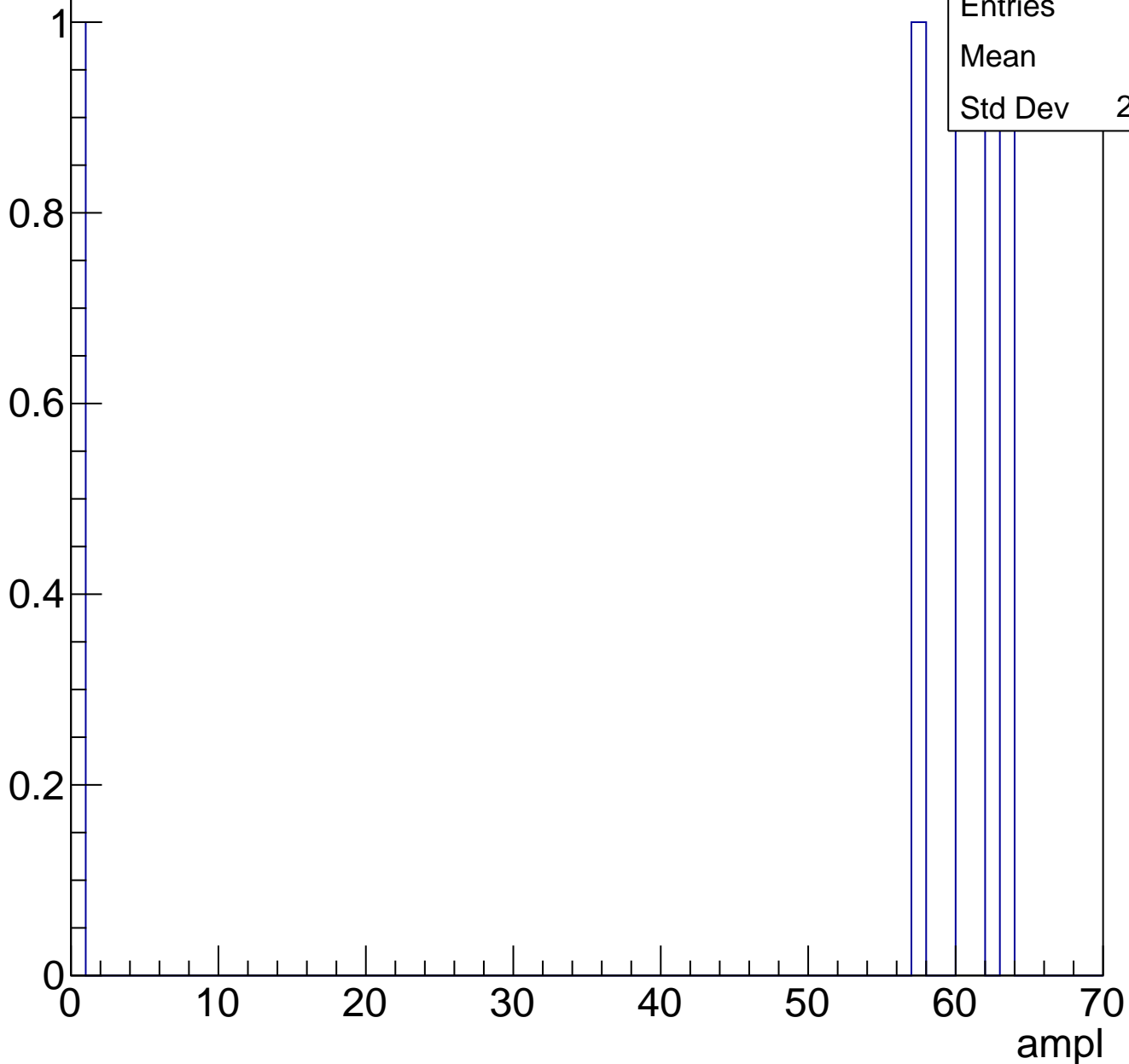
ampl



B1L102S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch110, adc0

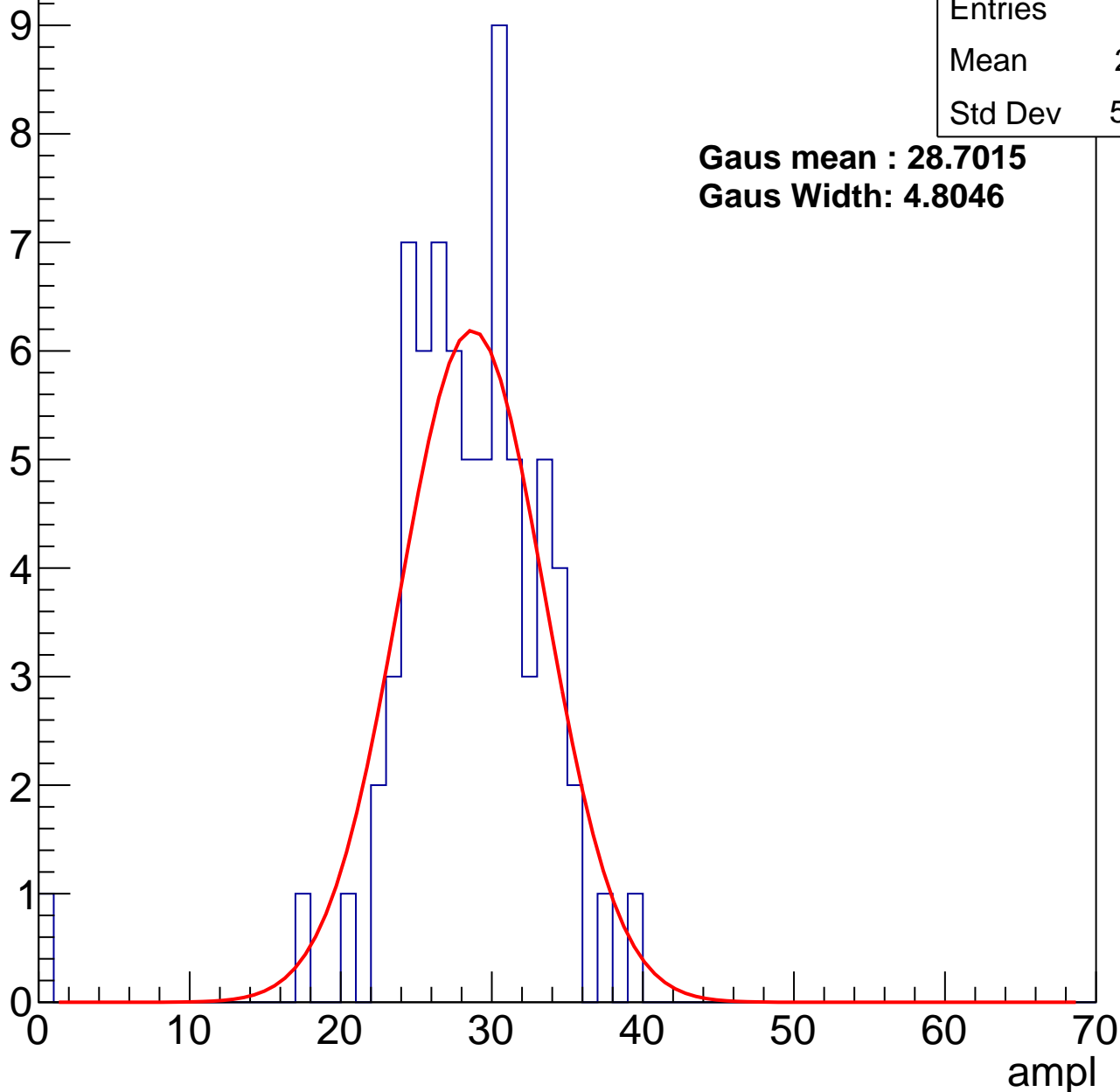
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 27.91 |
| Std Dev | 5.223 |

Gaus mean : 28.7015

Gaus Width: 4.8046



B1L102S, U15-ch110, adc1

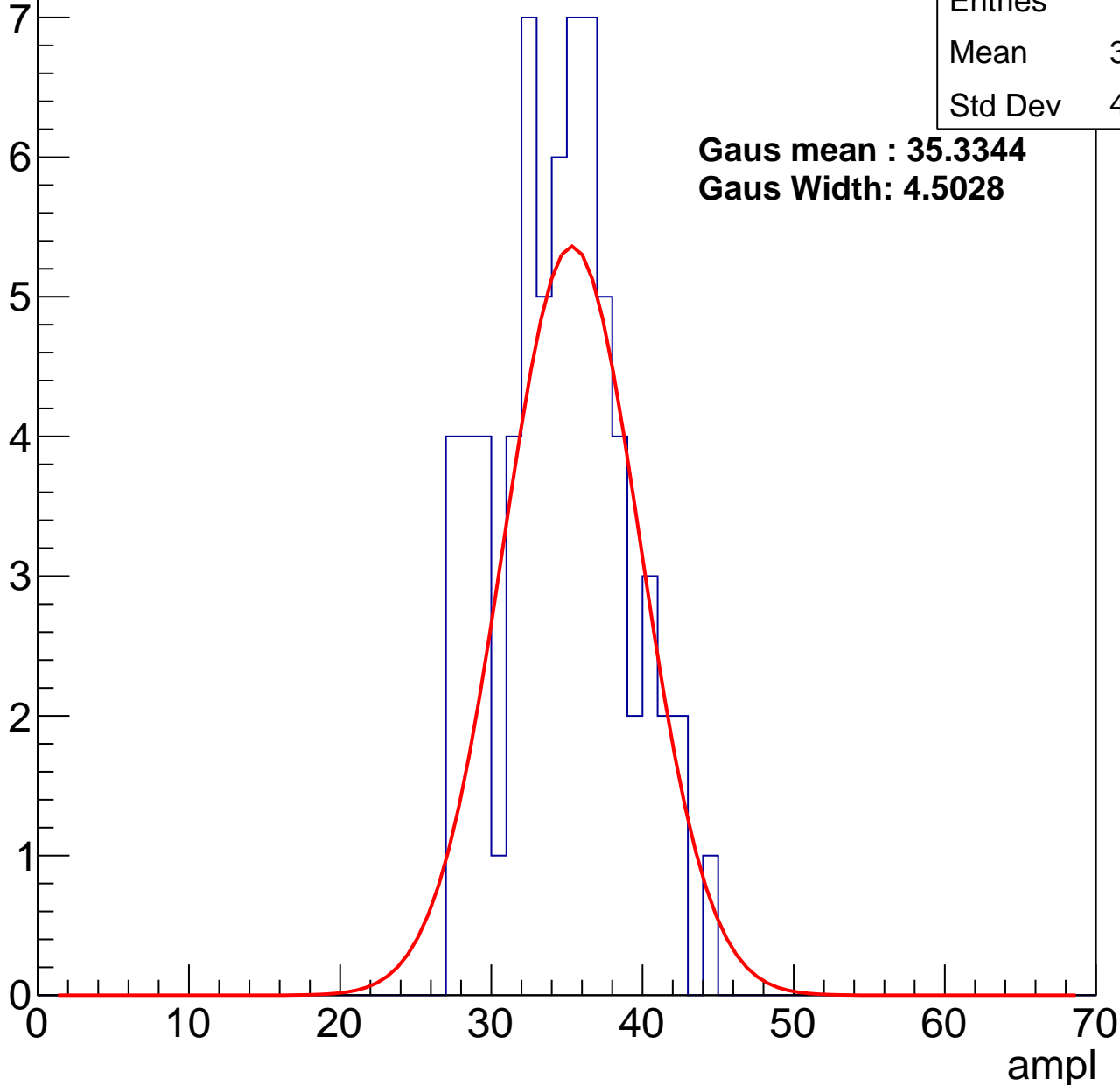
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 68 |
| Mean | 34.19 |
| Std Dev | 4.113 |

Gaus mean : 35.3344

Gaus Width: 4.5028



B1L102S, U15-ch110, adc2

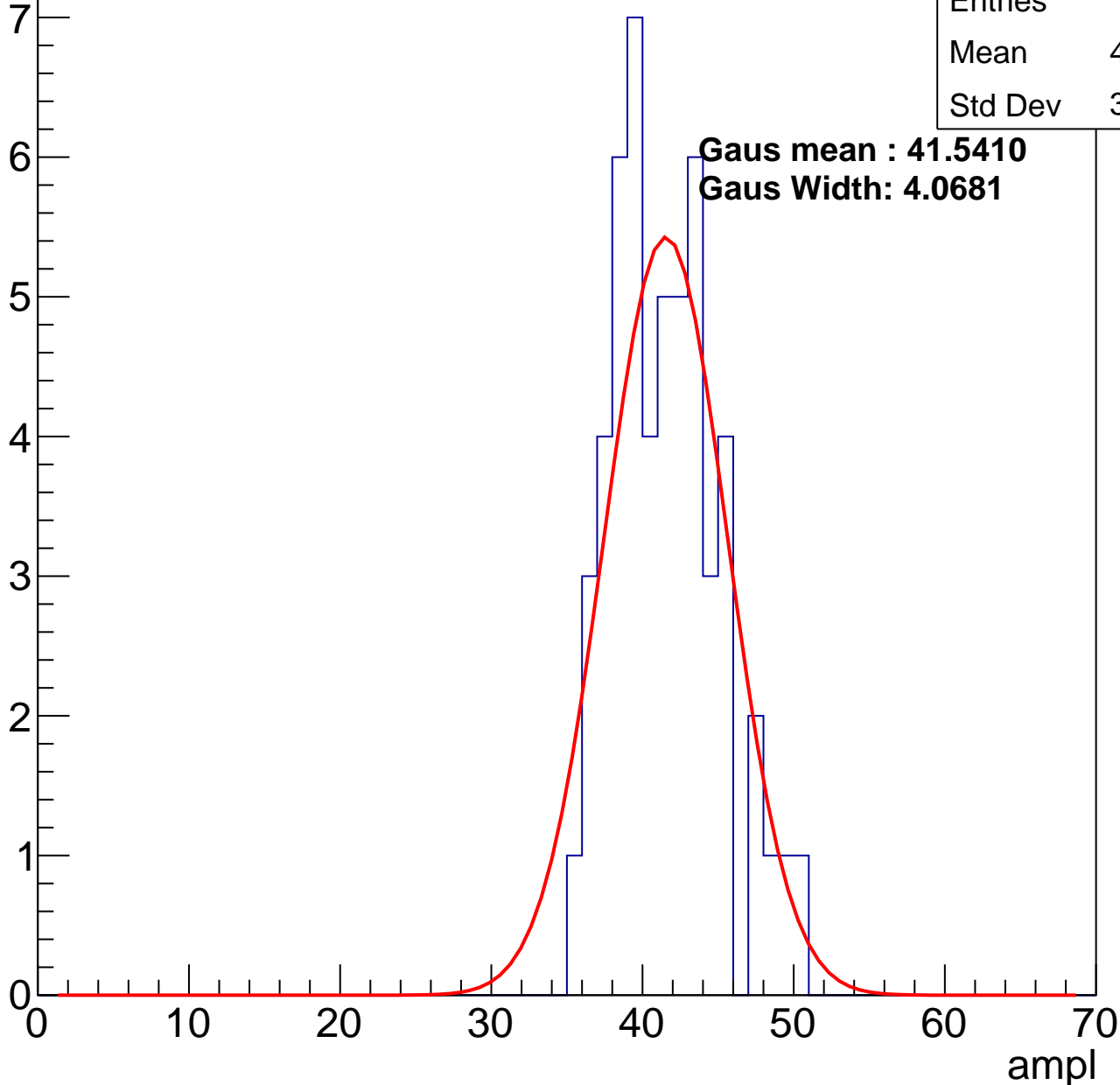
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 41.09 |
| Std Dev | 3.493 |

Gaus mean : 41.5410

Gaus Width: 4.0681

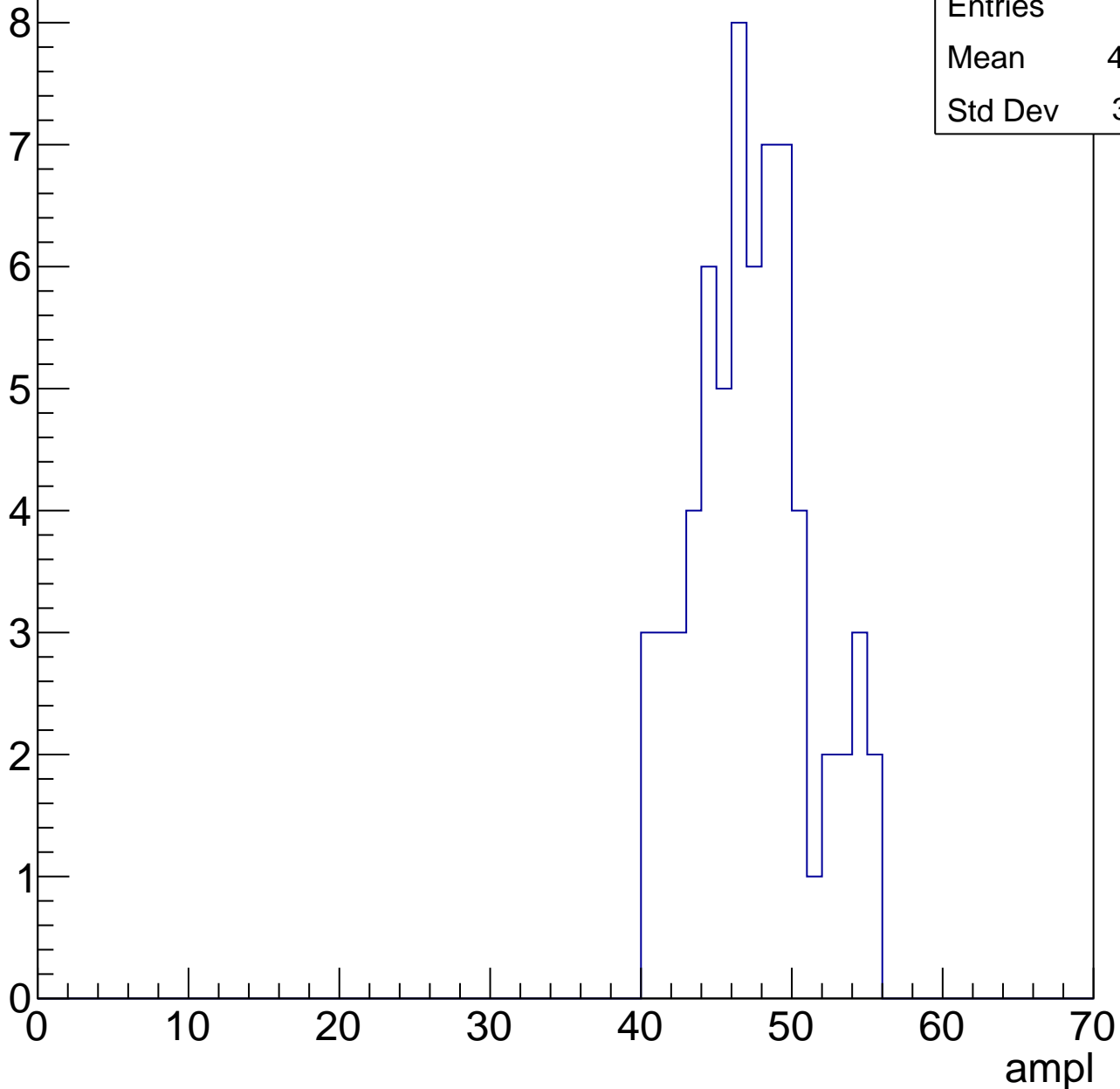


B1L102S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 46.85 |
| Std Dev | 3.811 |

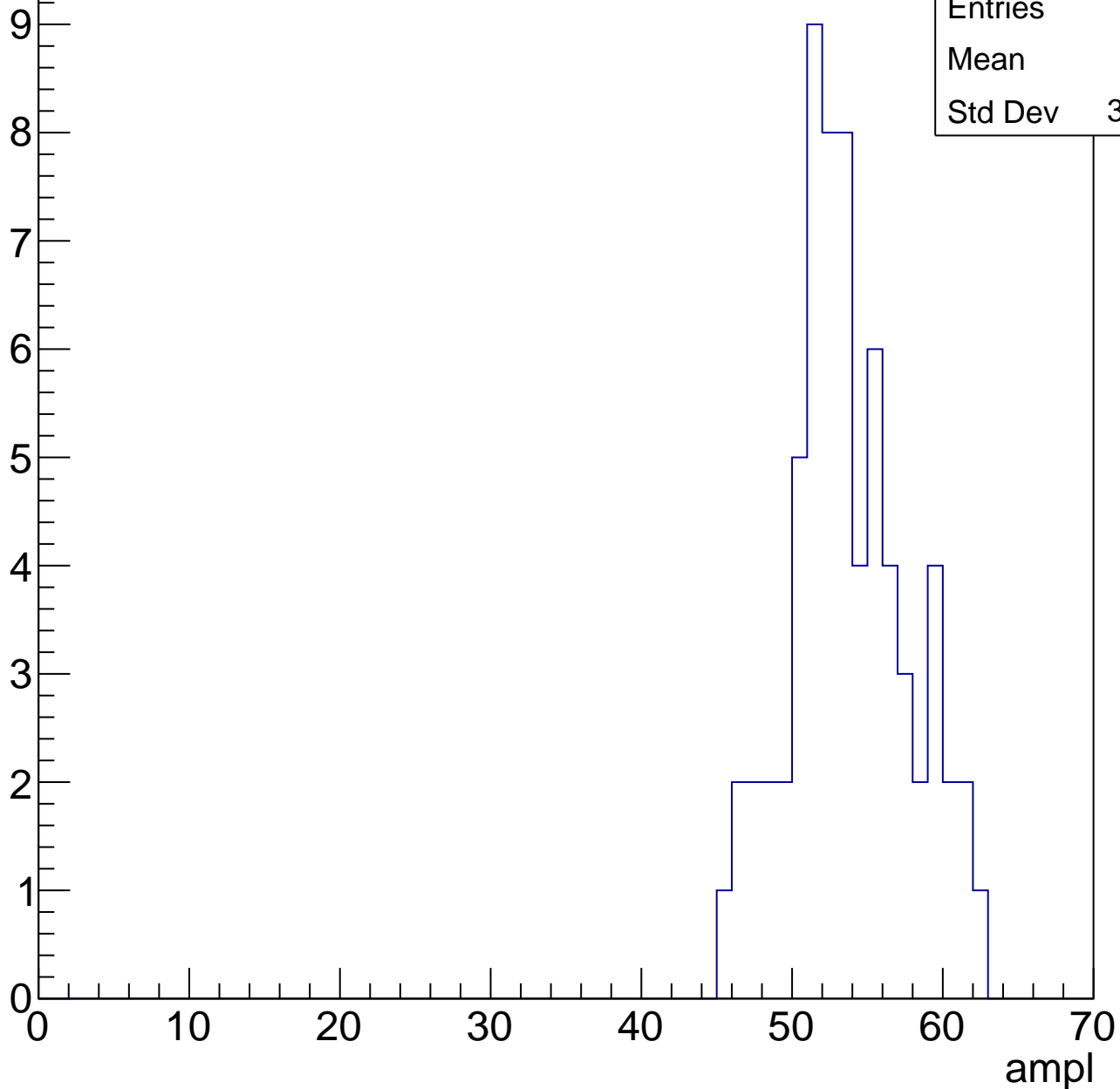


B1L102S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 53.3 |
| Std Dev | 3.887 |

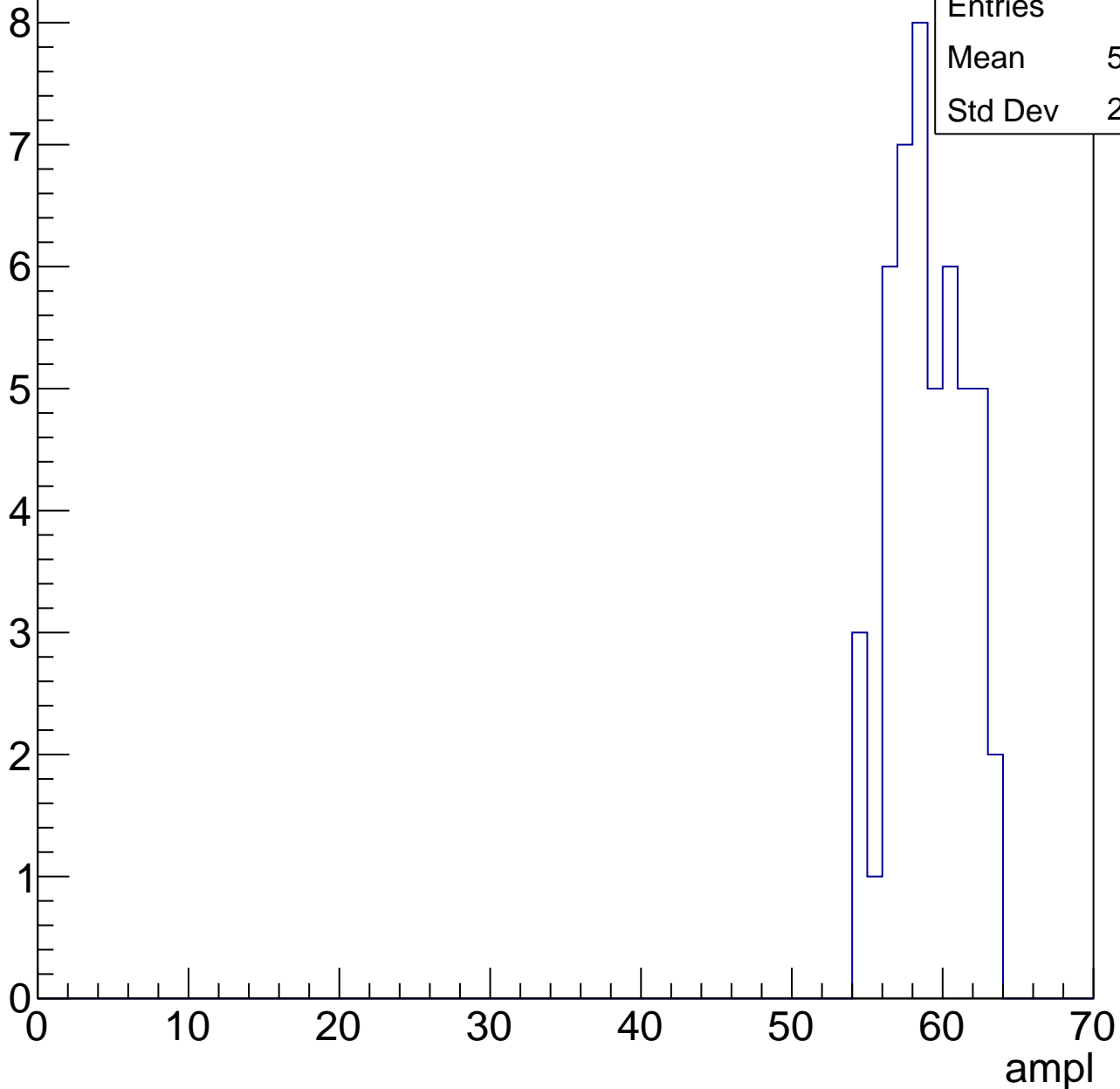


B1L102S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.58 |
| Std Dev | 2.396 |

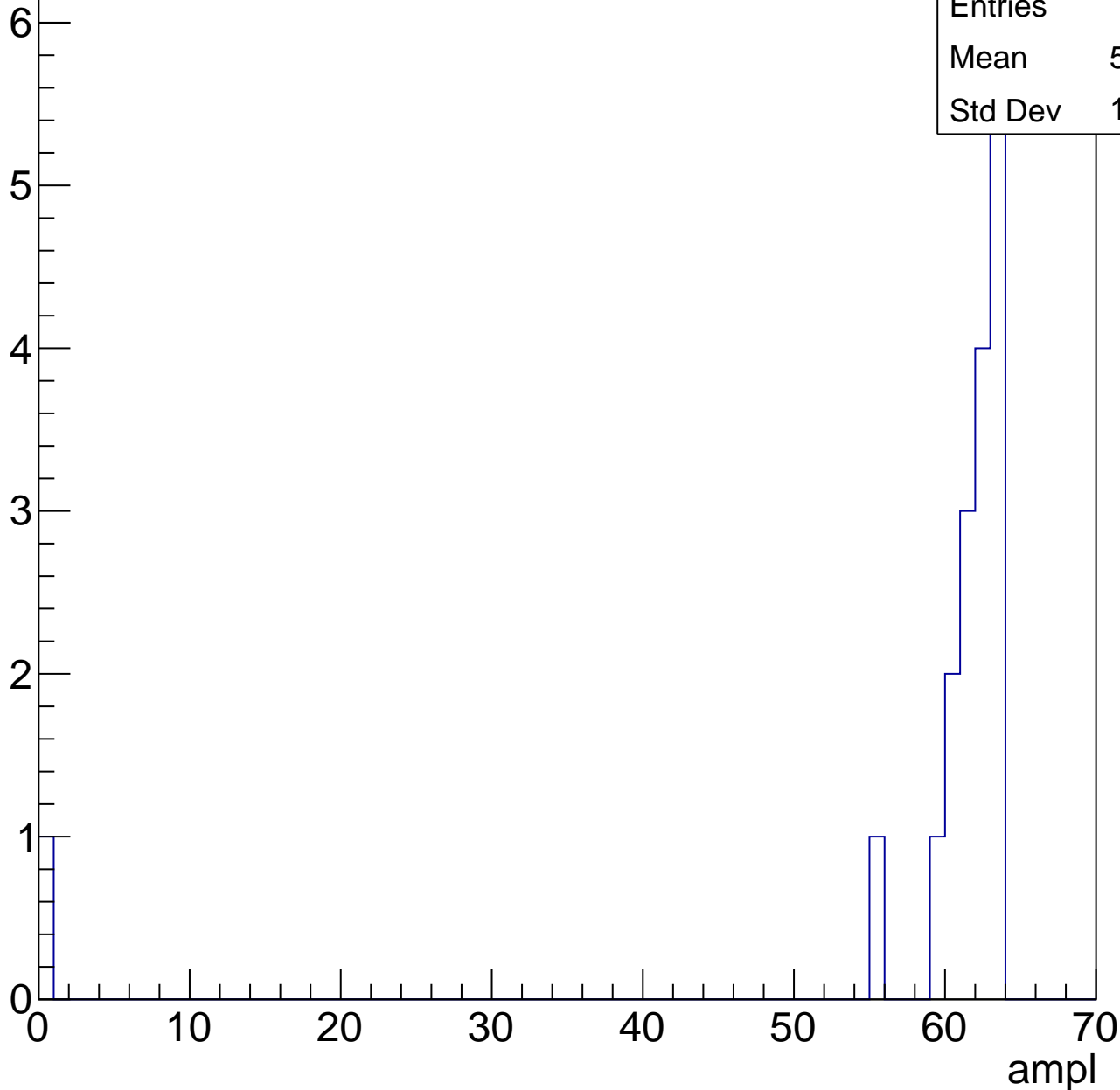


B1L102S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

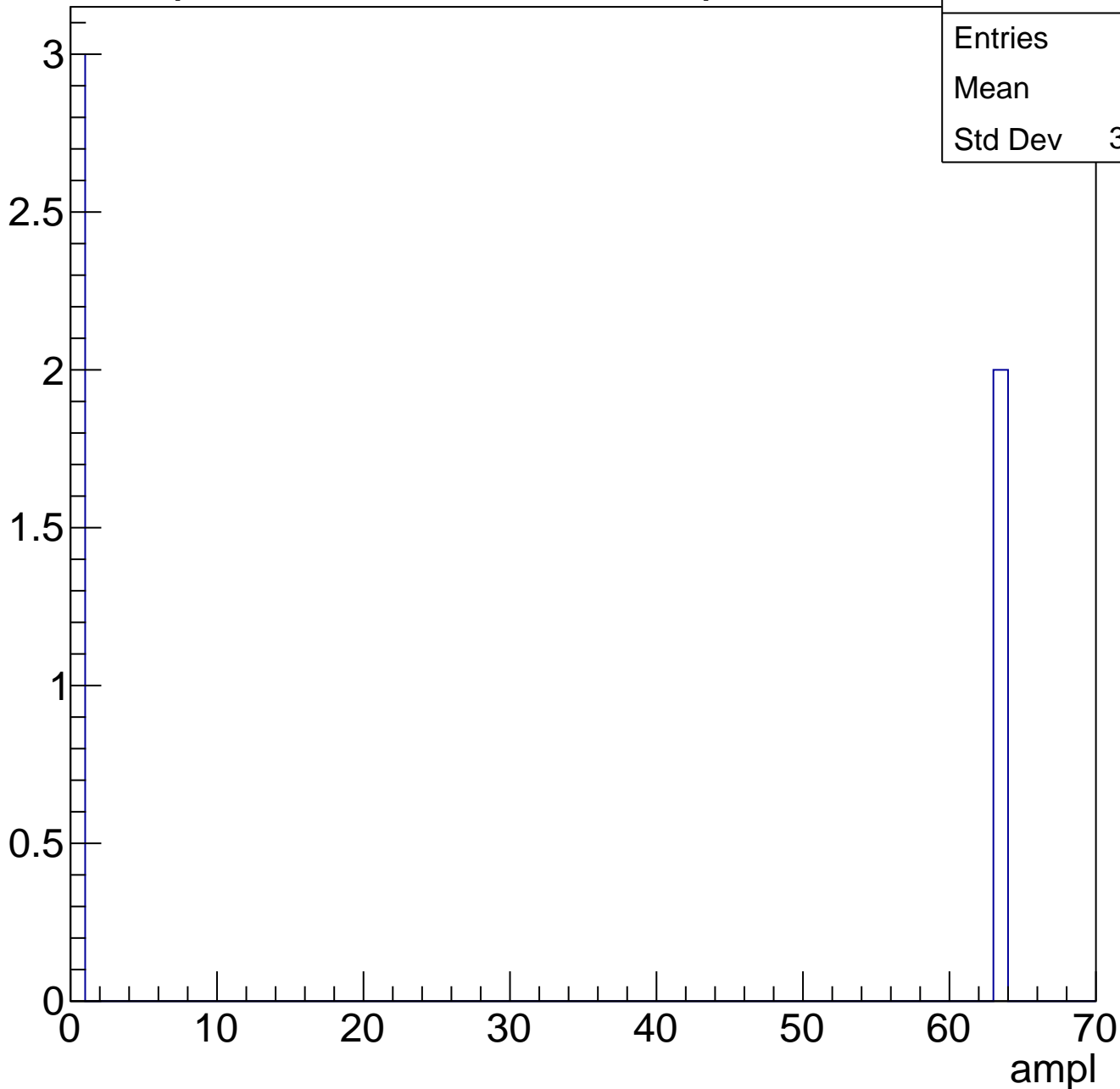
| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.94 |
| Std Dev | 14.19 |



B1L102S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch111, adc0

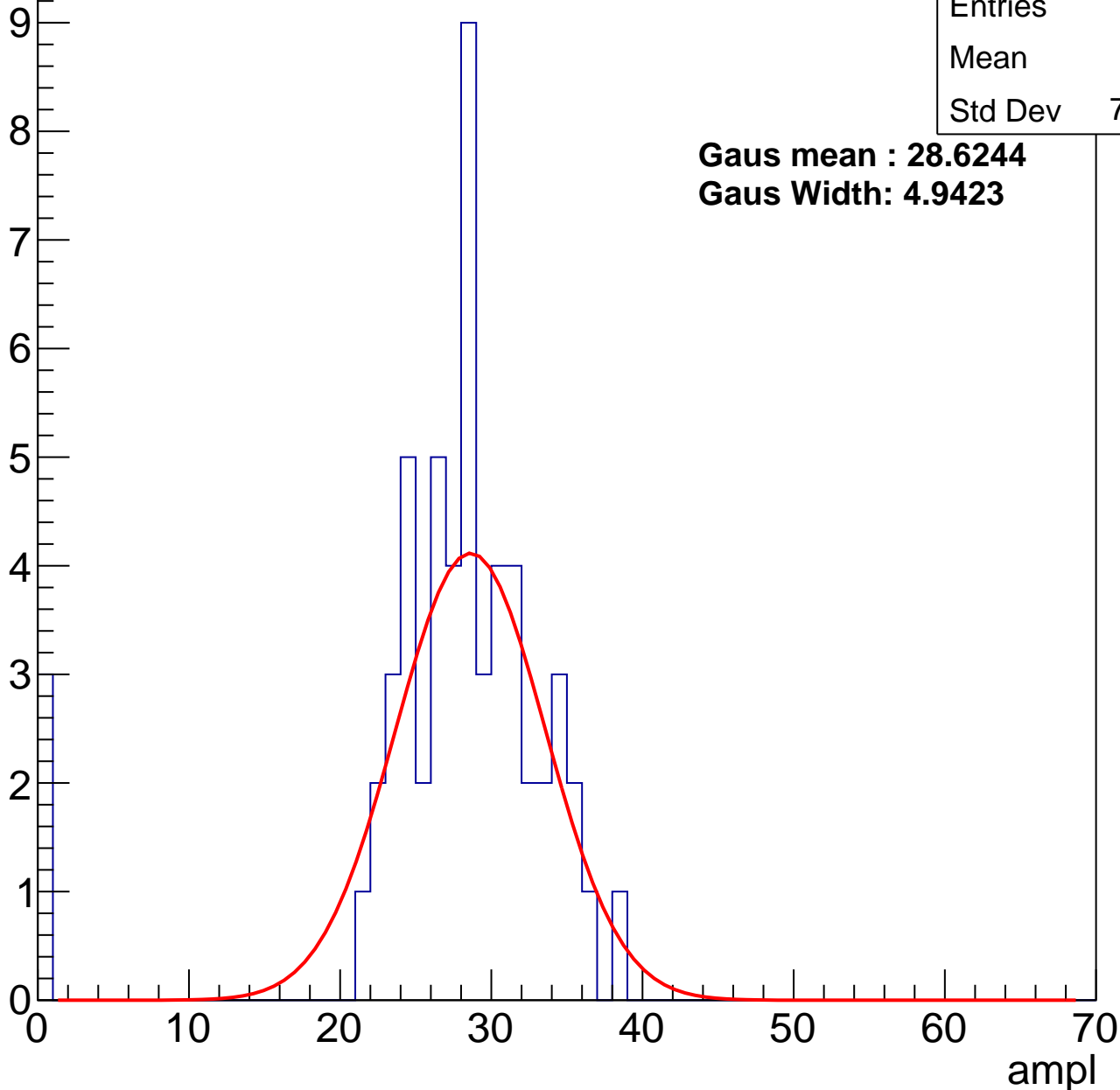
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 26.8 |
| Std Dev | 7.436 |

Gaus mean : 28.6244

Gaus Width: 4.9423



B1L102S, U15-ch111, adc1

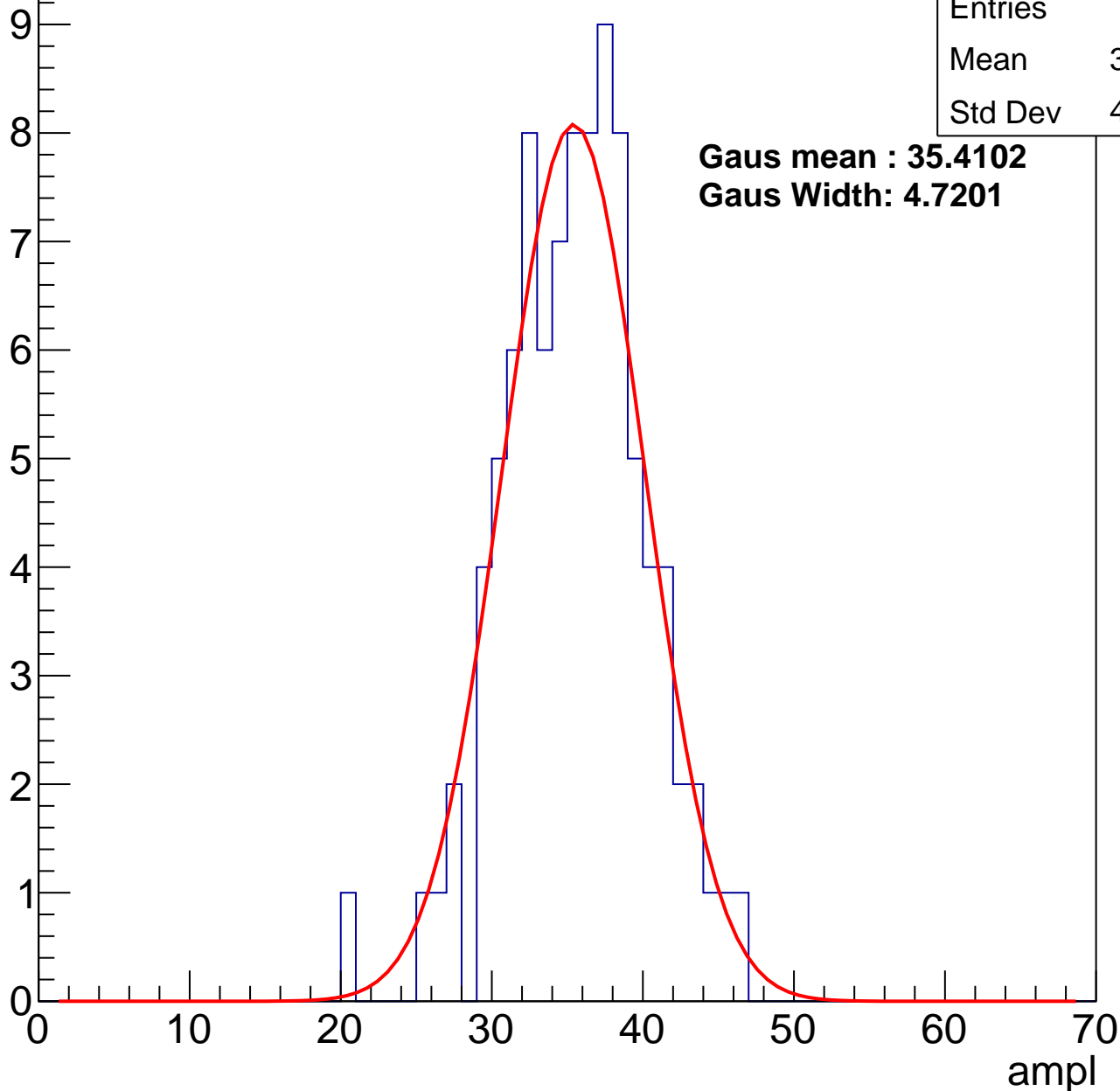
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 94 |
| Mean | 35.09 |
| Std Dev | 4.577 |

Gaus mean : 35.4102

Gaus Width: 4.7201



B1L102S, U15-ch111, adc2

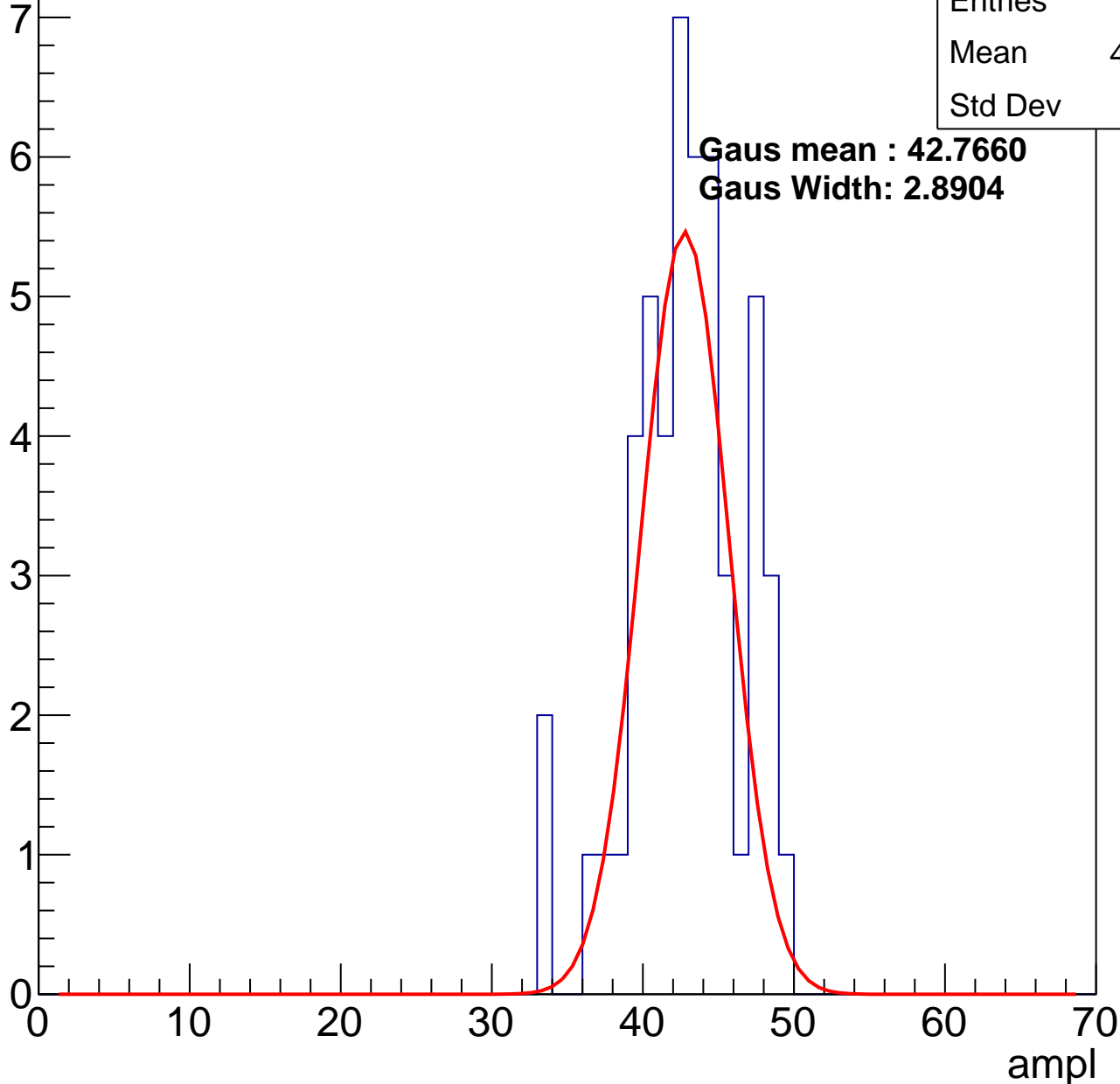
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 42.44 |
| Std Dev | 3.59 |

Gaus mean : 42.7660

Gaus Width: 2.8904

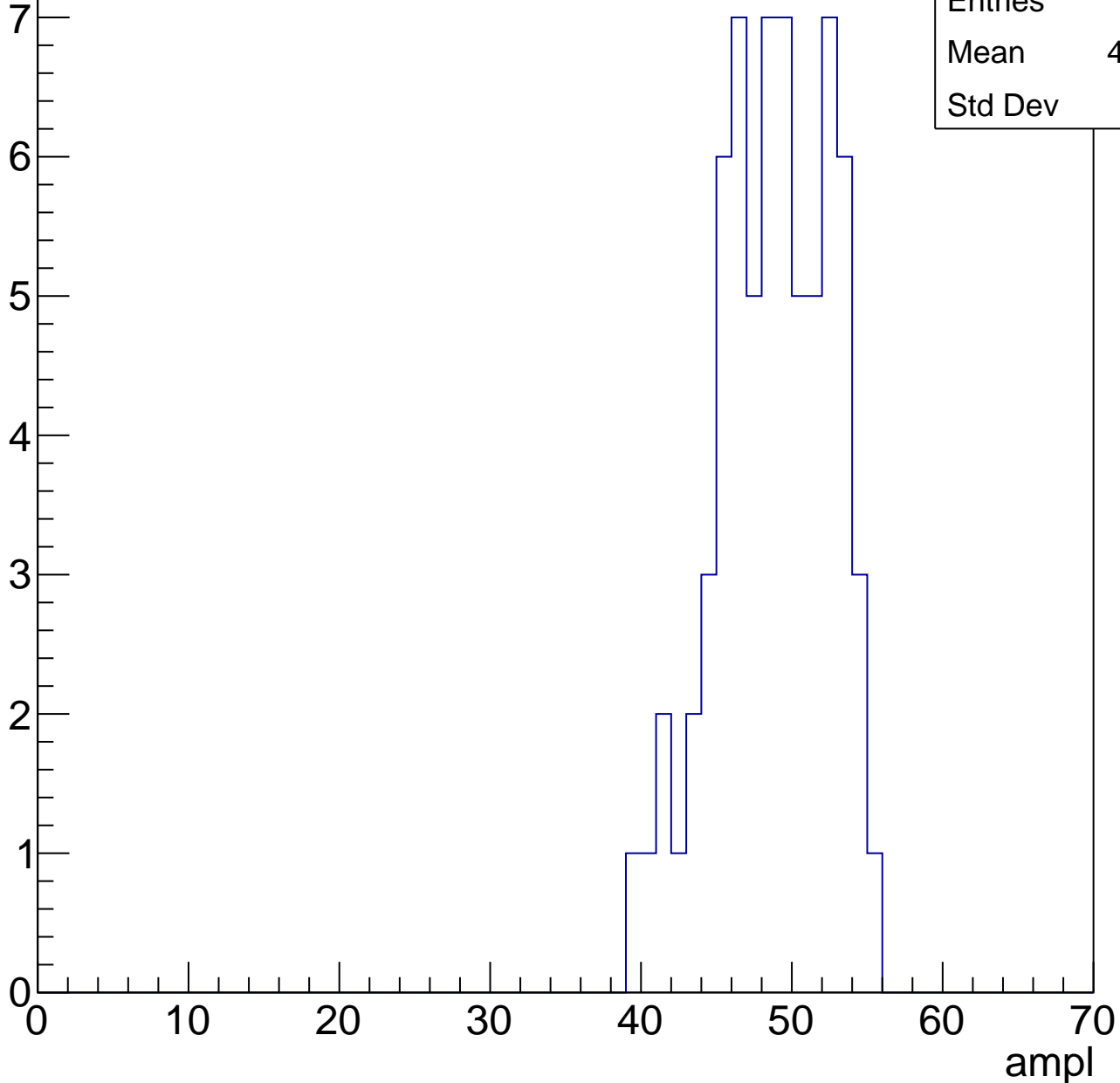


B1L102S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 48.28 |
| Std Dev | 3.71 |

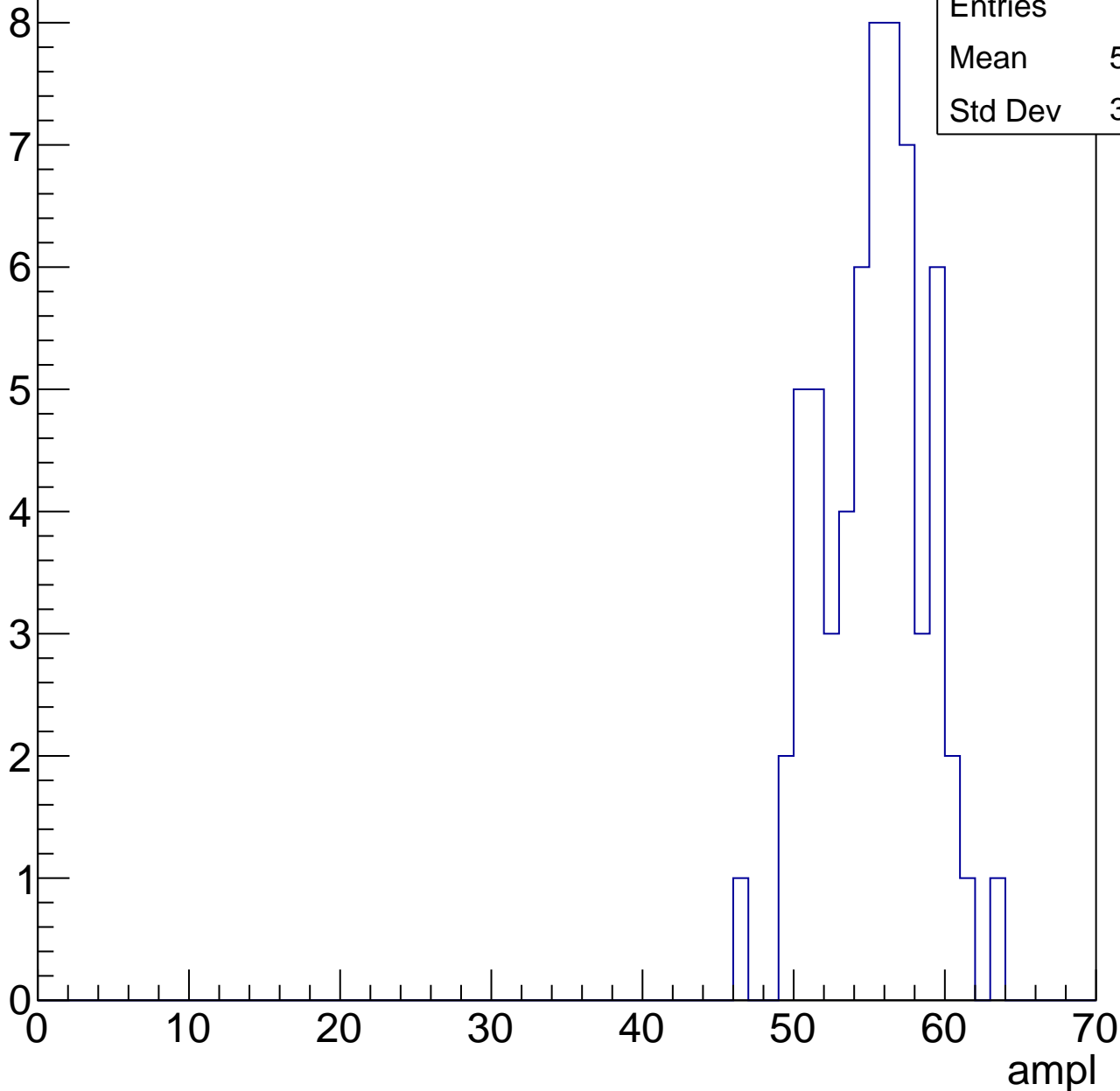


B1L102S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 54.84 |
| Std Dev | 3.394 |

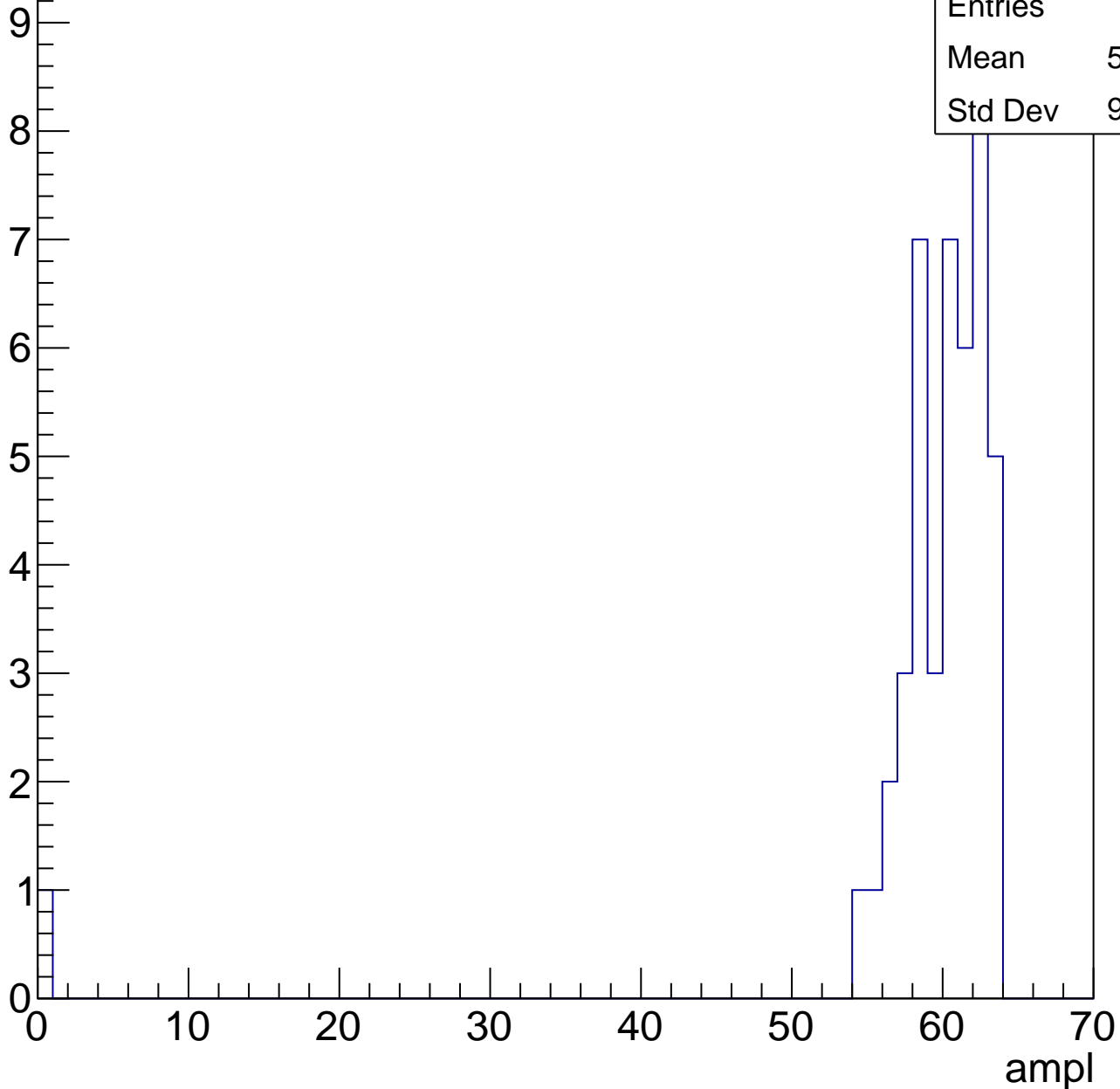


B1L102S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

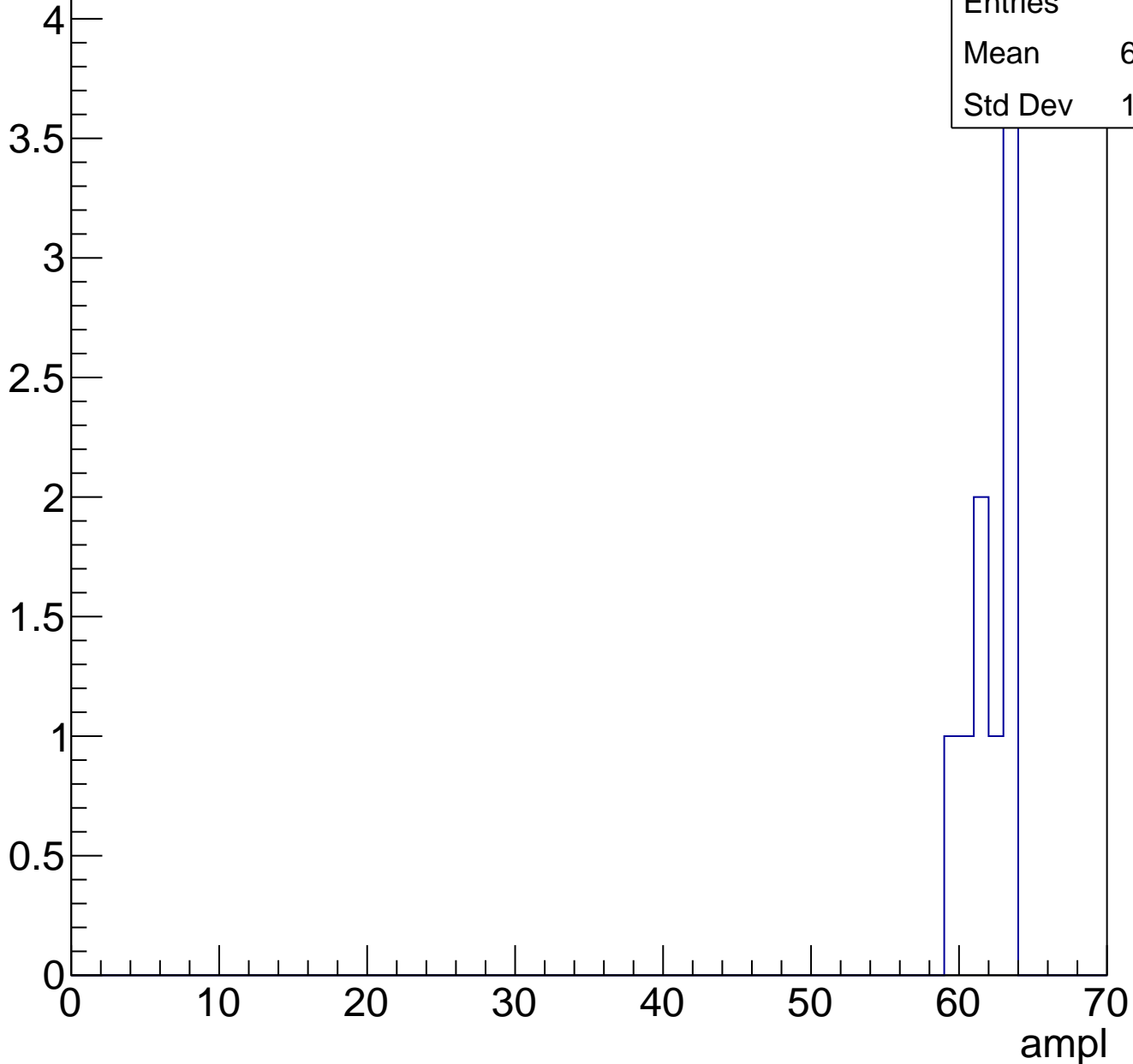
| | |
|---------|-------|
| Entries | 45 |
| Mean | 58.53 |
| Std Dev | 9.118 |



B1L102S, U15-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch112, adc0

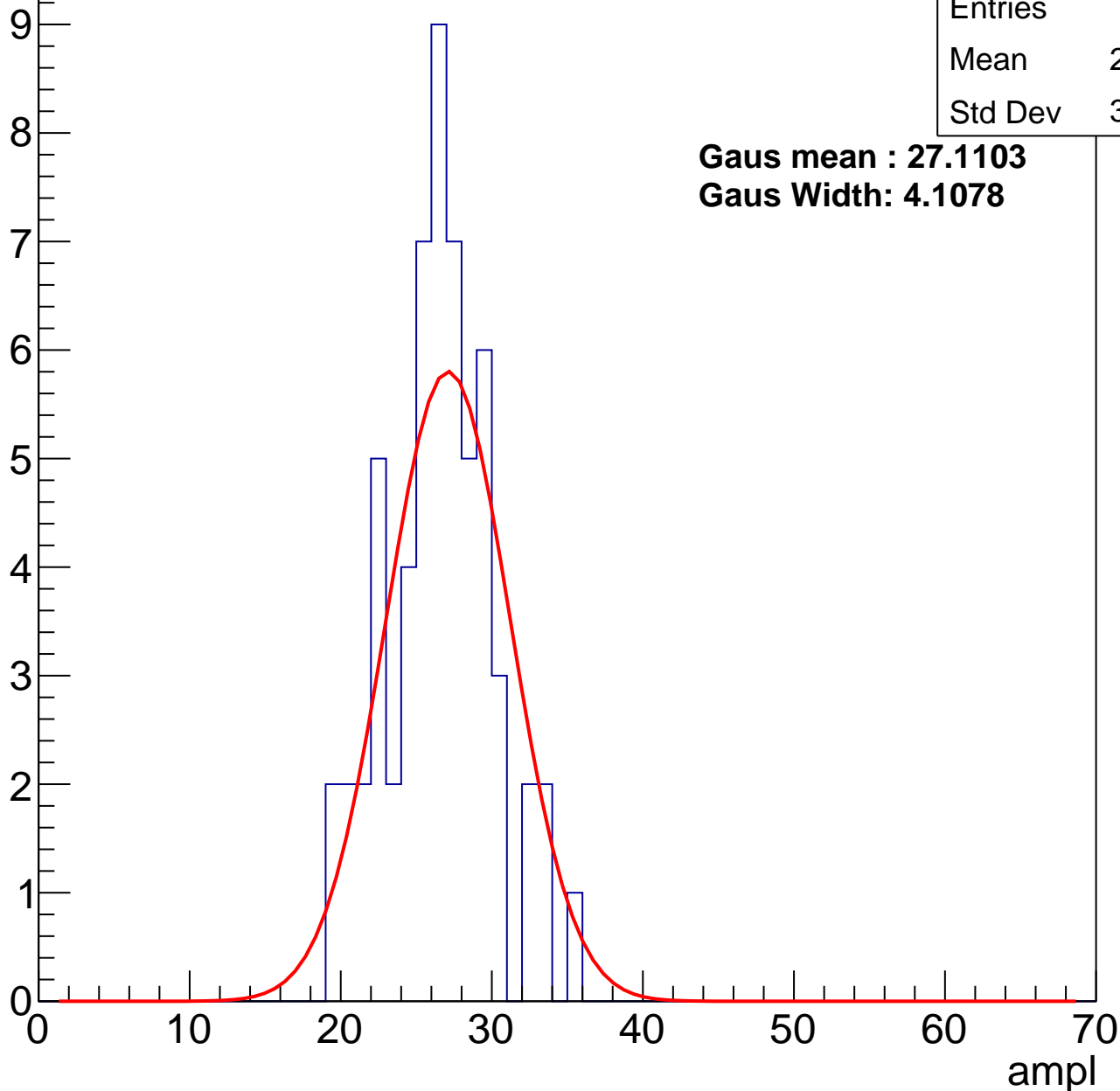
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 26.08 |
| Std Dev | 3.495 |

Gaus mean : 27.1103

Gaus Width: 4.1078



B1L102S, U15-ch112, adc1

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 66 |
| Mean | 32.59 |
| Std Dev | 3.601 |

Gaus mean : 33.2834

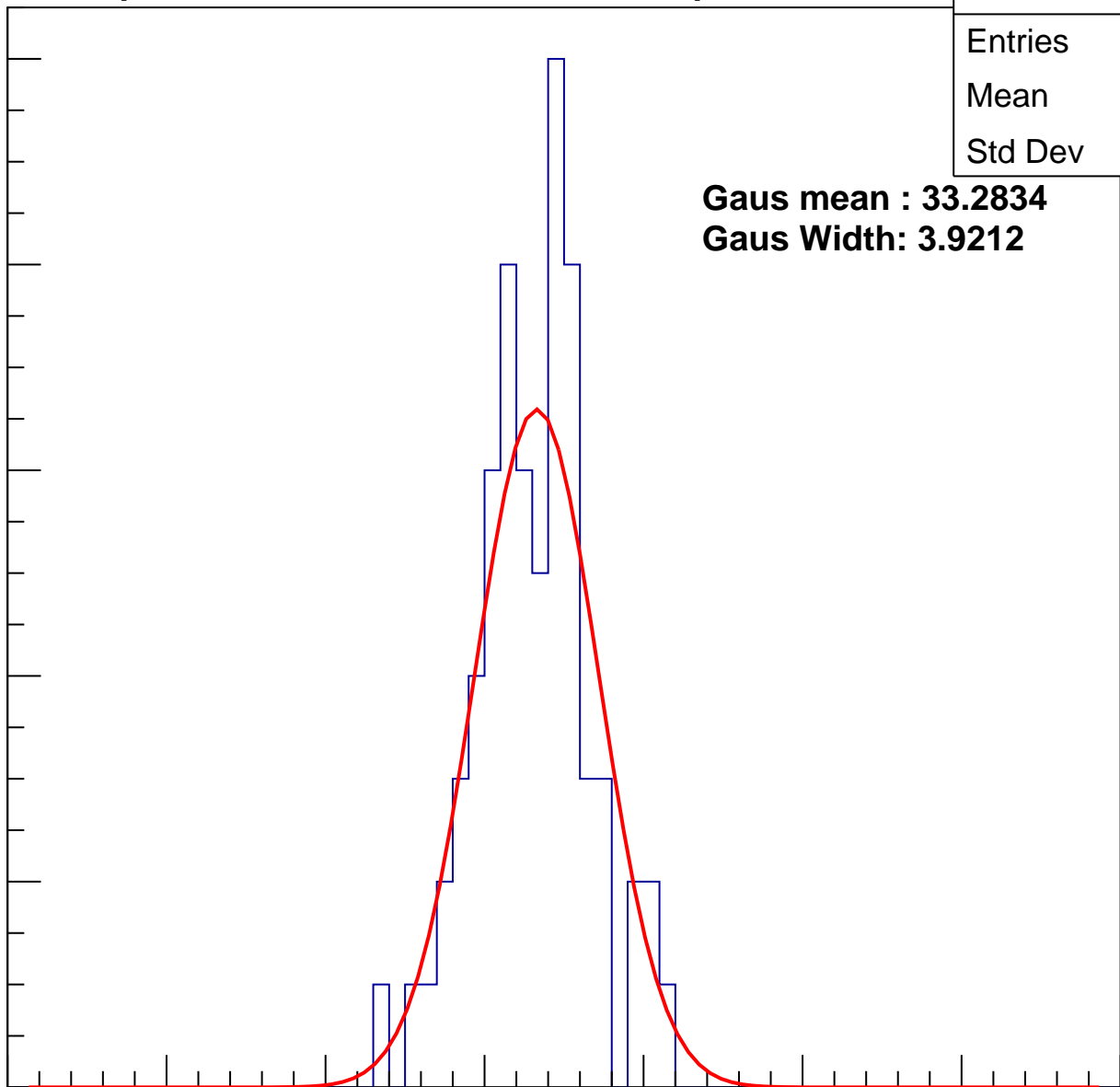
Gaus Width: 3.9212

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U15-ch112, adc2

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 73 |
| Mean | 39.51 |
| Std Dev | 3.942 |

Gaus mean : 40.8455

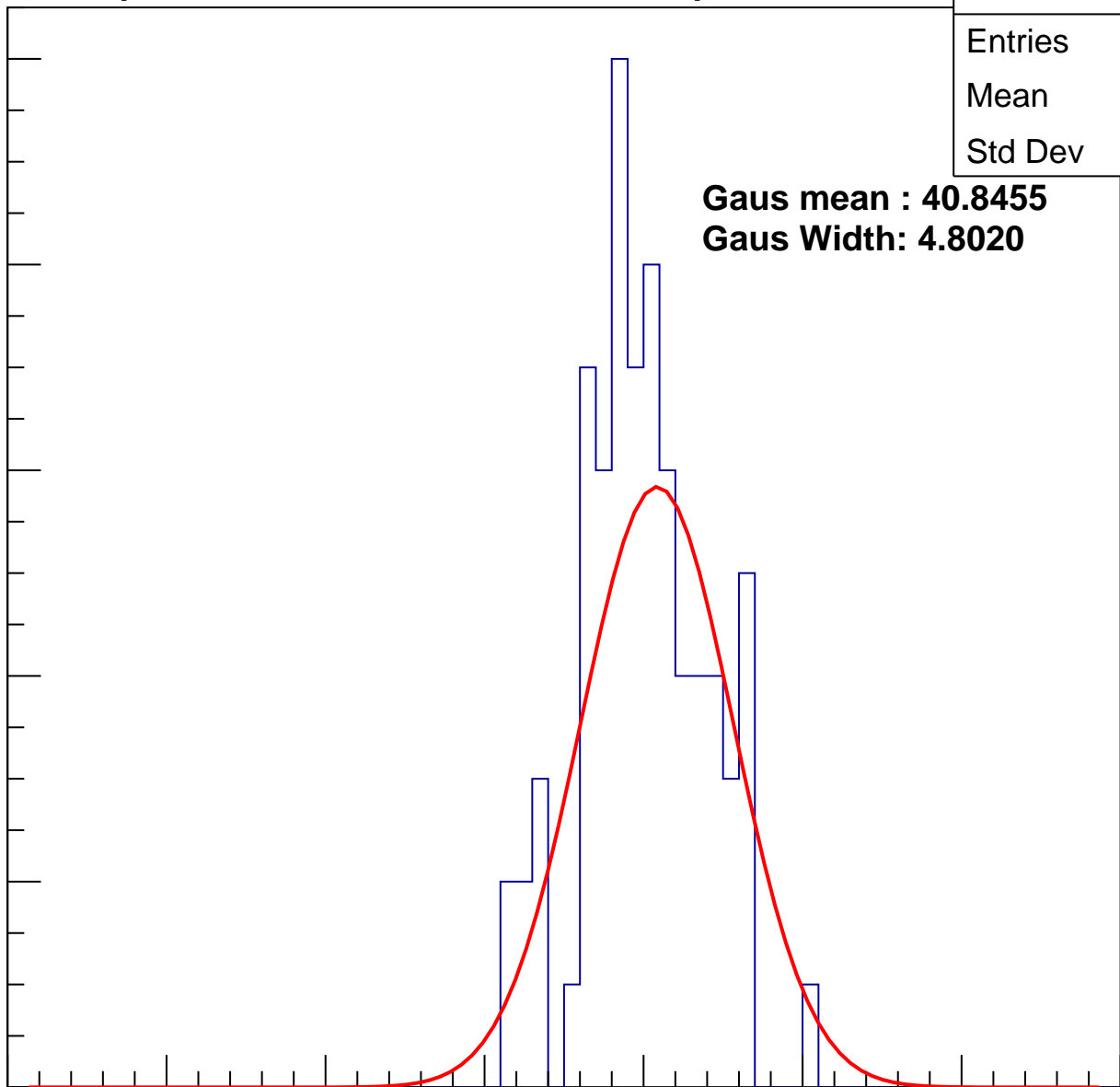
Gaus Width: 4.8020

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

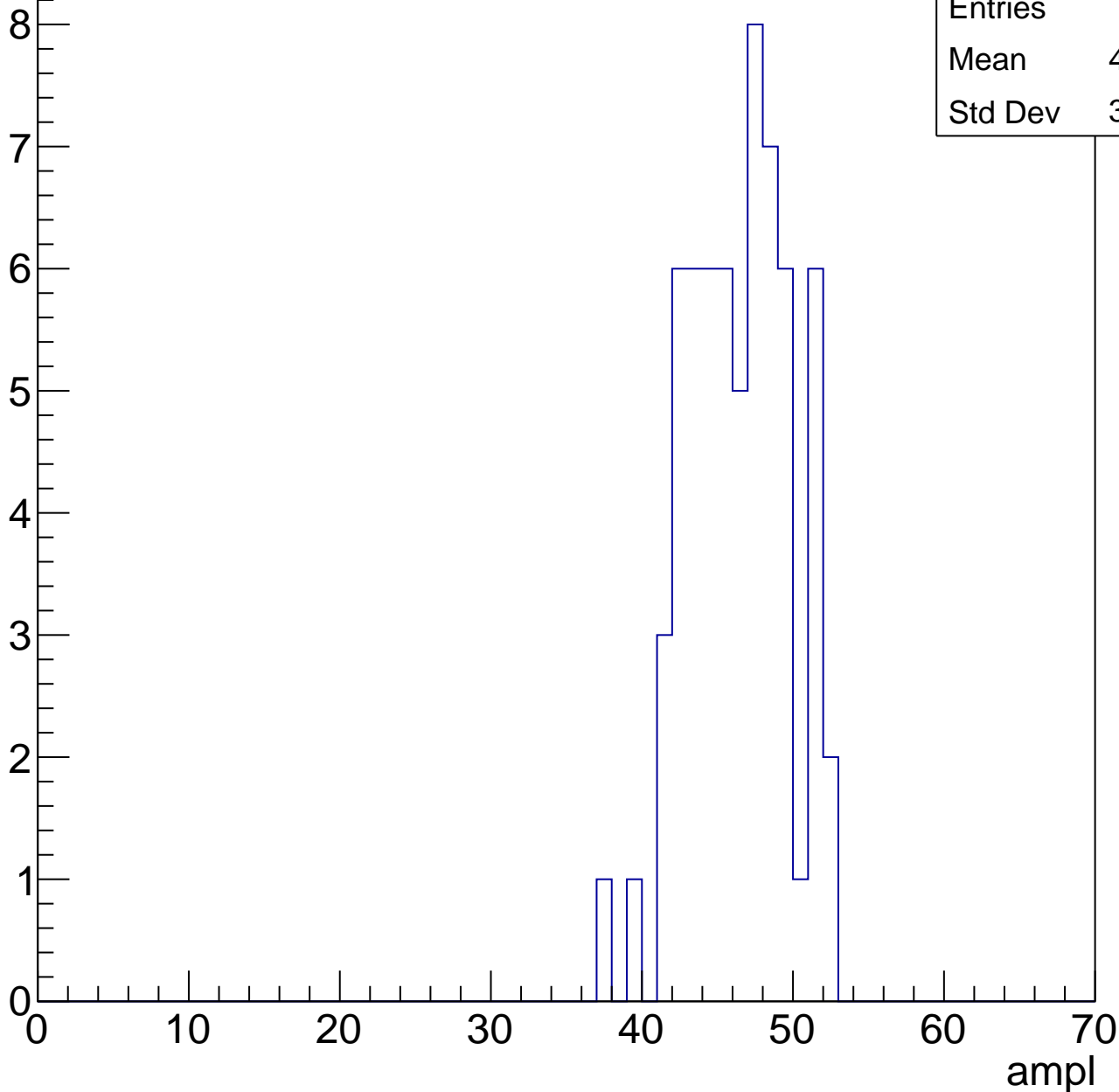


B1L102S, U15-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.92 |
| Std Dev | 3.337 |

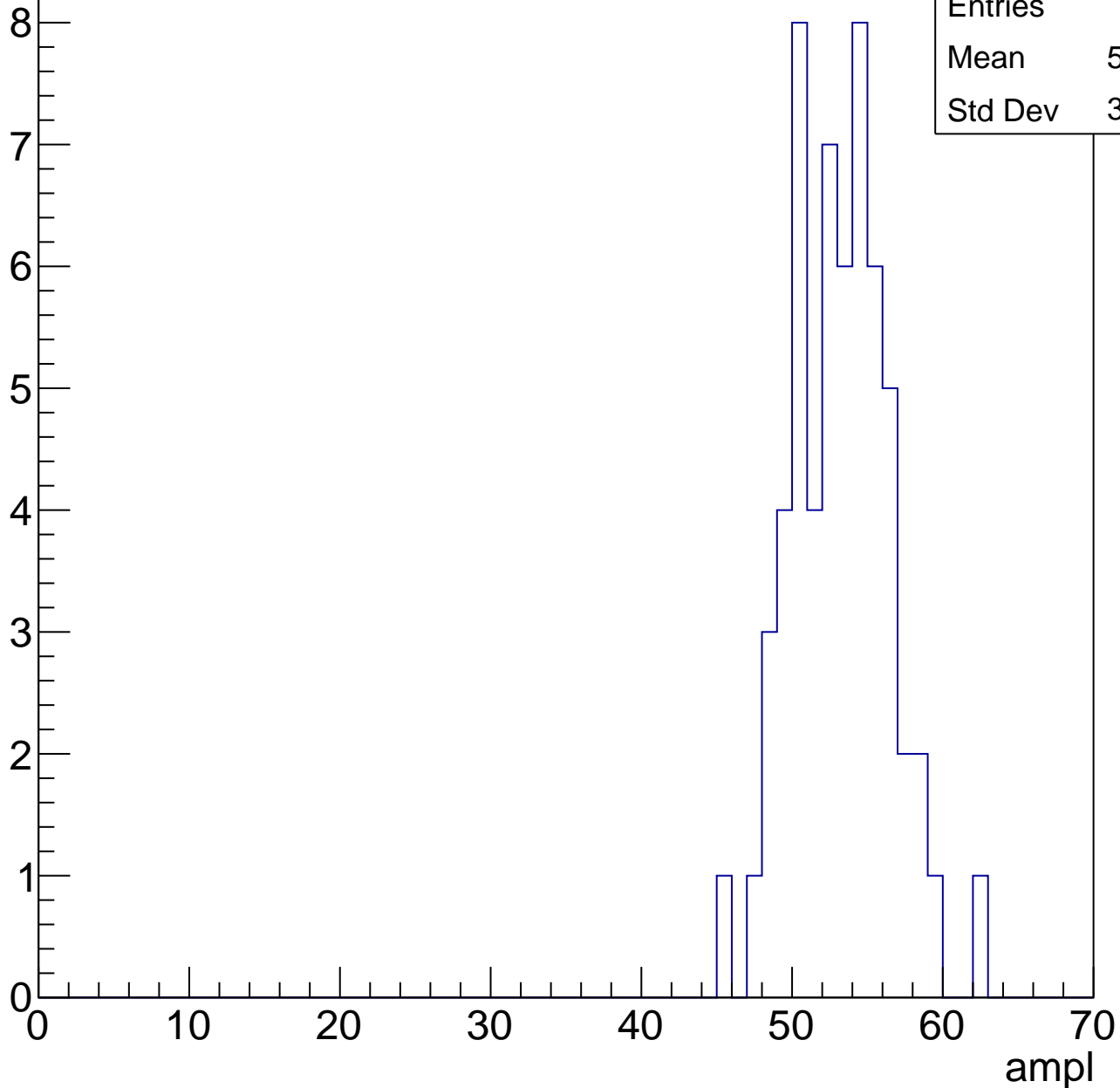


B1L102S, U15-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 52.73 |
| Std Dev | 3.204 |

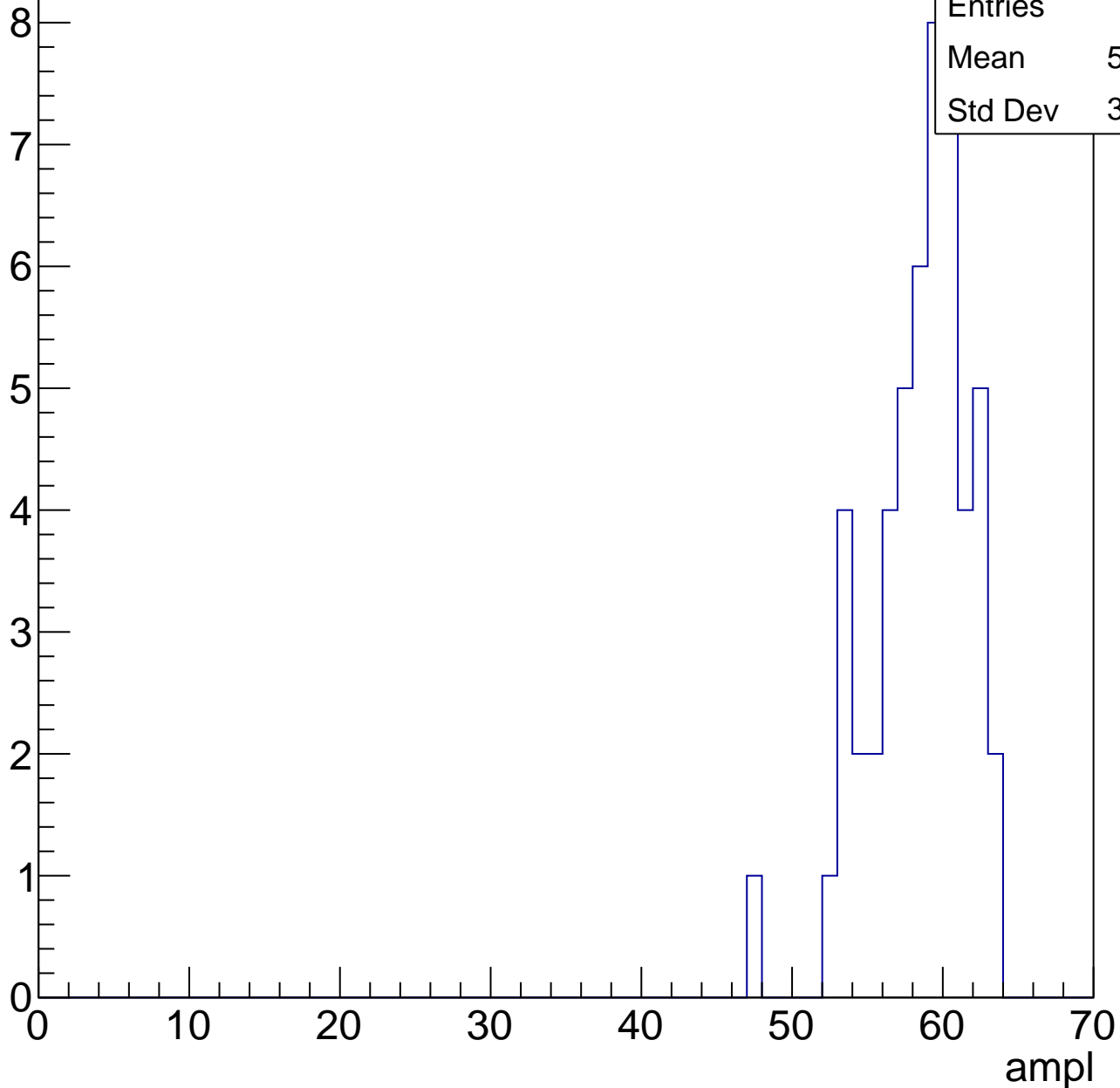


B1L102S, U15-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 58.04 |
| Std Dev | 3.204 |

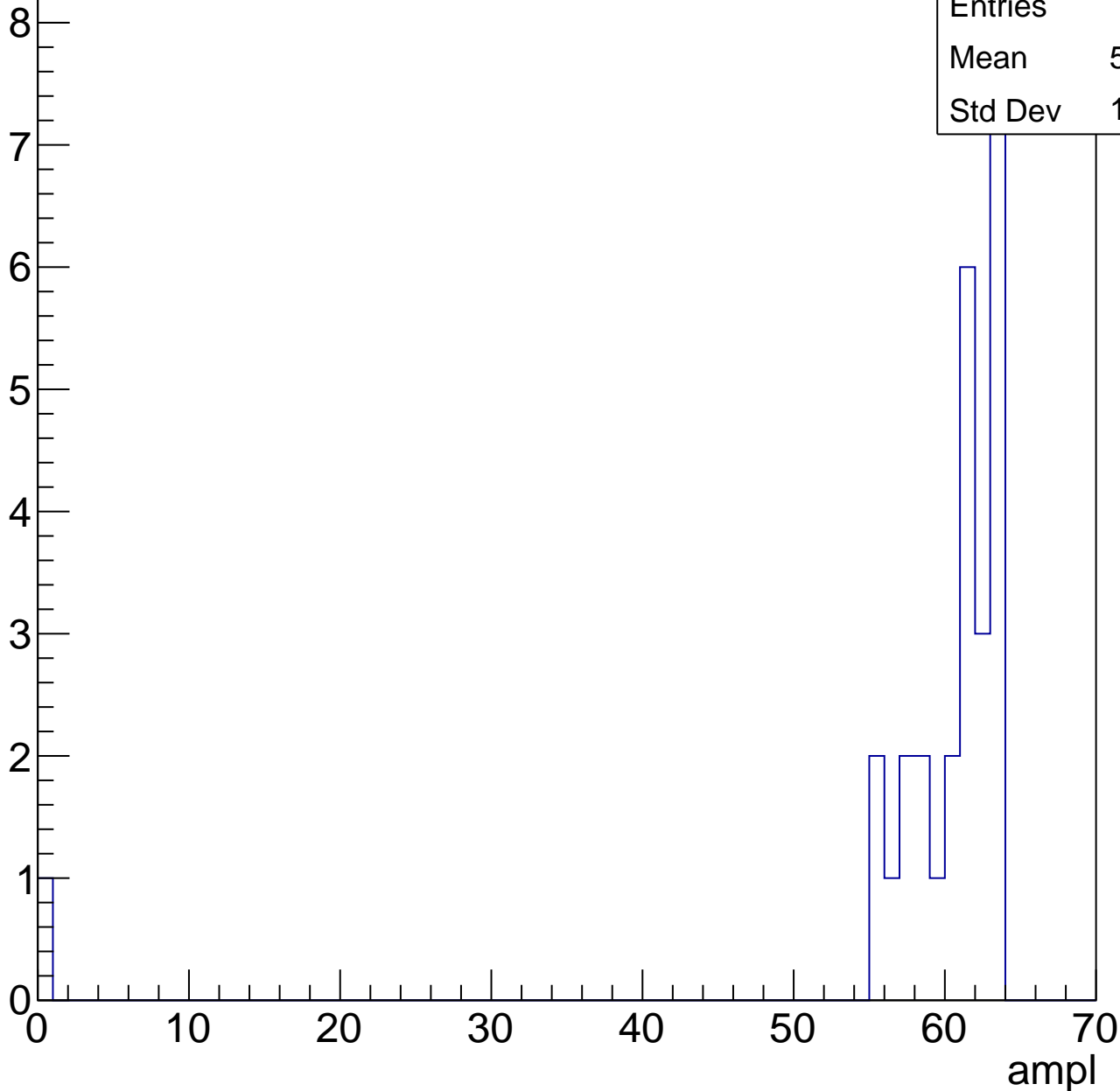


B1L102S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

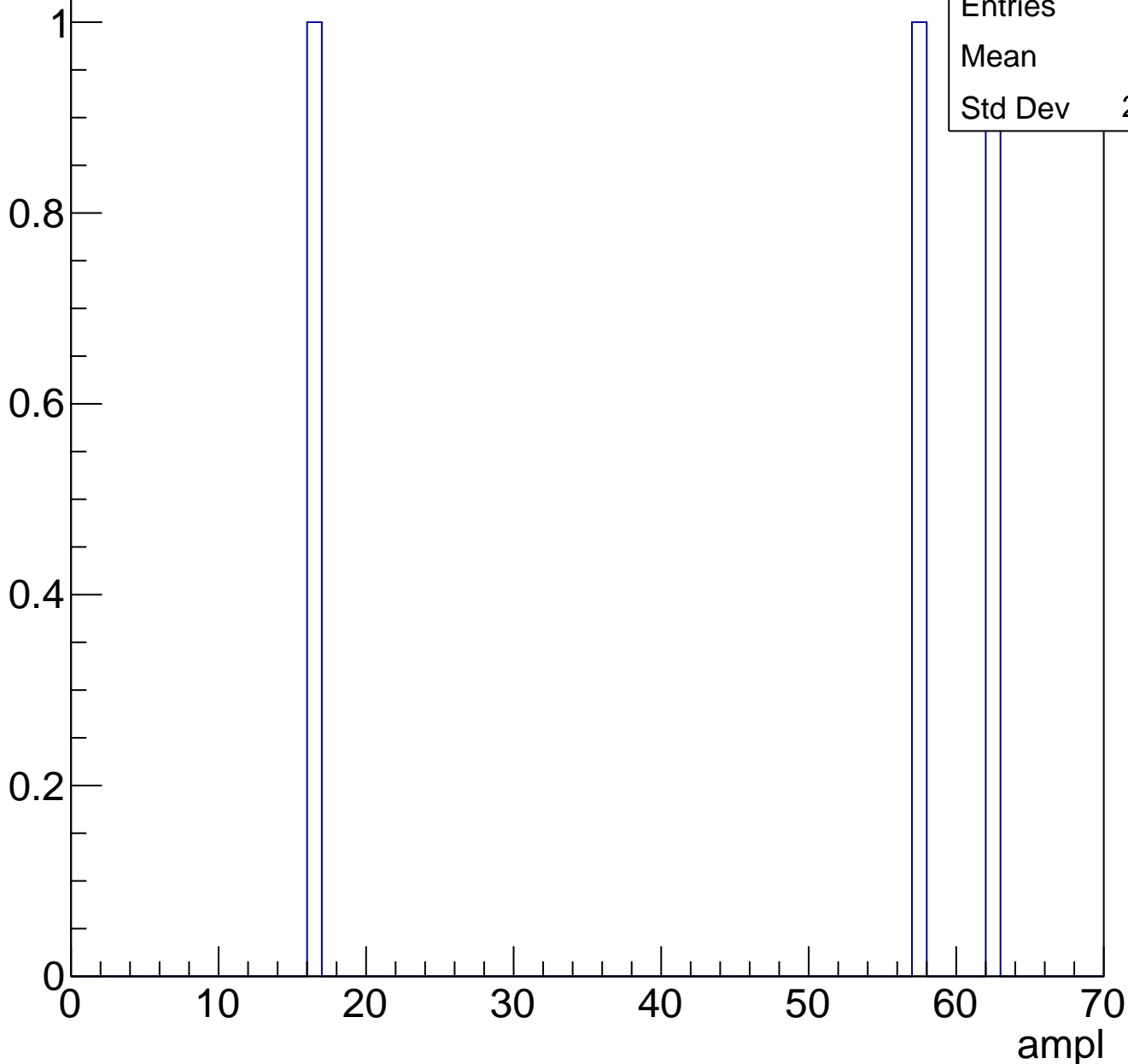
| | |
|---------|-------|
| Entries | 28 |
| Mean | 58.25 |
| Std Dev | 11.49 |



B1L102S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|-------|
| Entries | 3 |
| Mean | 45 |
| Std Dev | 20.61 |

B1L102S, U15-ch113, adc0

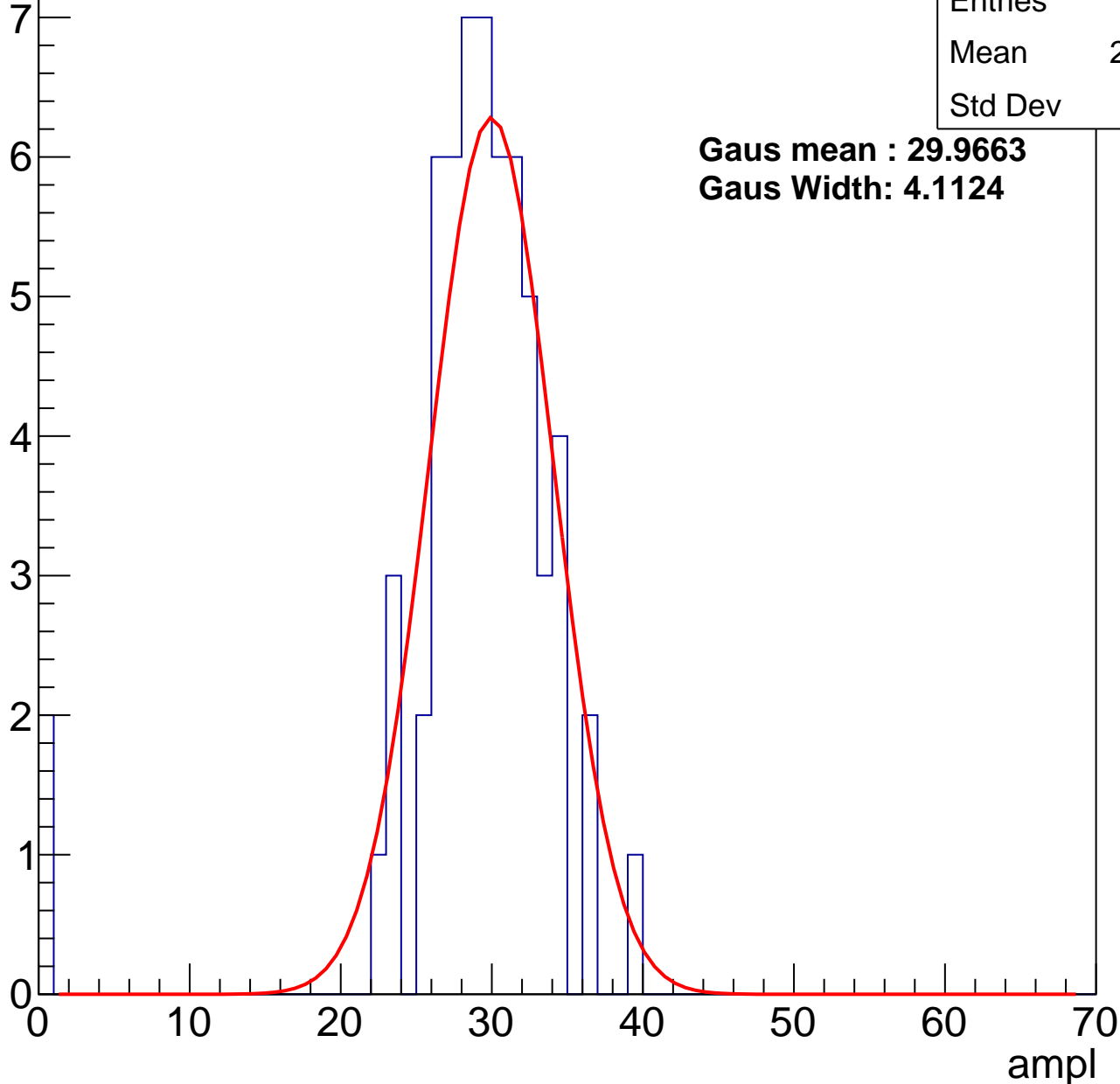
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 28.36 |
| Std Dev | 6.21 |

Gaus mean : 29.9663

Gaus Width: 4.1124



B1L102S, U15-ch113, adc1

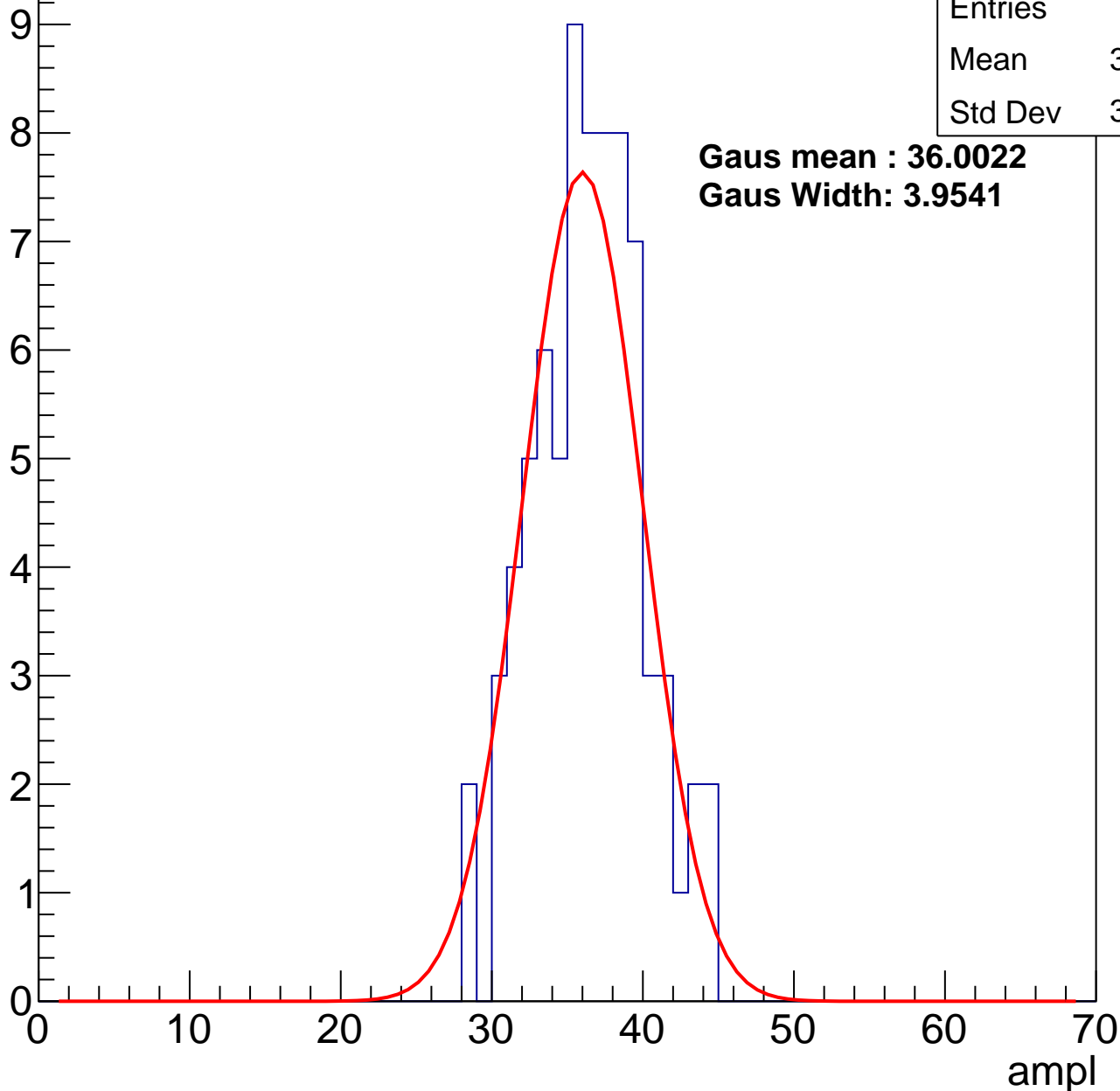
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 35.96 |
| Std Dev | 3.604 |

Gaus mean : 36.0022

Gaus Width: 3.9541



B1L102S, U15-ch113, adc2

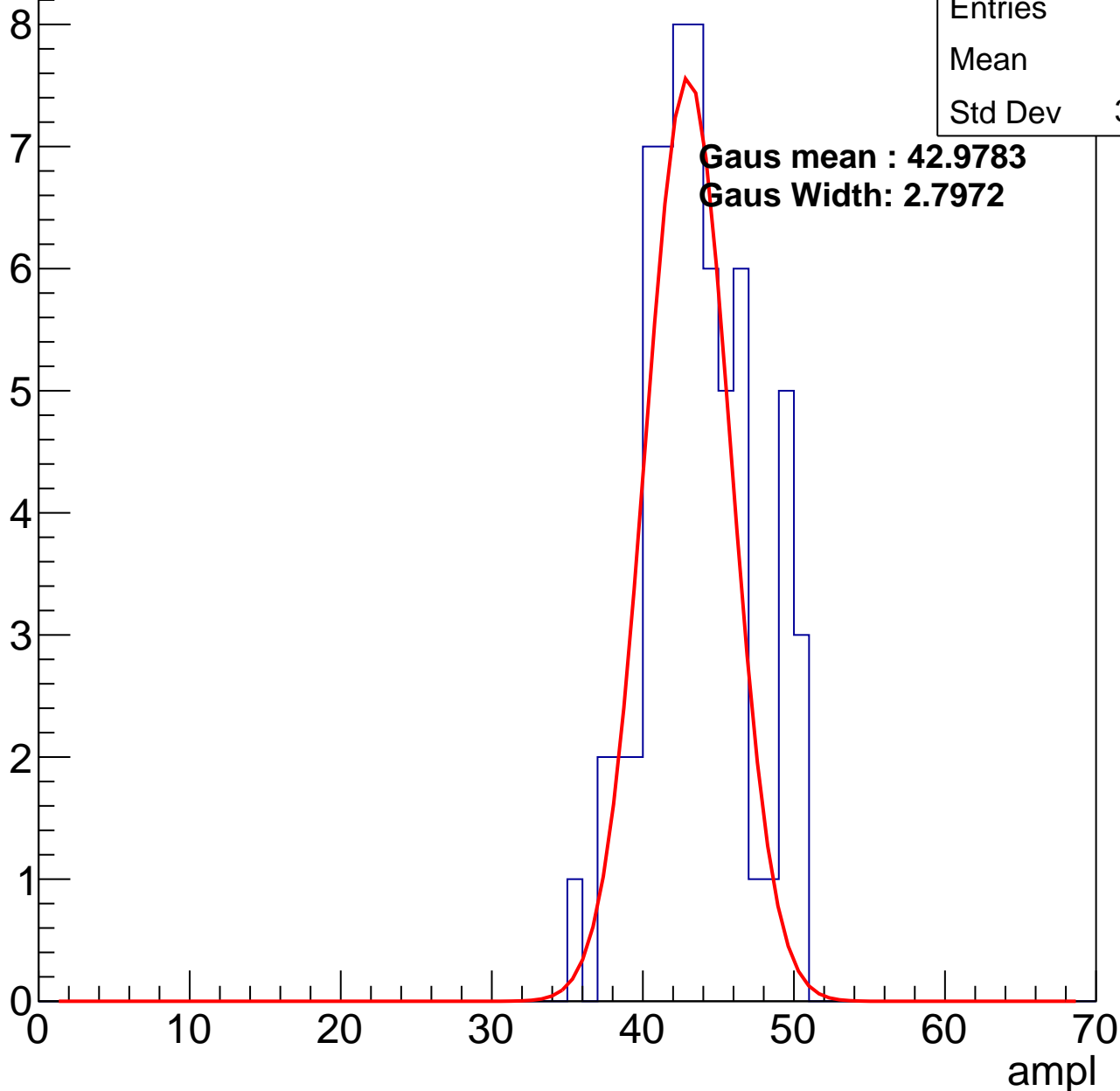
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 43.2 |
| Std Dev | 3.451 |

Gaus mean : 42.9783

Gaus Width: 2.7972

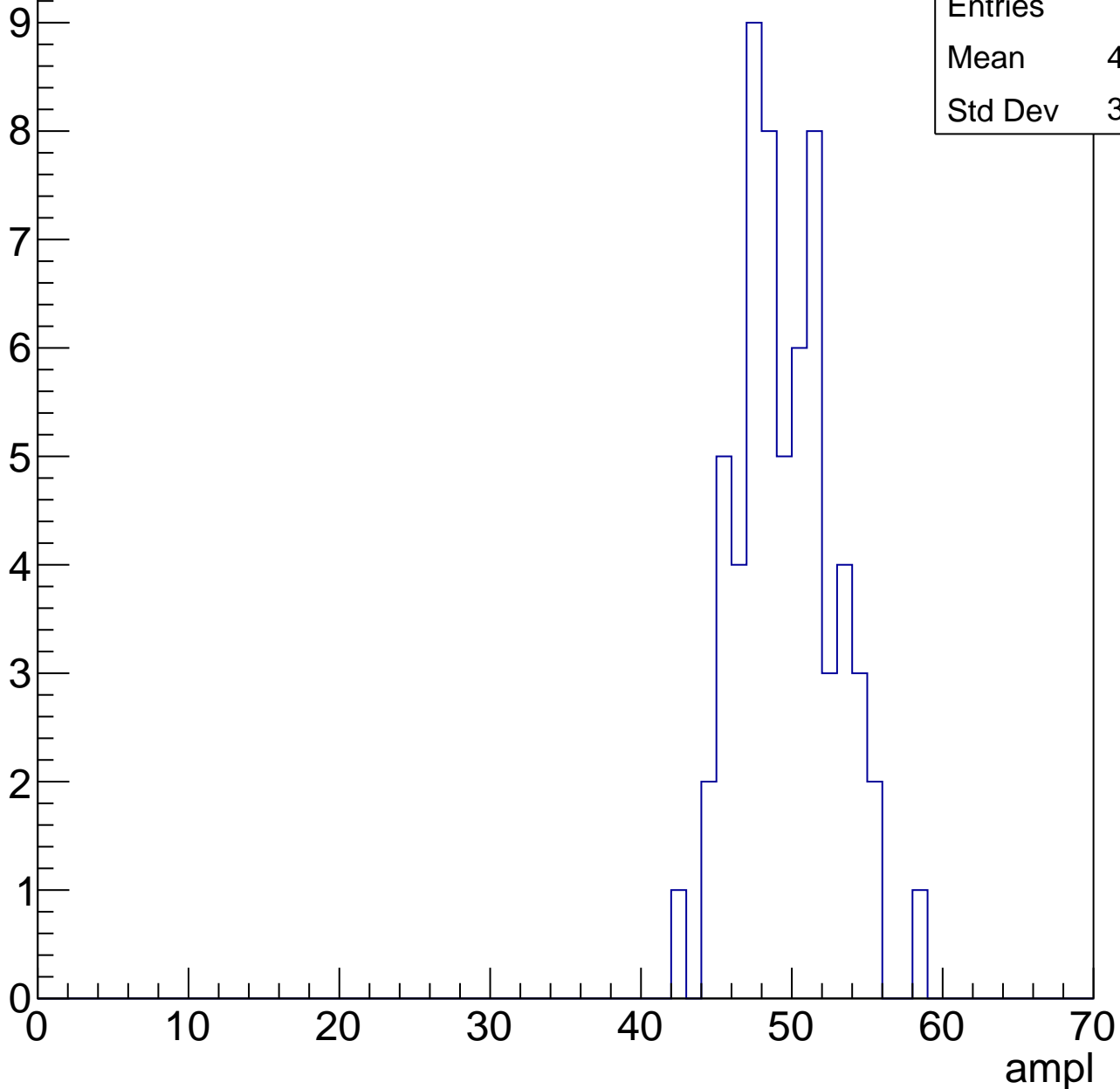


B1L102S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 49.13 |
| Std Dev | 3.175 |

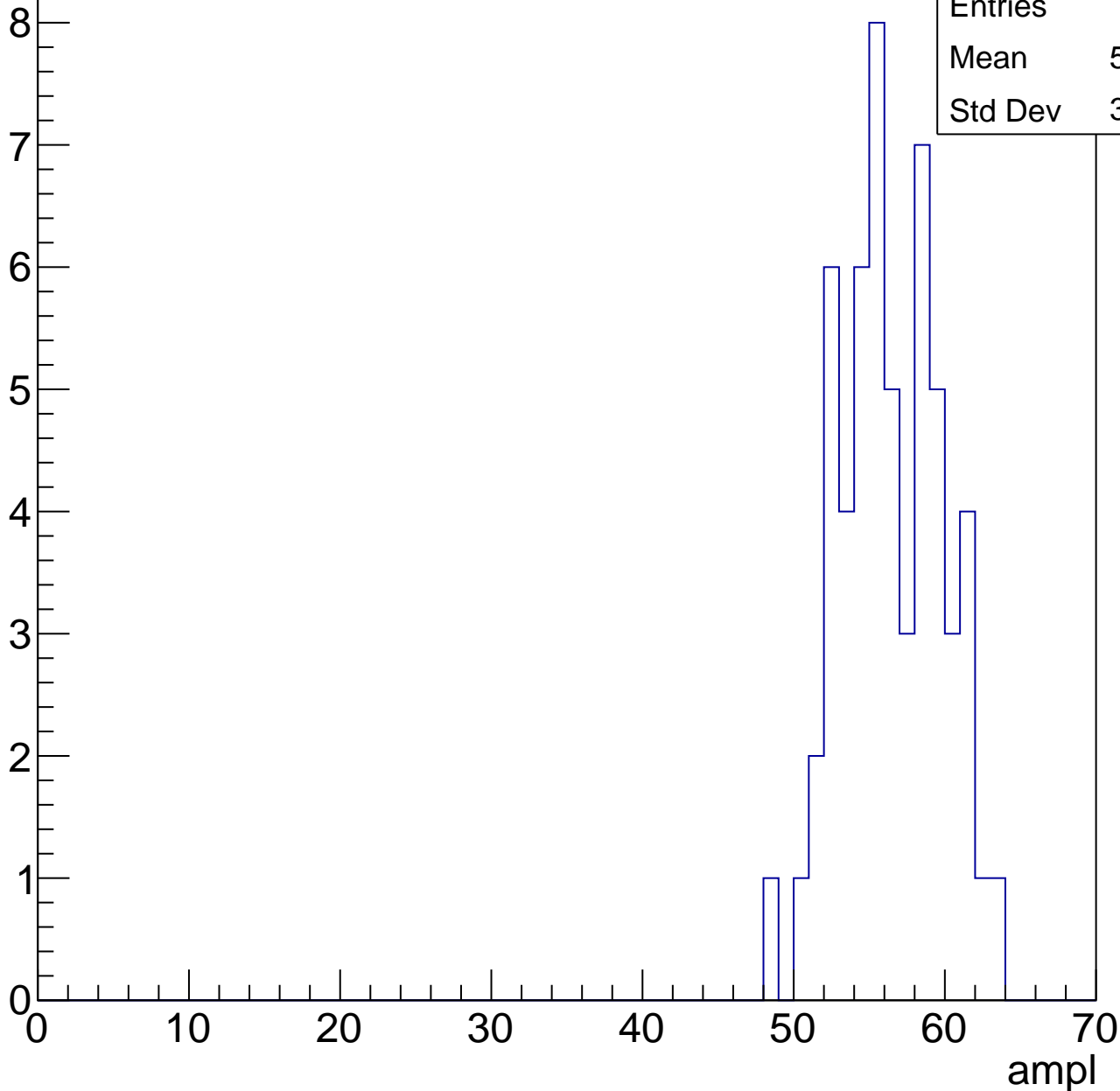


B1L102S, U15-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

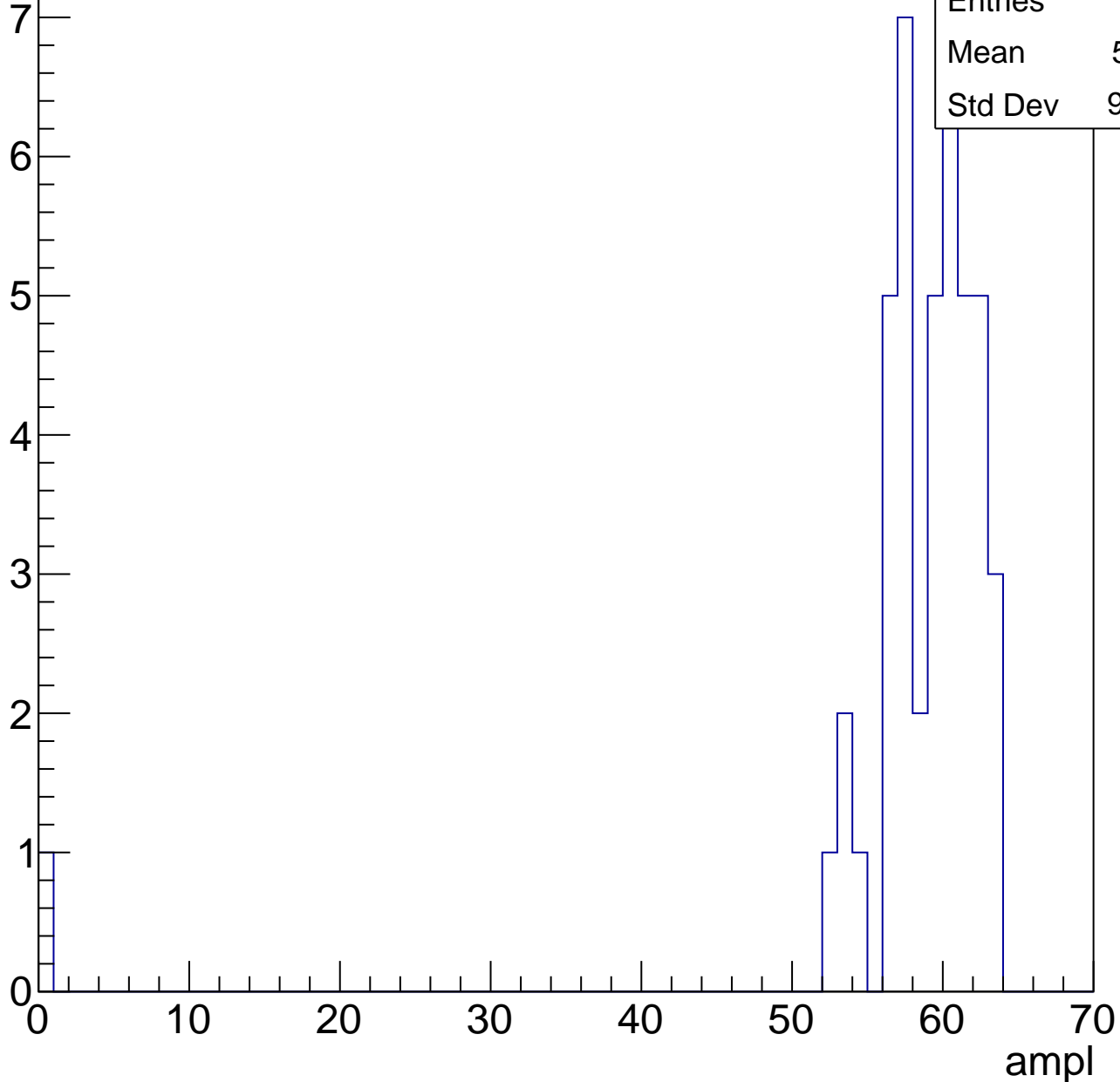
| | |
|---------|-------|
| Entries | 57 |
| Mean | 55.95 |
| Std Dev | 3.306 |



B1L102S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

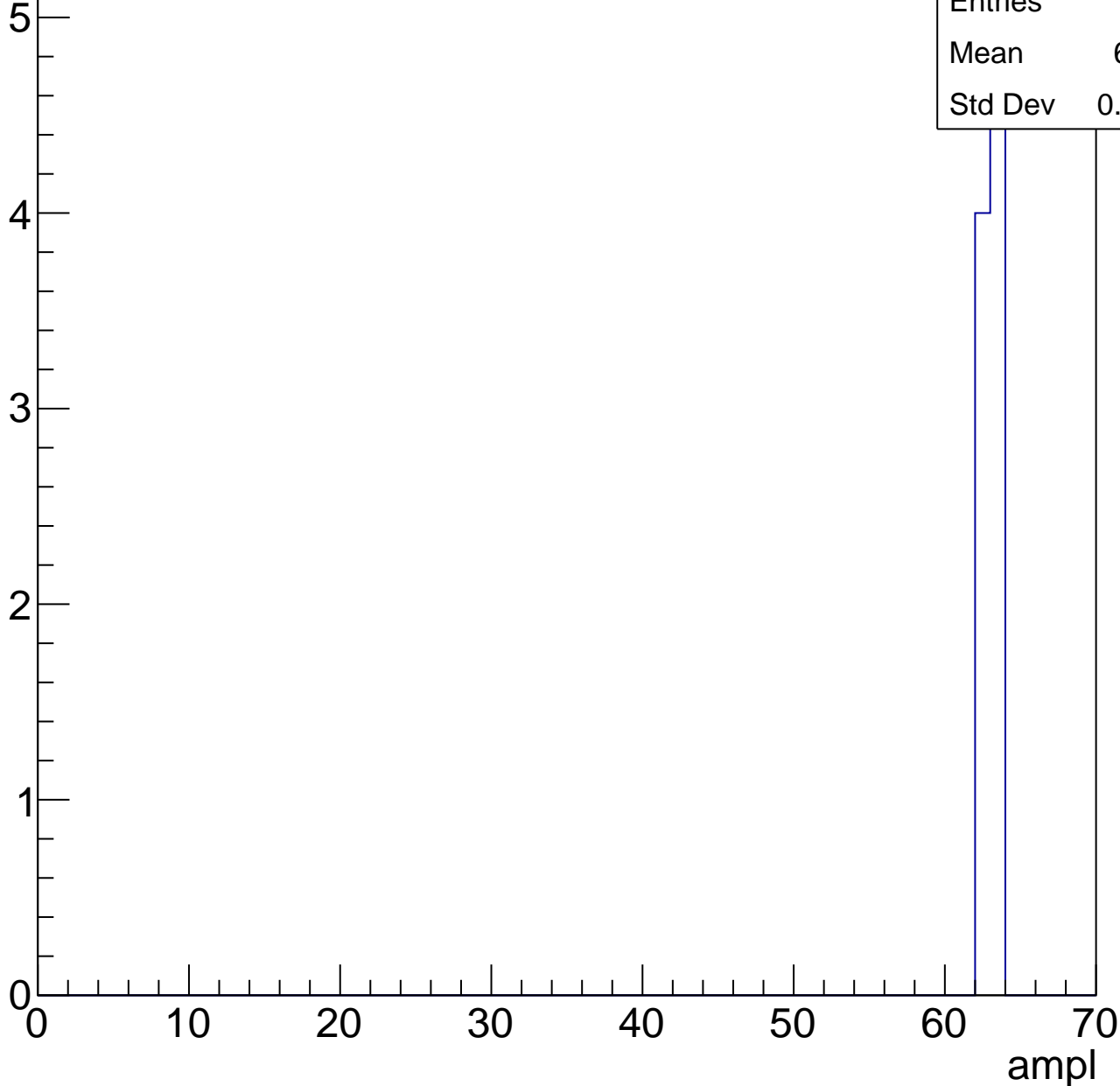


B1L102S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

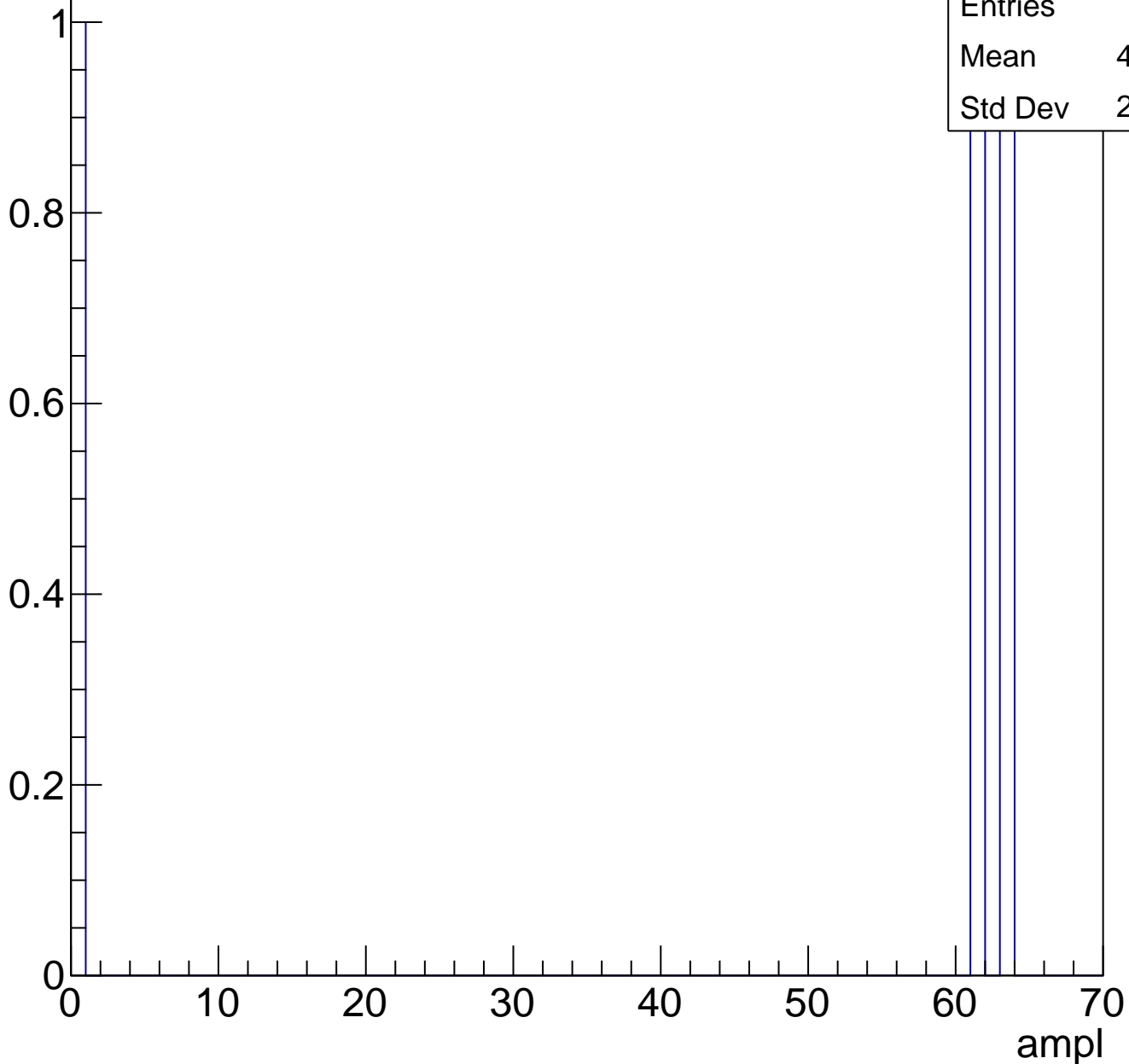
| | |
|---------|--------|
| Entries | 9 |
| Mean | 62.56 |
| Std Dev | 0.4969 |



B1L102S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch114, adc0

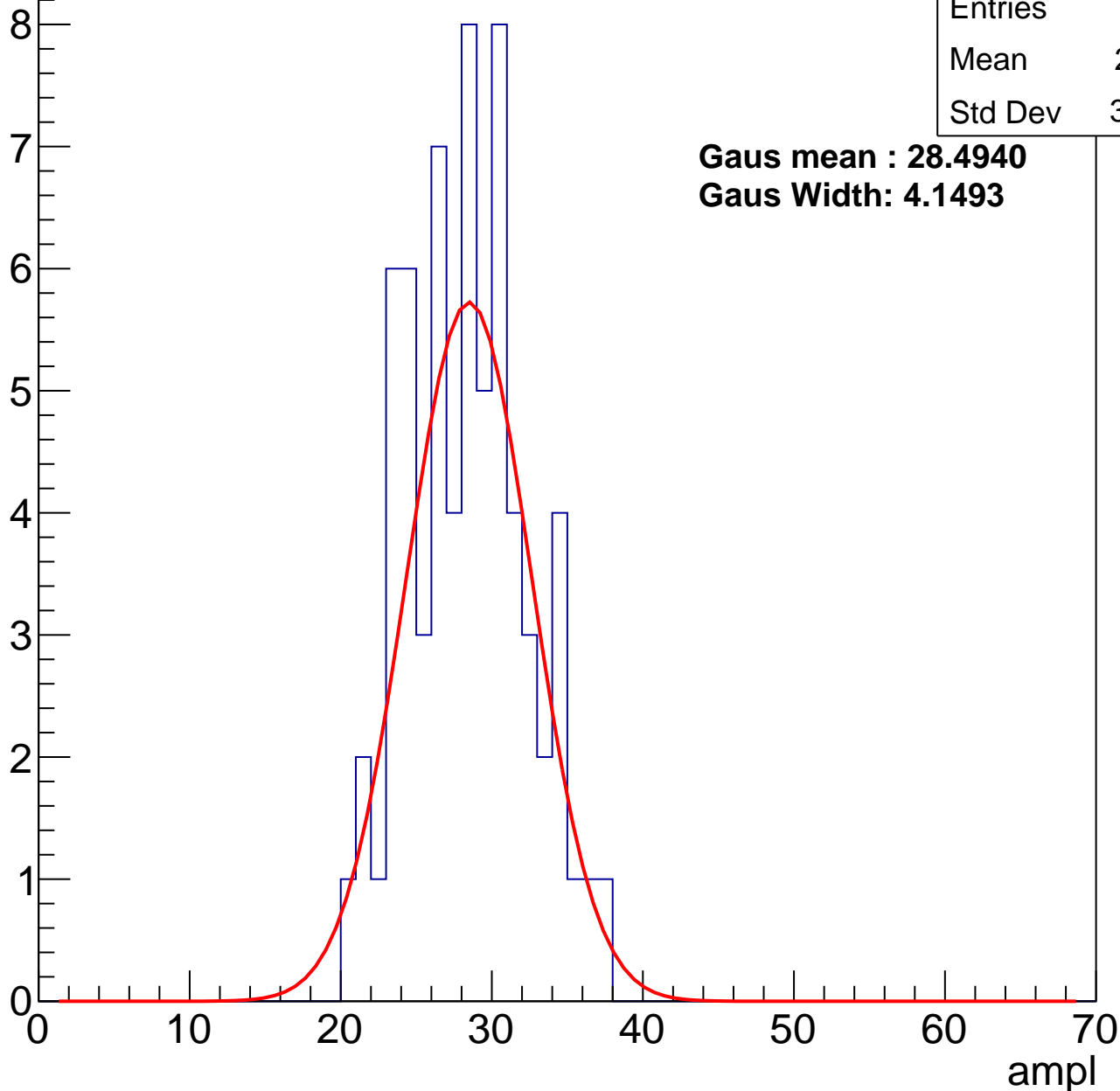
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 27.91 |
| Std Dev | 3.885 |

Gaus mean : 28.4940

Gaus Width: 4.1493



B1L102S, U15-ch114, adc1

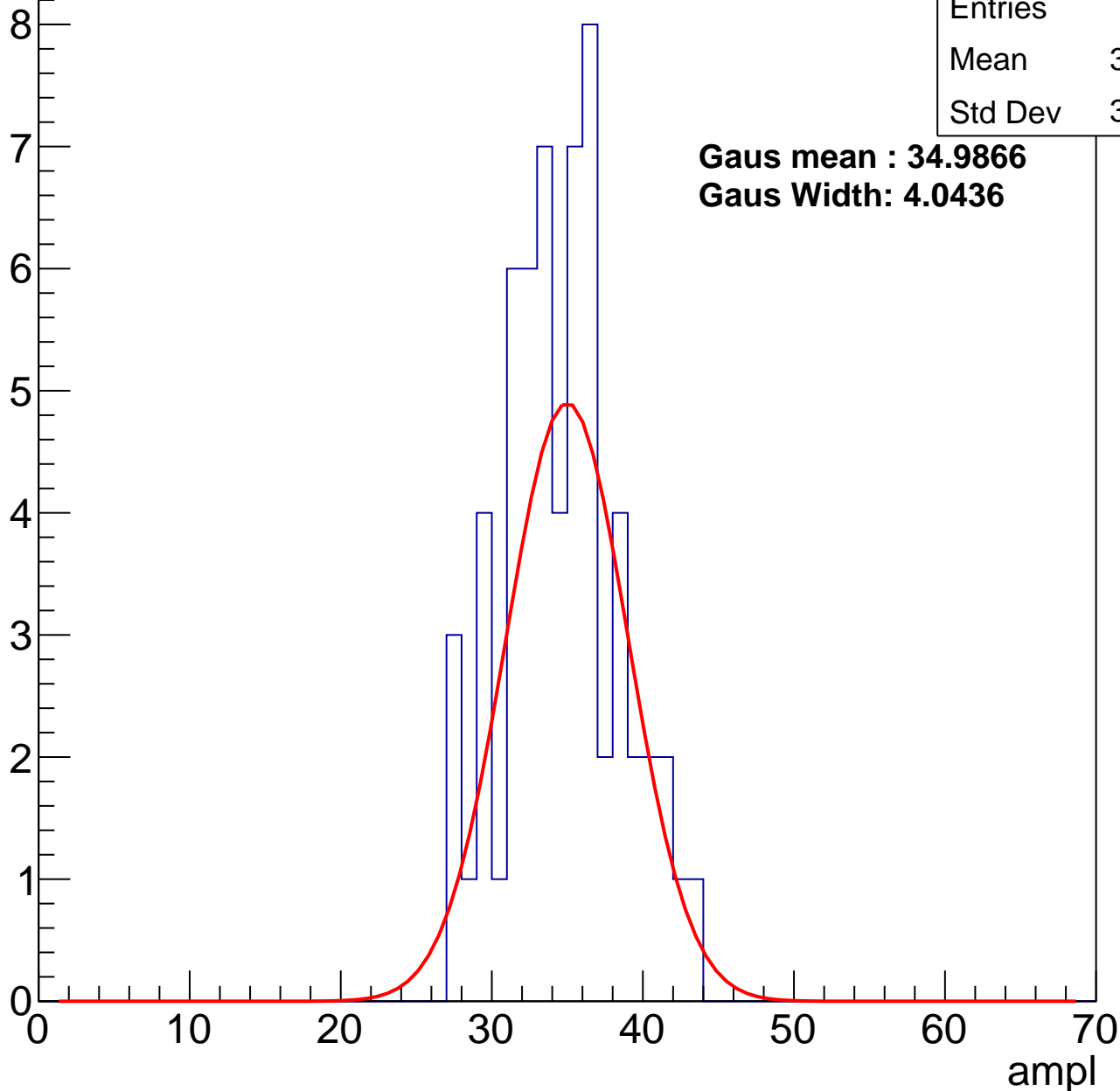
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 34.16 |
| Std Dev | 3.773 |

Gaus mean : 34.9866

Gaus Width: 4.0436



B1L102S, U15-ch114, adc2

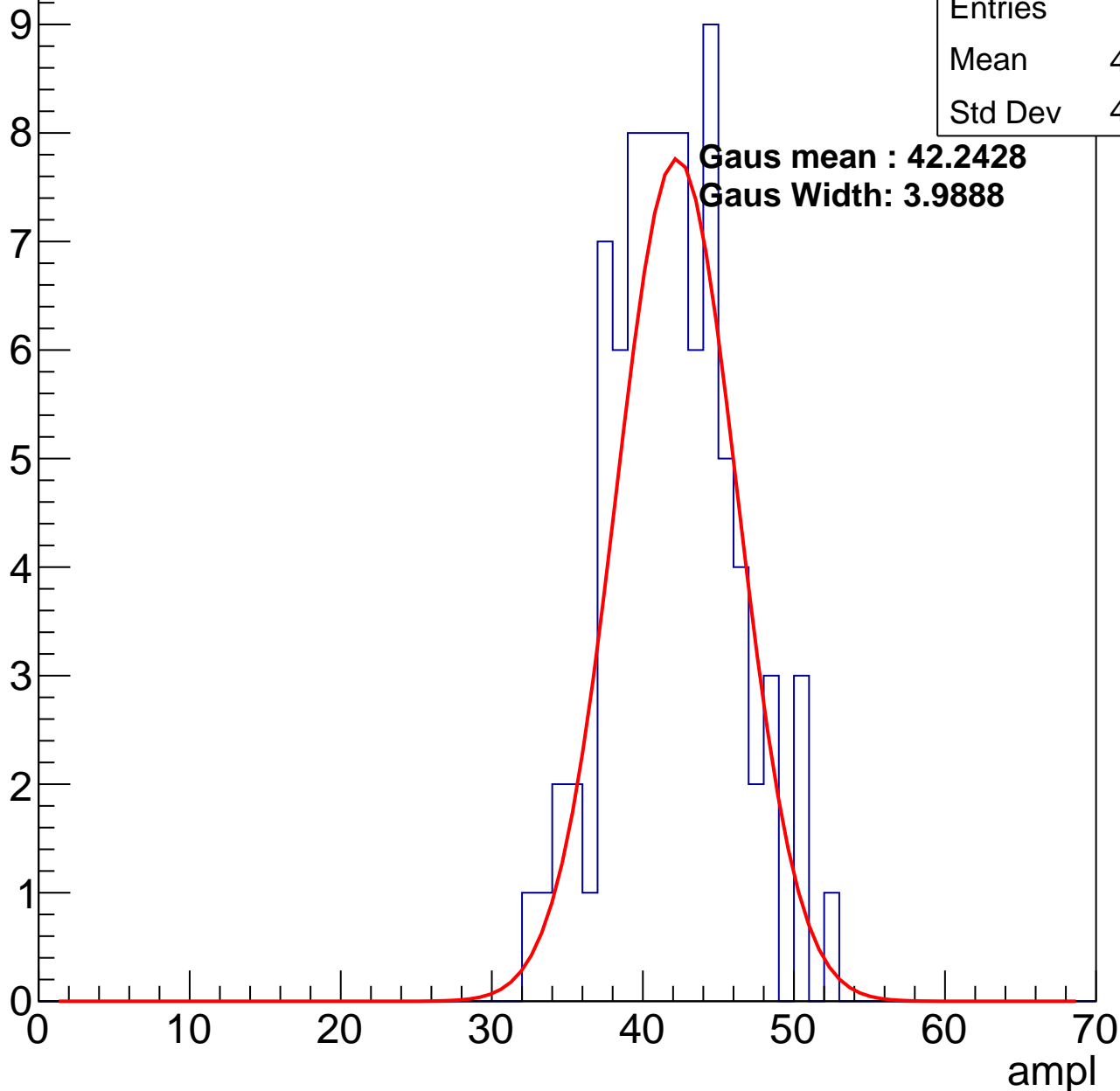
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 85 |
| Mean | 41.47 |
| Std Dev | 4.063 |

Gaus mean : 42.2428

Gaus Width: 3.9888

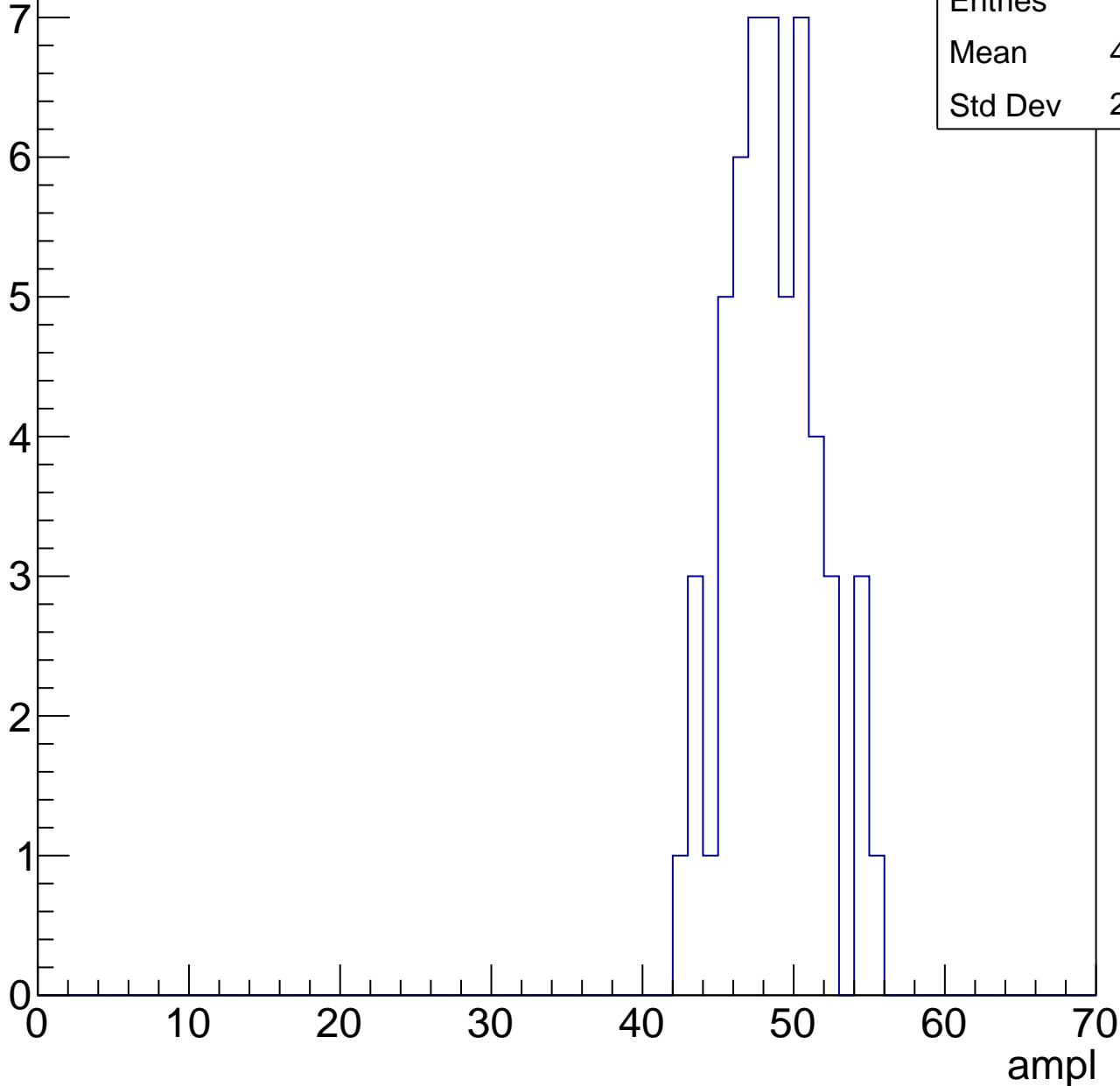


B1L102S, U15-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 48.17 |
| Std Dev | 2.995 |



B1L102S, U15-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 65 |
| Mean | 54.66 |
| Std Dev | 3.448 |

Entry

10

8

6

4

2

0

0

10

20

30

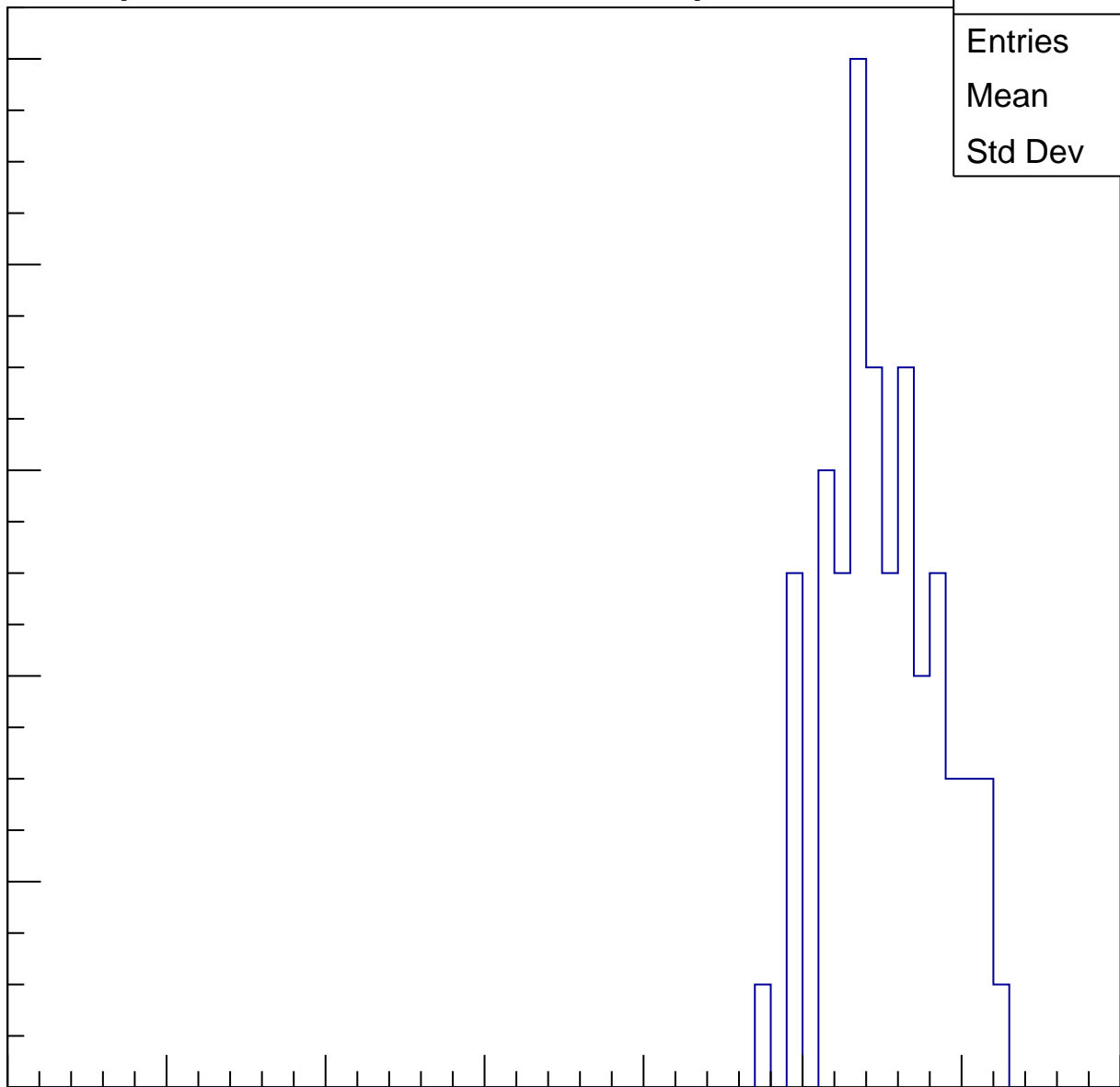
40

50

60

70

ampl

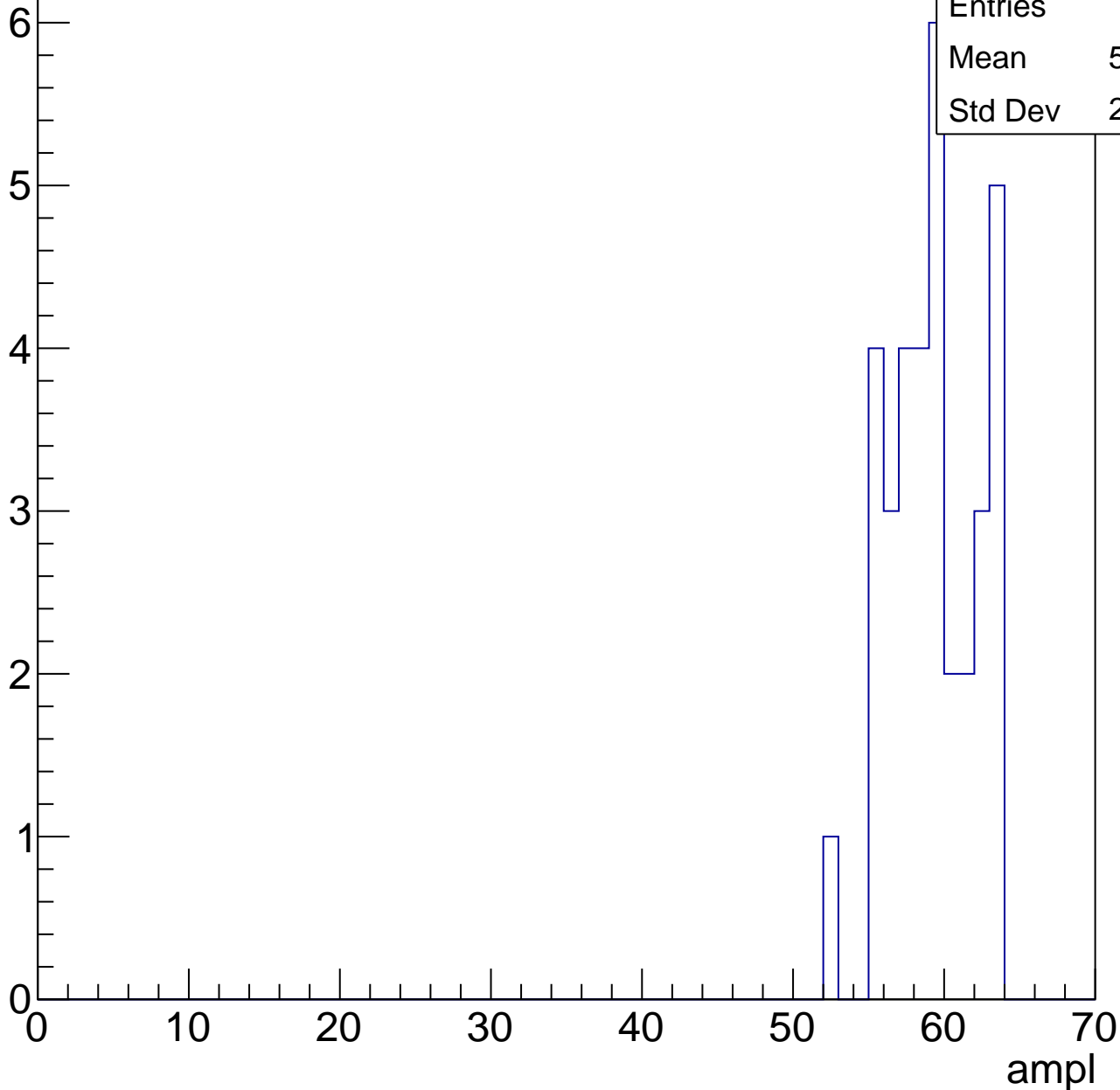


B1L102S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 34 |
| Mean | 58.74 |
| Std Dev | 2.842 |

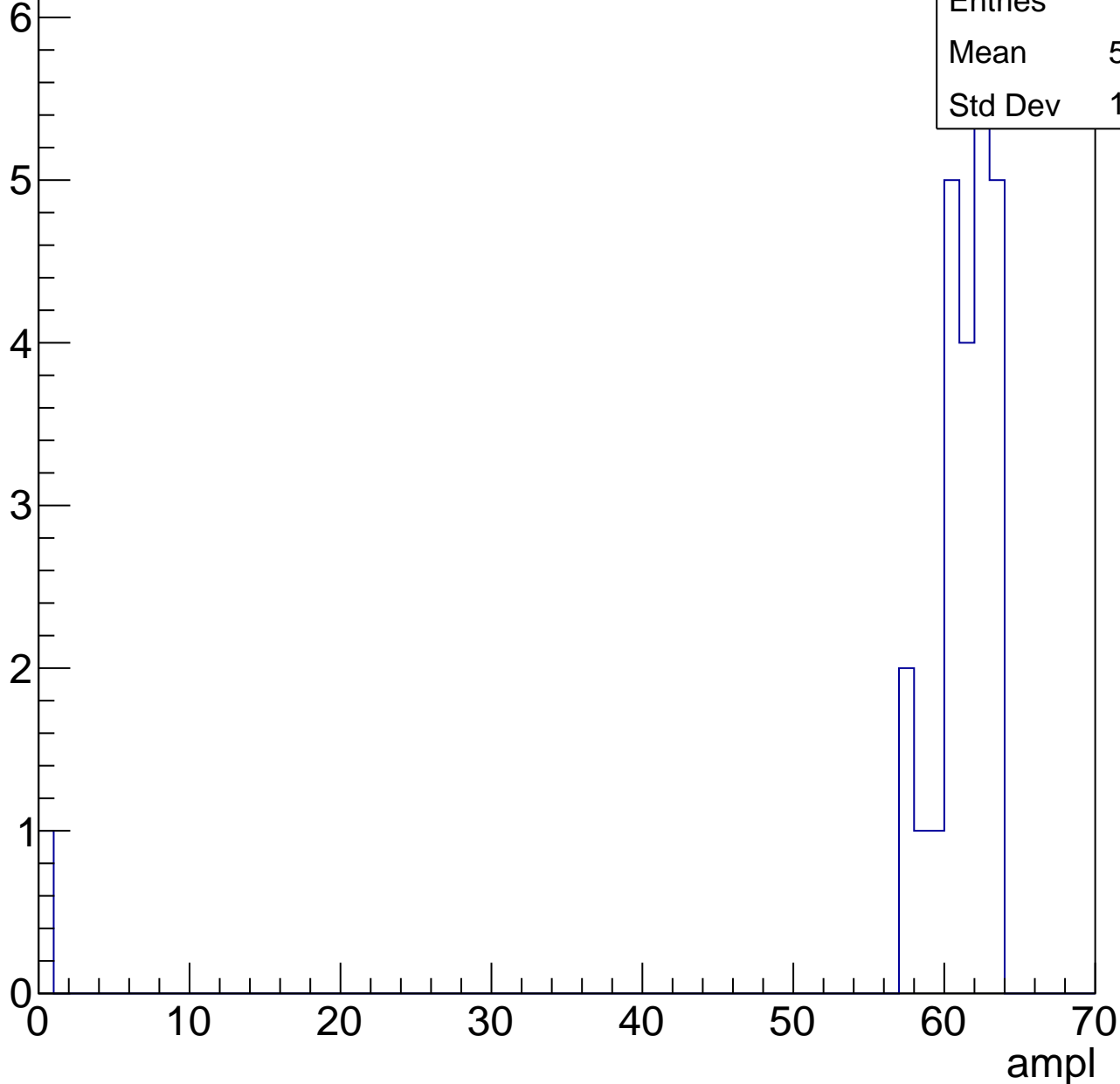


B1L102S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

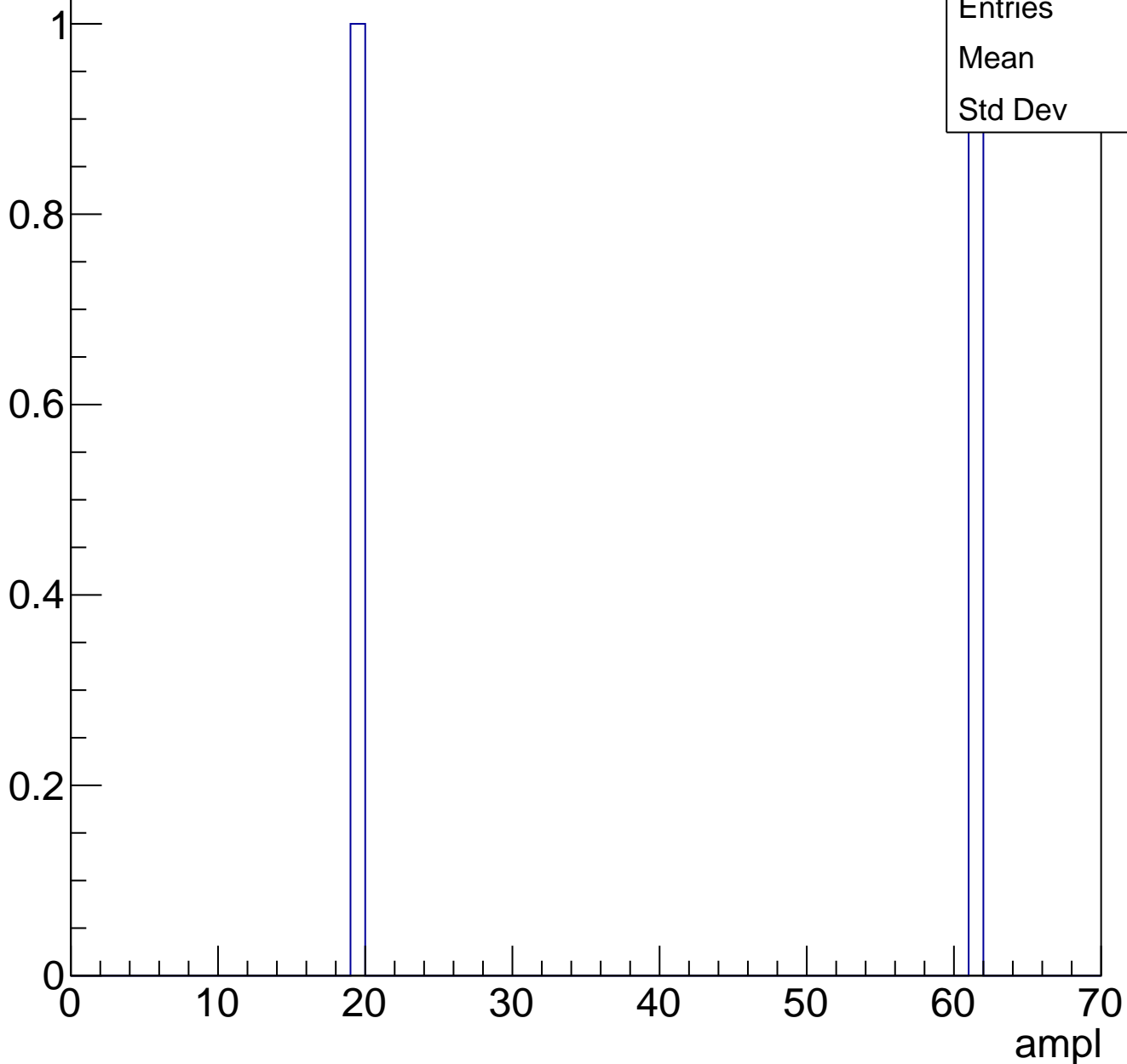
| | |
|---------|-------|
| Entries | 25 |
| Mean | 58.48 |
| Std Dev | 12.06 |



B1L102S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch115, adc0

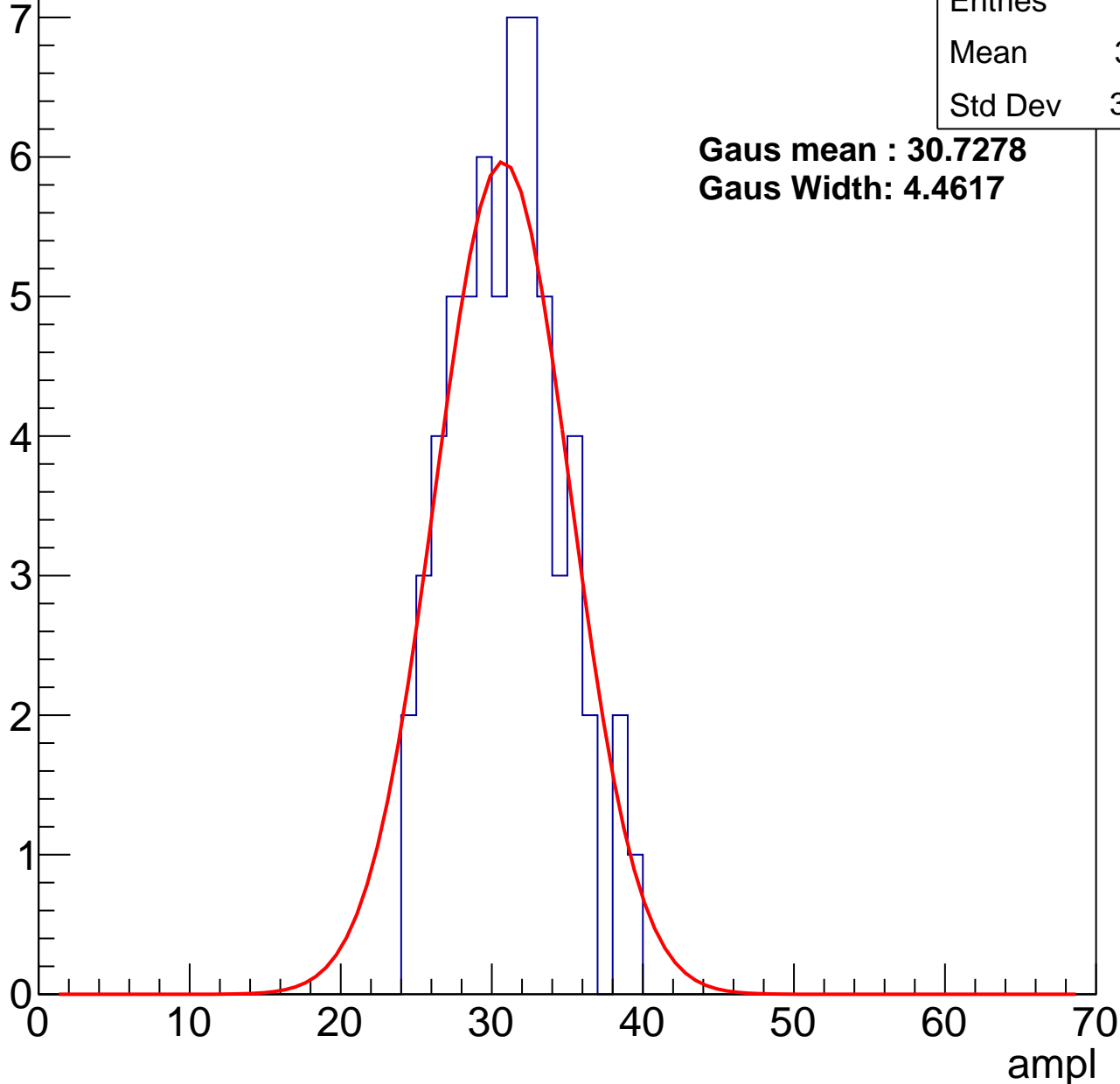
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 30.51 |
| Std Dev | 3.565 |

Gaus mean : 30.7278

Gaus Width: 4.4617



B1L102S, U15-ch115, adc1

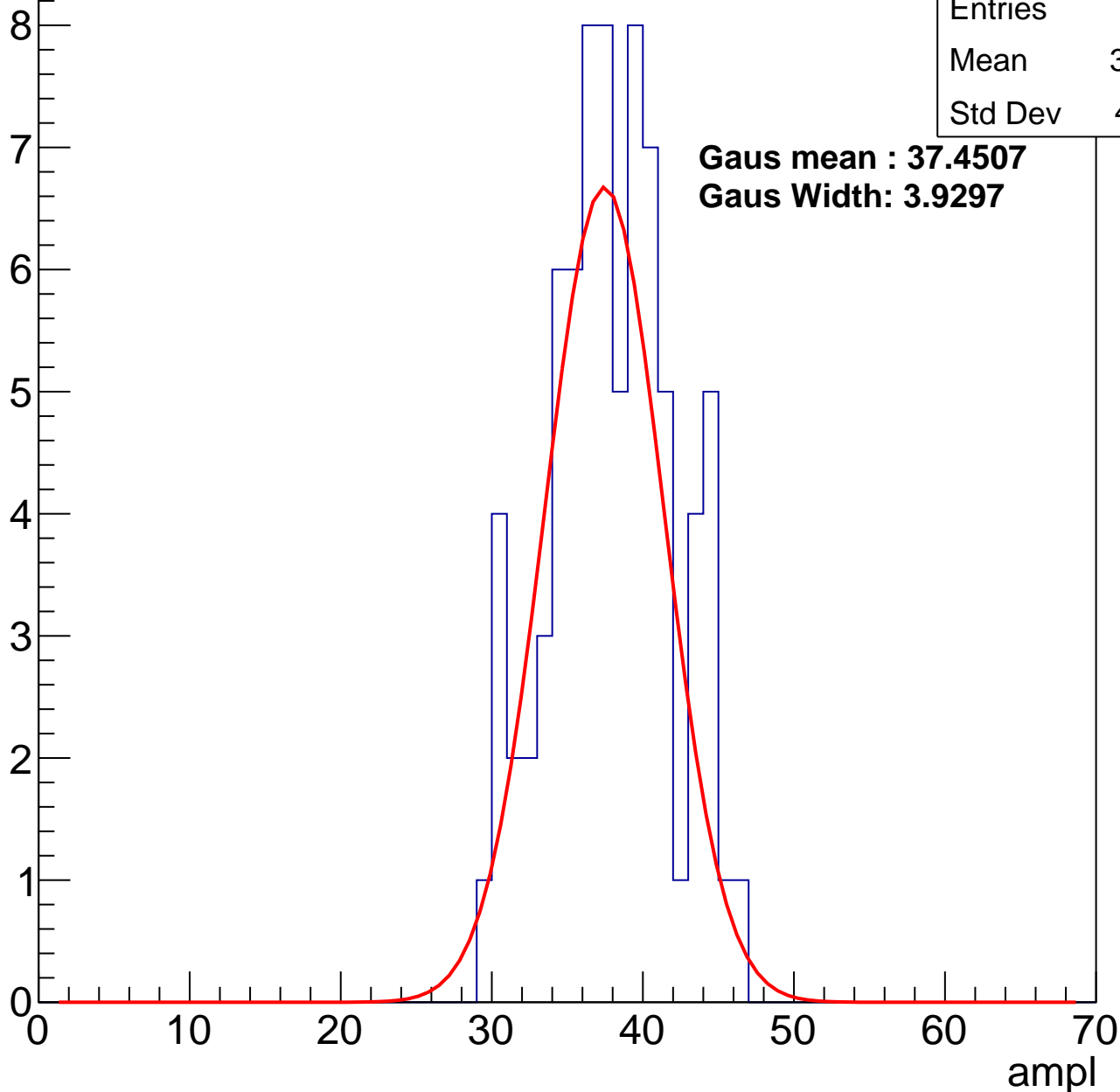
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 77 |
| Mean | 37.45 |
| Std Dev | 4.011 |

Gaus mean : 37.4507

Gaus Width: 3.9297



B1L102S, U15-ch115, adc2

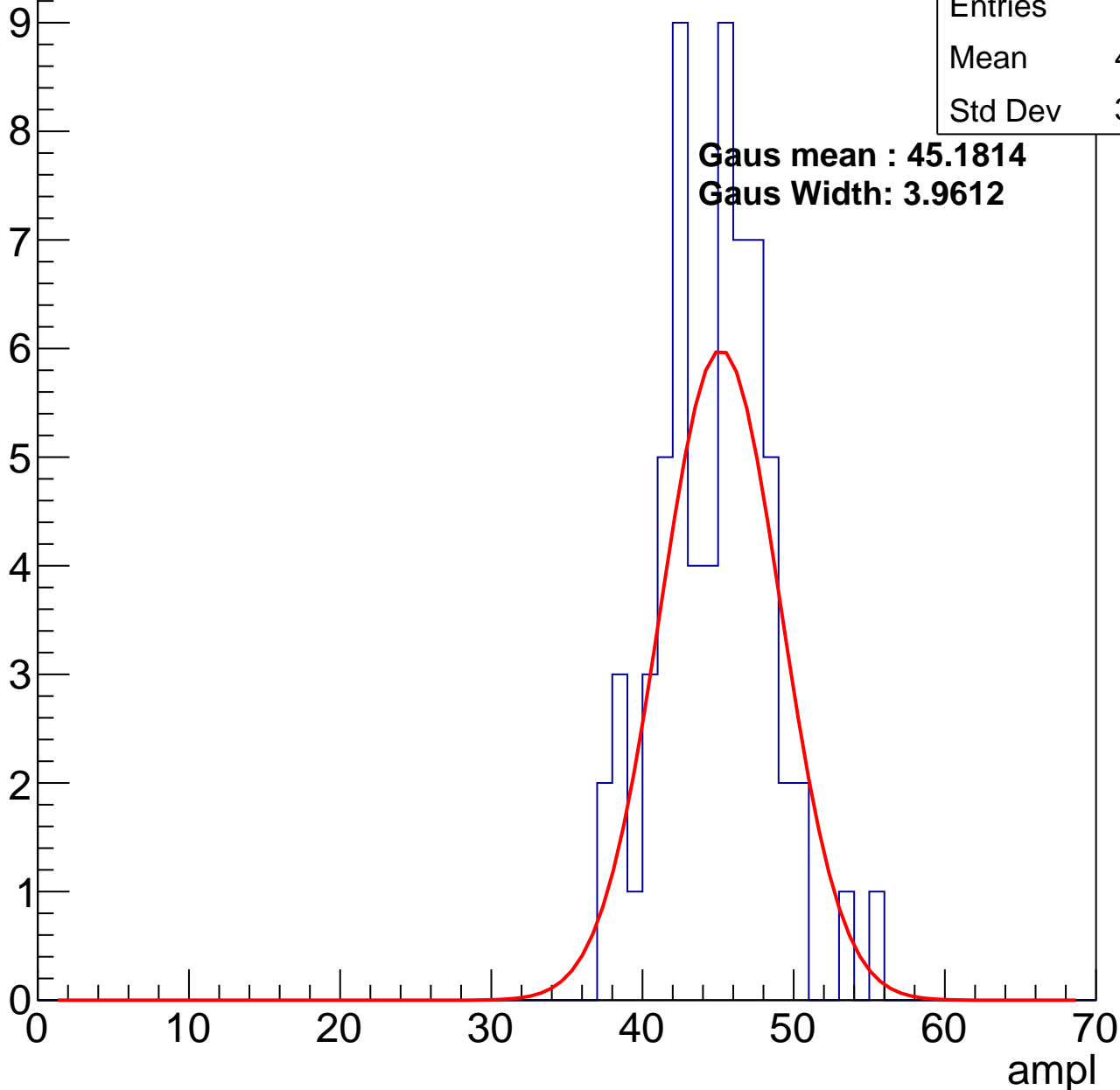
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 44.31 |
| Std Dev | 3.641 |

Gaus mean : 45.1814

Gaus Width: 3.9612

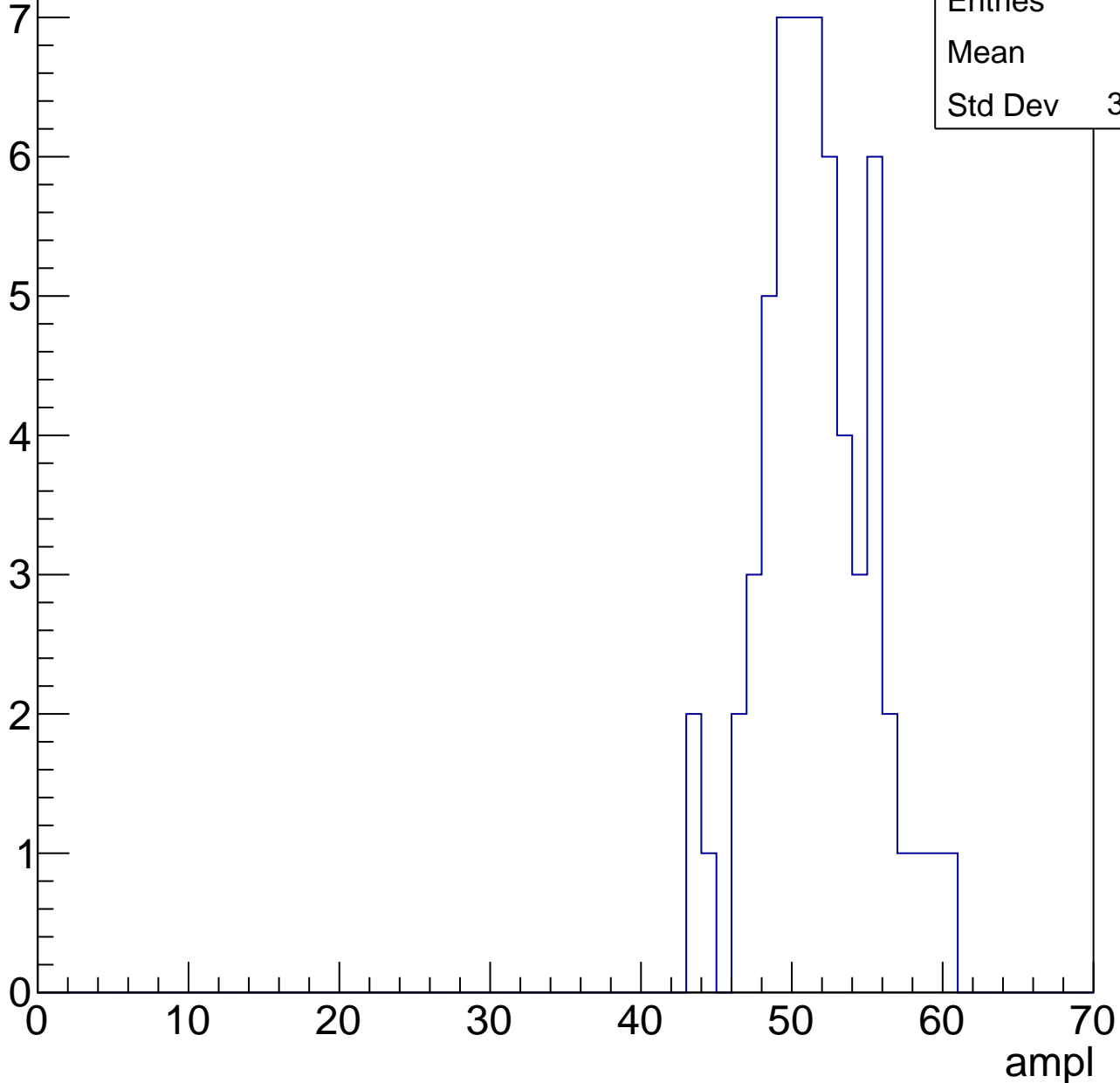


B1L102S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 51.1 |
| Std Dev | 3.635 |

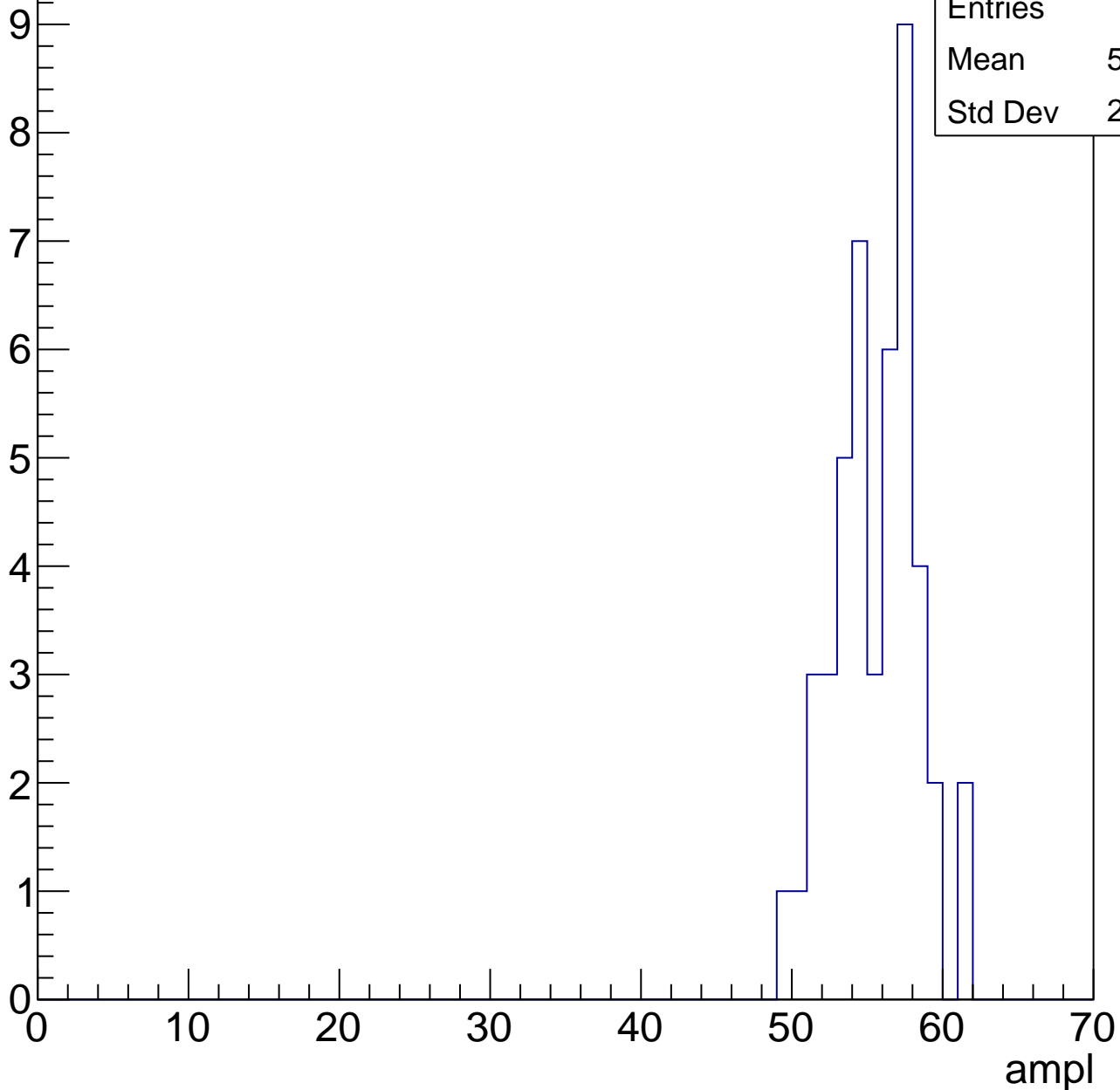


B1L102S, U15-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 46 |
| Mean | 55.15 |
| Std Dev | 2.734 |

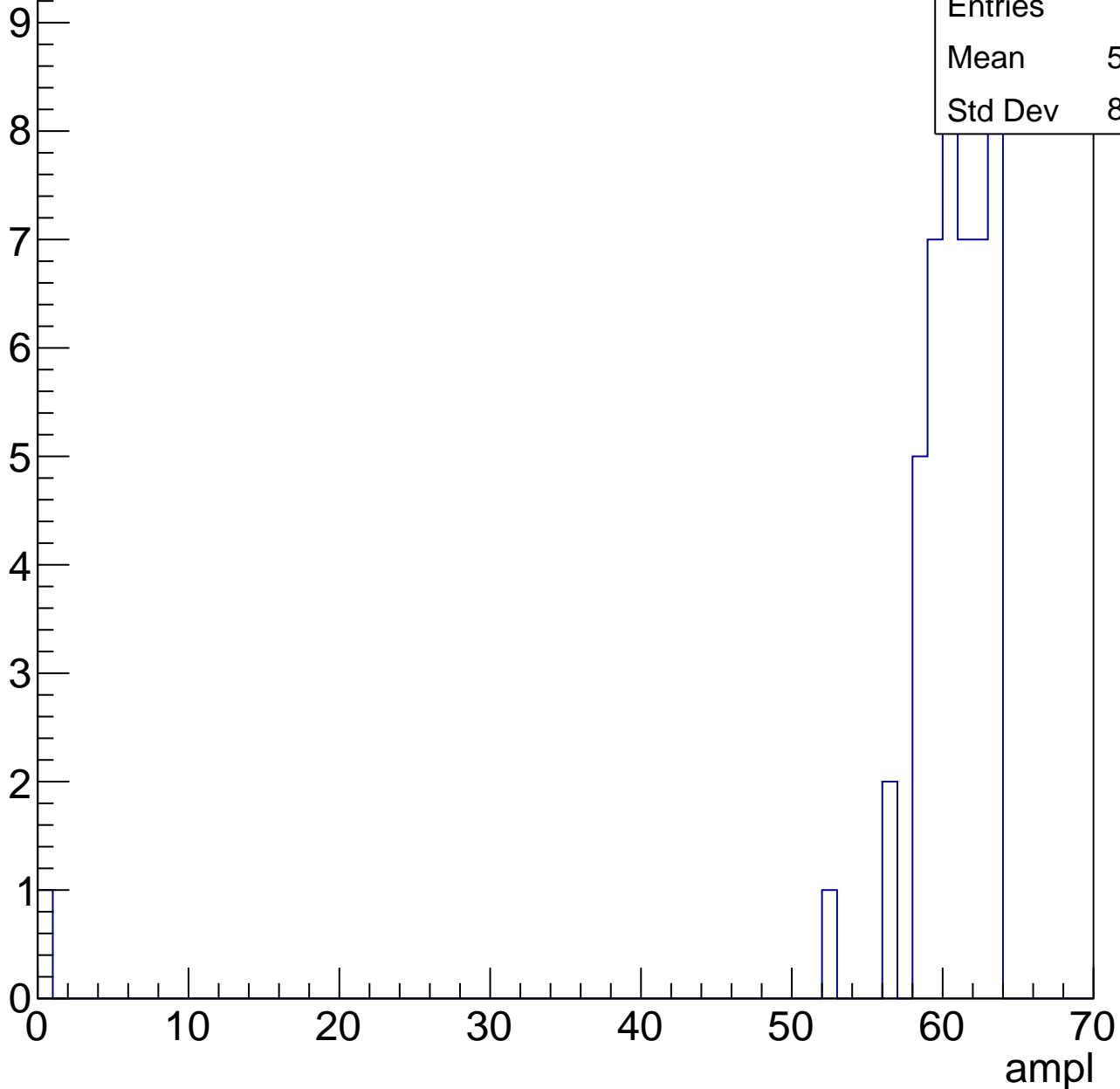


B1L102S, U15-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

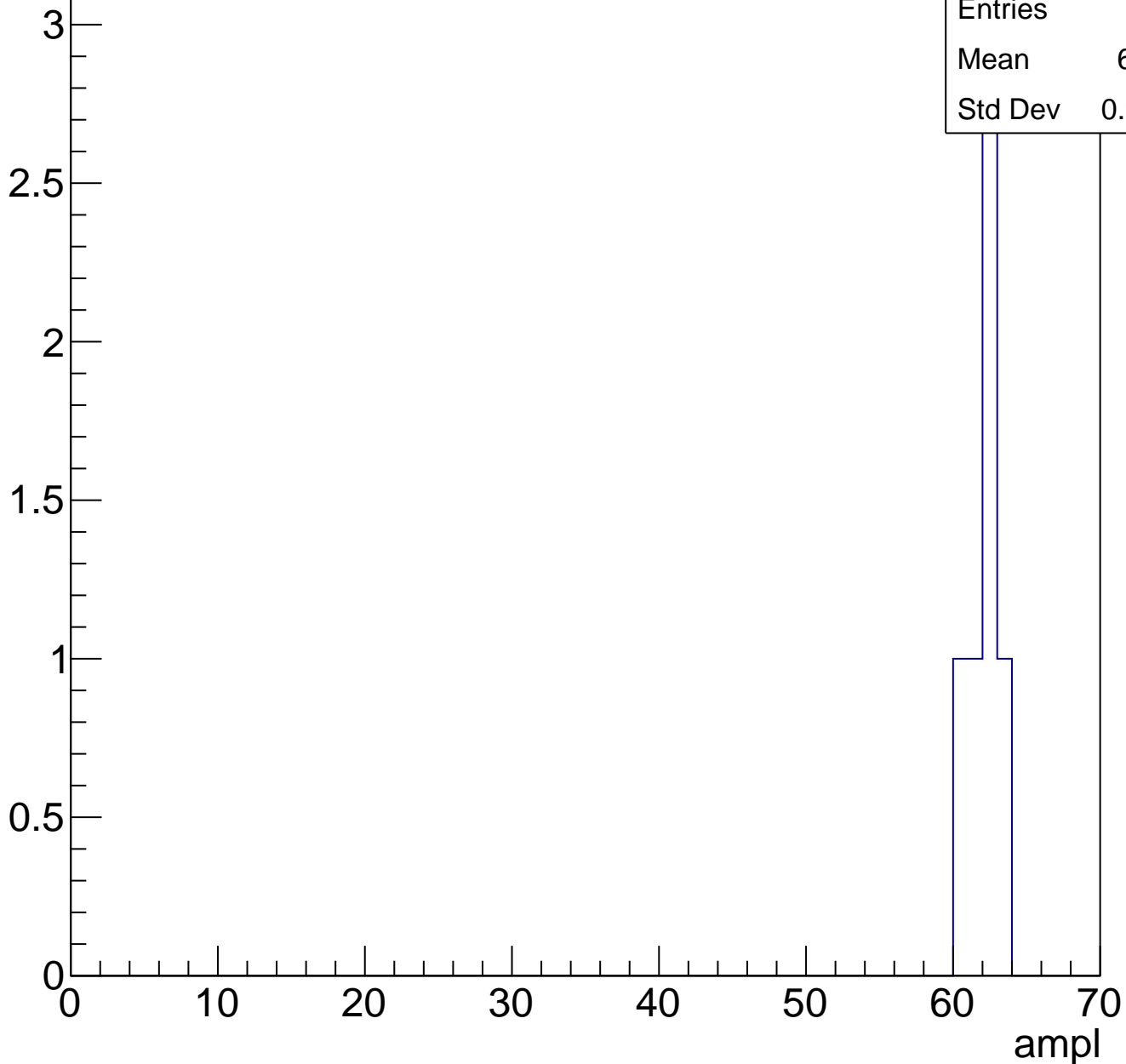
| | |
|---------|-------|
| Entries | 47 |
| Mean | 59.04 |
| Std Dev | 8.987 |



B1L102S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch116, adc0

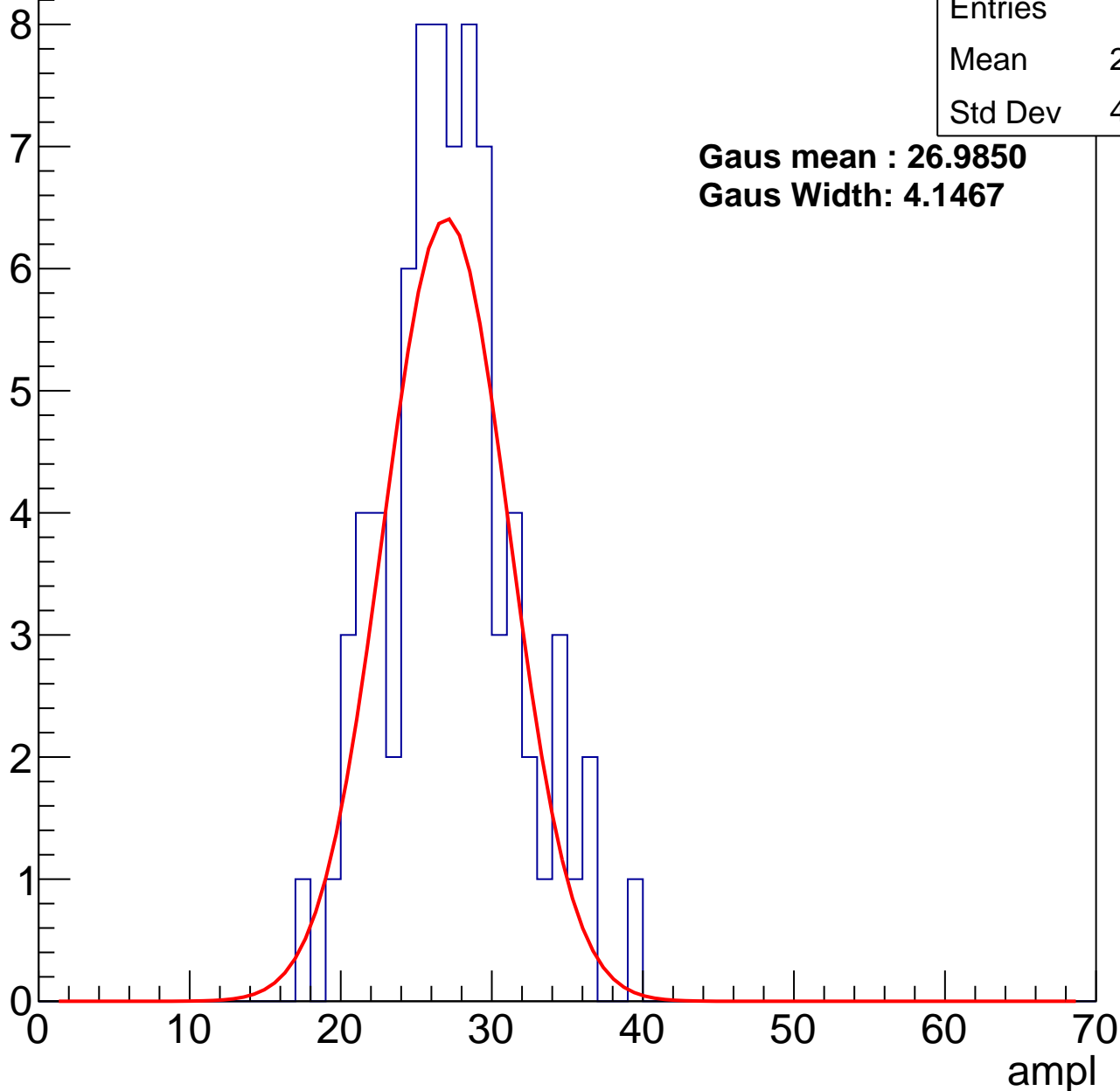
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 26.86 |
| Std Dev | 4.306 |

Gaus mean : 26.9850

Gaus Width: 4.1467



B1L102S, U15-ch116, adc1

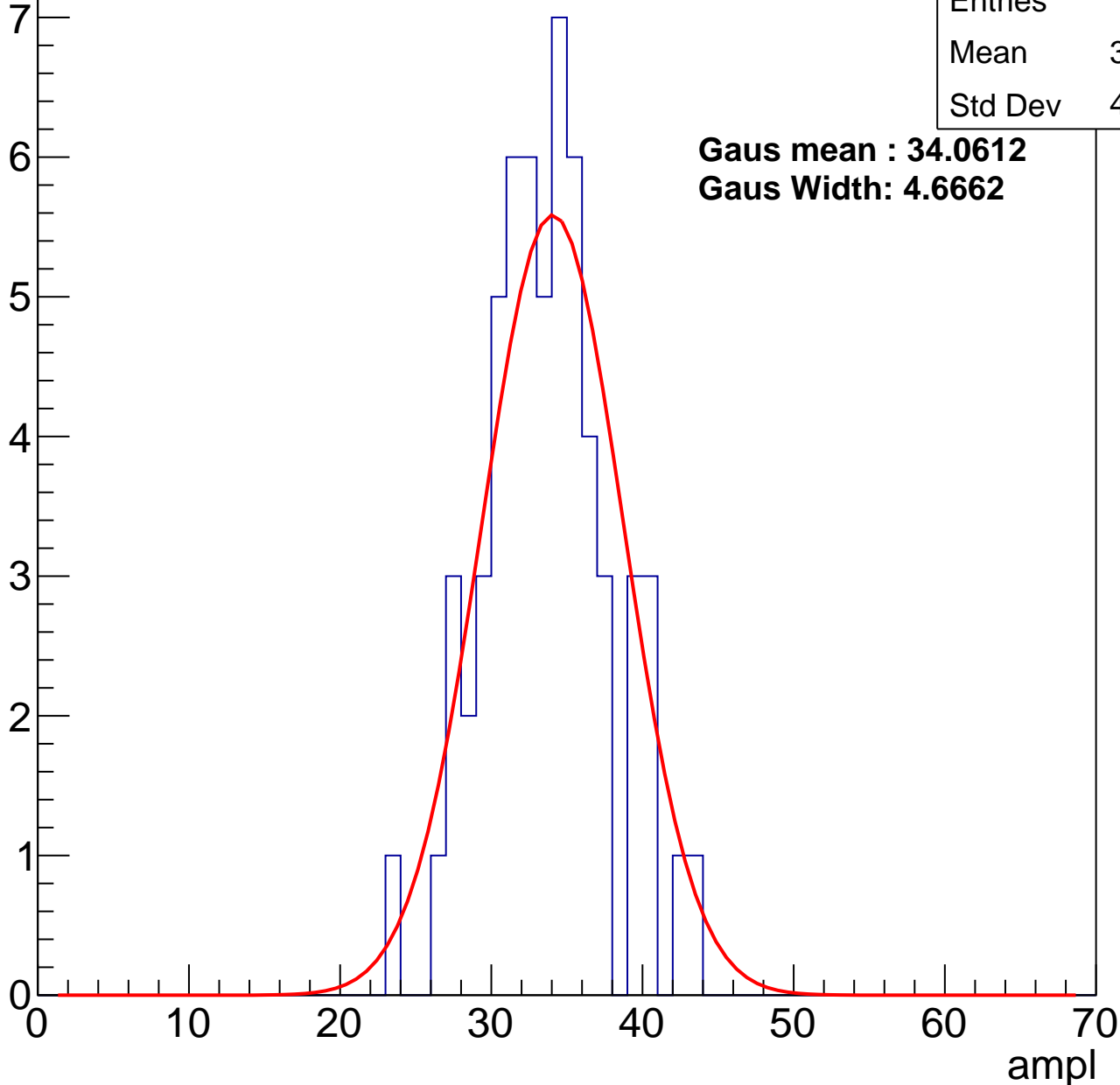
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 33.18 |
| Std Dev | 4.039 |

Gaus mean : 34.0612

Gaus Width: 4.6662



B1L102S, U15-ch116, adc2

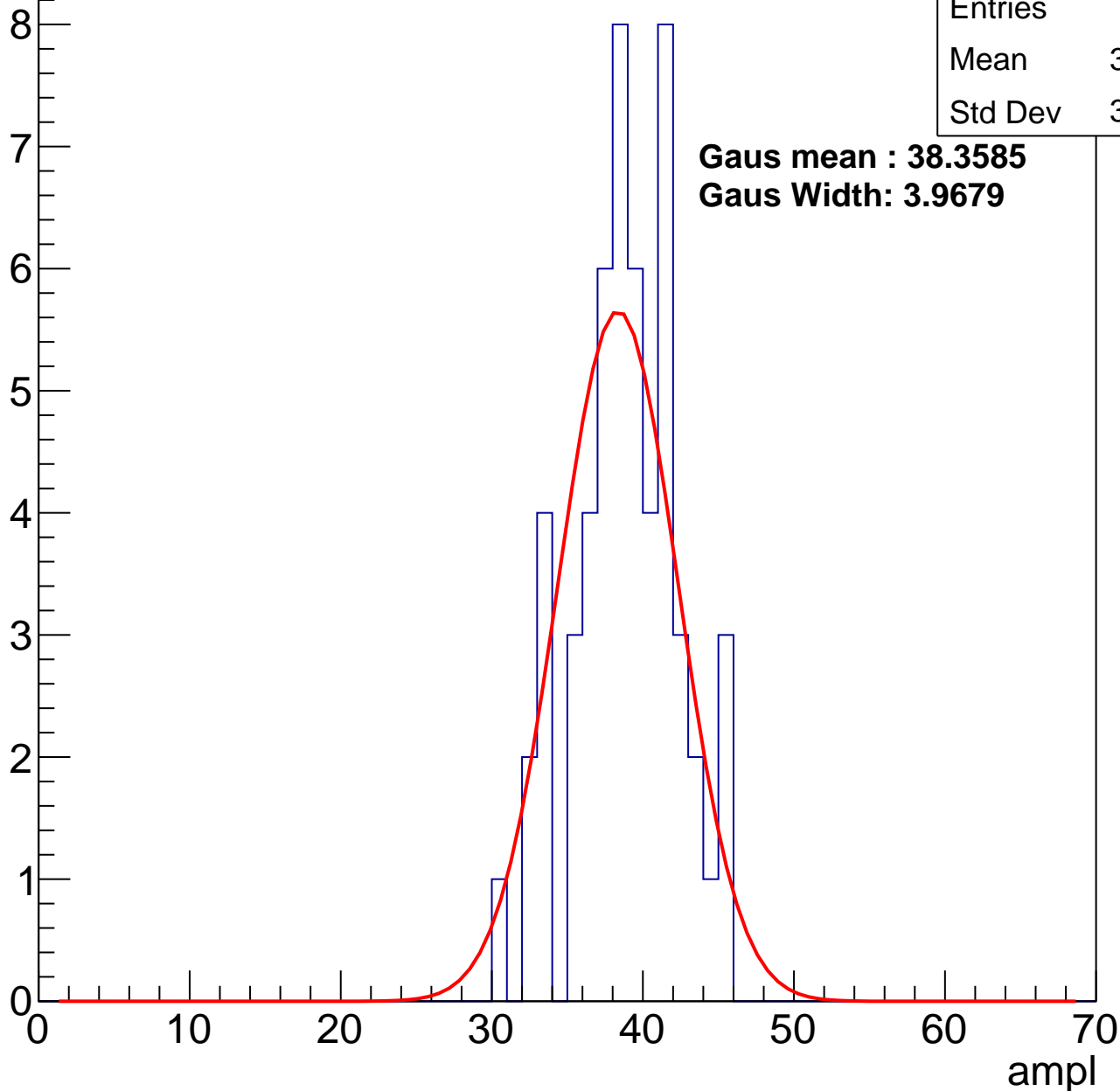
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 38.44 |
| Std Dev | 3.437 |

Gaus mean : 38.3585

Gaus Width: 3.9679

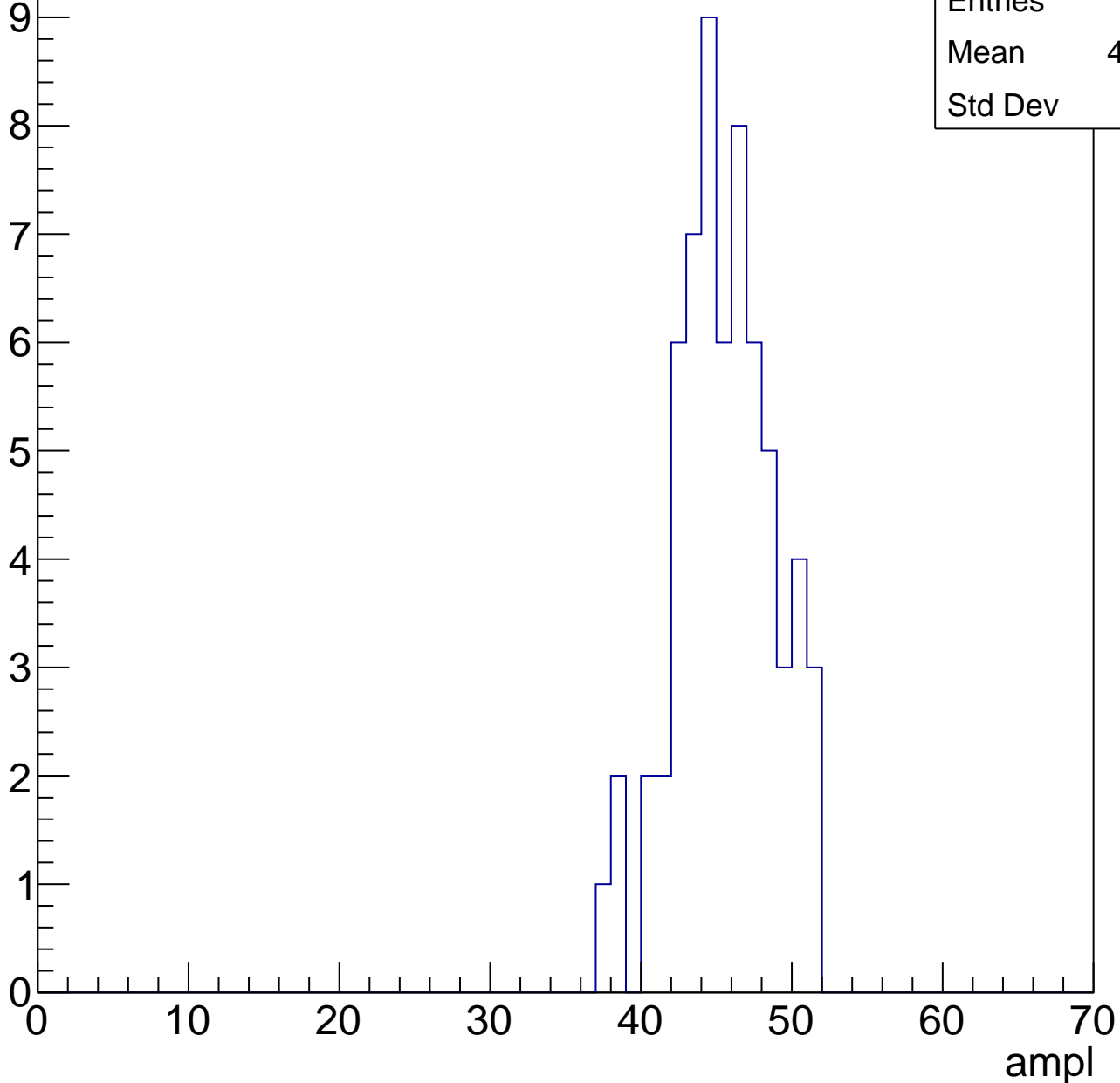


B1L102S, U15-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 64 |
| Mean | 45.06 |
| Std Dev | 3.23 |

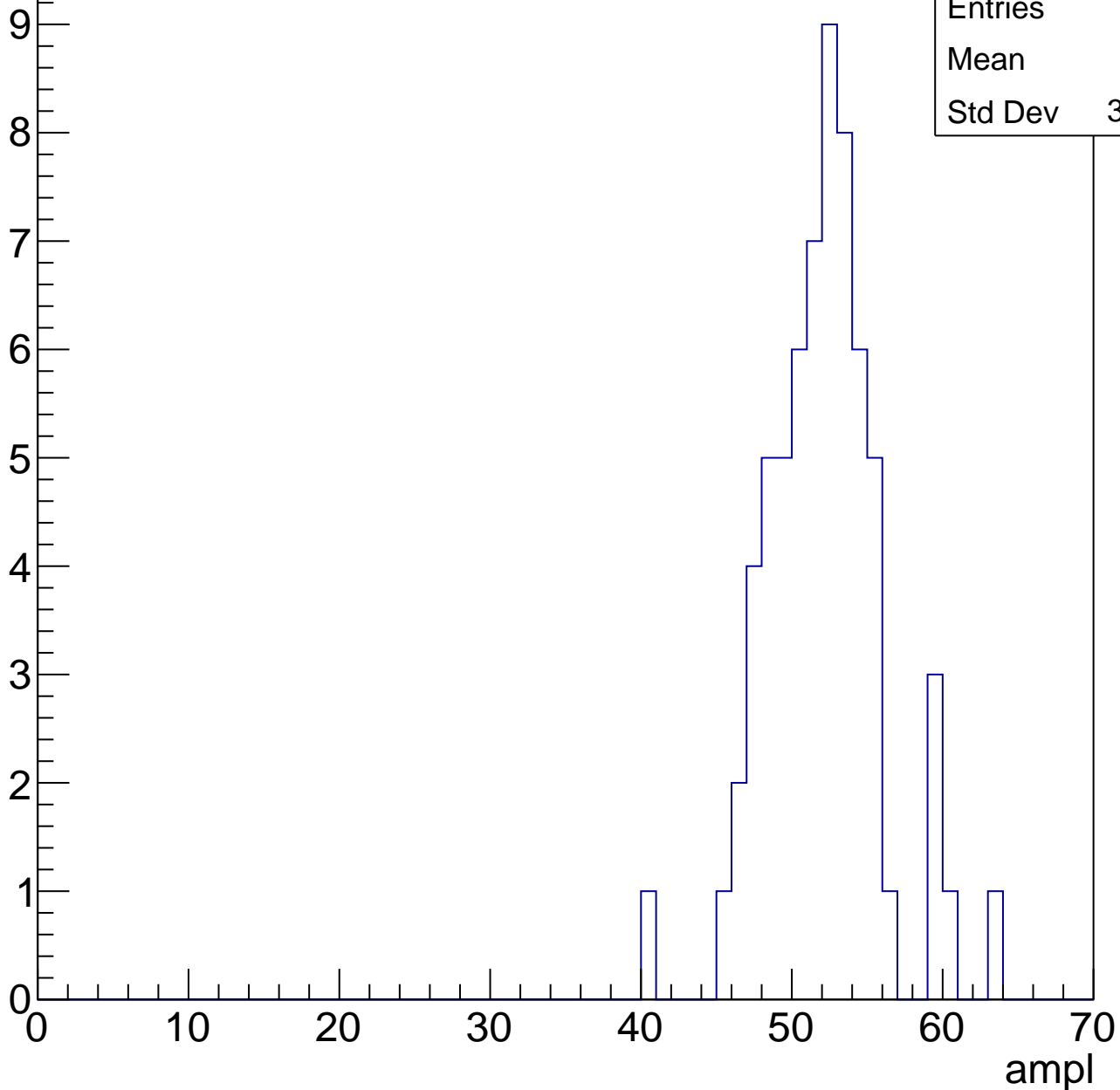


B1L102S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 51.6 |
| Std Dev | 3.794 |

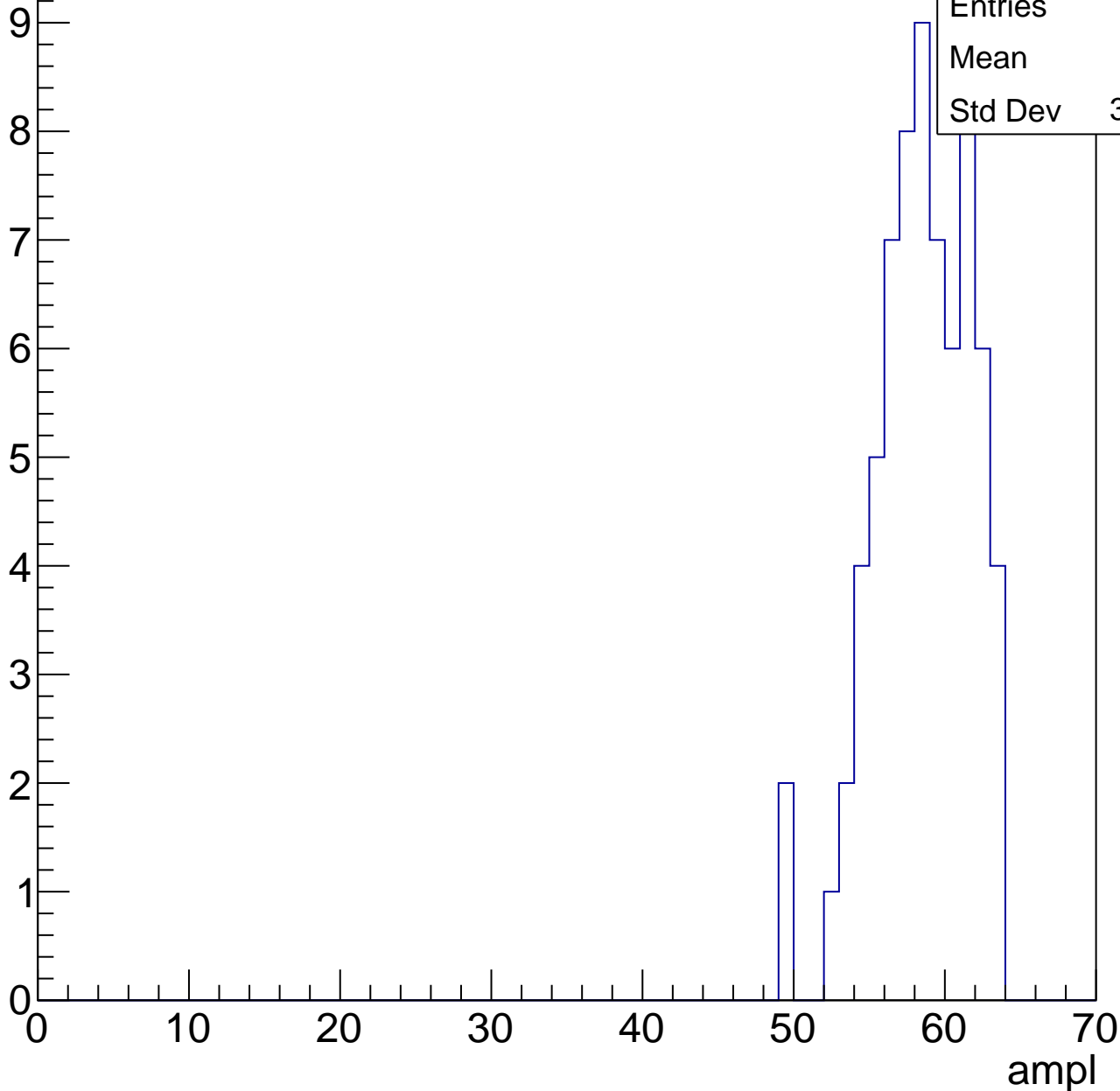


B1L102S, U15-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 58 |
| Std Dev | 3.167 |

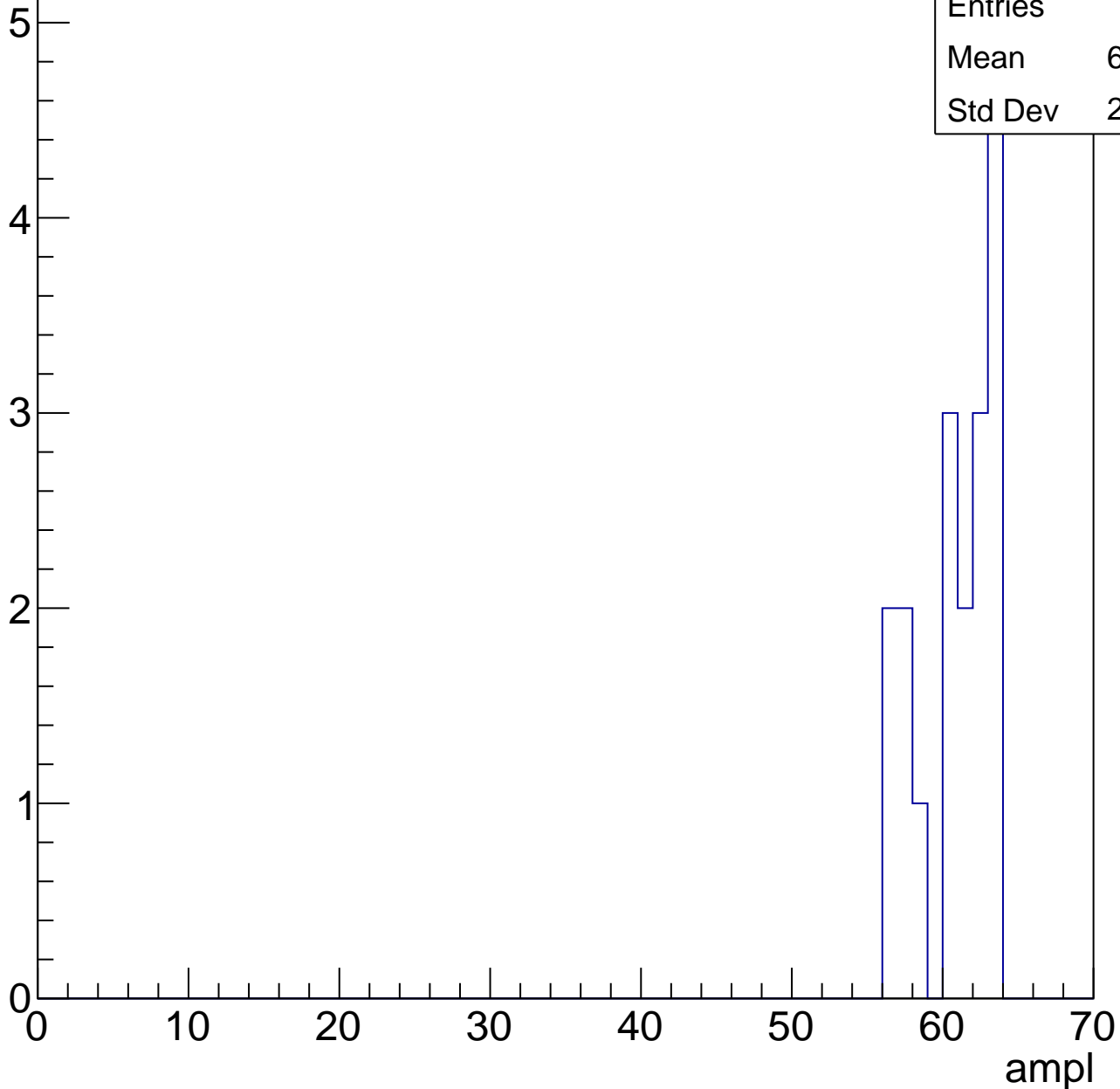


B1L102S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 60.39 |
| Std Dev | 2.475 |



B1L102S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch117, adc0

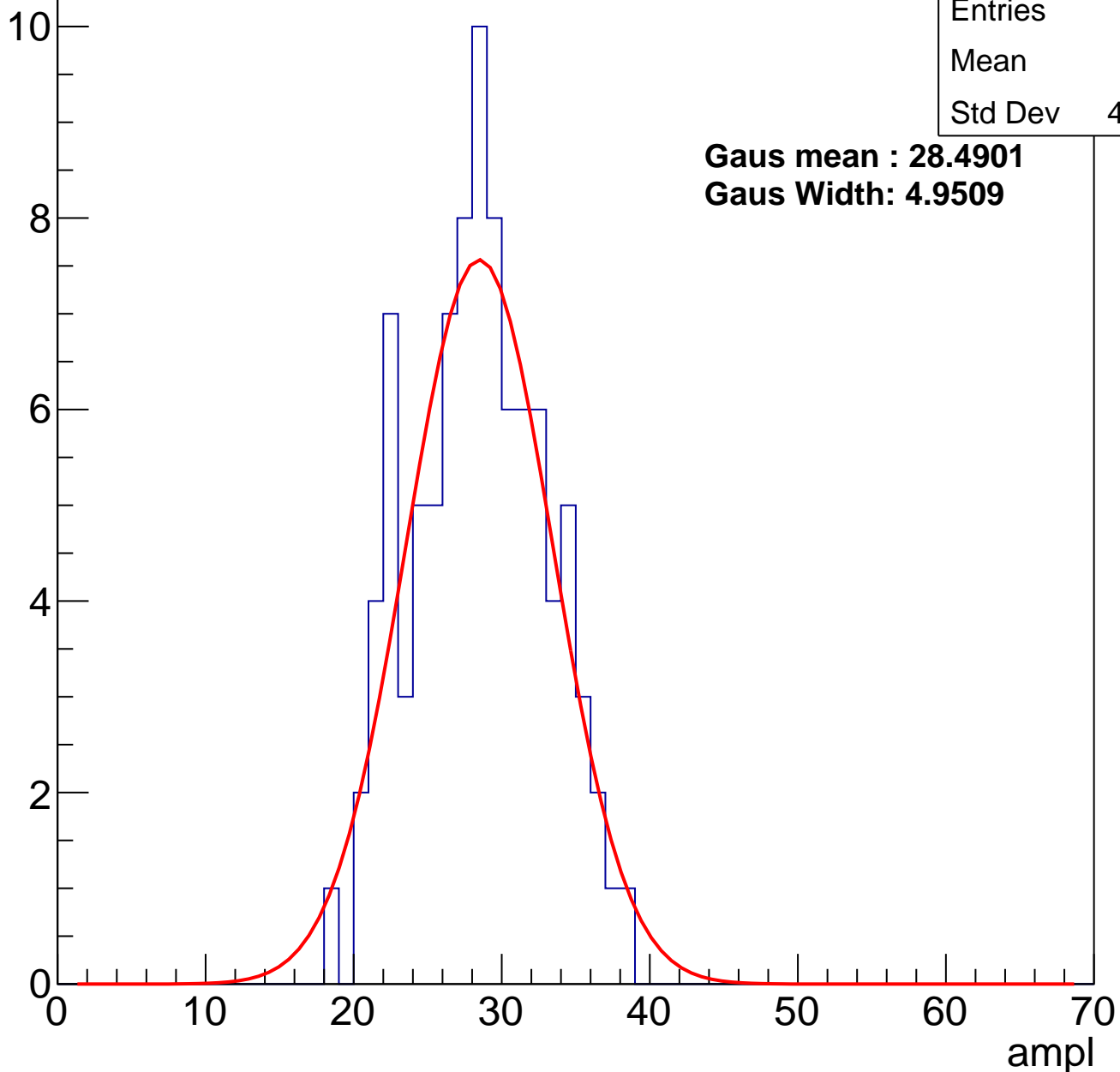
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 94 |
| Mean | 28 |
| Std Dev | 4.412 |

Gaus mean : 28.4901

Gaus Width: 4.9509

Entry



B1L102S, U15-ch117, adc1

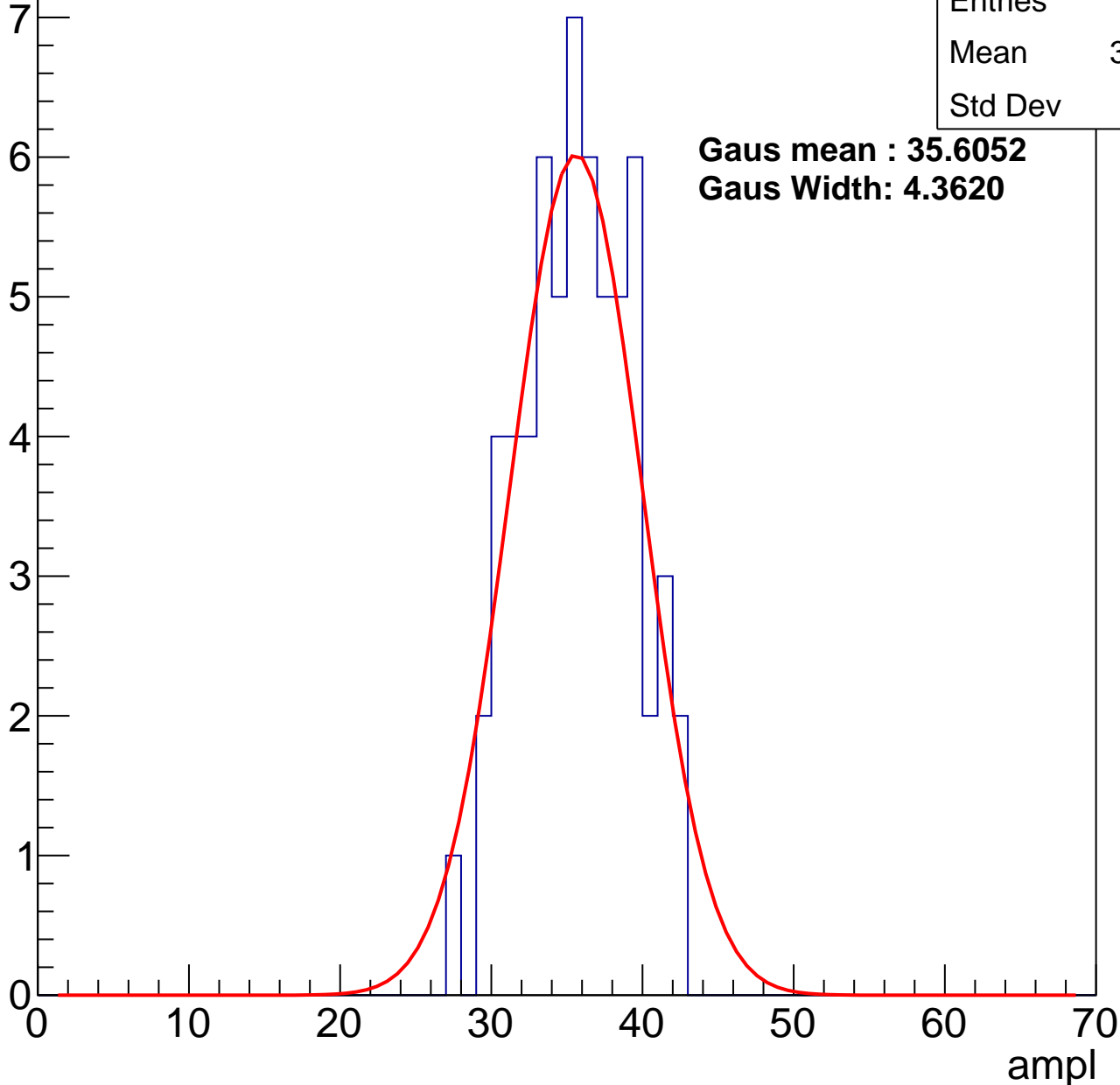
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 35.19 |
| Std Dev | 3.56 |

Gaus mean : 35.6052

Gaus Width: 4.3620



B1L102S, U15-ch117, adc2

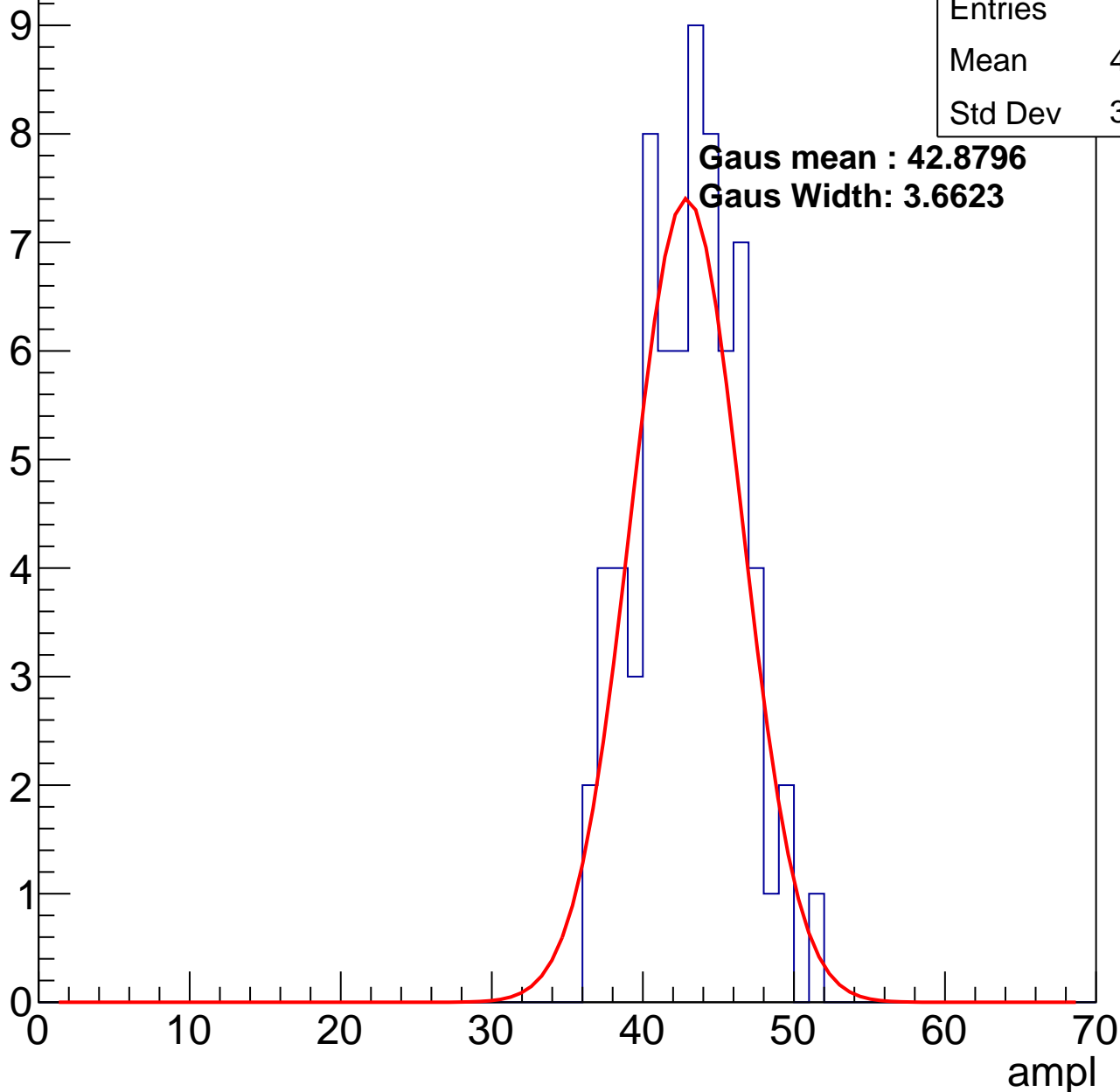
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.58 |
| Std Dev | 3.355 |

Gaus mean : 42.8796

Gaus Width: 3.6623

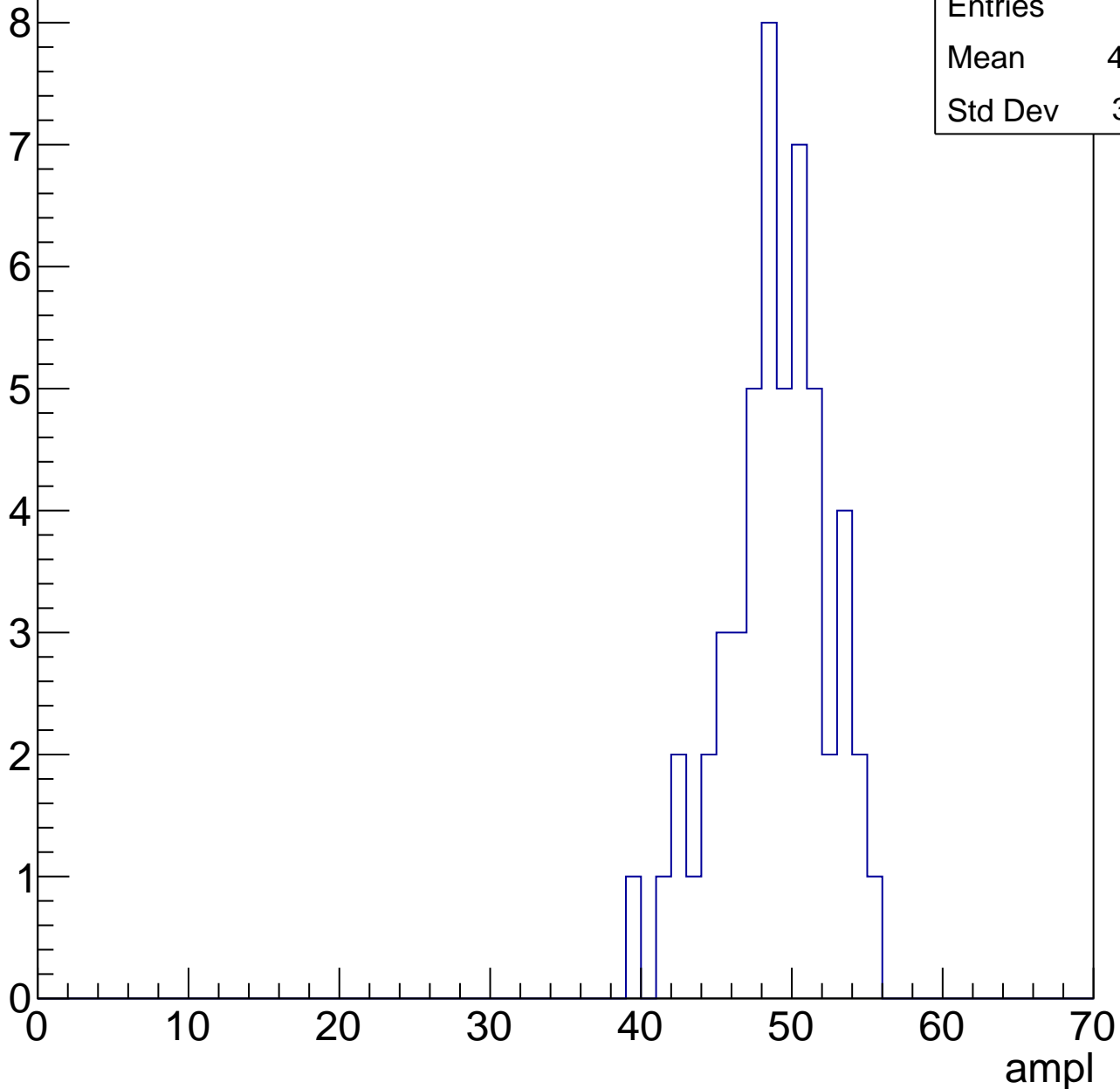


B1L102S, U15-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 48.38 |
| Std Dev | 3.471 |

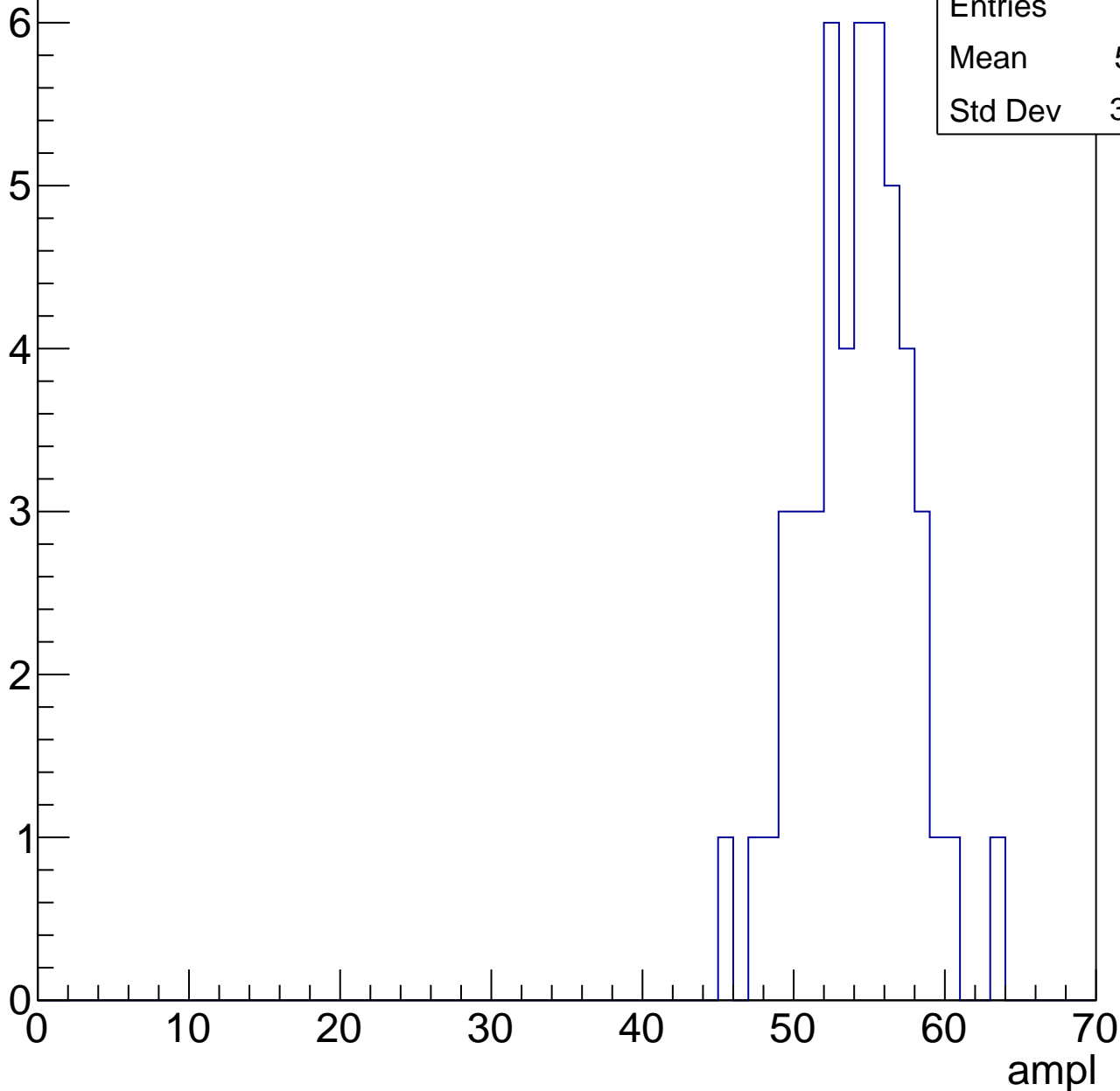


B1L102S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 53.71 |
| Std Dev | 3.476 |

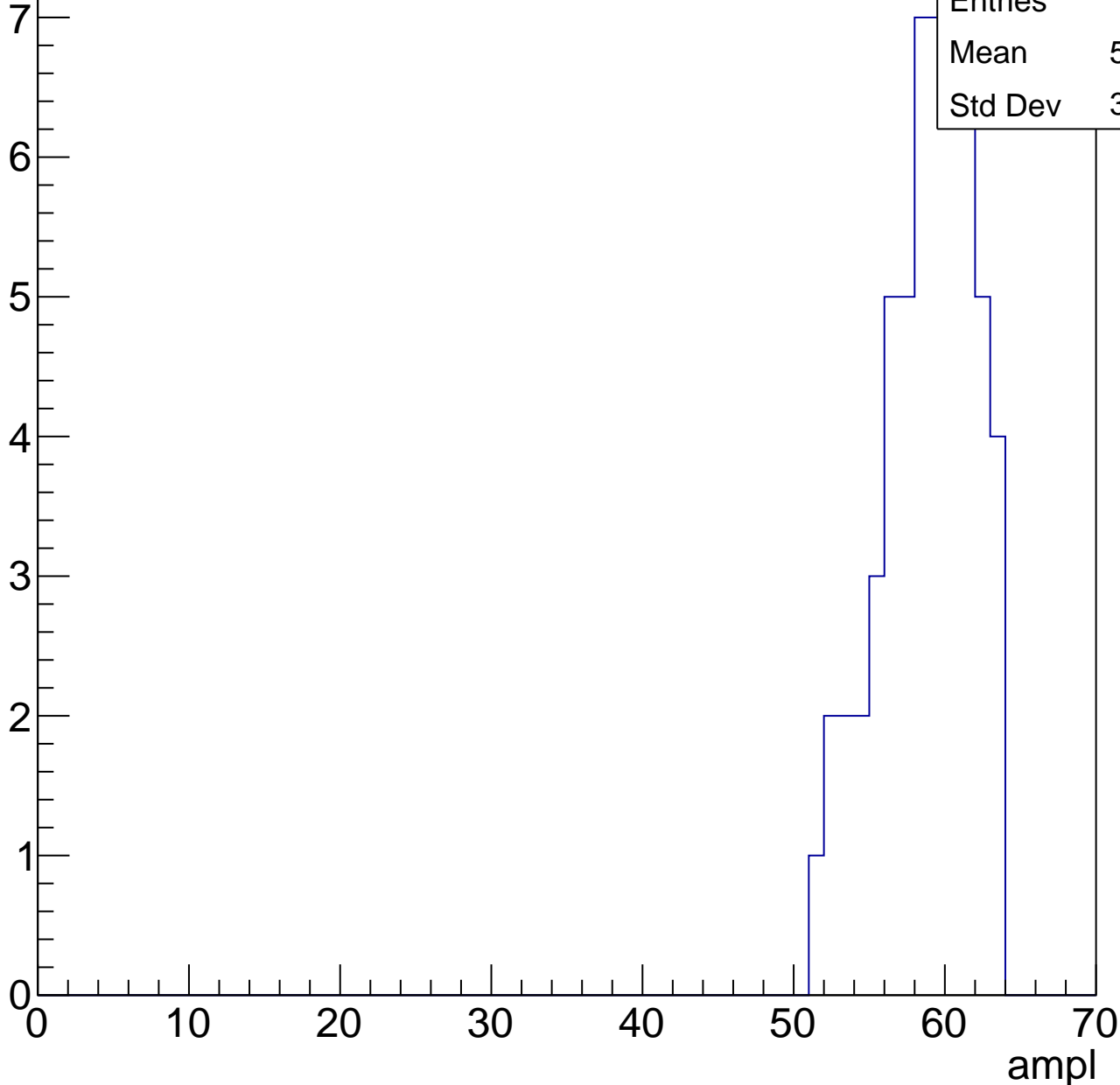


B1L102S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 57 |
| Mean | 58.37 |
| Std Dev | 3.036 |

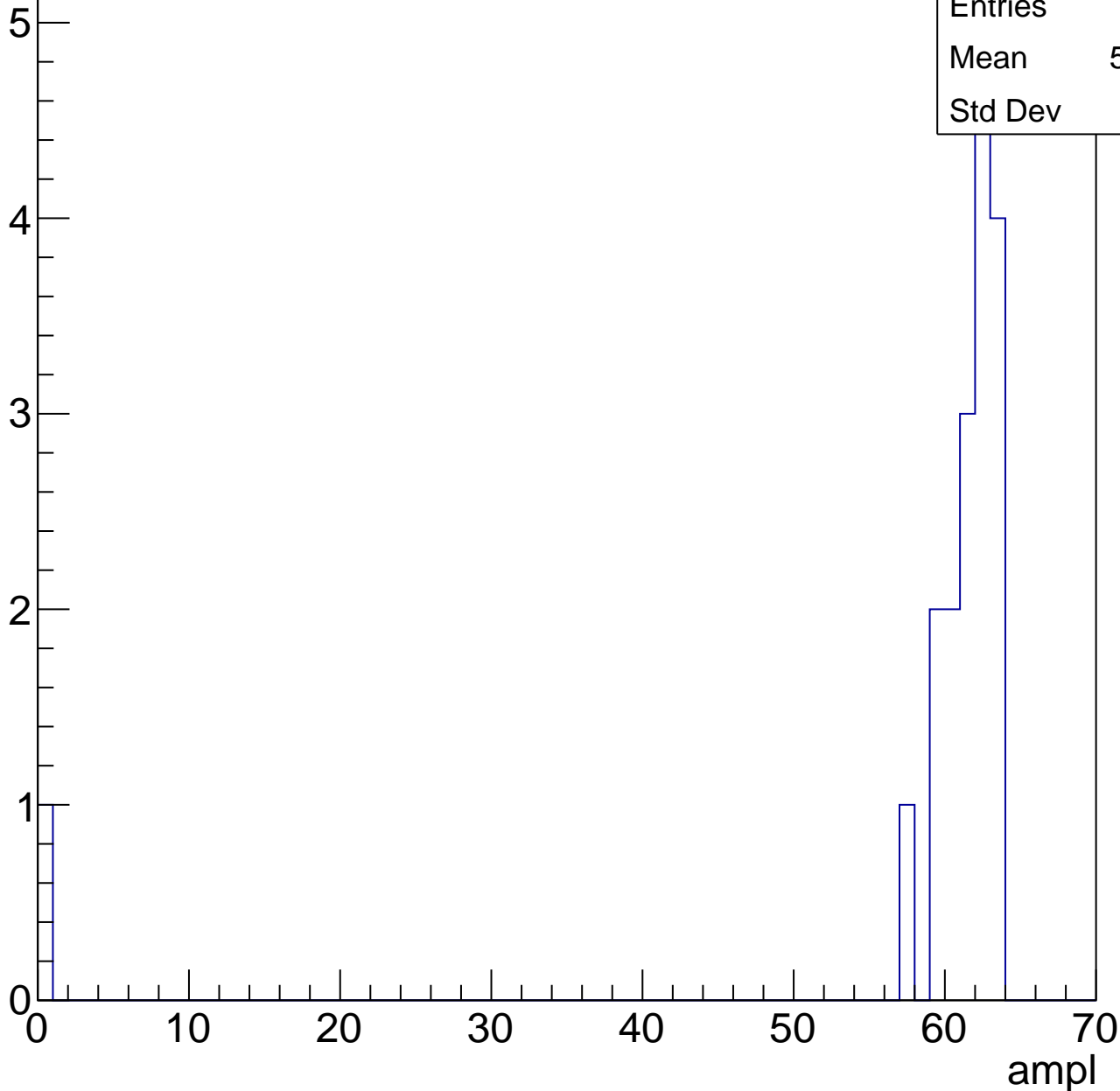


B1L102S, U15-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 18 |
| Mean | 57.78 |
| Std Dev | 14.1 |



B1L102S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch118, adc0

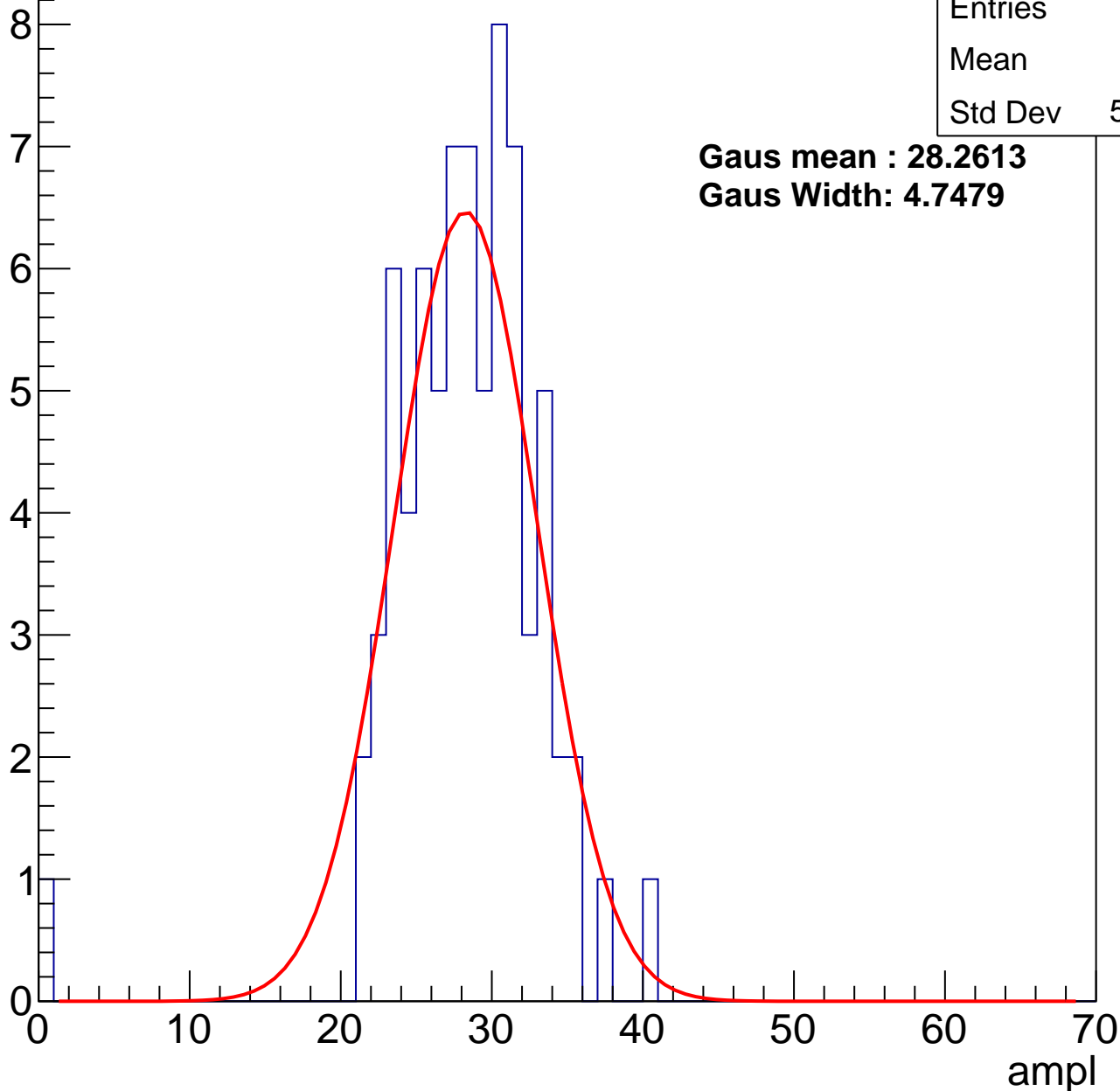
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 75 |
| Mean | 27.8 |
| Std Dev | 5.099 |

Gaus mean : 28.2613

Gaus Width: 4.7479



B1L102S, U15-ch118, adc1

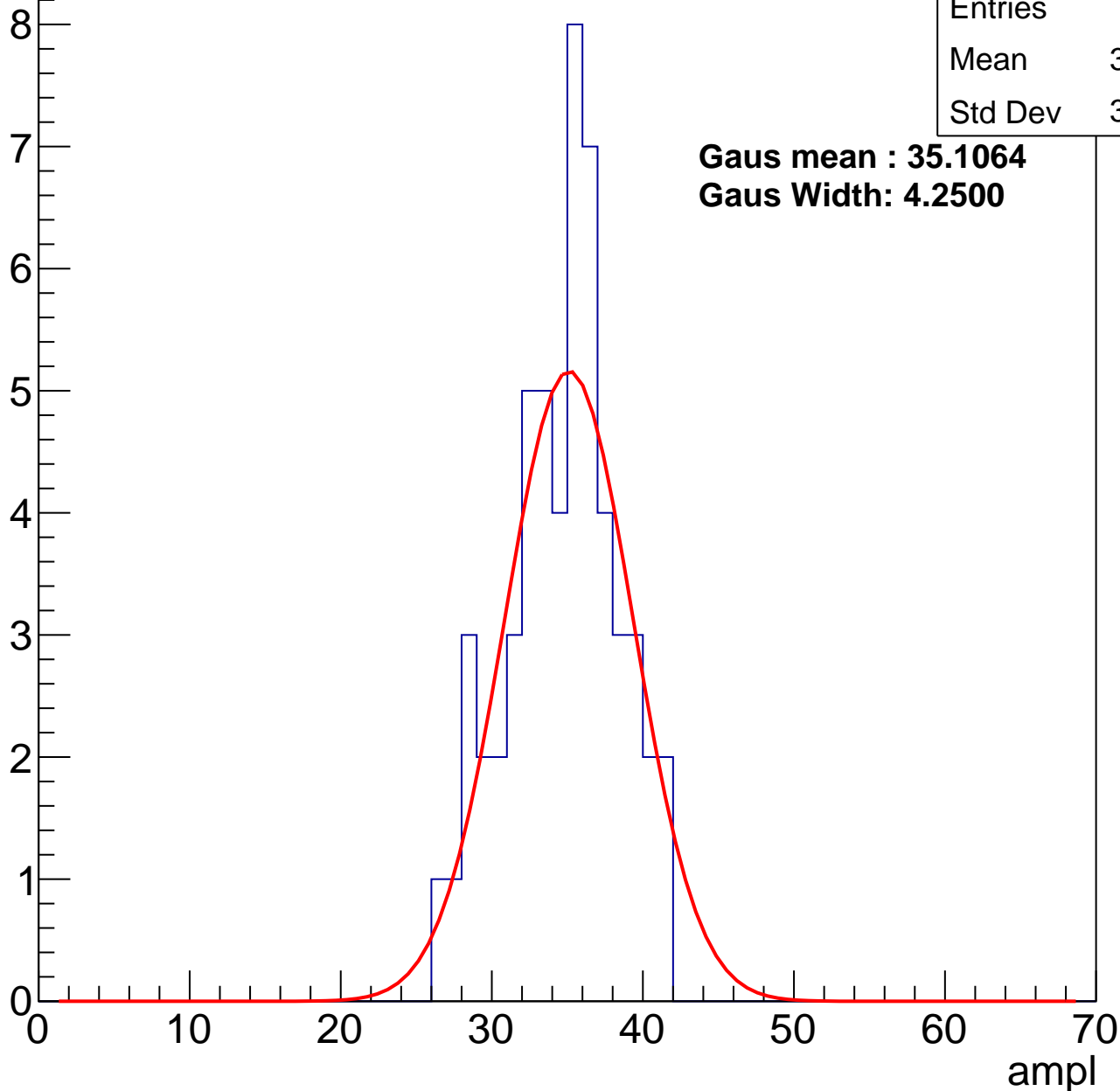
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 34.22 |
| Std Dev | 3.612 |

Gaus mean : 35.1064

Gaus Width: 4.2500



B1L102S, U15-ch118, adc2

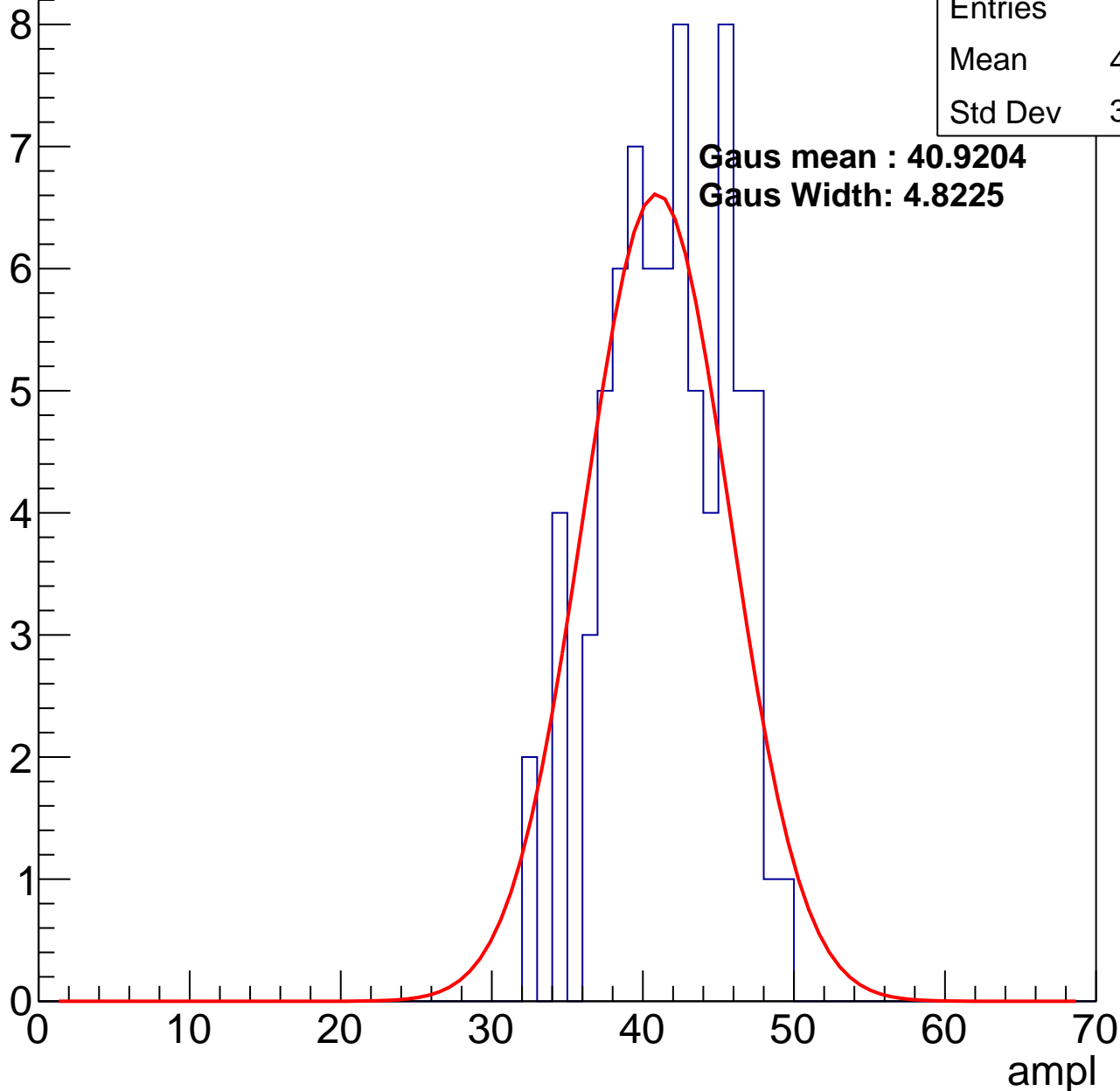
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 41.17 |
| Std Dev | 3.988 |

Gaus mean : 40.9204

Gaus Width: 4.8225

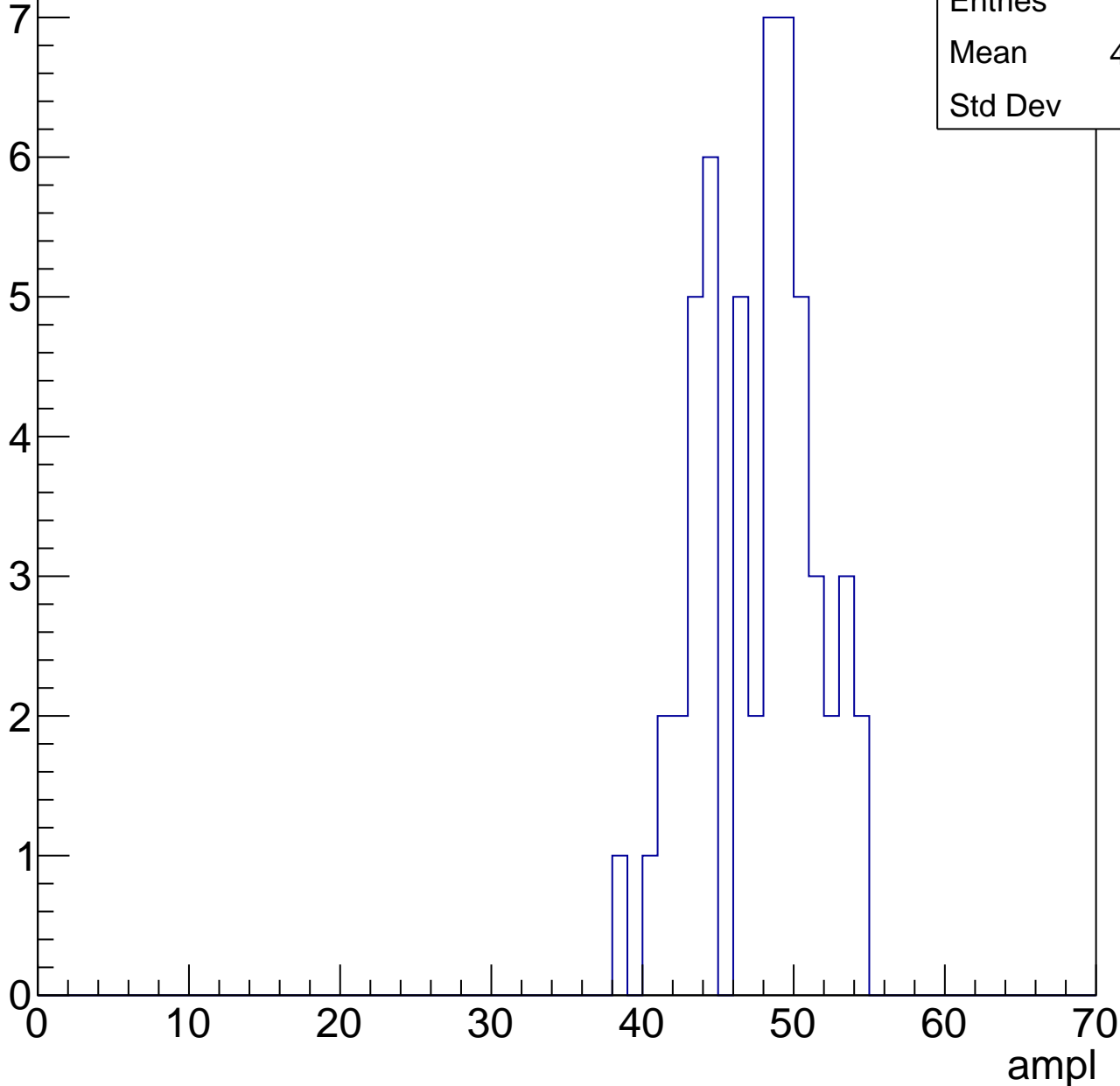


B1L102S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 47.17 |
| Std Dev | 3.82 |

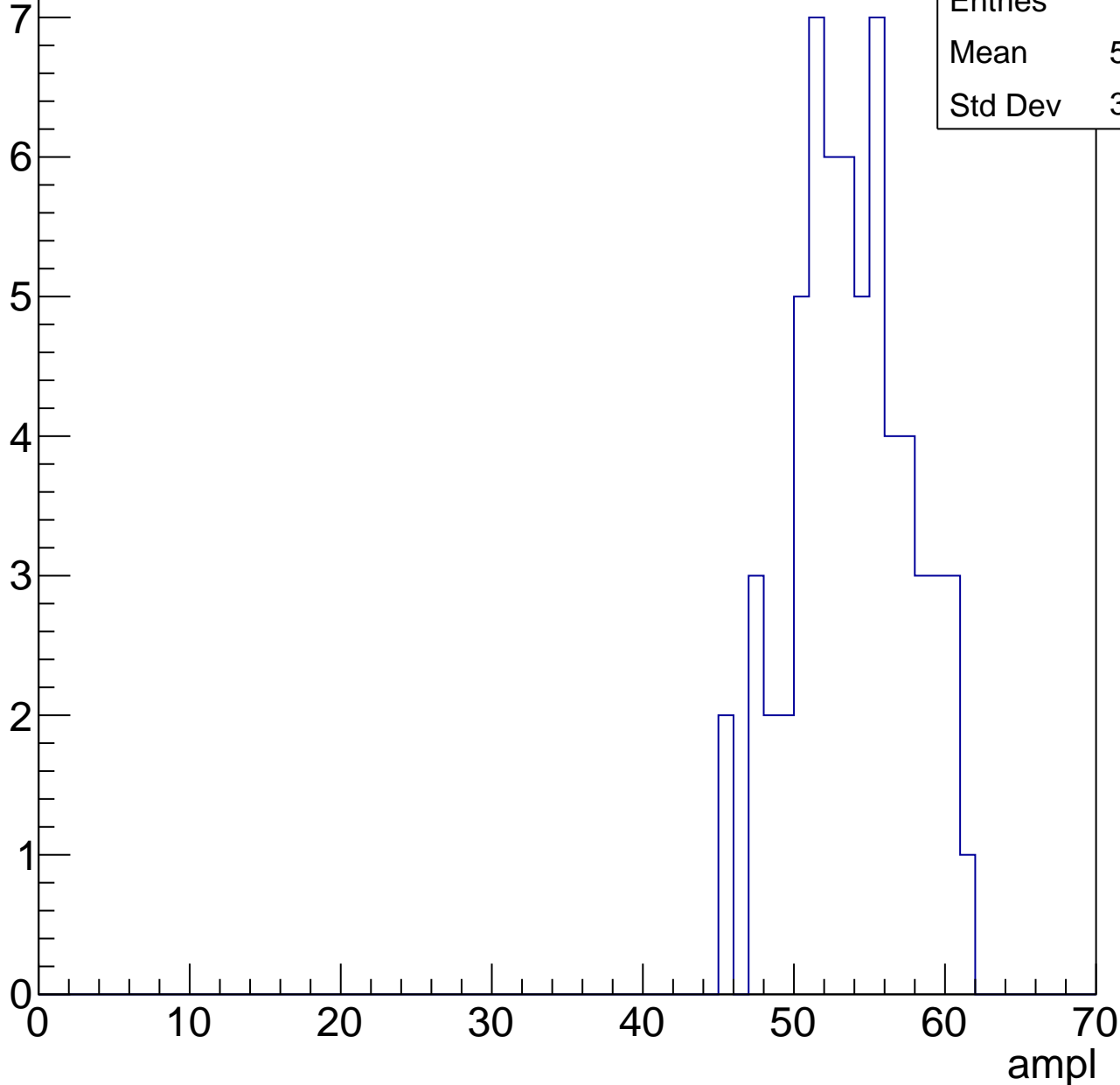


B1L102S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 53.35 |
| Std Dev | 3.814 |

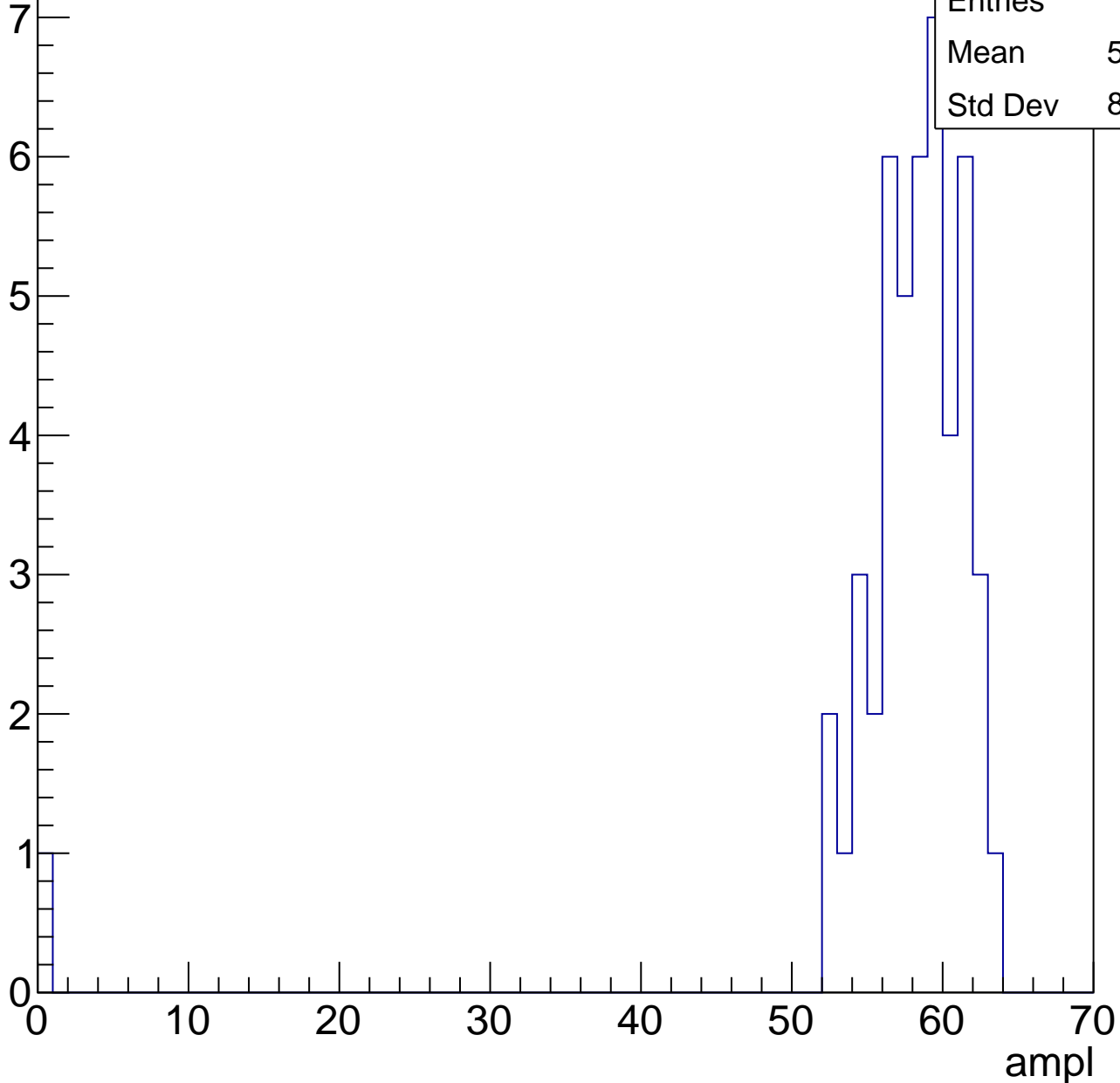


B1L102S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 56.72 |
| Std Dev | 8.788 |

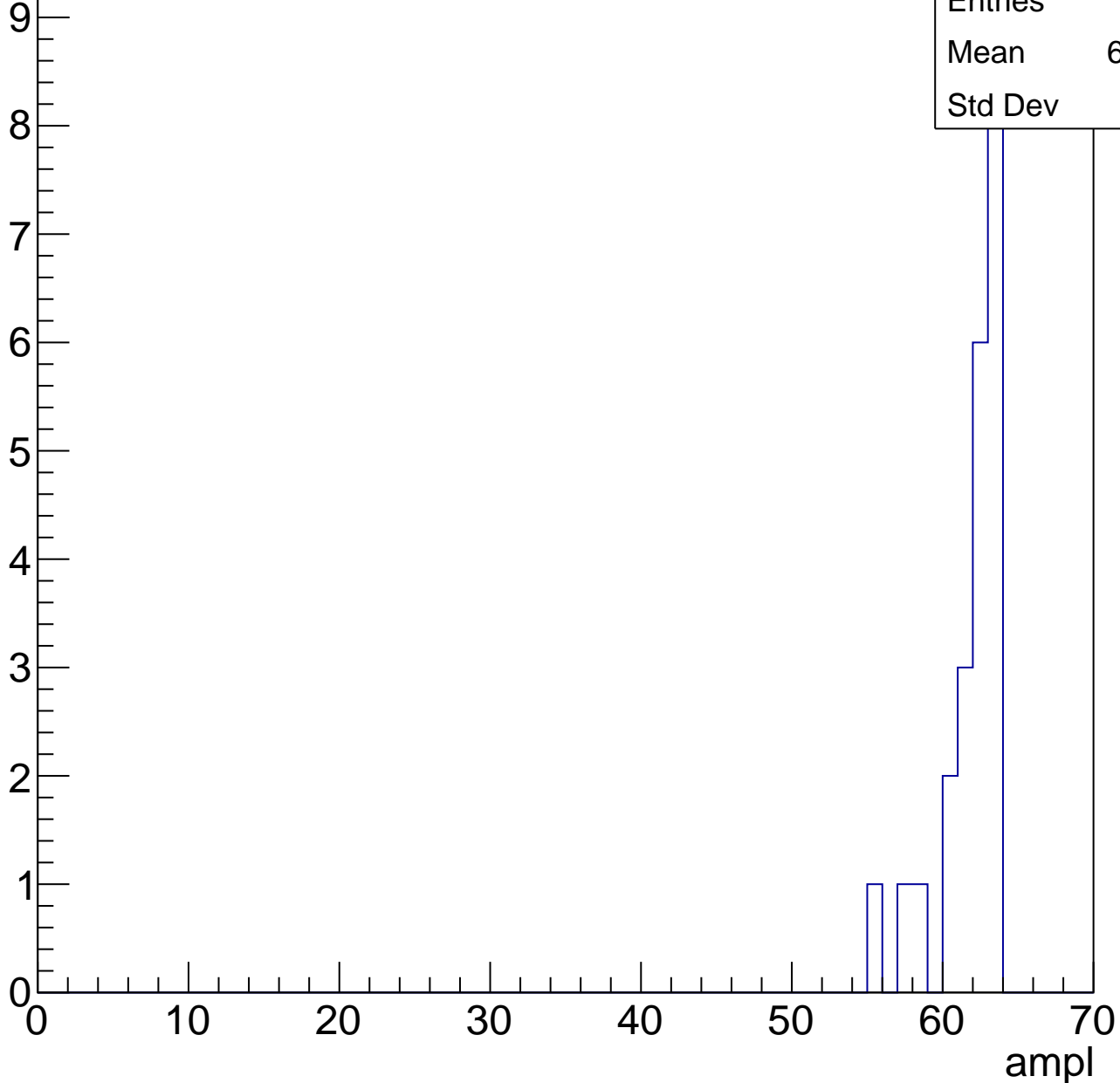


B1L102S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

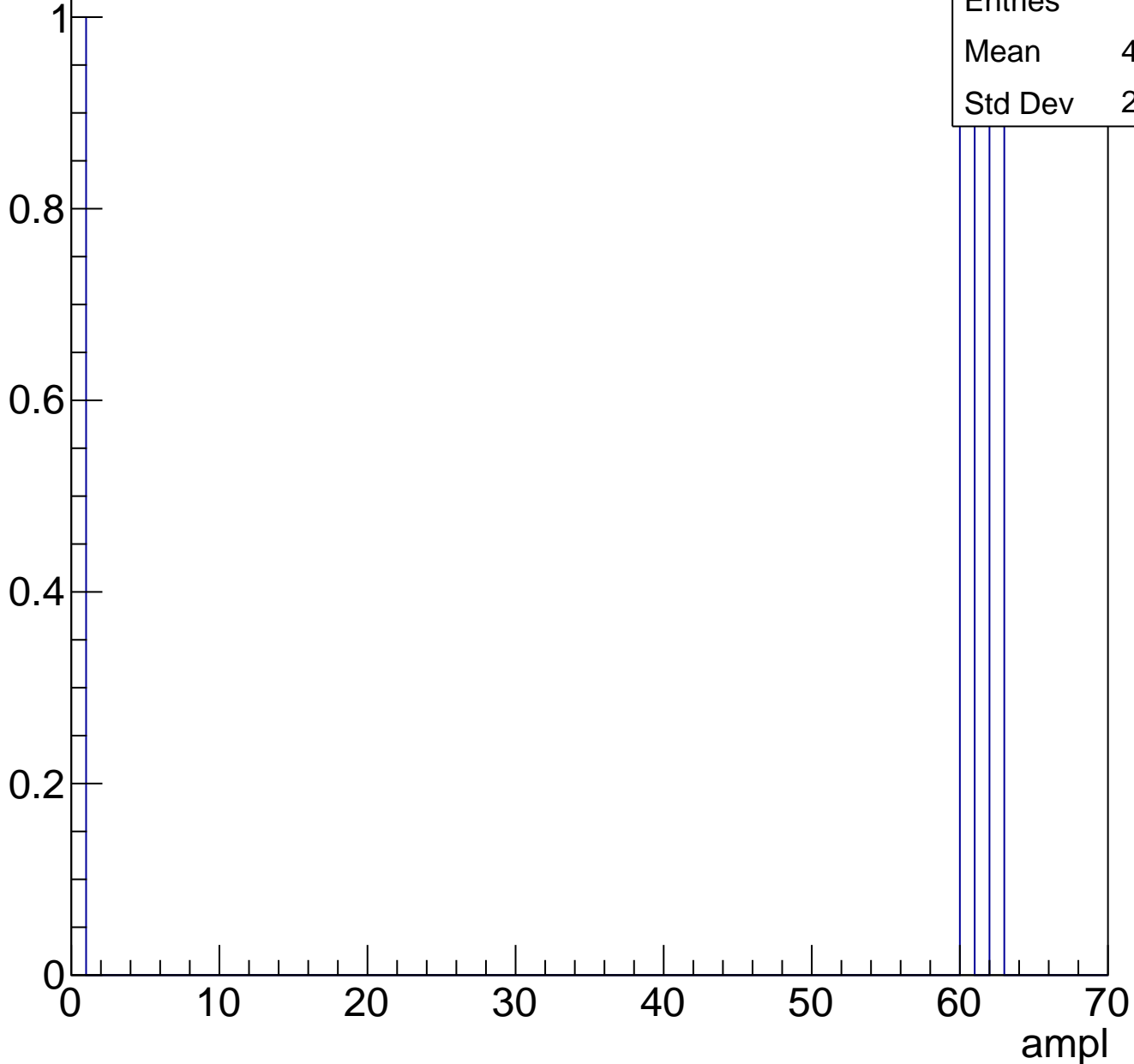
| | |
|---------|-------|
| Entries | 23 |
| Mean | 61.39 |
| Std Dev | 2.1 |



B1L102S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch119, adc0

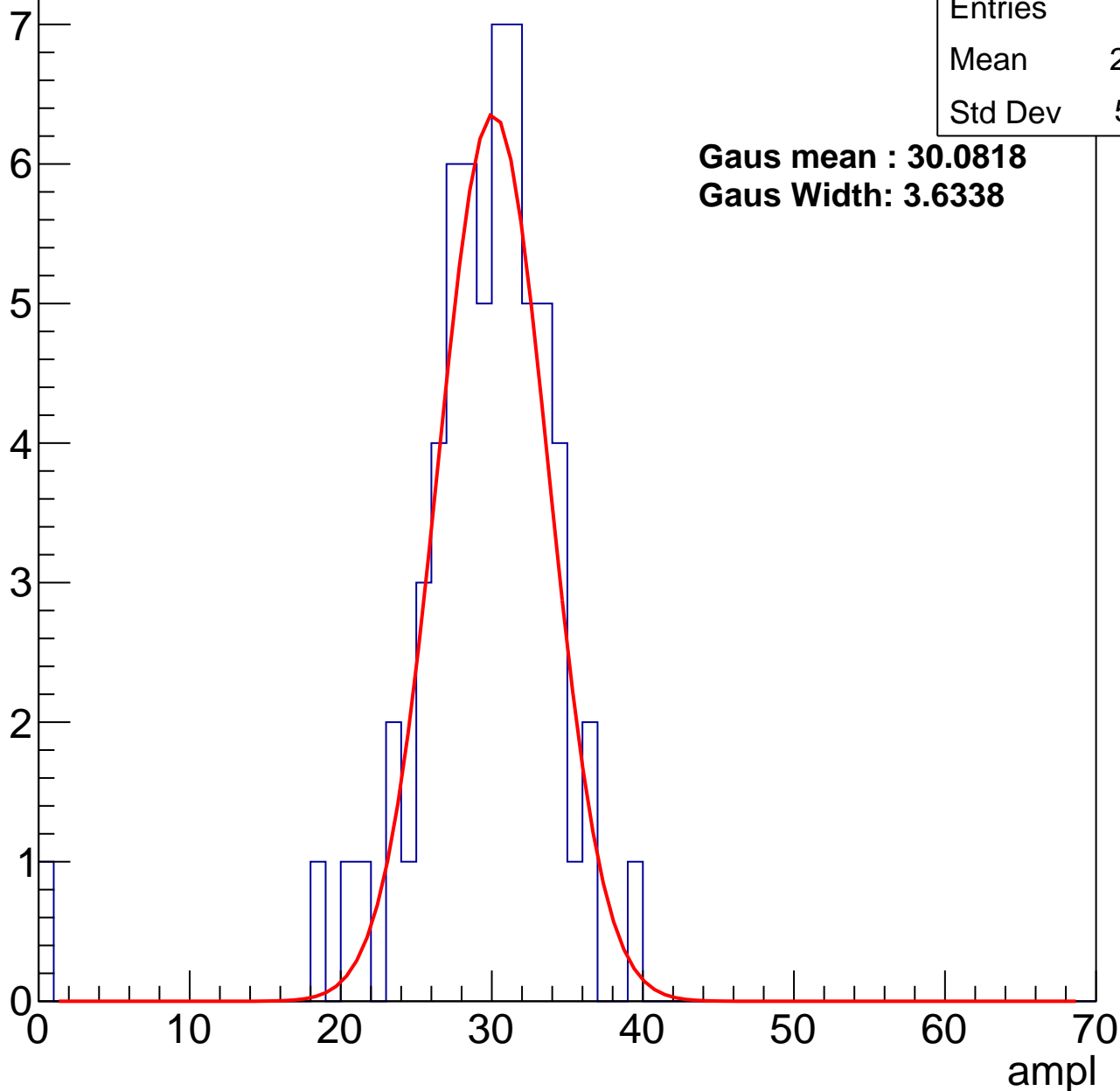
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 28.84 |
| Std Dev | 5.361 |

Gaus mean : 30.0818

Gaus Width: 3.6338



B1L102S, U15-ch119, adc1

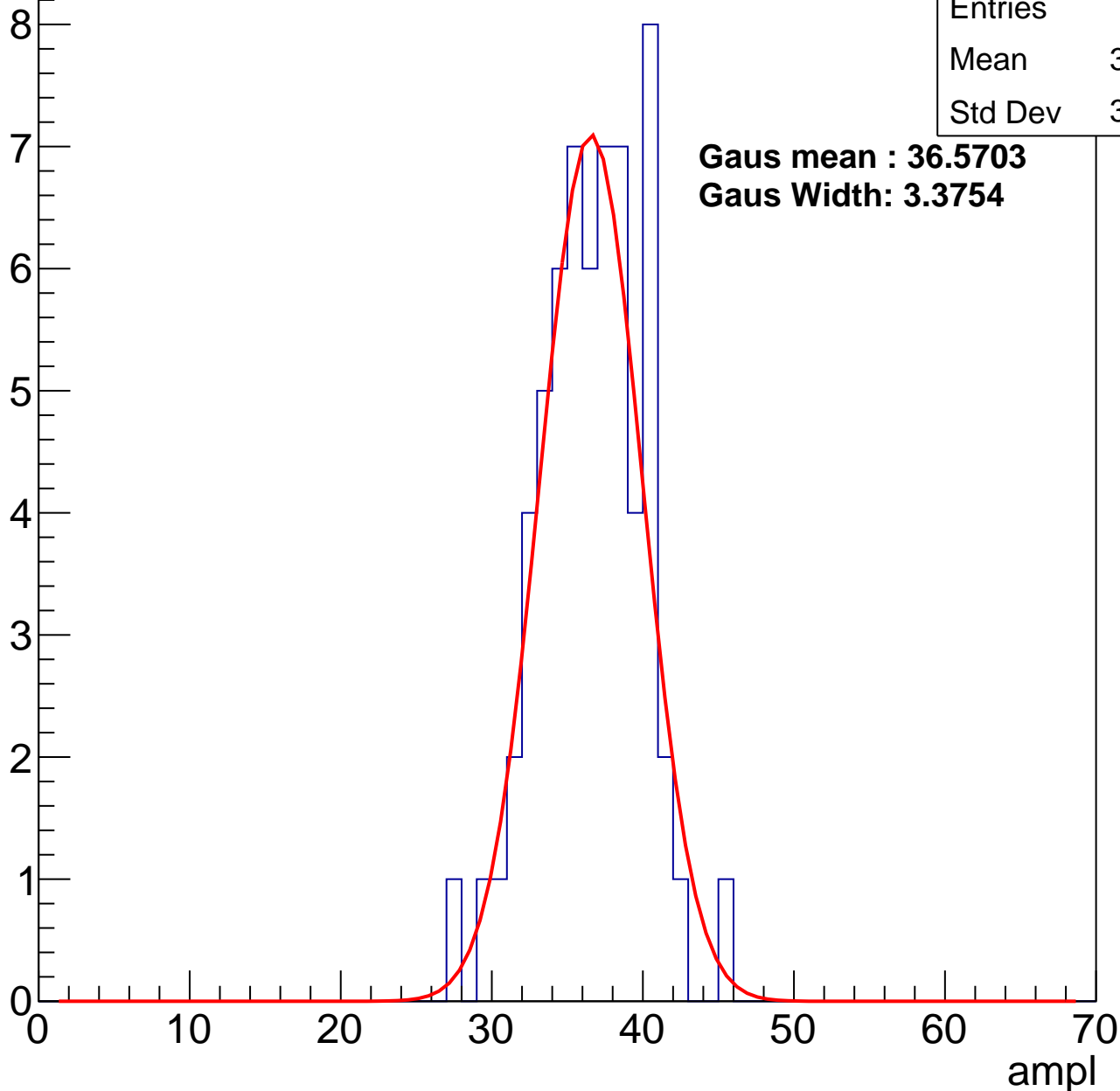
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.13 |
| Std Dev | 3.383 |

Gaus mean : 36.5703

Gaus Width: 3.3754



B1L102S, U15-ch119, adc2

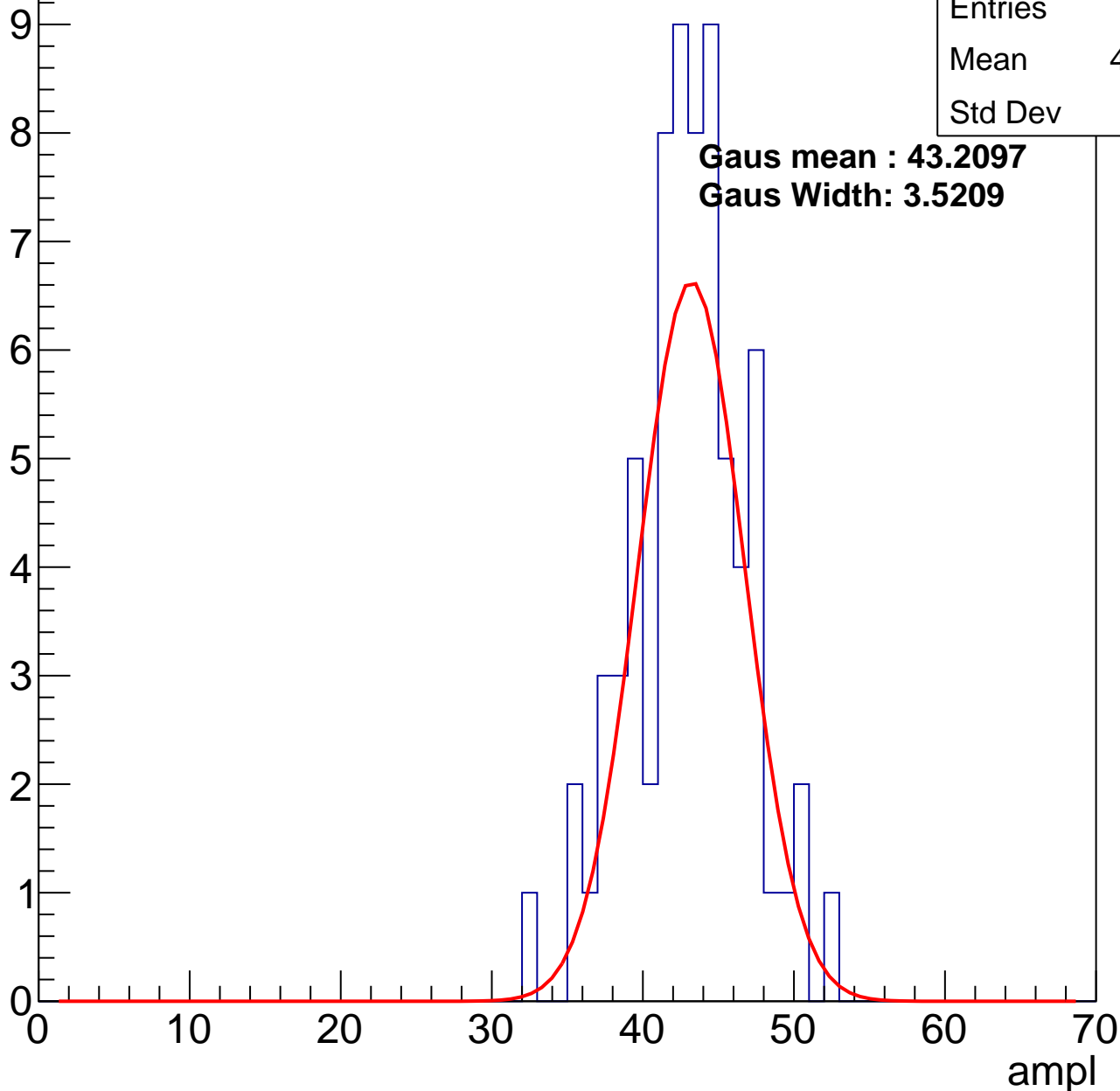
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 42.59 |
| Std Dev | 3.77 |

Gaus mean : 43.2097

Gaus Width: 3.5209

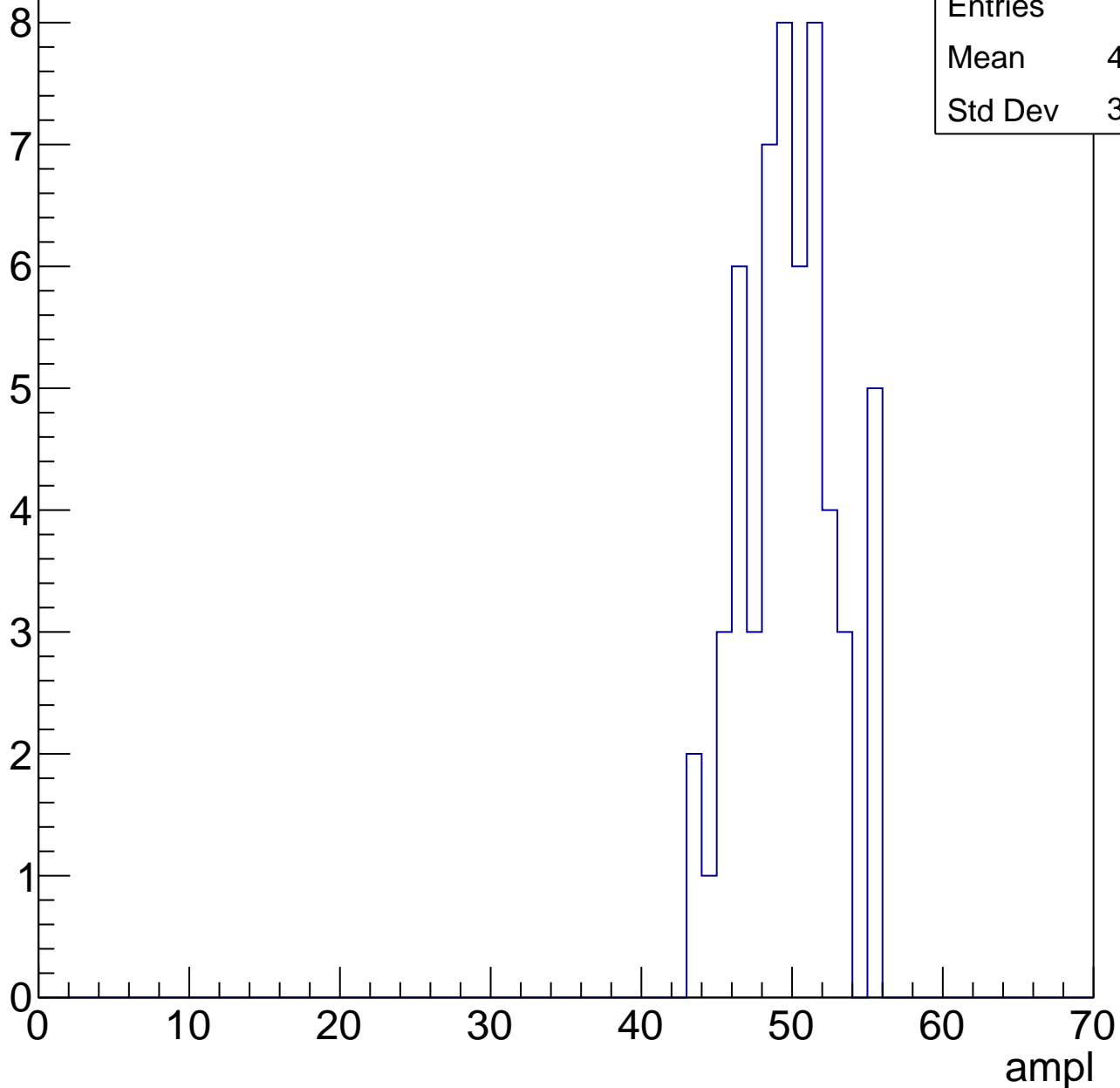


B1L102S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 49.29 |
| Std Dev | 3.034 |

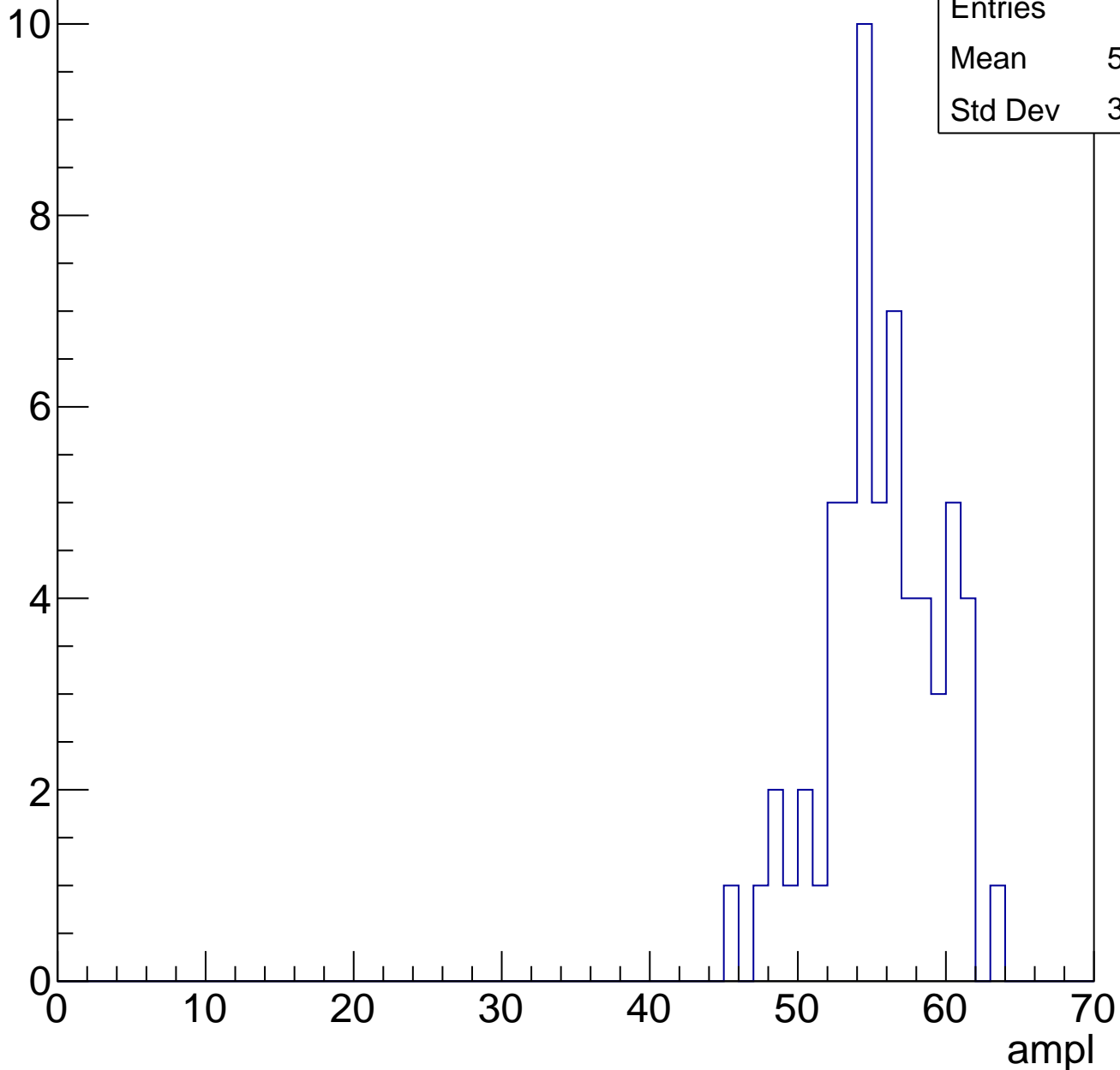


B1L102S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 61 |
| Mean | 55.15 |
| Std Dev | 3.793 |

Entry

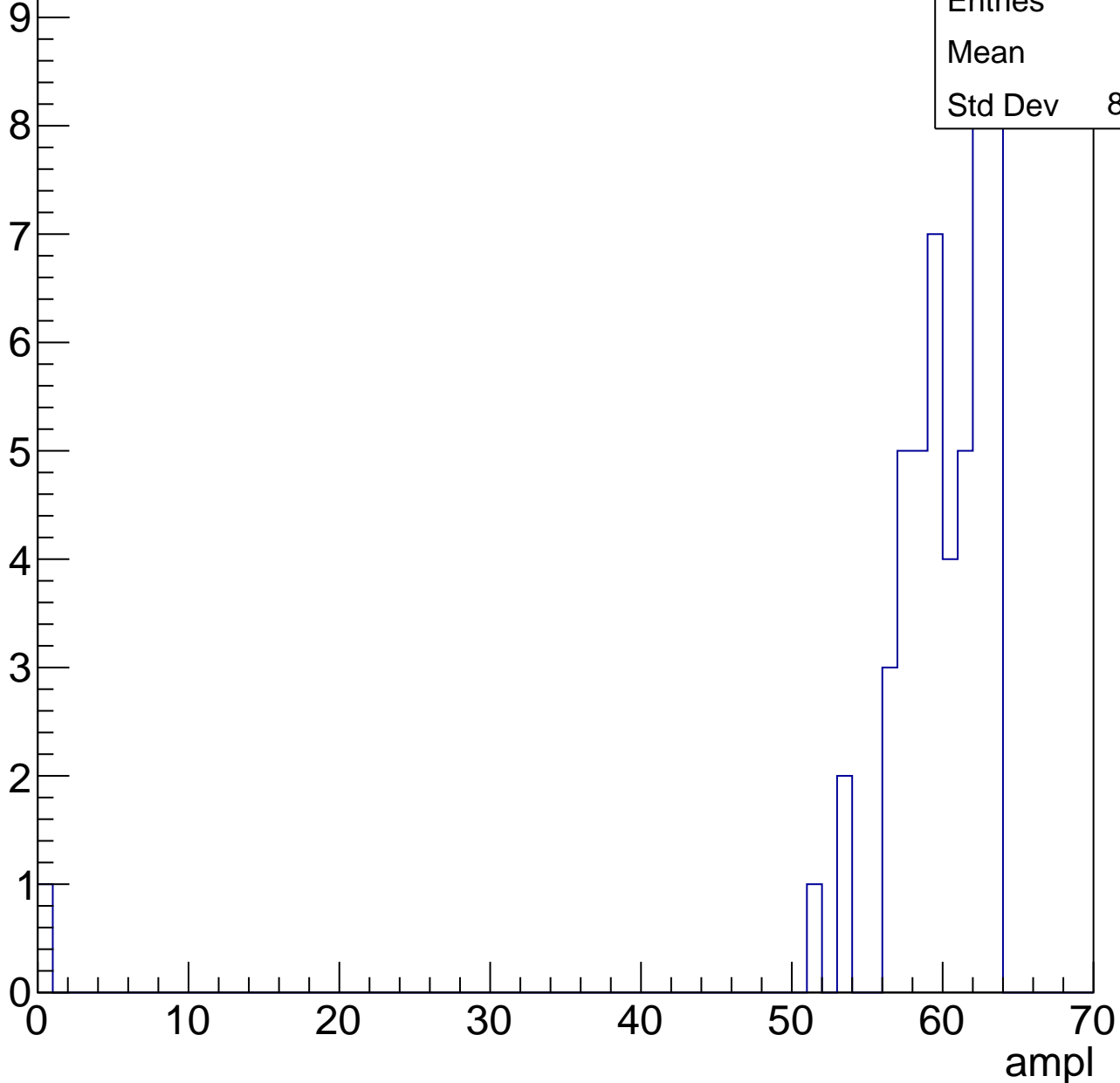


B1L102S, U15-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 58.4 |
| Std Dev | 8.814 |



B1L102S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

6

Mean

60.17

Std Dev

2.115

B1L102S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch120, adc0

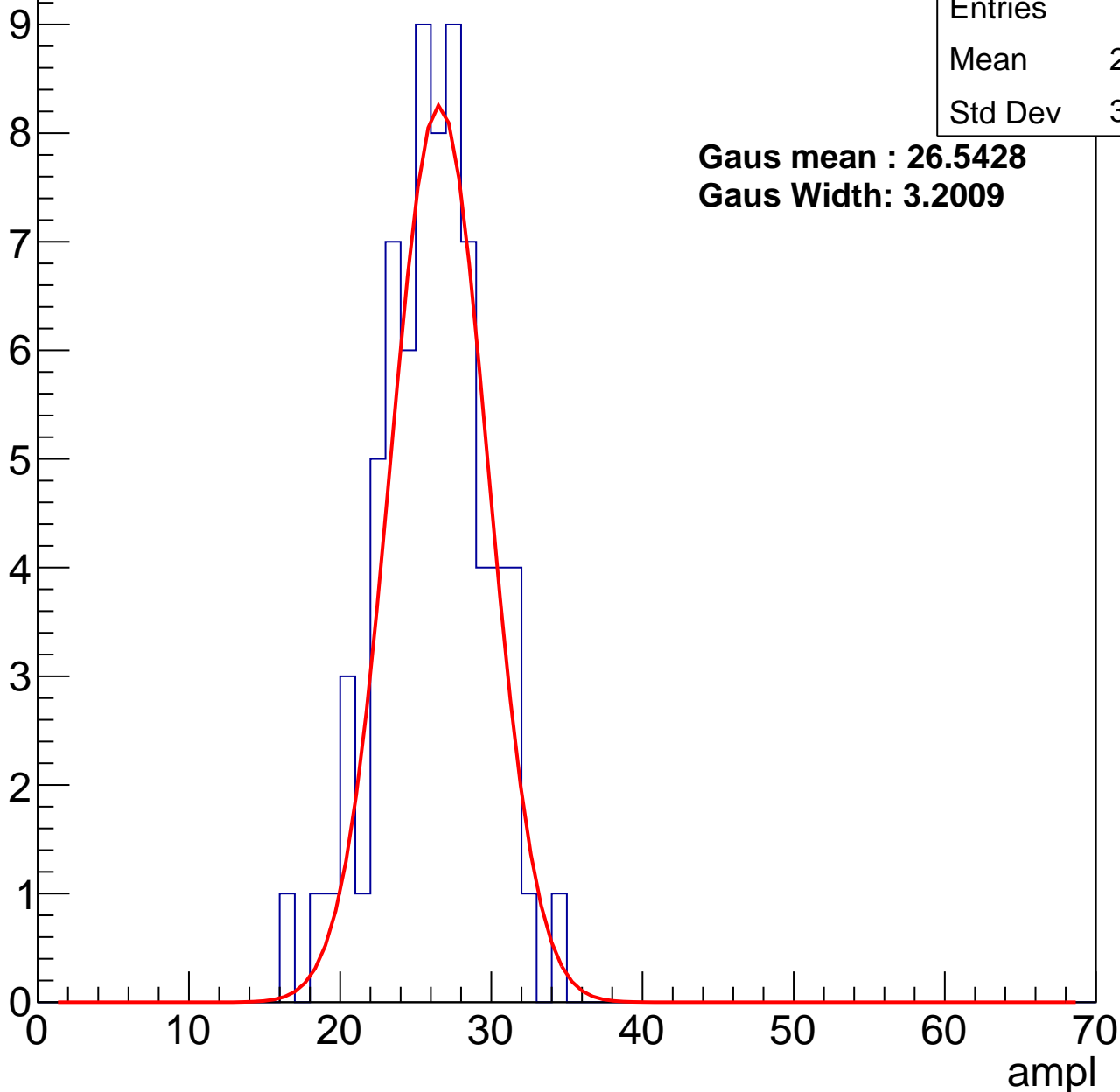
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 72 |
| Mean | 25.65 |
| Std Dev | 3.437 |

Gaus mean : 26.5428

Gaus Width: 3.2009



B1L102S, U15-ch120, adc1

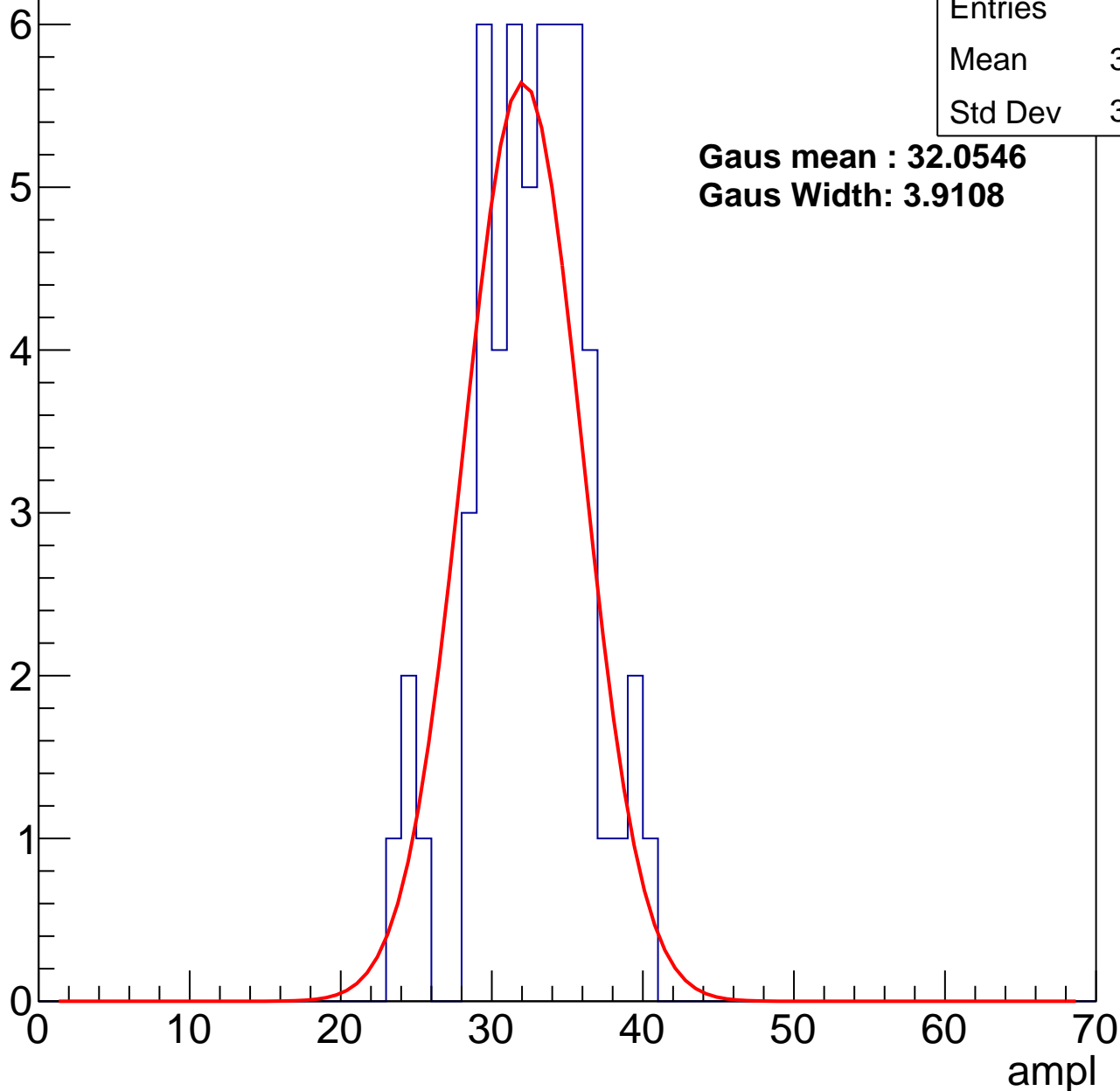
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 32.16 |
| Std Dev | 3.697 |

Gaus mean : 32.0546

Gaus Width: 3.9108



B1L102S, U15-ch120, adc2

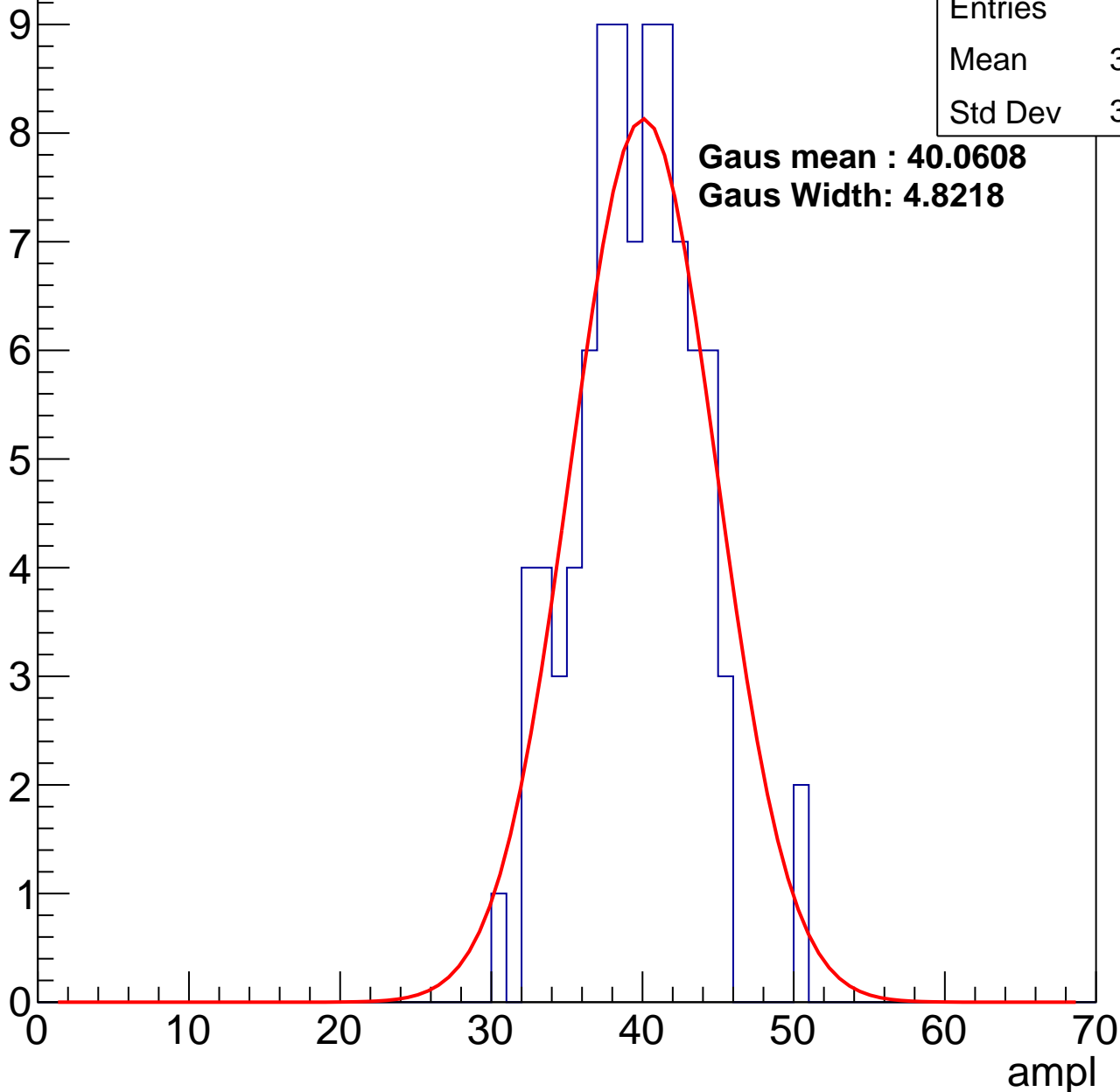
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 89 |
| Mean | 39.06 |
| Std Dev | 3.913 |

Gaus mean : 40.0608

Gaus Width: 4.8218

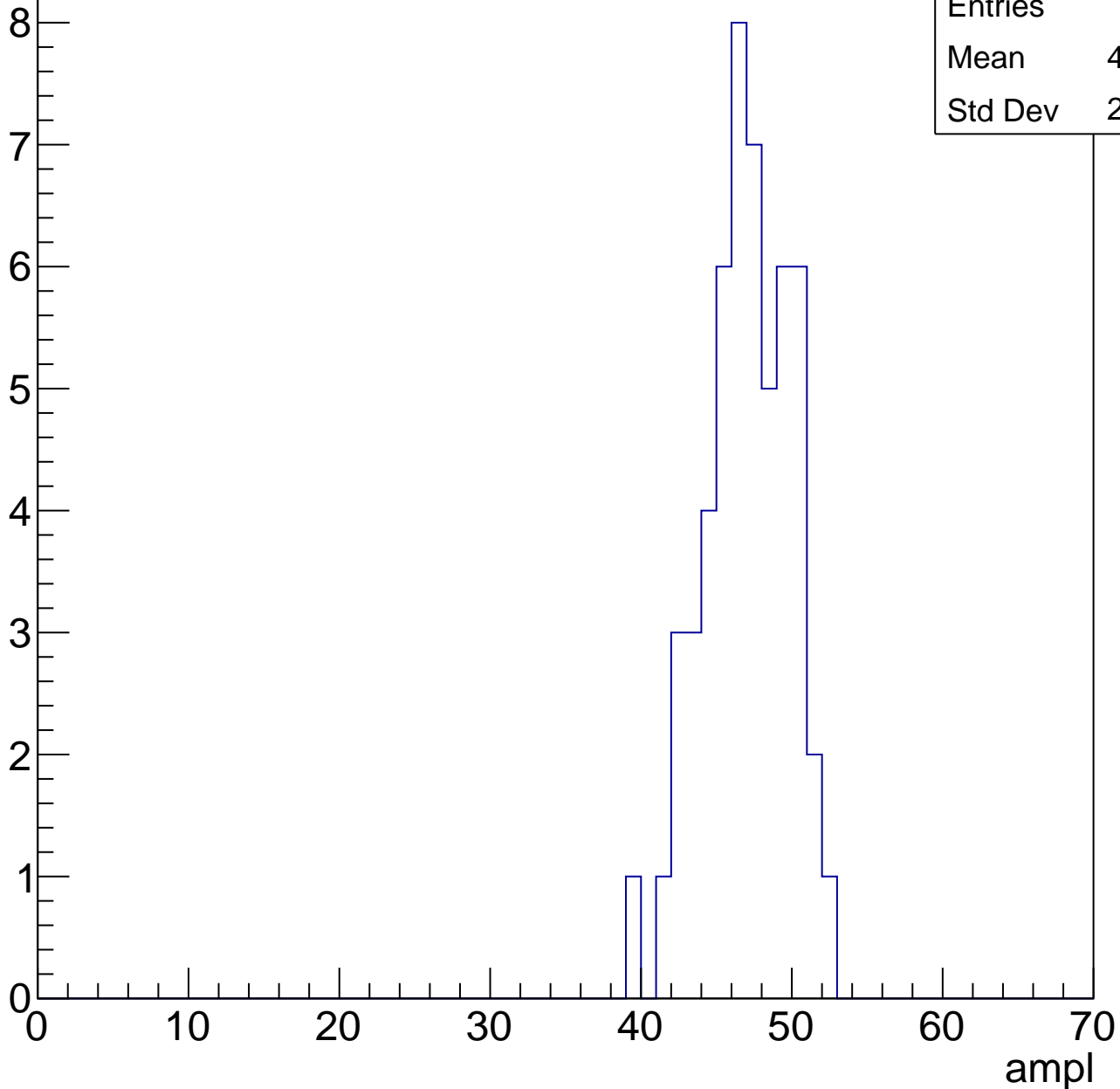


B1L102S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 46.53 |
| Std Dev | 2.812 |

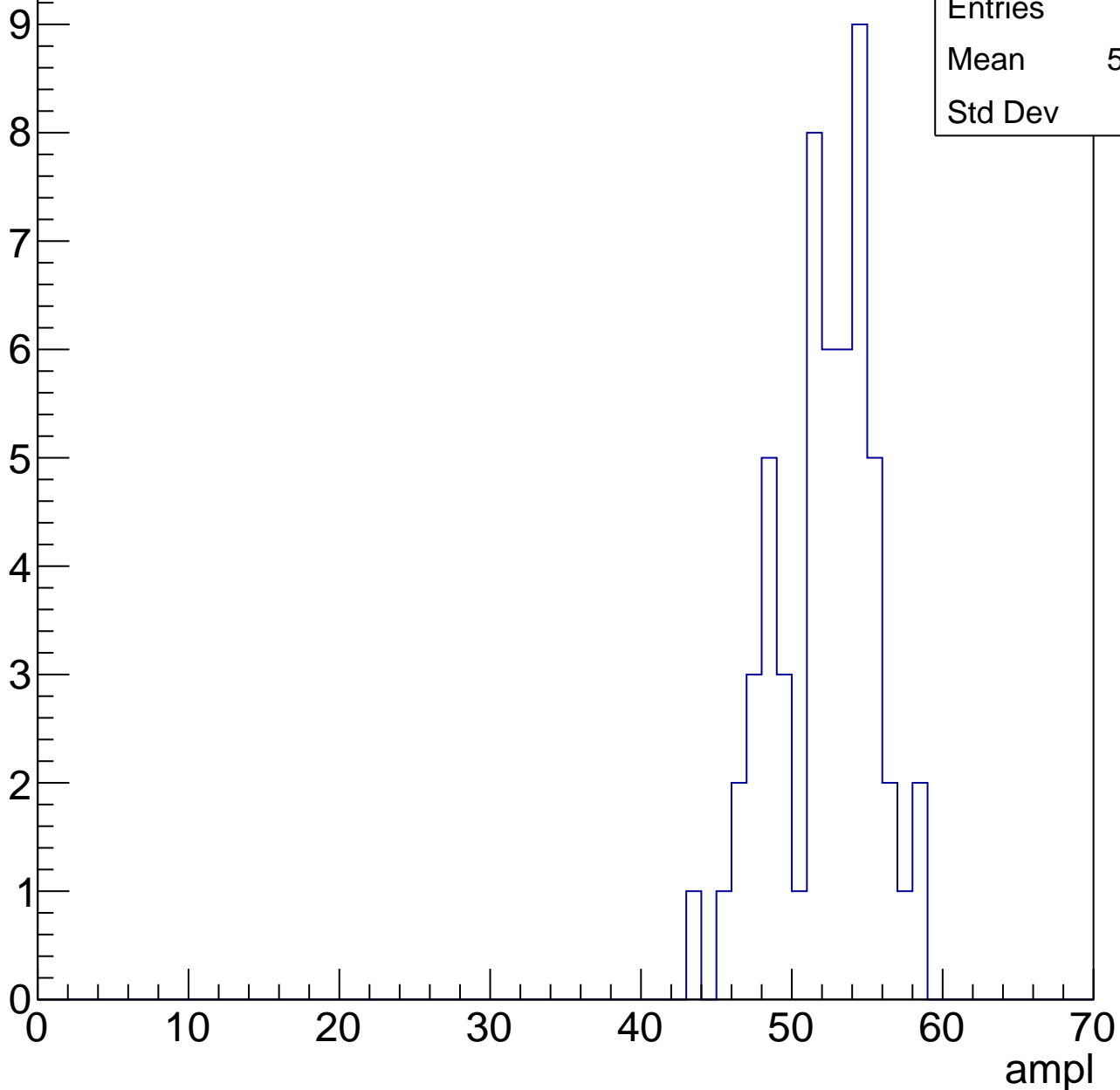


B1L102S, U15-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 55 |
| Mean | 51.67 |
| Std Dev | 3.32 |

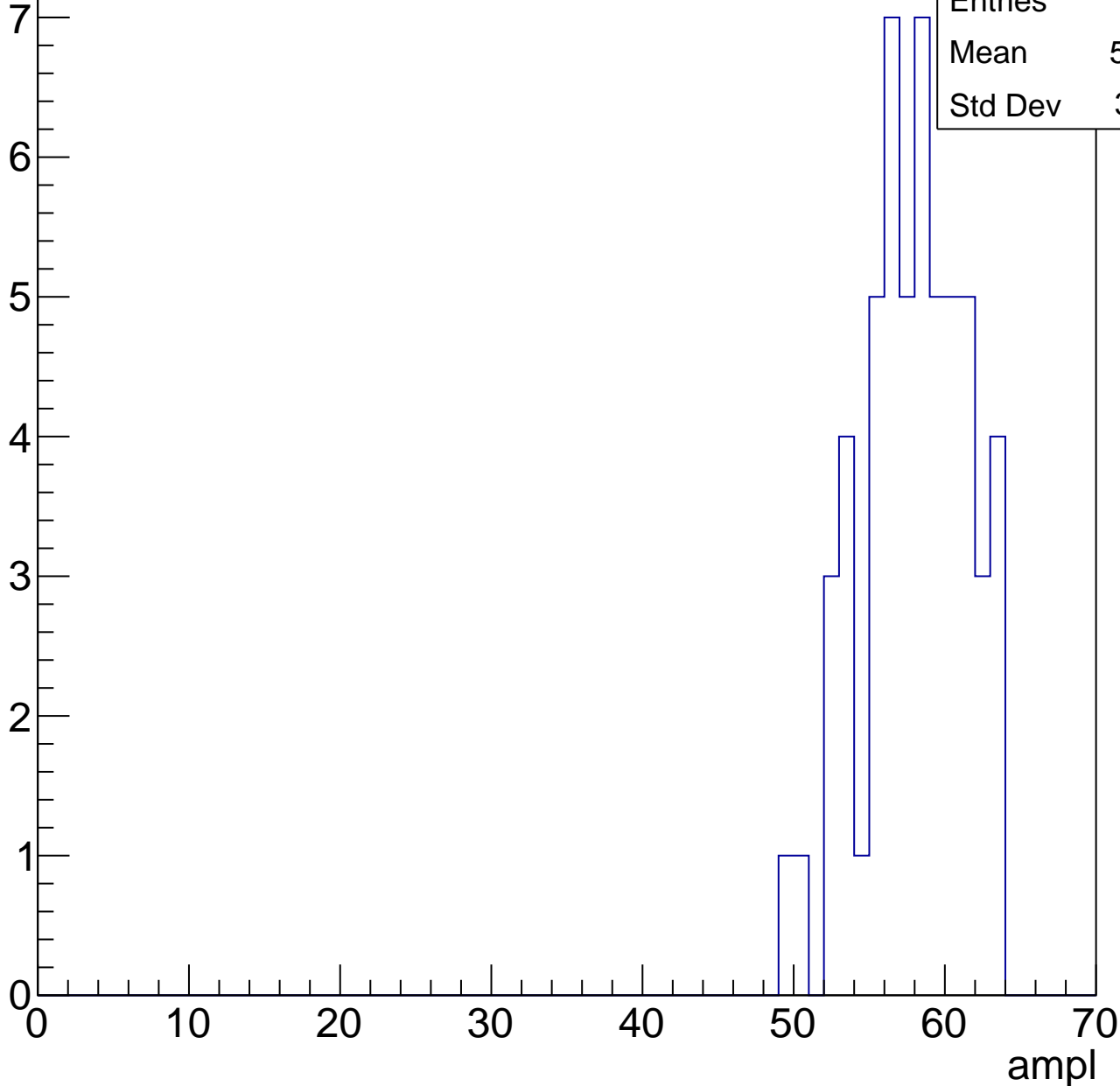


B1L102S, U15-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 56 |
| Mean | 57.45 |
| Std Dev | 3.401 |

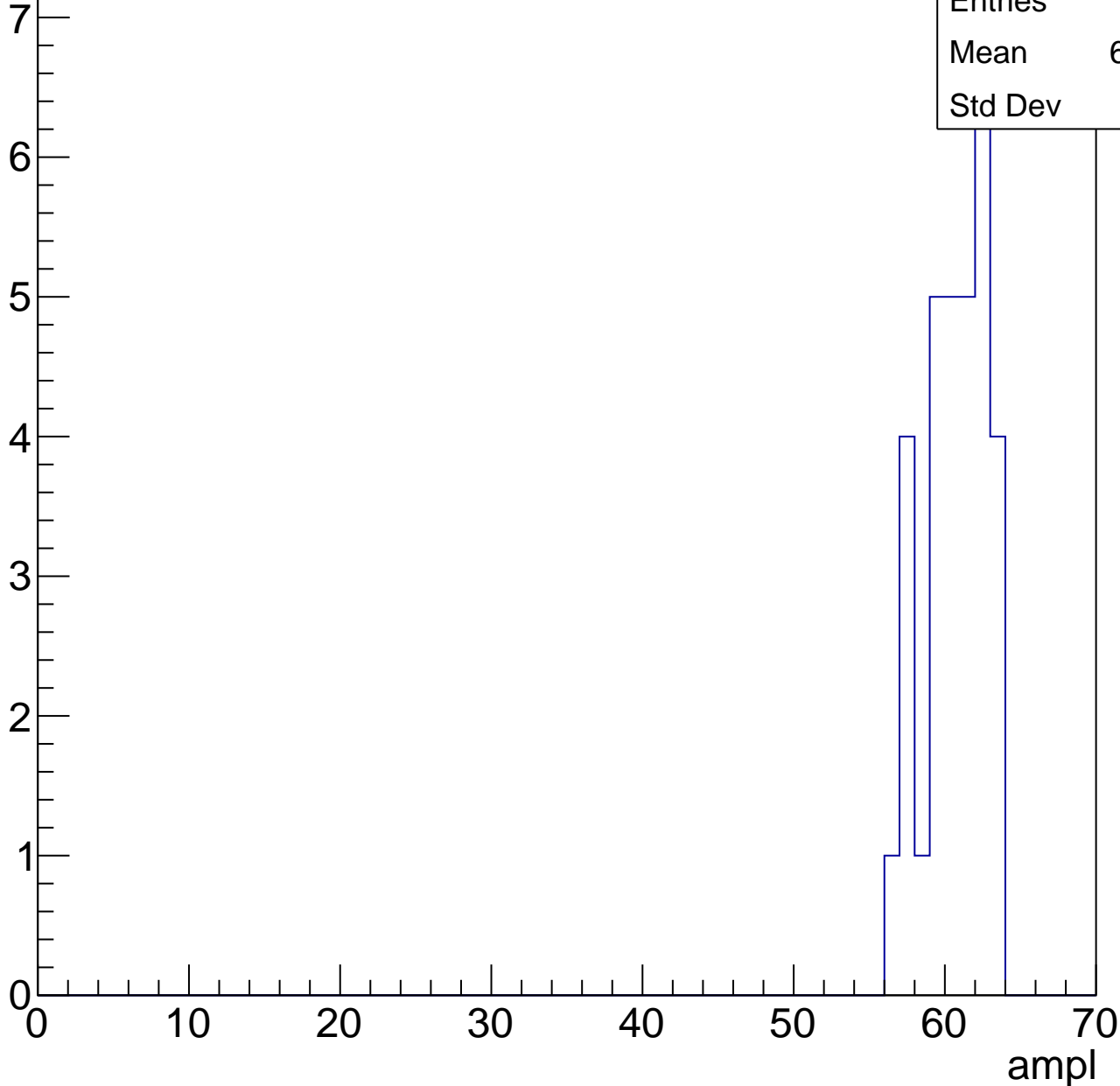


B1L102S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

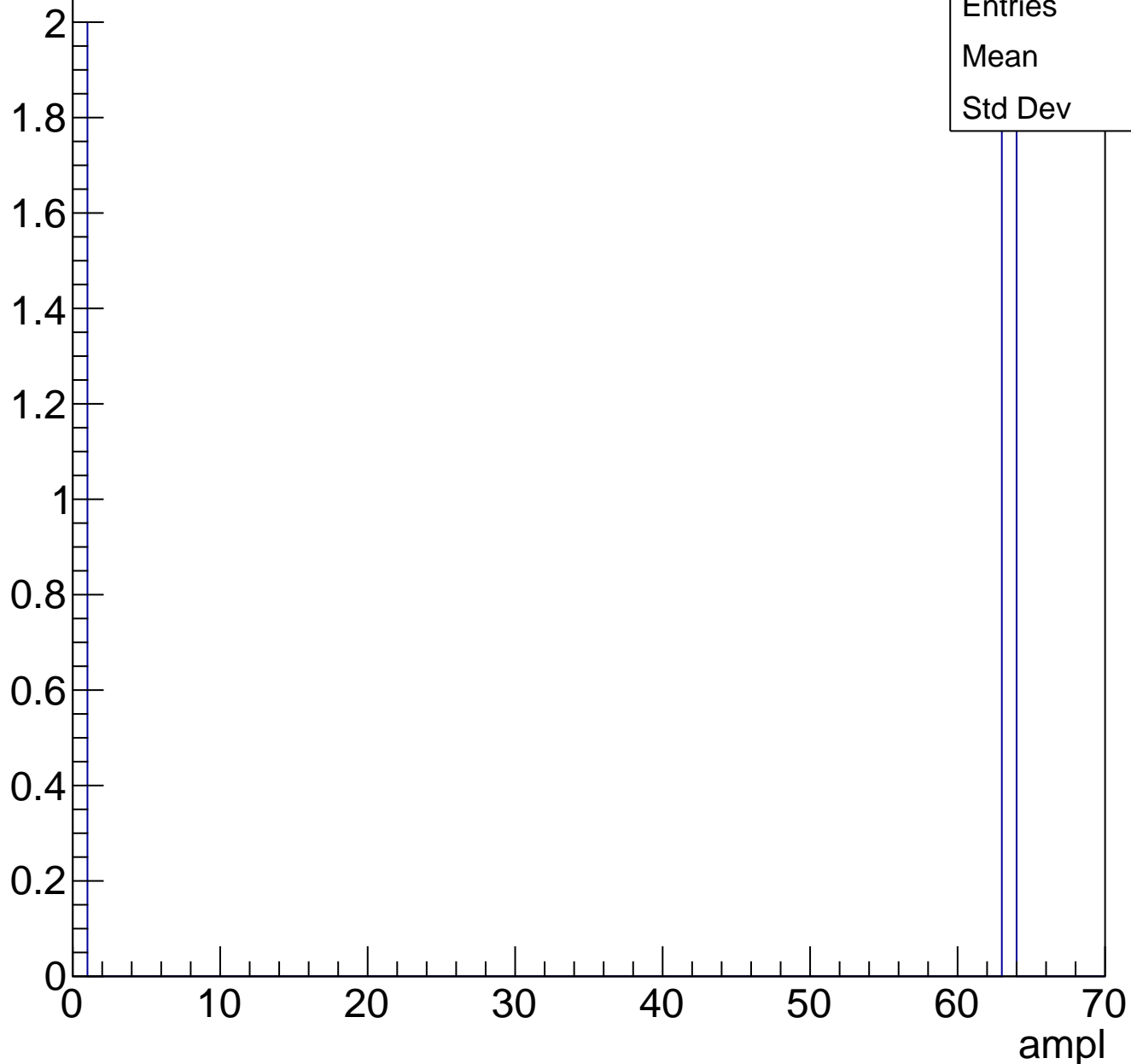
| | |
|---------|-------|
| Entries | 32 |
| Mean | 60.25 |
| Std Dev | 2 |



B1L102S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch121, adc0

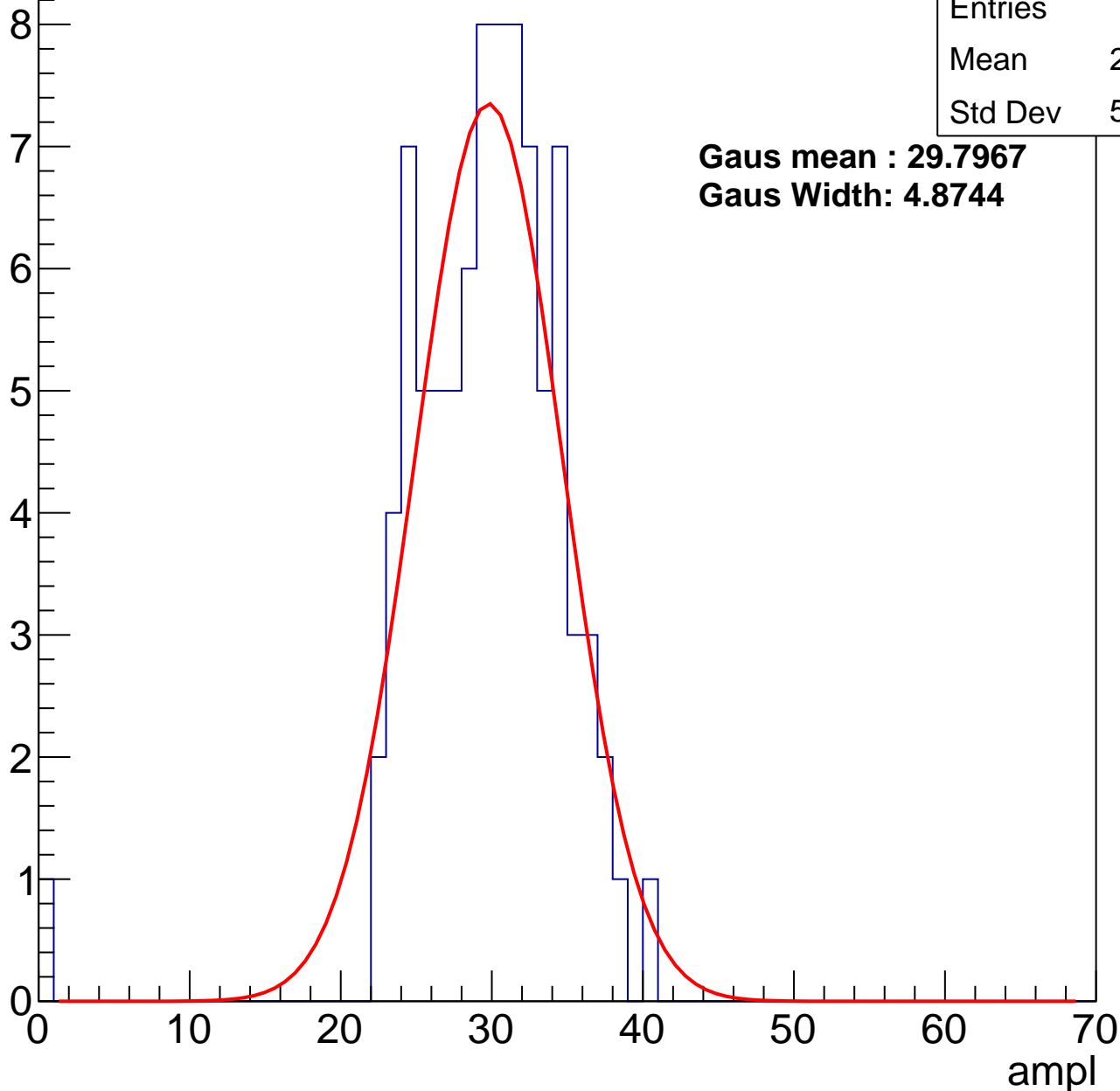
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 88 |
| Mean | 29.25 |
| Std Dev | 5.164 |

Gaus mean : 29.7967

Gaus Width: 4.8744



B1L102S, U15-ch121, adc1

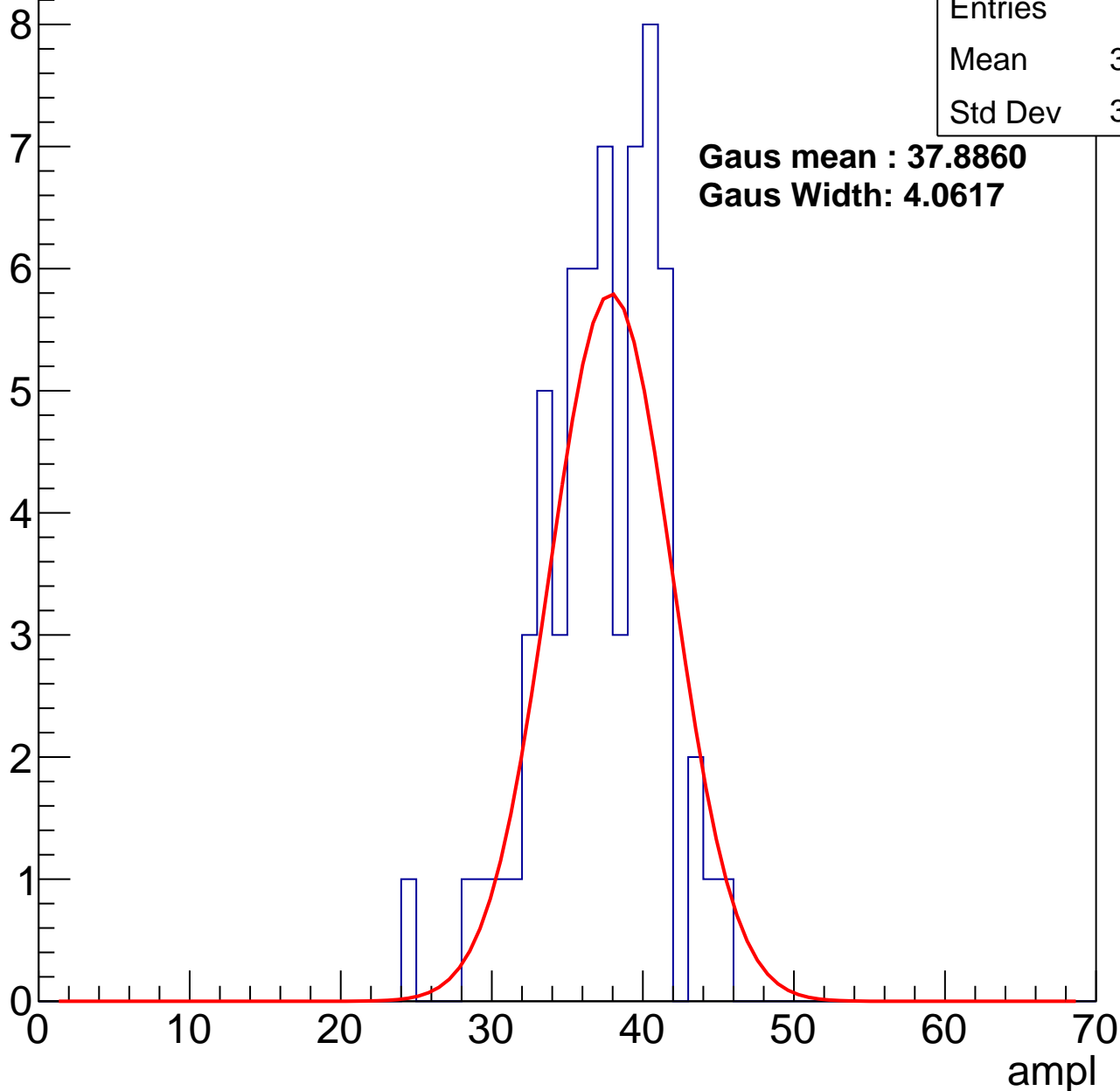
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 36.79 |
| Std Dev | 3.969 |

Gaus mean : 37.8860

Gaus Width: 4.0617



B1L102S, U15-ch121, adc2

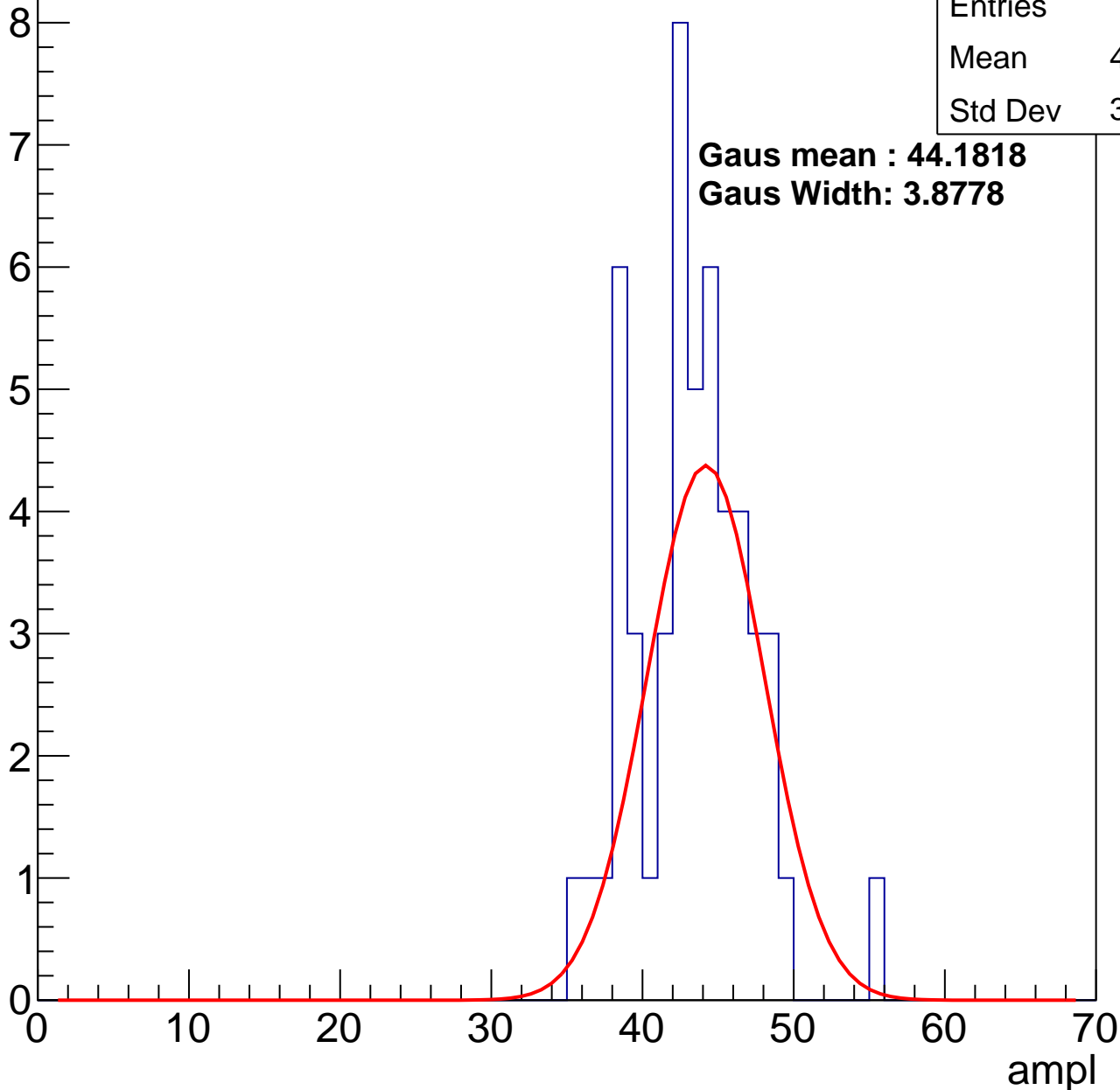
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 42.82 |
| Std Dev | 3.797 |

Gaus mean : 44.1818

Gaus Width: 3.8778

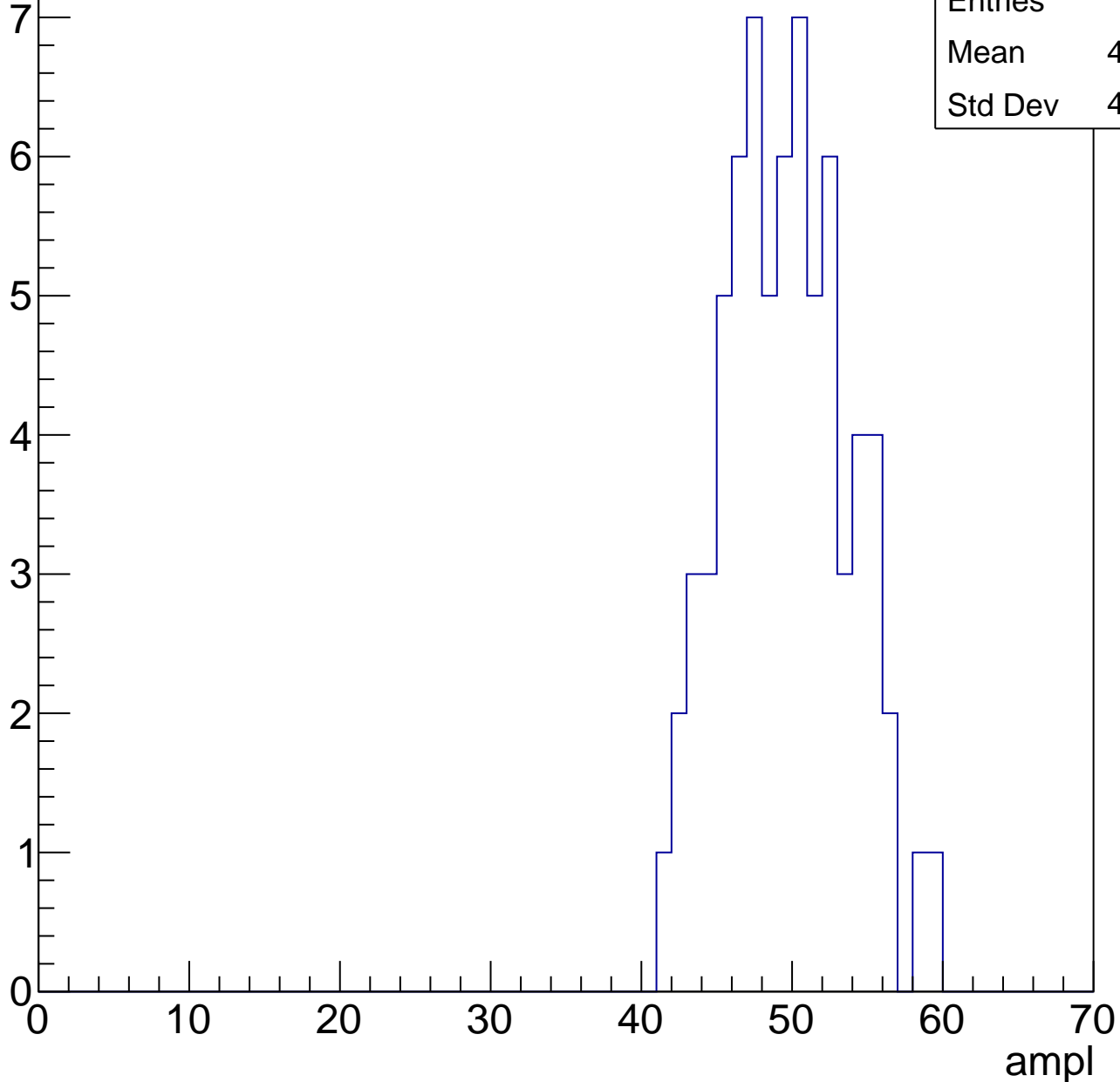


B1L102S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 49.17 |
| Std Dev | 4.045 |

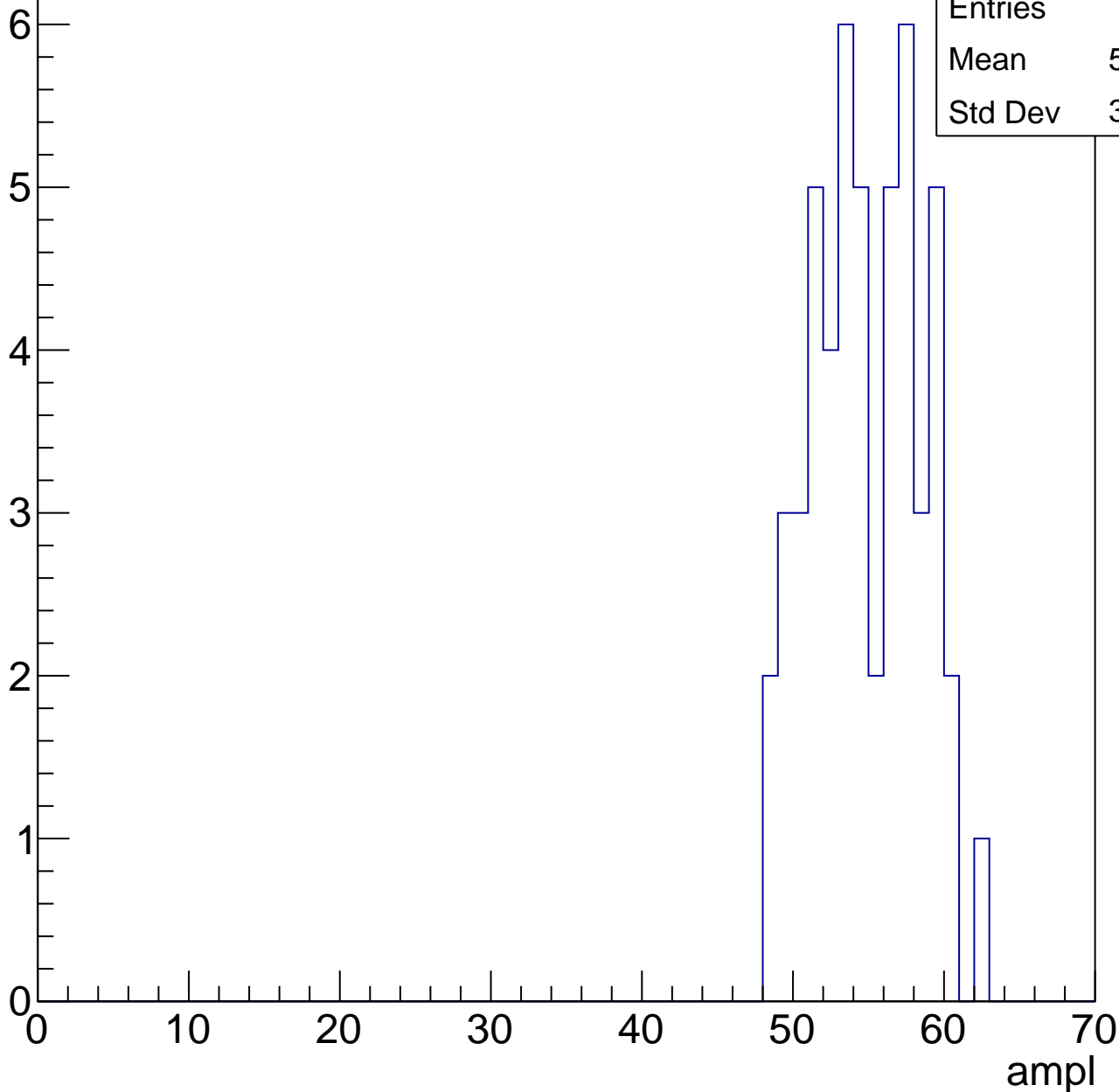


B1L102S, U15-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 52 |
| Mean | 54.37 |
| Std Dev | 3.508 |

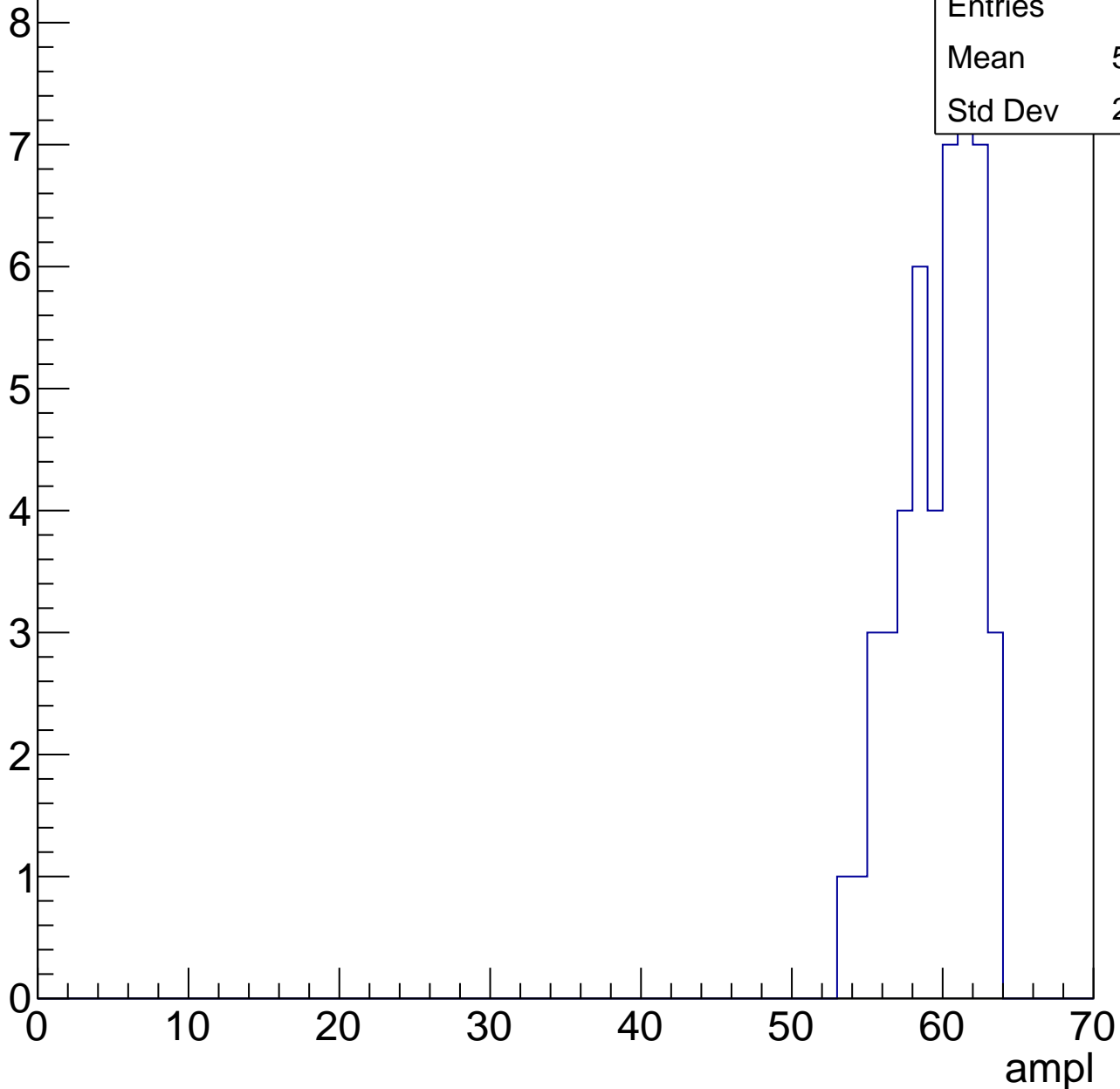


B1L102S, U15-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

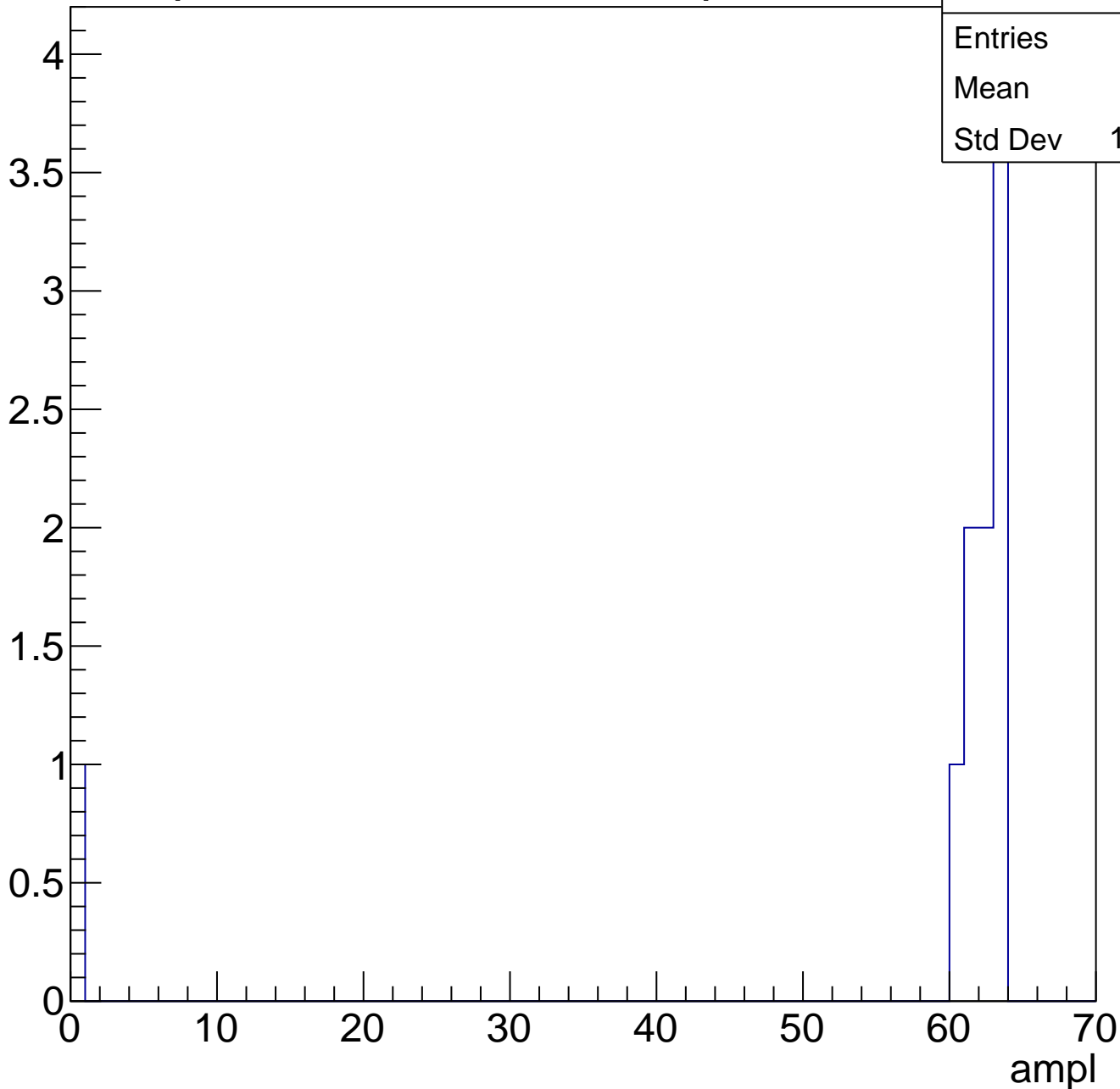
| | |
|---------|-------|
| Entries | 47 |
| Mean | 59.21 |
| Std Dev | 2.551 |



B1L102S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

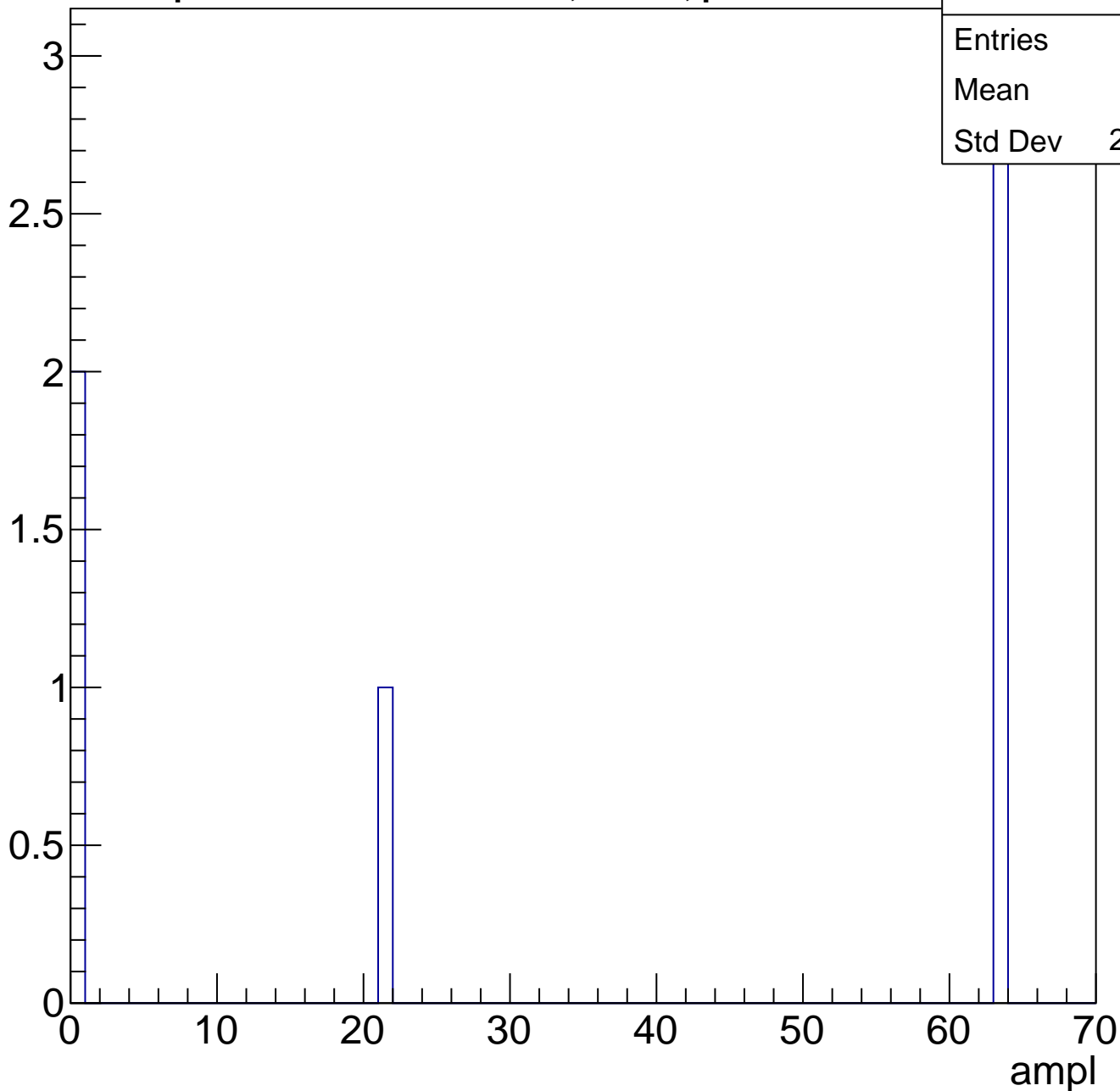
Entry



B1L102S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch122, adc0

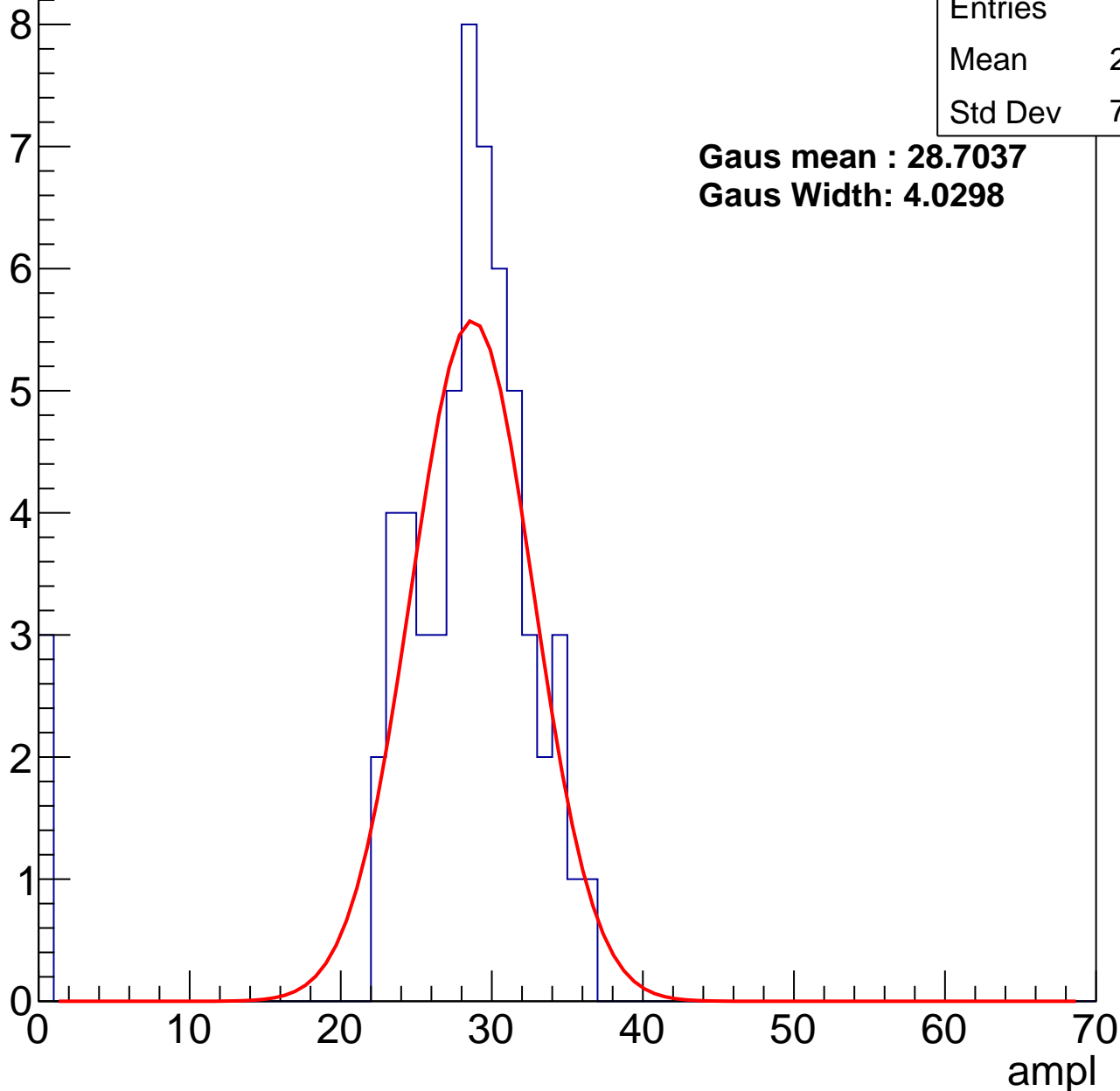
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 26.95 |
| Std Dev | 7.025 |

Gaus mean : 28.7037

Gaus Width: 4.0298



B1L102S, U15-ch122, adc1

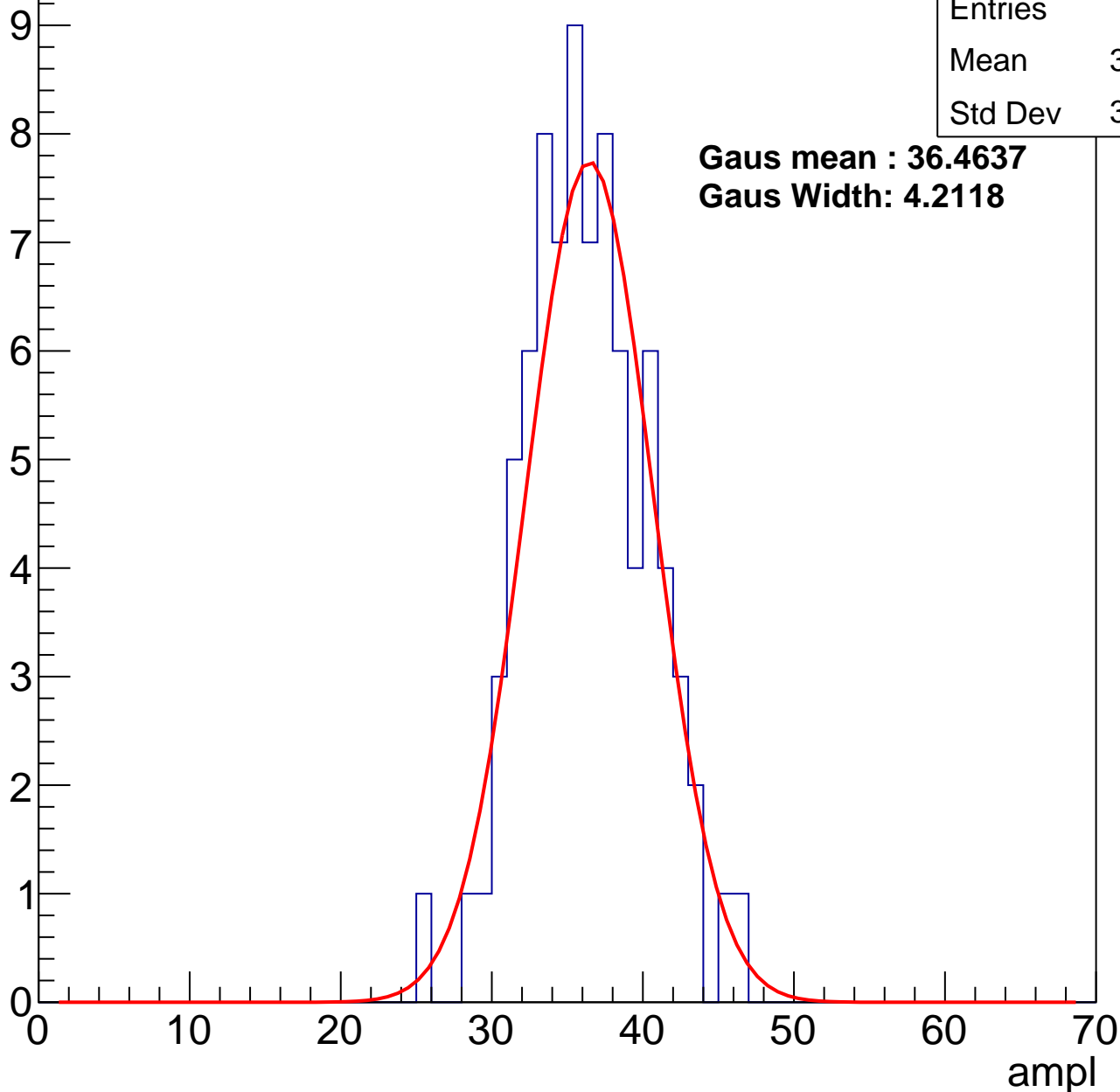
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 83 |
| Mean | 35.84 |
| Std Dev | 3.989 |

Gaus mean : 36.4637

Gaus Width: 4.2118



B1L102S, U15-ch122, adc2

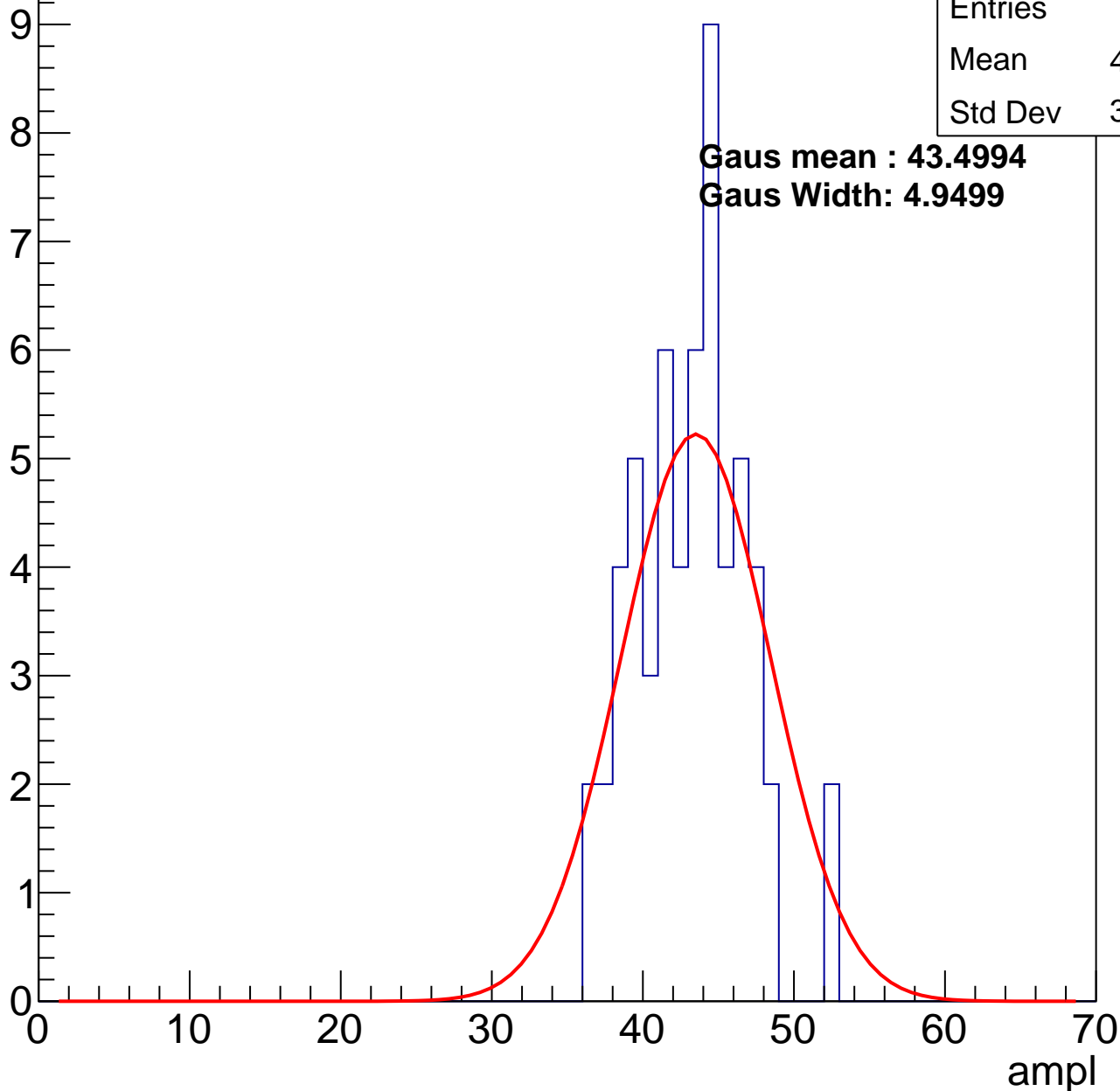
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 58 |
| Mean | 42.74 |
| Std Dev | 3.589 |

Gaus mean : 43.4994

Gaus Width: 4.9499

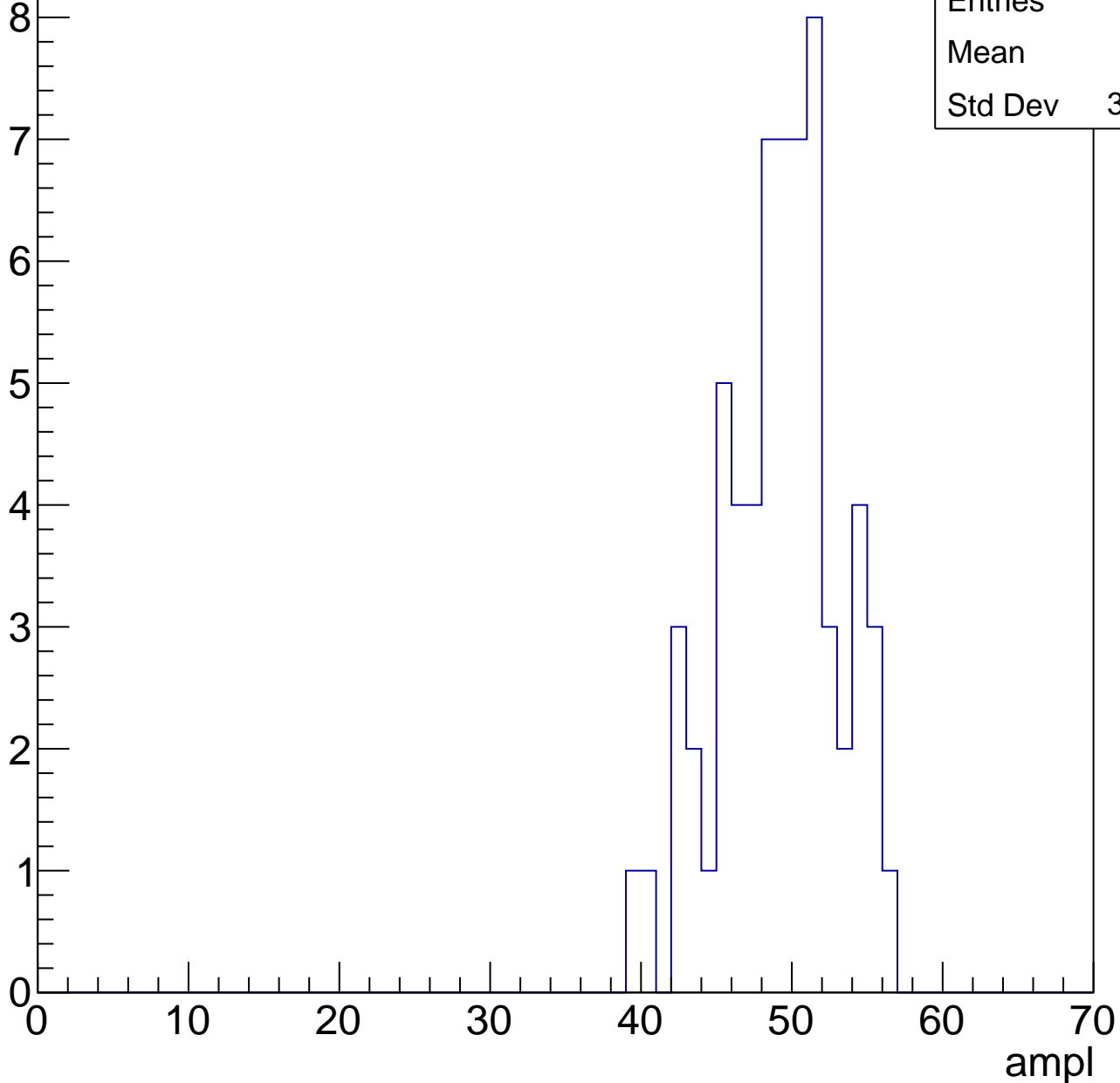


B1L102S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 63 |
| Mean | 48.7 |
| Std Dev | 3.807 |

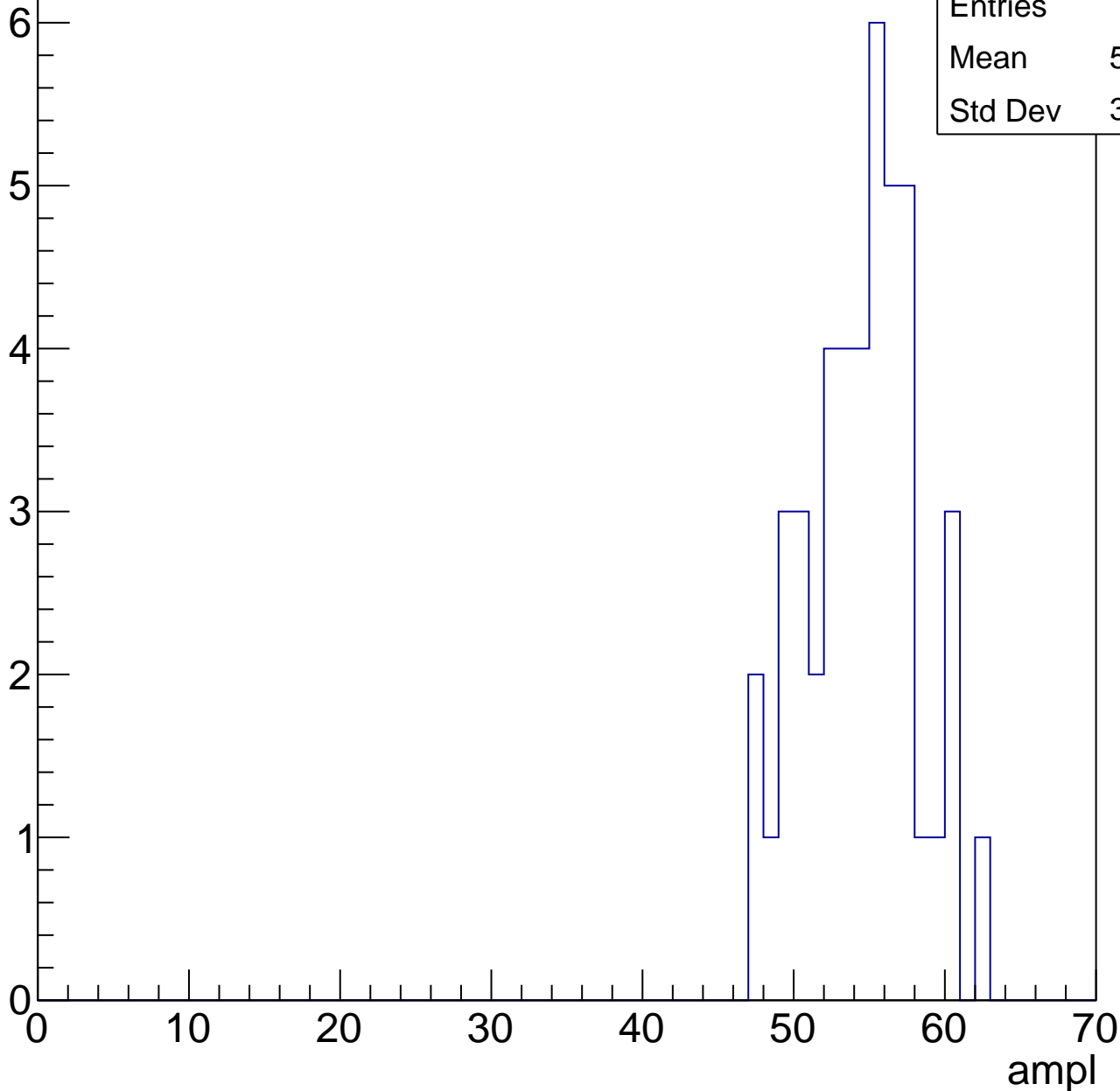


B1L102S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 45 |
| Mean | 54.02 |
| Std Dev | 3.587 |

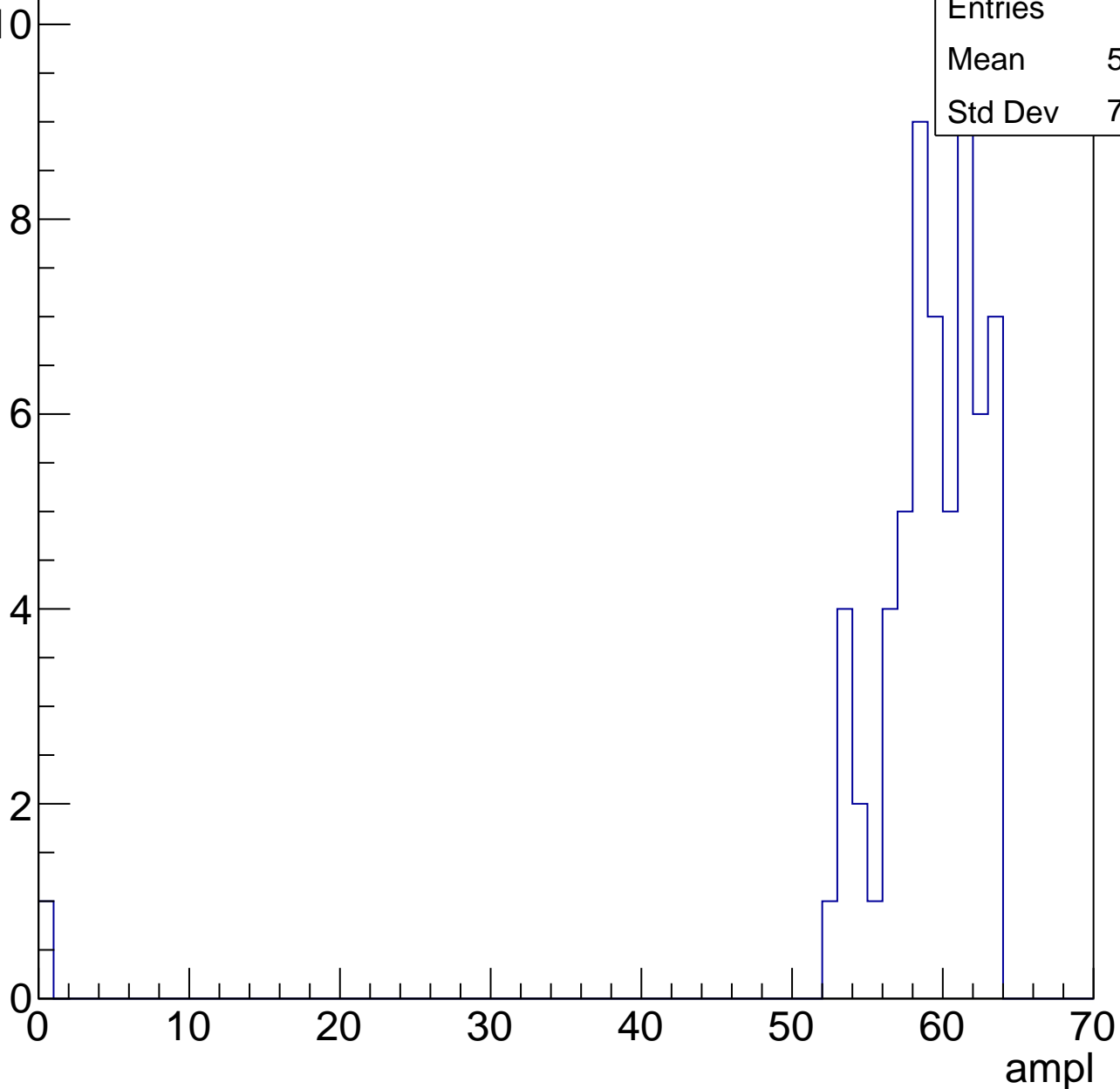


B1L102S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

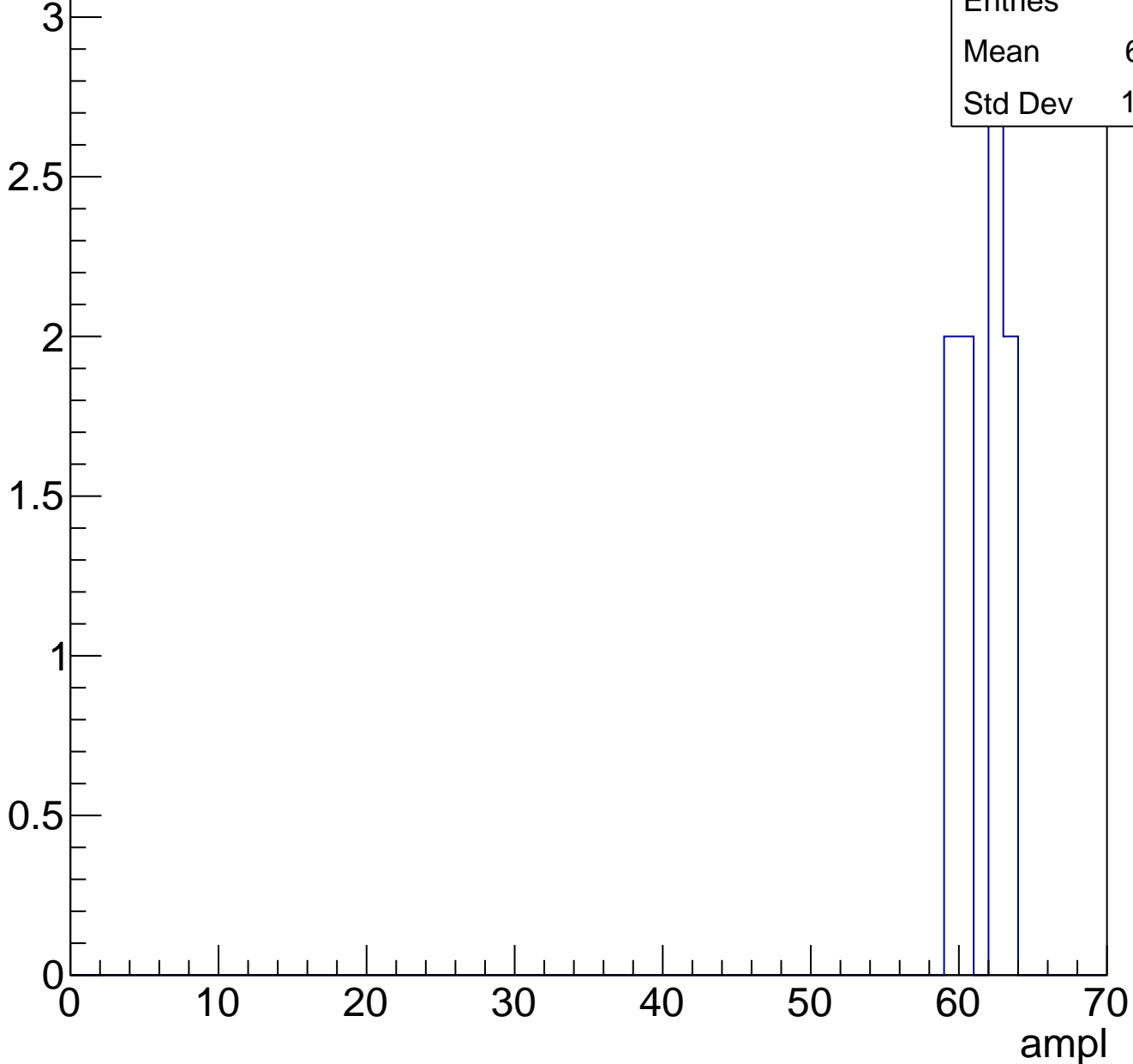
| | |
|---------|-------|
| Entries | 62 |
| Mean | 57.97 |
| Std Dev | 7.982 |



B1L102S, U15-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch123, adc0

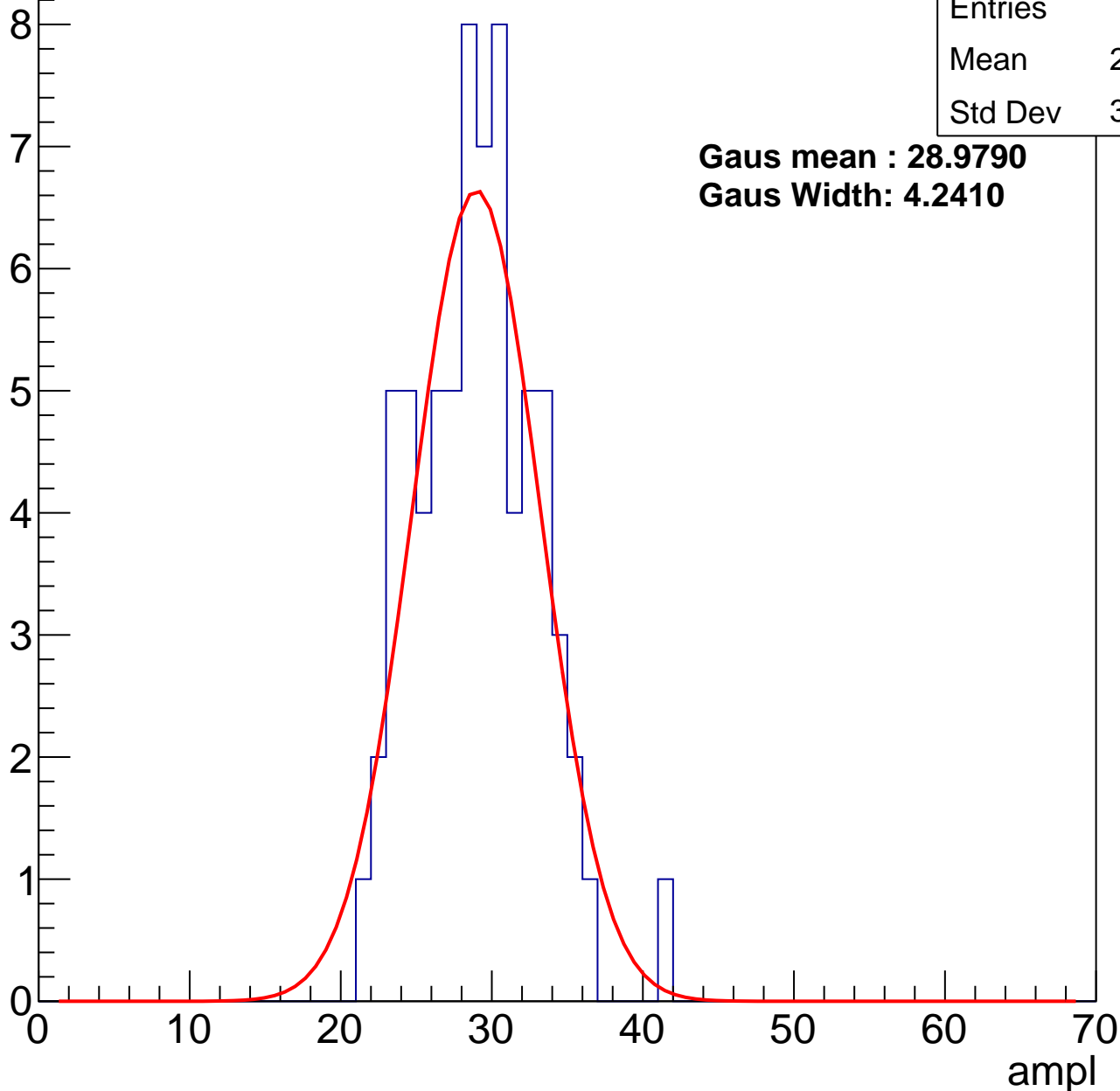
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 71 |
| Mean | 28.59 |
| Std Dev | 3.902 |

Gaus mean : 28.9790

Gaus Width: 4.2410



B1L102S, U15-ch123, adc1

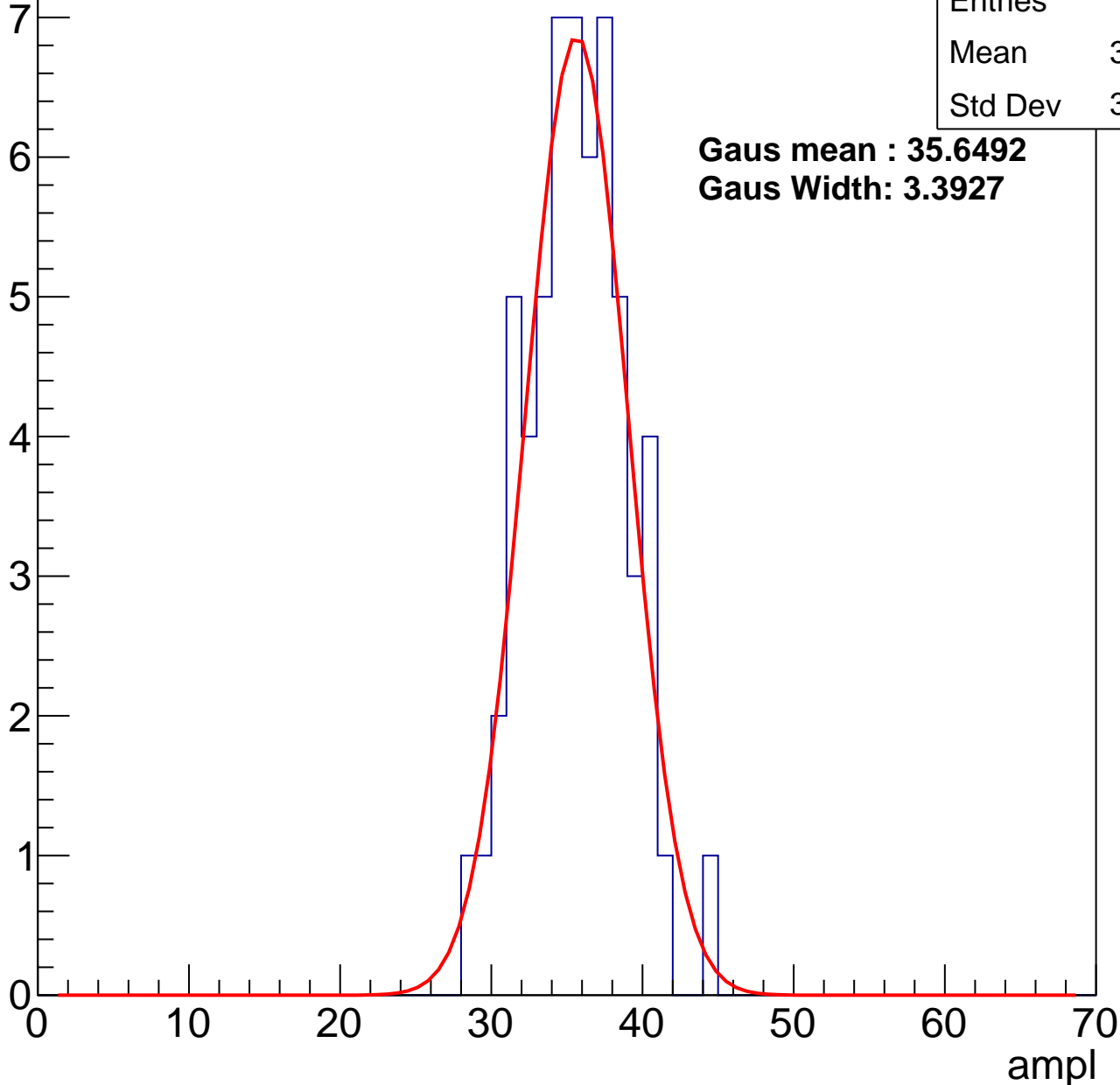
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 59 |
| Mean | 35.17 |
| Std Dev | 3.242 |

Gaus mean : 35.6492

Gaus Width: 3.3927



B1L102S, U15-ch123, adc2

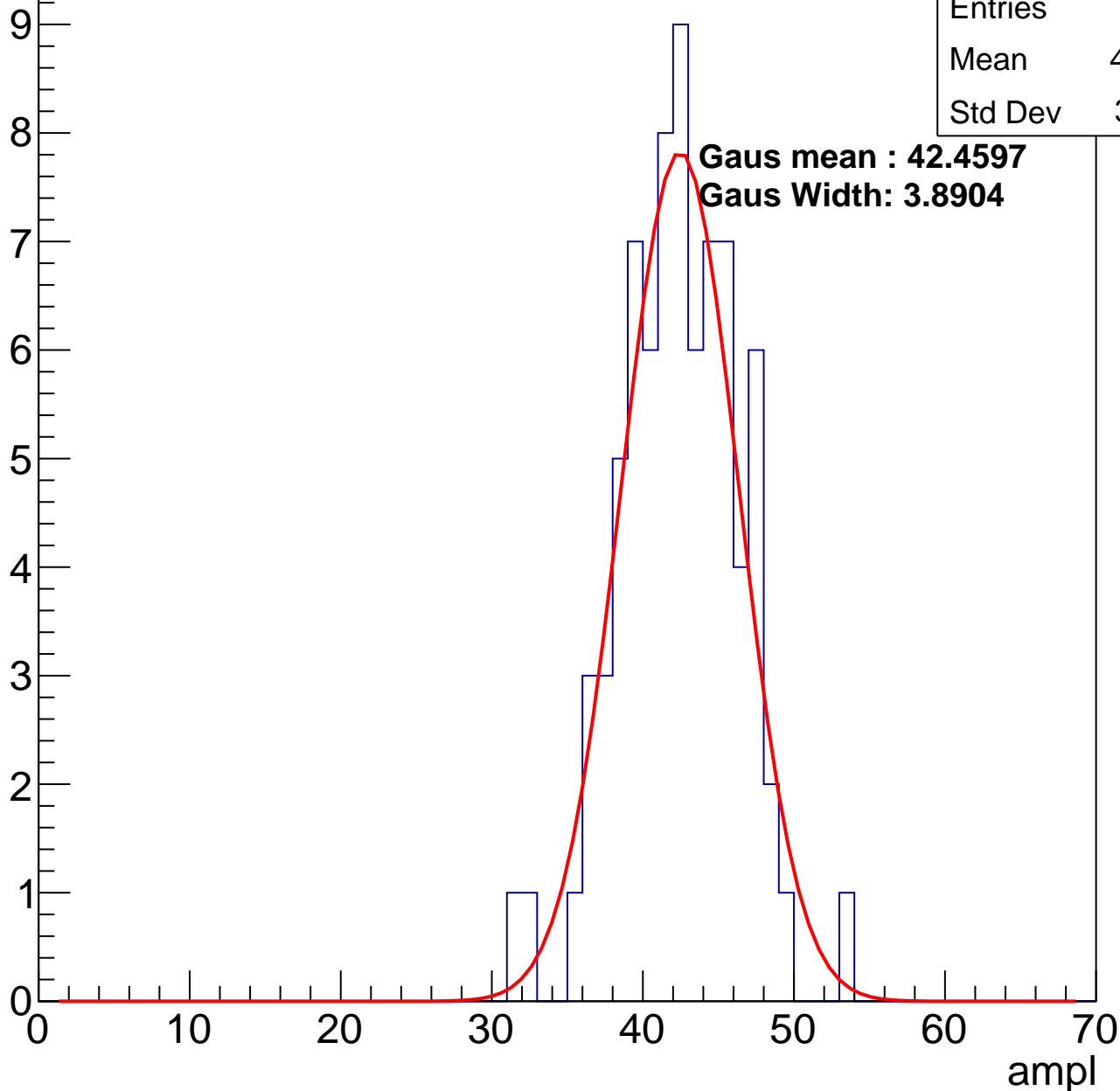
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 41.94 |
| Std Dev | 3.891 |

Gaus mean : 42.4597

Gaus Width: 3.8904

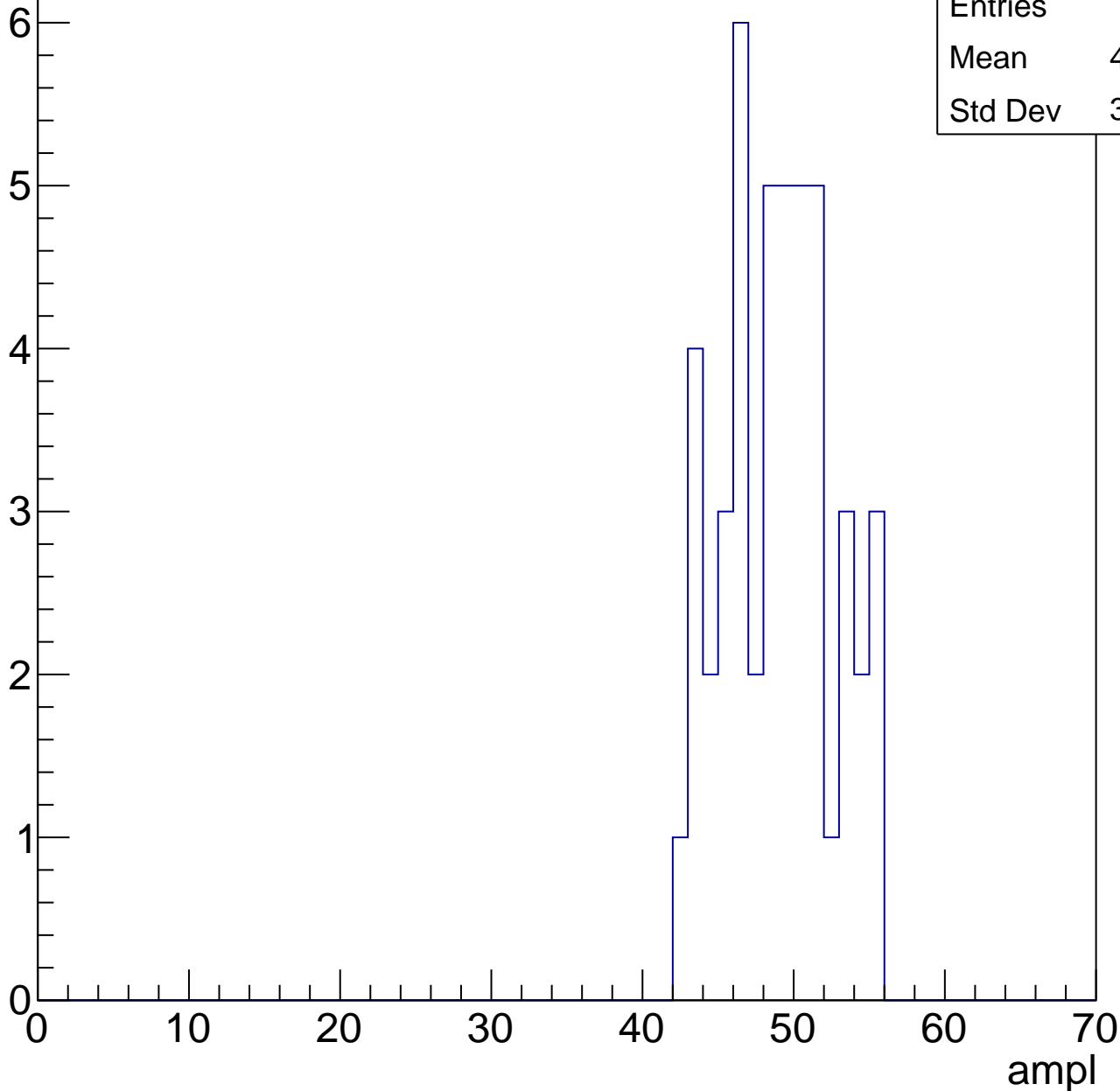


B1L102S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 47 |
| Mean | 48.53 |
| Std Dev | 3.536 |

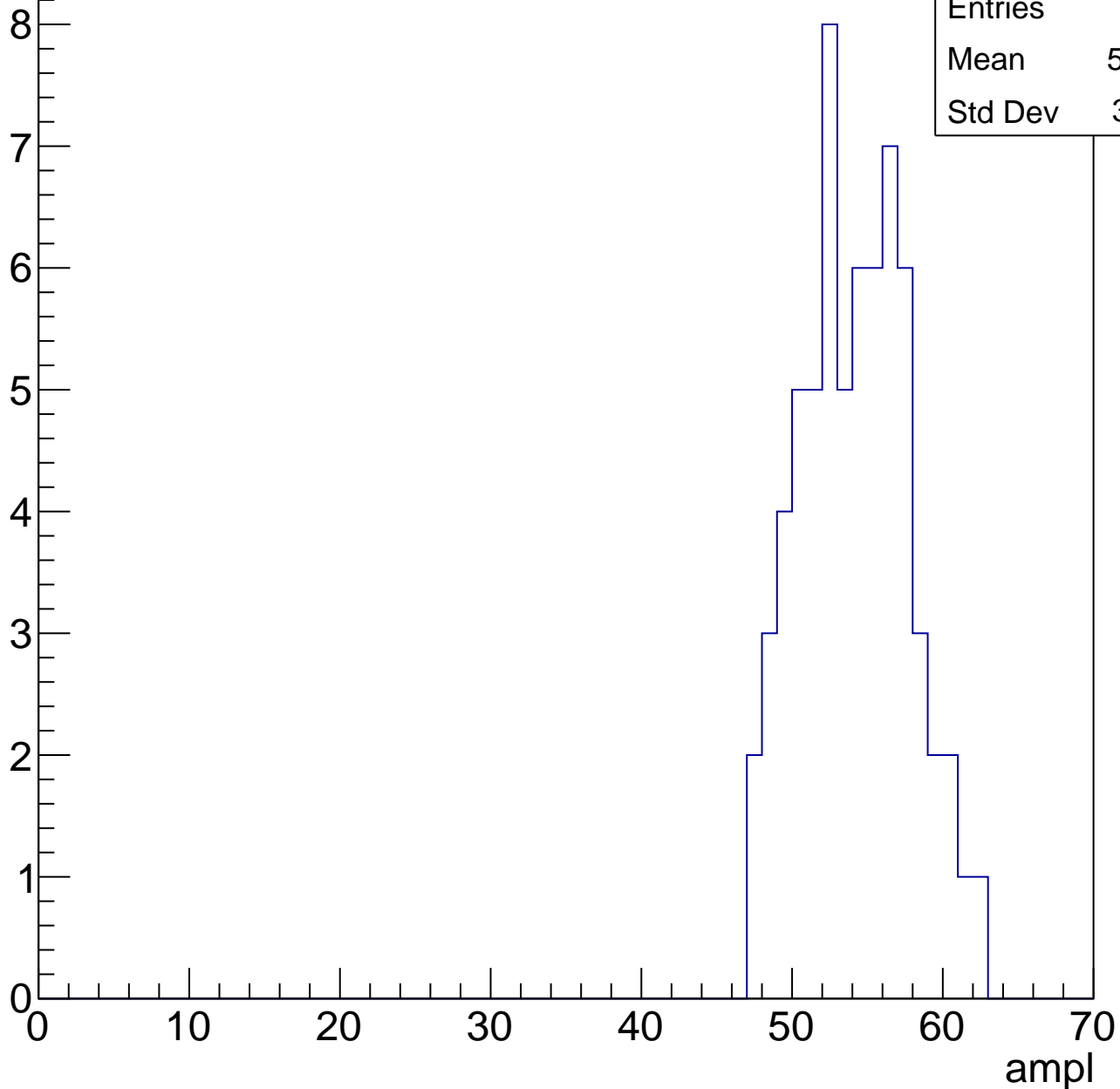


B1L102S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 53.68 |
| Std Dev | 3.551 |

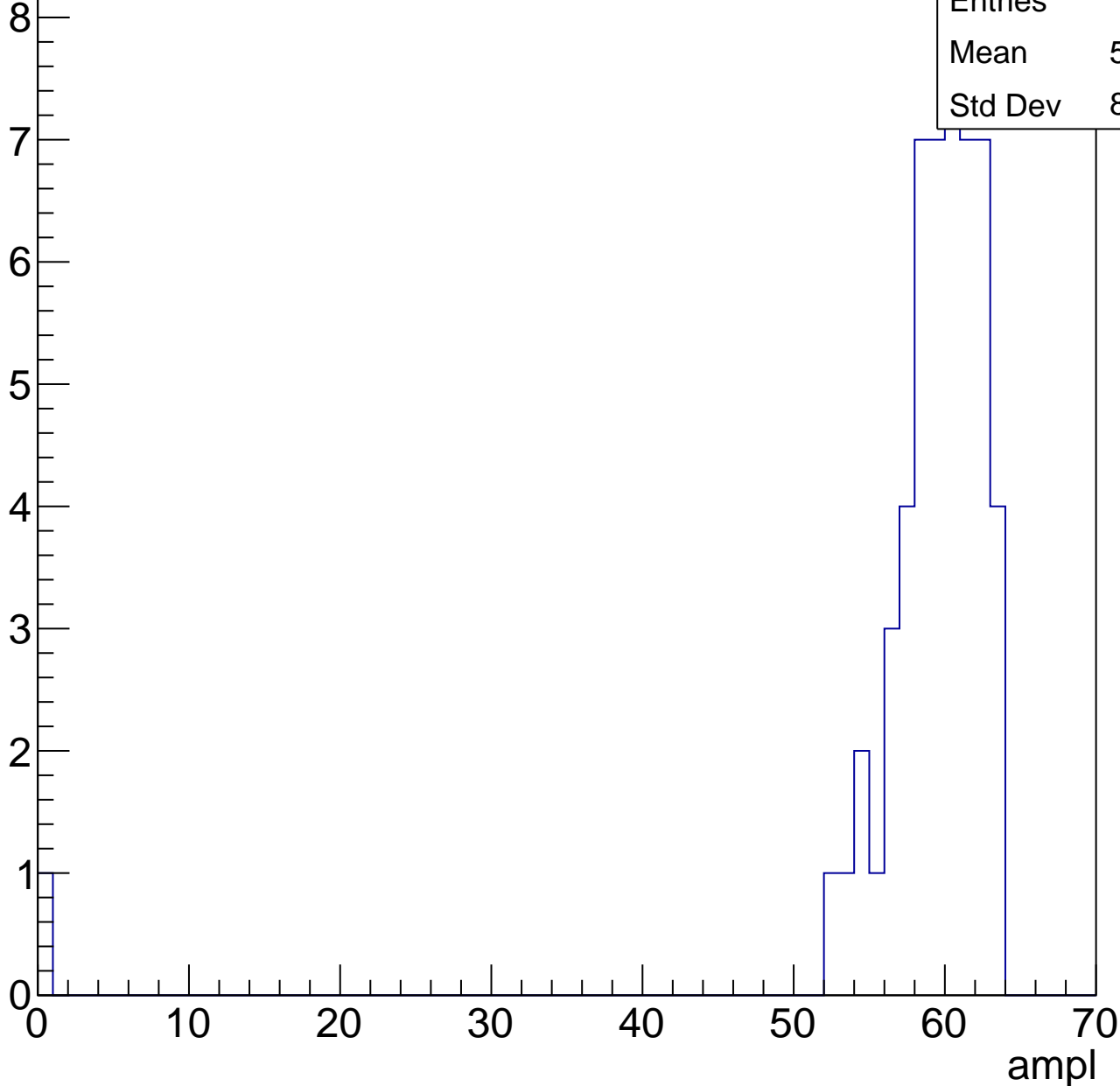


B1L102S, U15-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 53 |
| Mean | 58.04 |
| Std Dev | 8.463 |

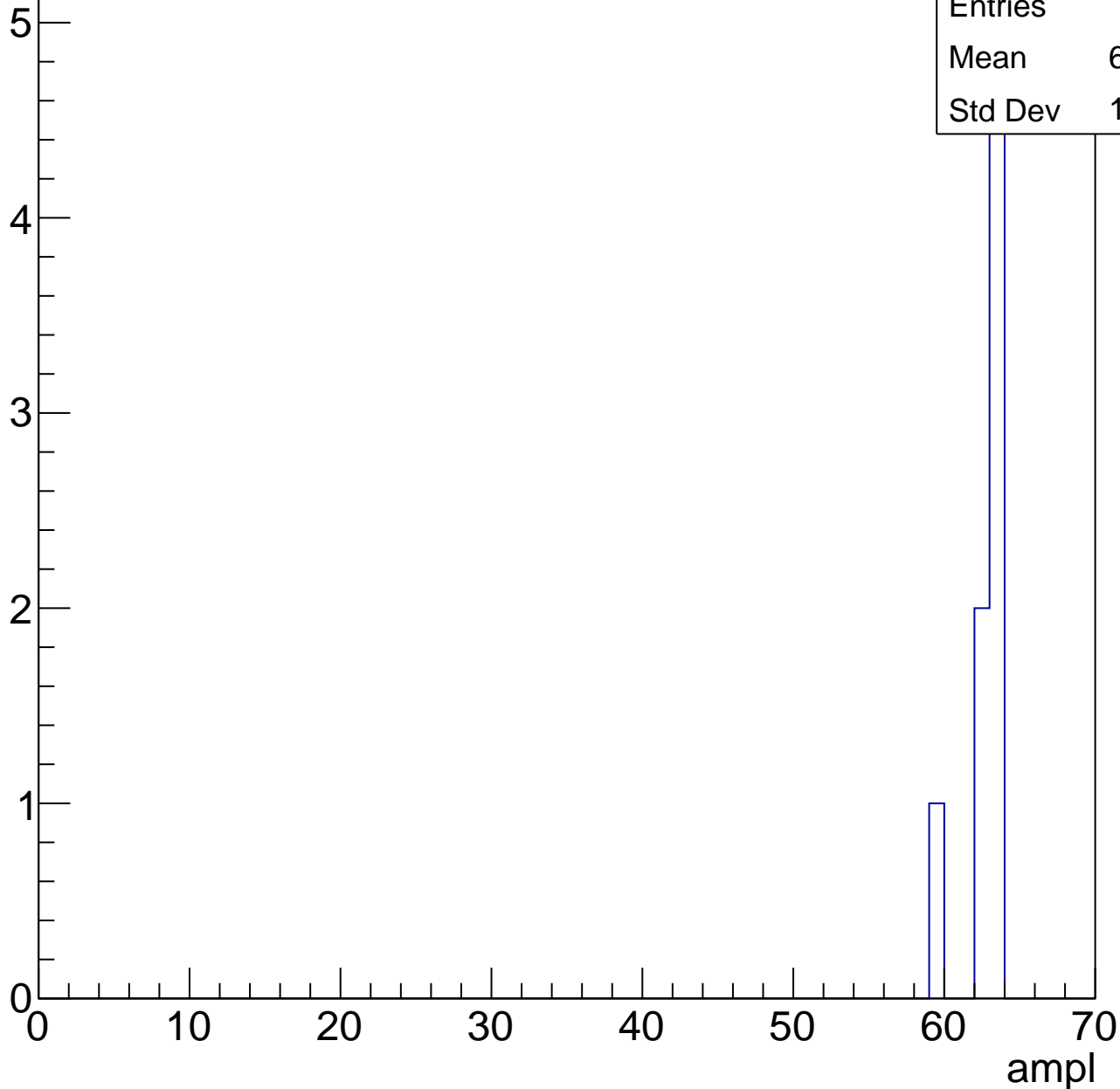


B1L102S, U15-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 8 |
| Mean | 62.25 |
| Std Dev | 1.299 |



B1L102S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

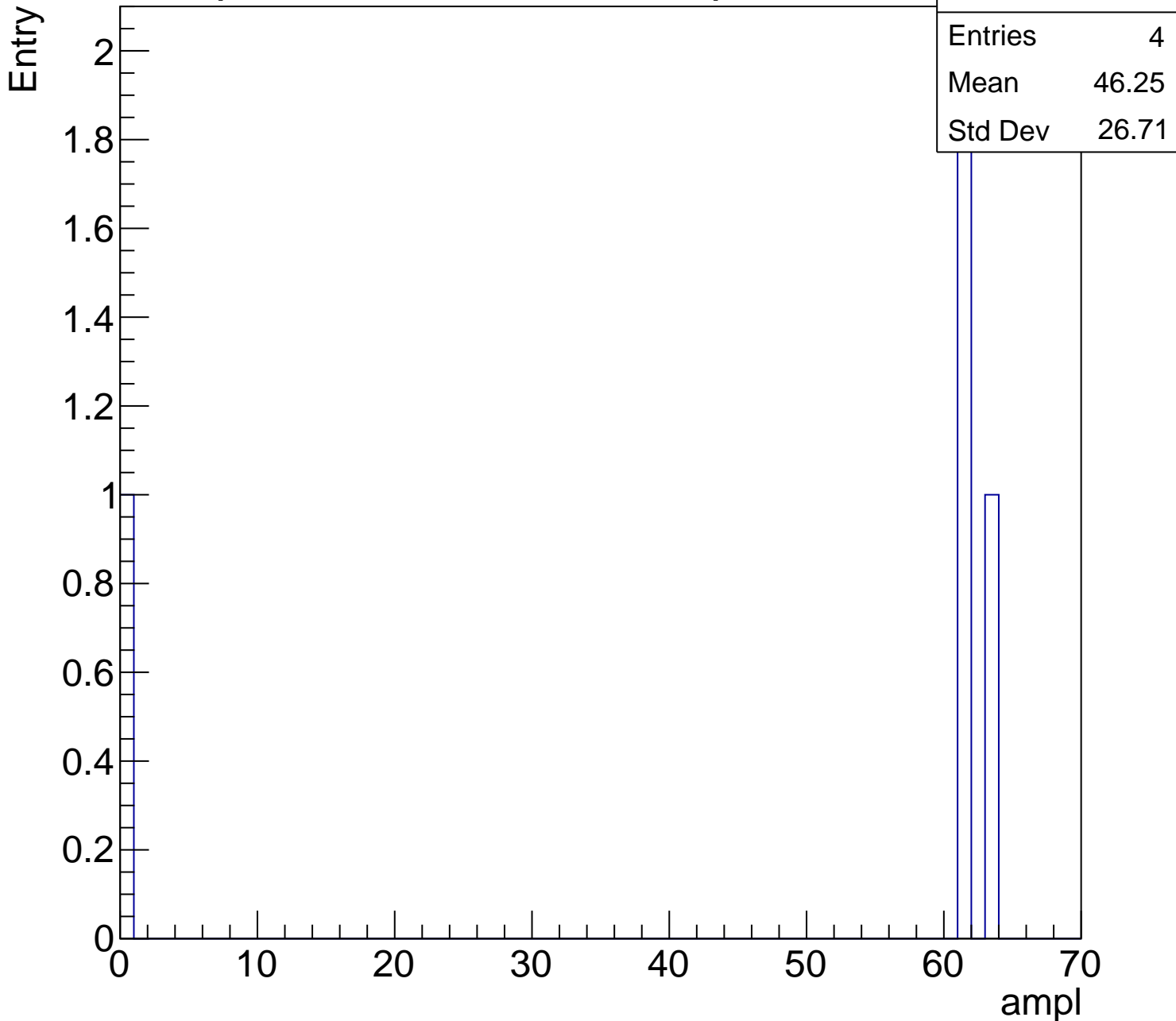
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

| | |
|---------|-------|
| Entries | 4 |
| Mean | 46.25 |
| Std Dev | 26.71 |

0 10 20 30 40 50 60 70

ampl



B1L102S, U15-ch124, adc0

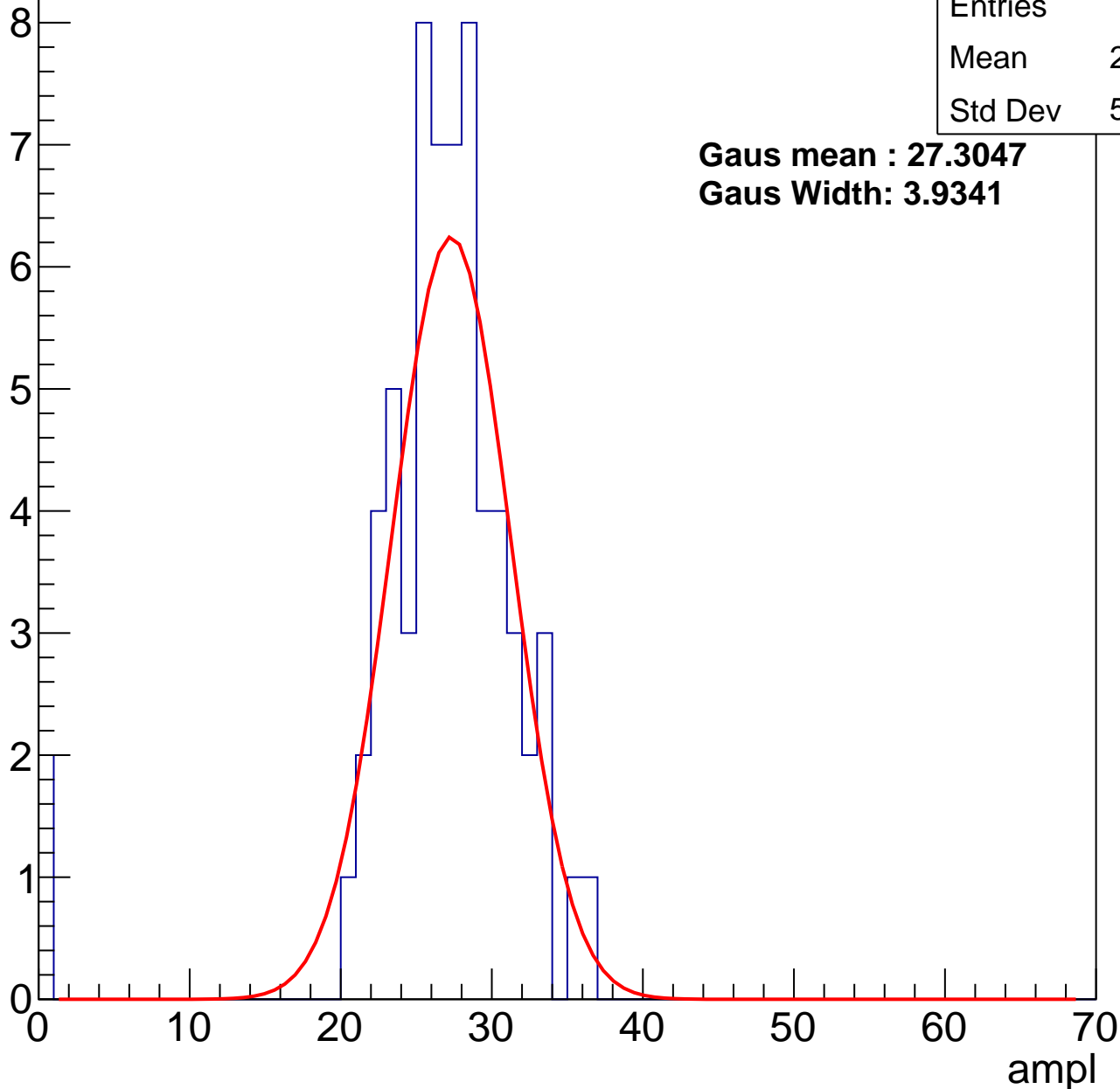
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 26.08 |
| Std Dev | 5.792 |

Gaus mean : 27.3047

Gaus Width: 3.9341



B1L102S, U15-ch124, adc1

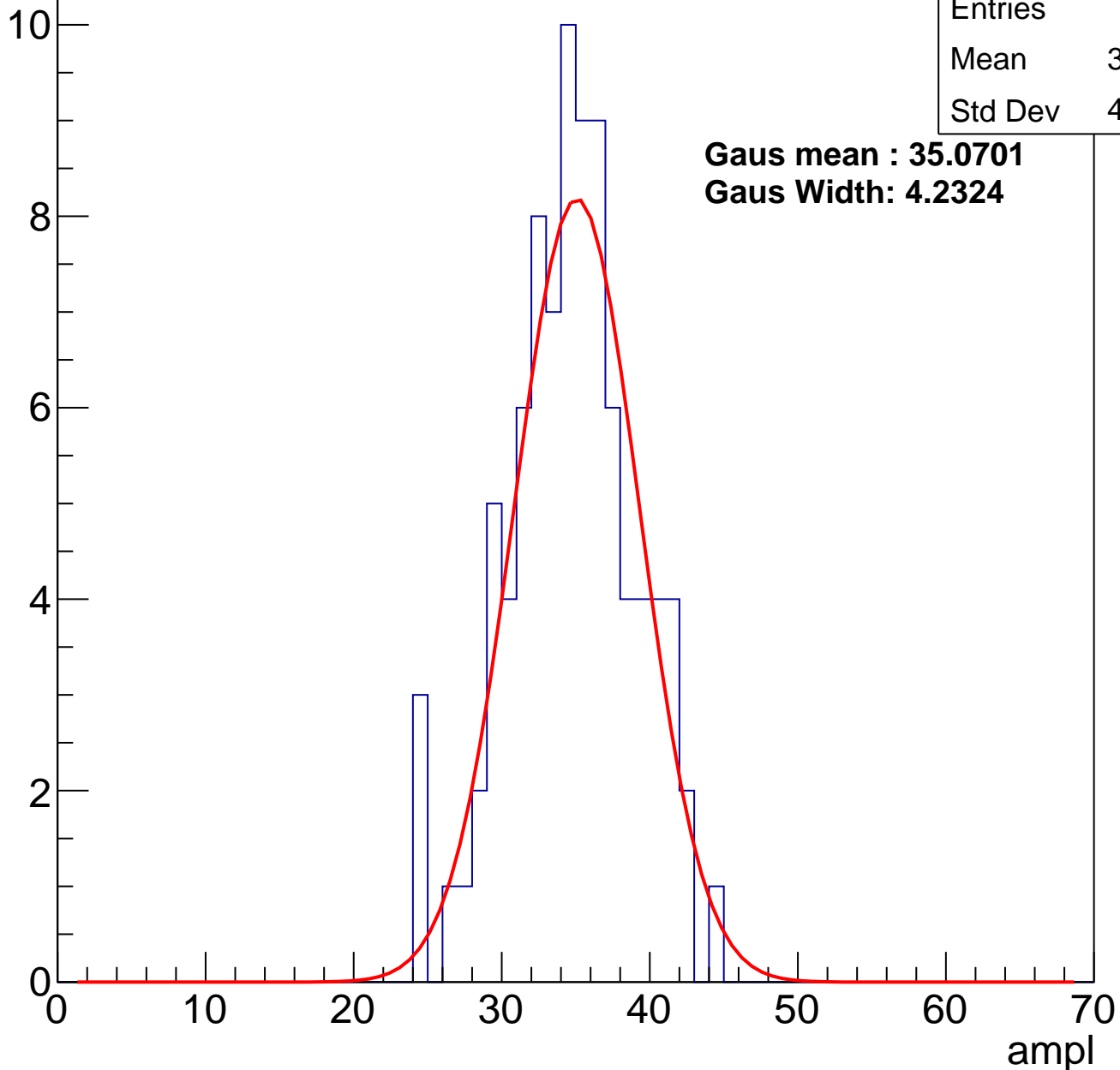
calib_packv5_042523_0143.root, FC#11, port A2

| | |
|---------|-------|
| Entries | 90 |
| Mean | 34.22 |
| Std Dev | 4.208 |

Gaus mean : 35.0701

Gaus Width: 4.2324

Entry



B1L102S, U15-ch124, adc2

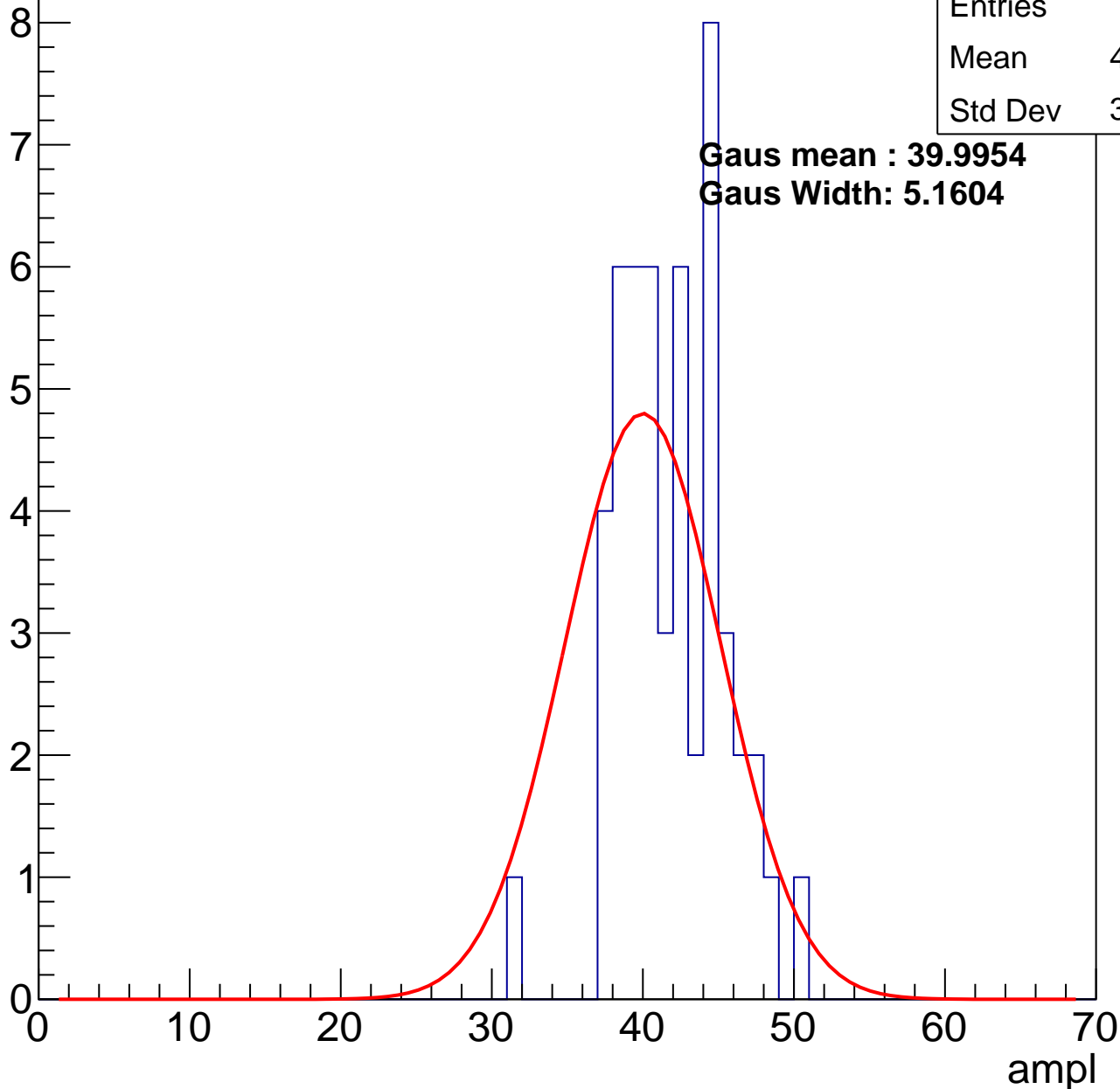
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 51 |
| Mean | 41.43 |
| Std Dev | 3.499 |

Gaus mean : 39.9954

Gaus Width: 5.1604

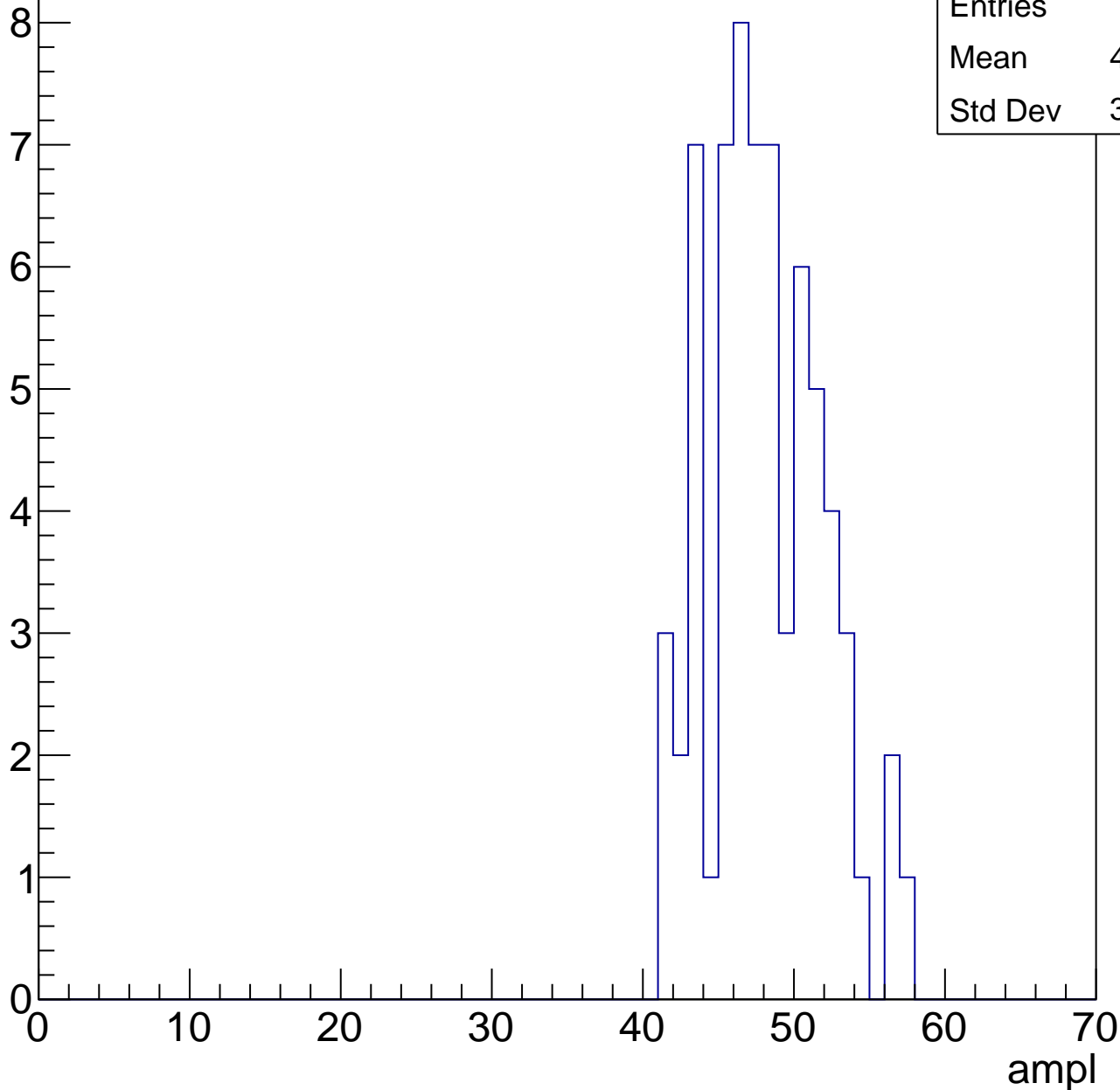


B1L102S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 47.64 |
| Std Dev | 3.784 |

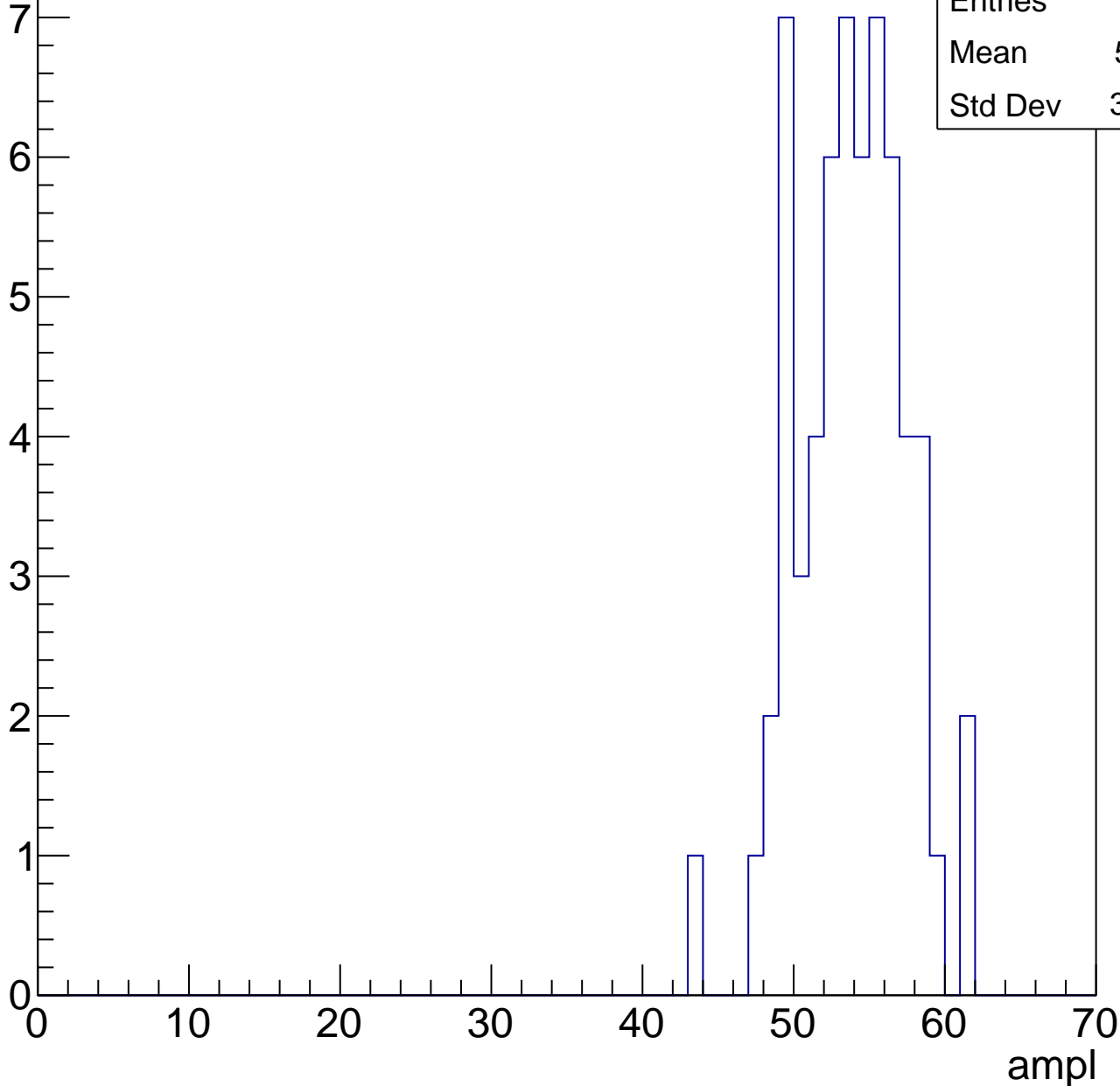


B1L102S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 53.31 |
| Std Dev | 3.523 |



B1L102S, U15-ch124, adc5

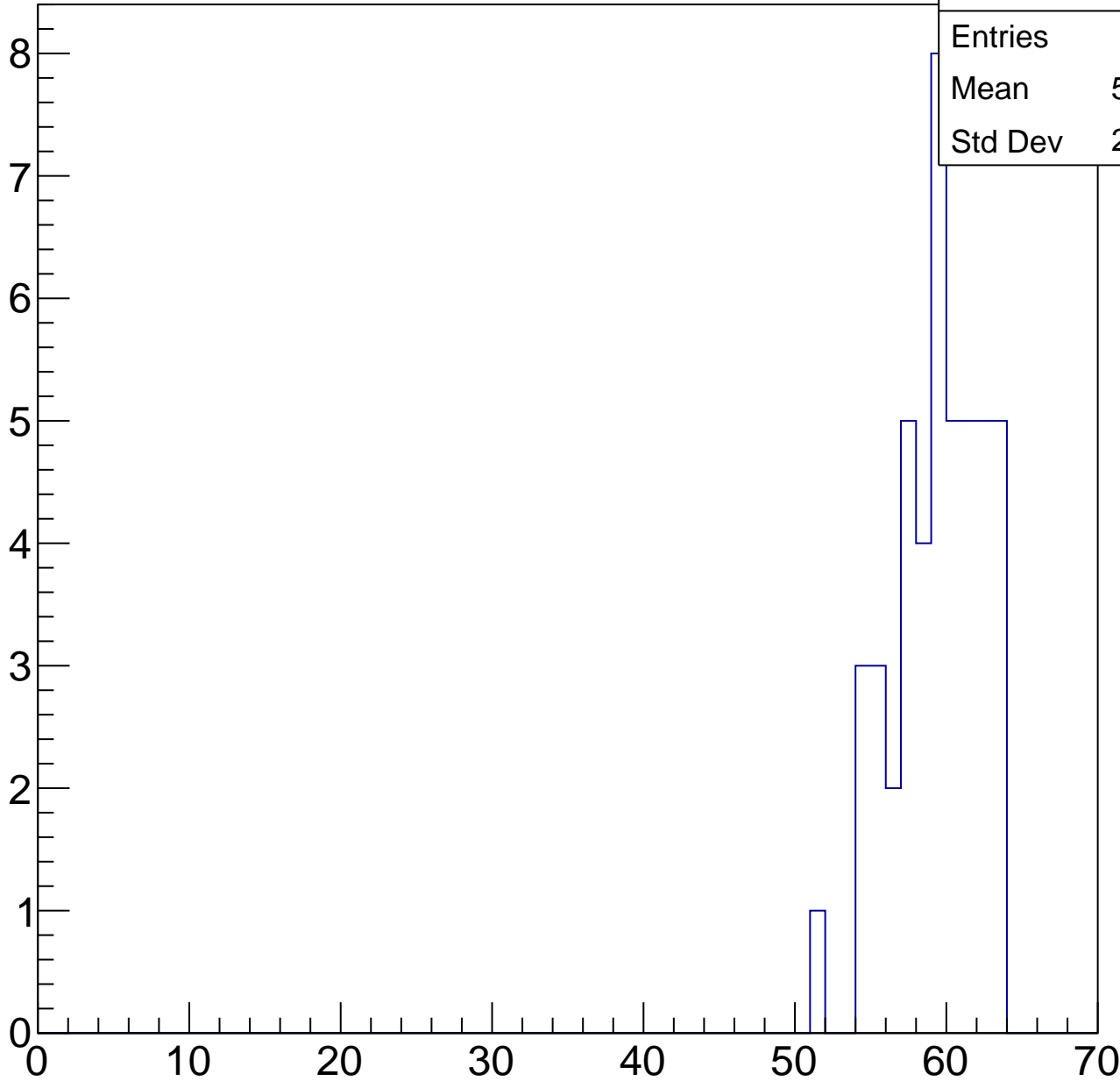
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

| | |
|---------|-------|
| Entries | 46 |
| Mean | 58.89 |
| Std Dev | 2.868 |

ampl

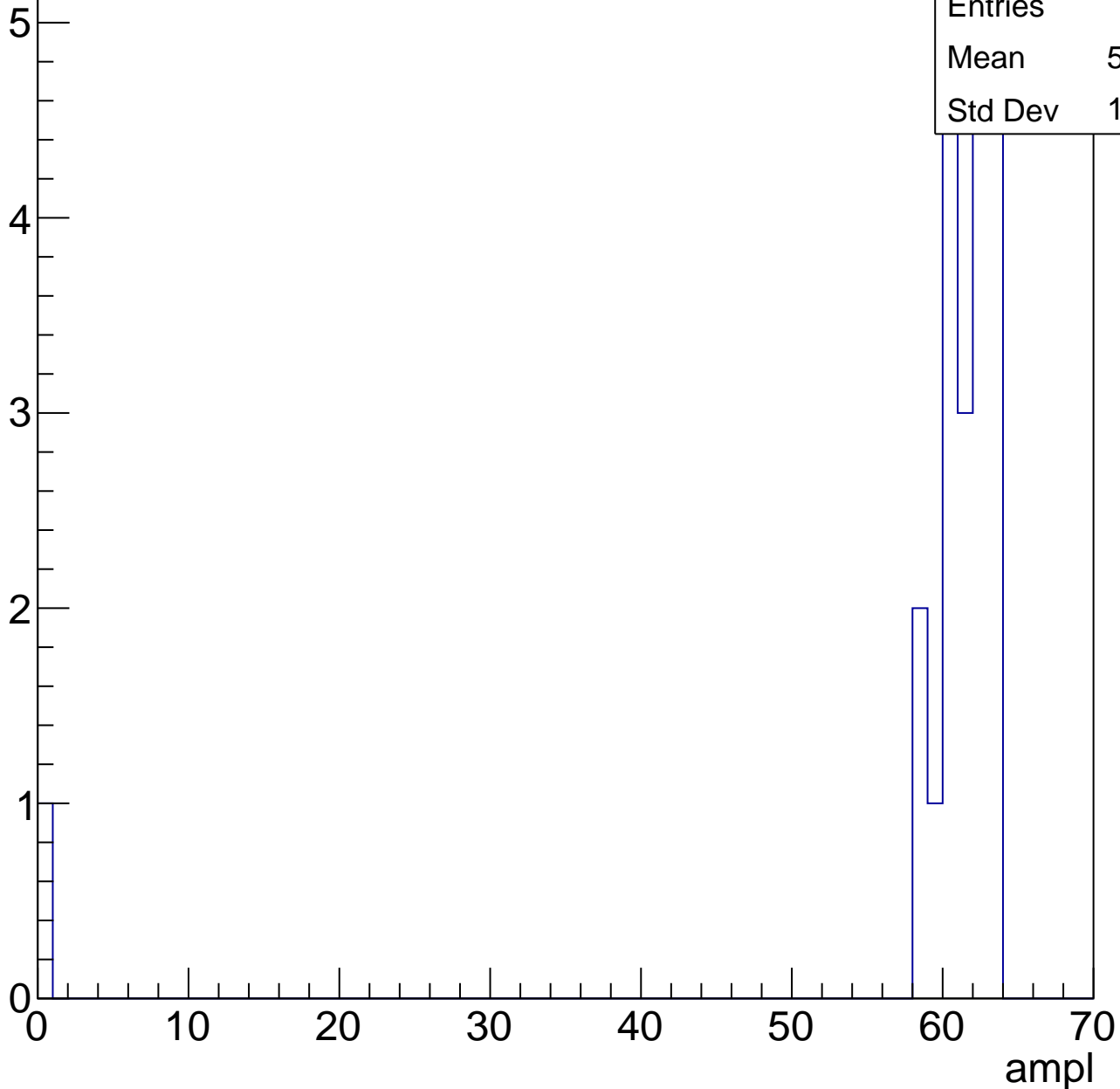


B1L102S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 22 |
| Mean | 58.32 |
| Std Dev | 12.82 |



B1L102S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 0 |
| Mean | 0 |
| Std Dev | 0 |

B1L102S, U15-ch125, adc0

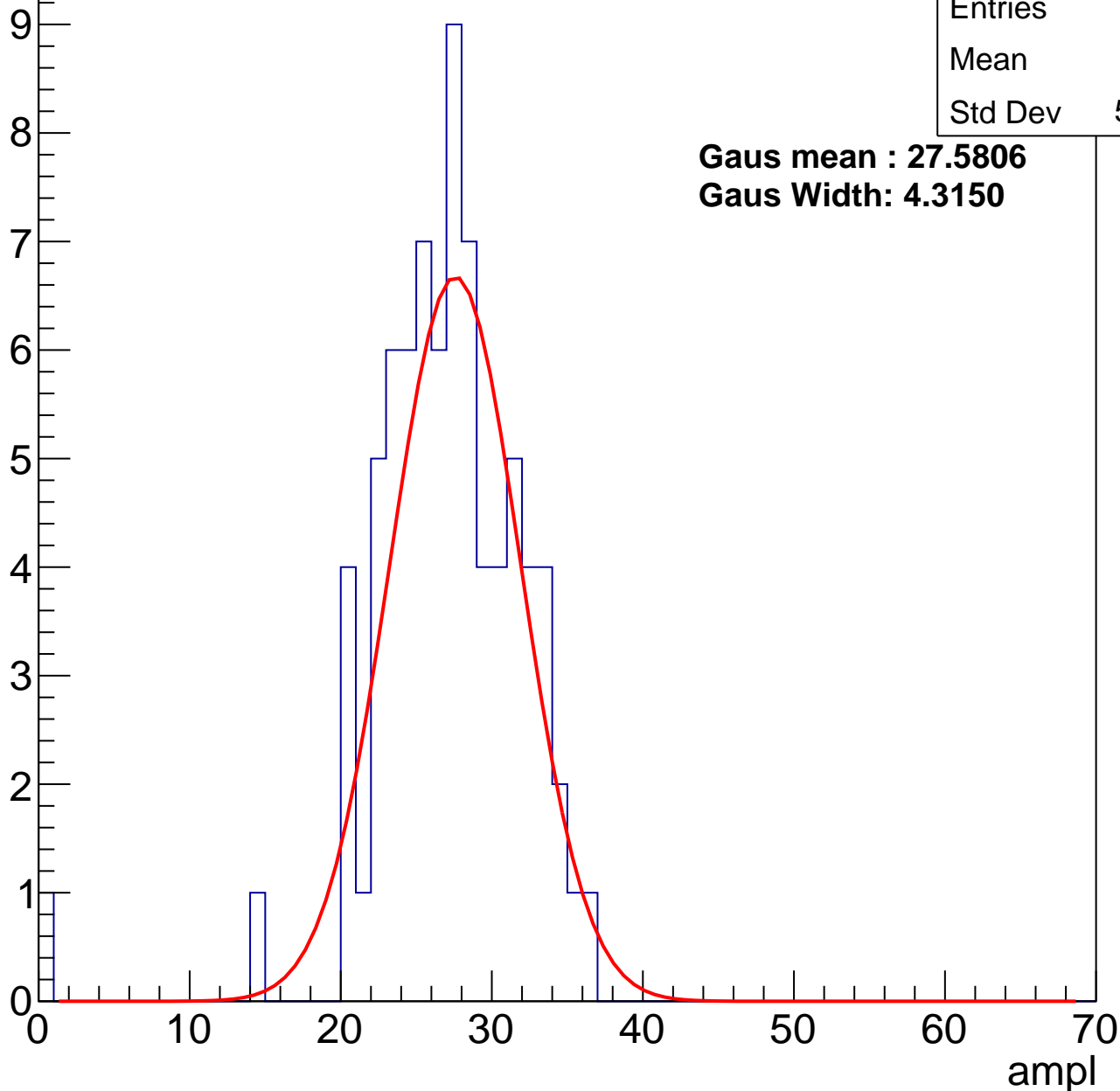
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 78 |
| Mean | 26.5 |
| Std Dev | 5.121 |

Gaus mean : 27.5806

Gaus Width: 4.3150



B1L102S, U15-ch125, adc1

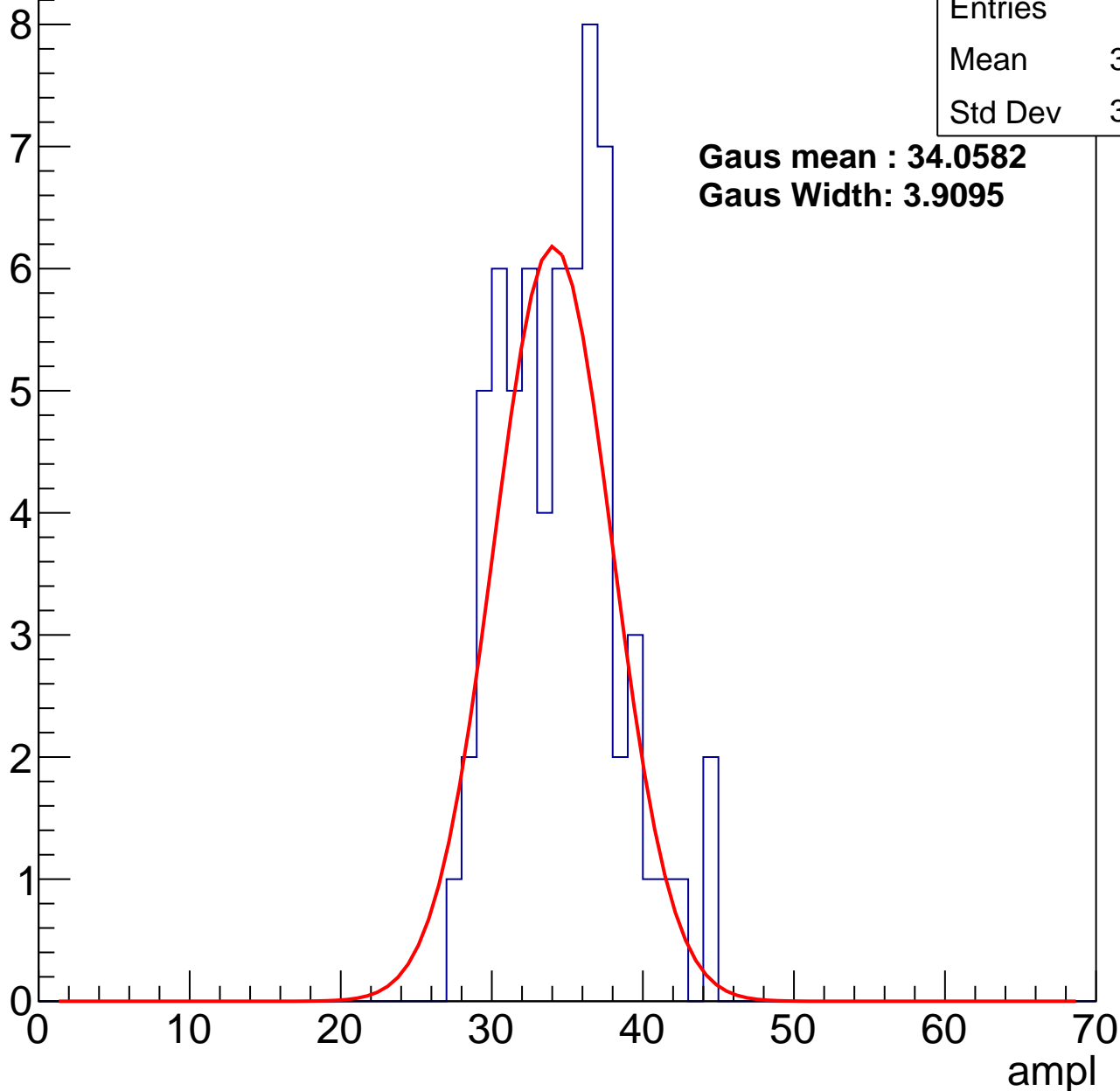
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 66 |
| Mean | 34.12 |
| Std Dev | 3.836 |

Gaus mean : 34.0582

Gaus Width: 3.9095



B1L102S, U15-ch125, adc2

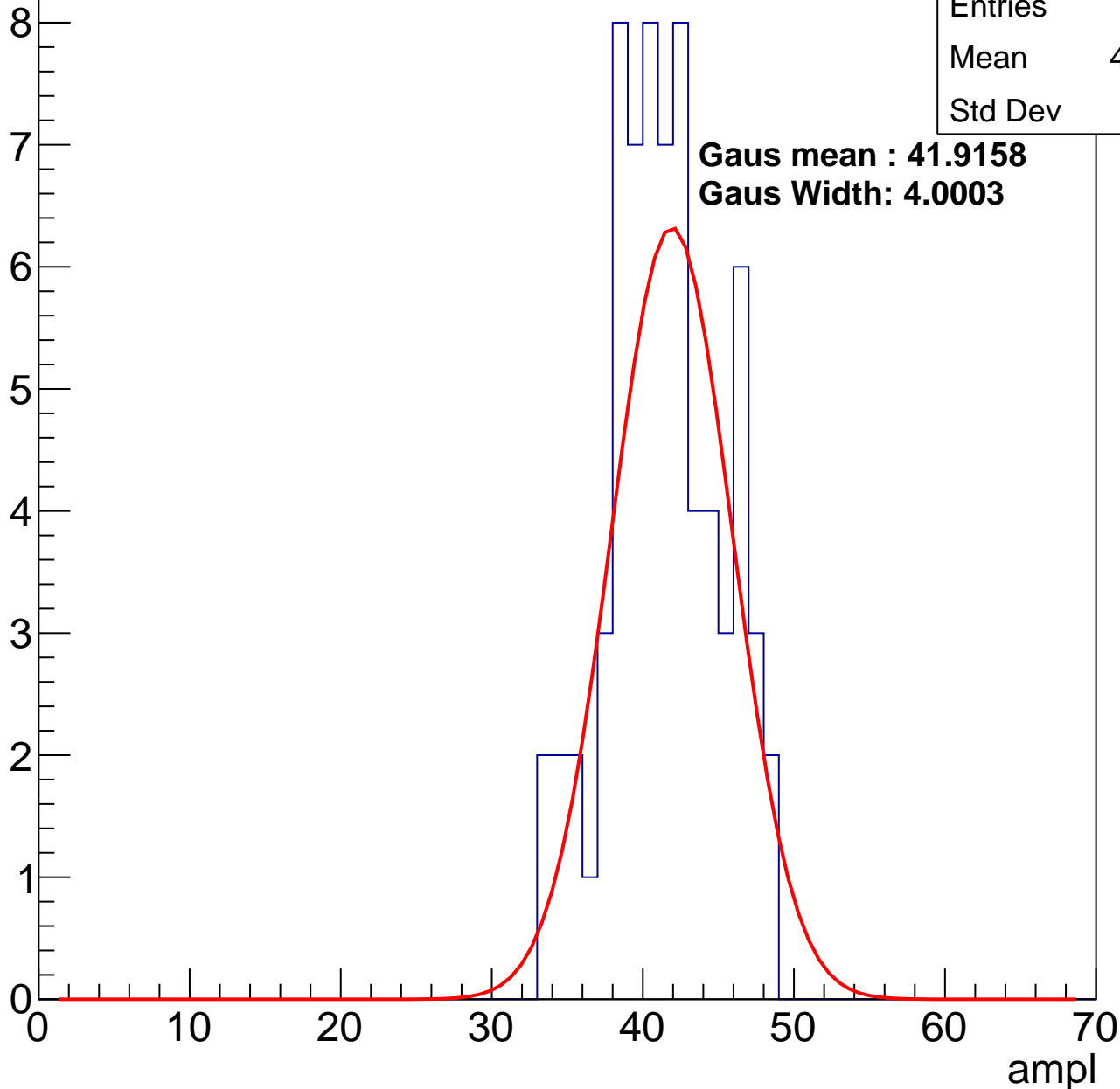
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 70 |
| Mean | 40.96 |
| Std Dev | 3.67 |

Gaus mean : 41.9158

Gaus Width: 4.0003

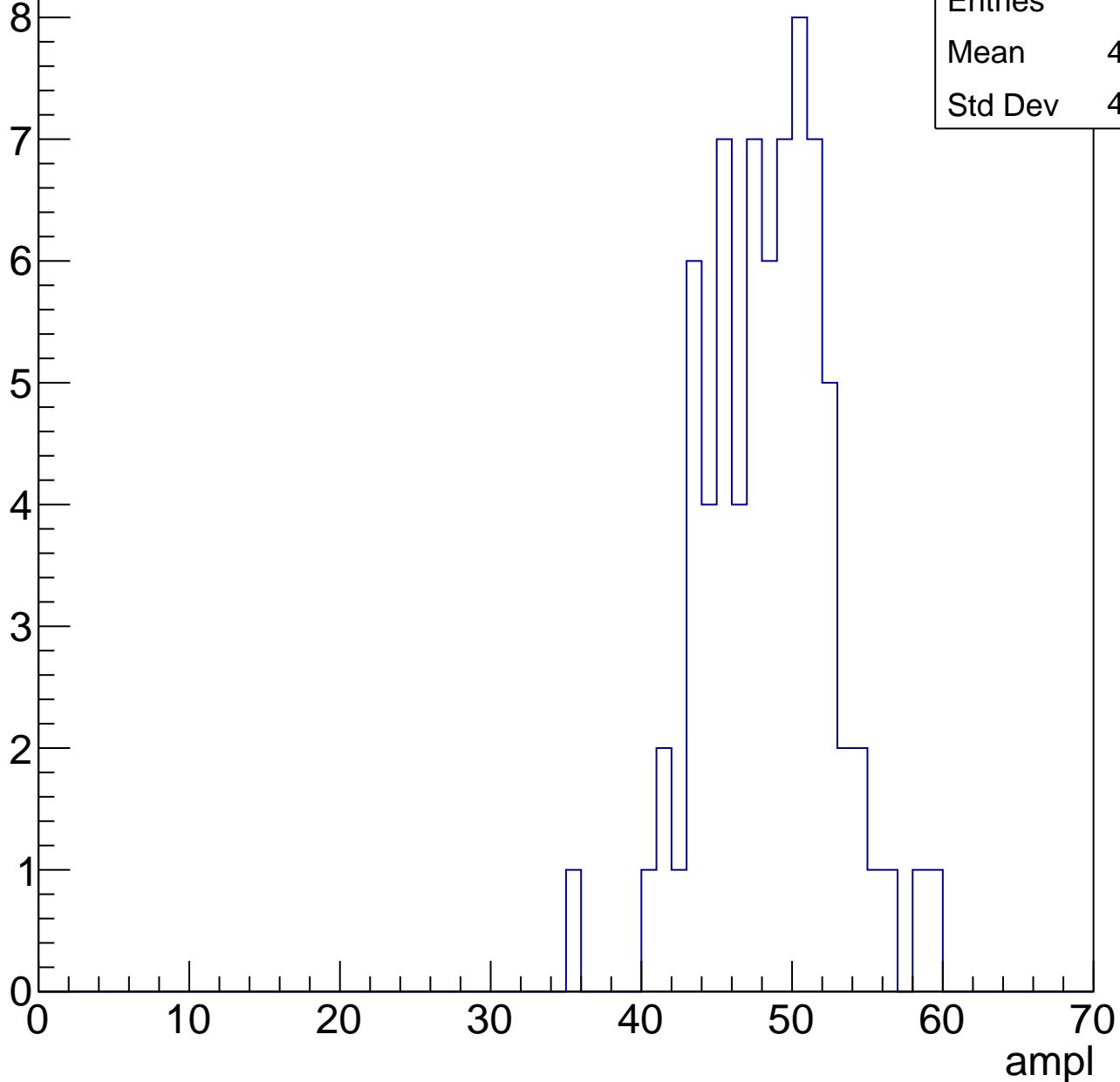


B1L102S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 74 |
| Mean | 47.99 |
| Std Dev | 4.199 |

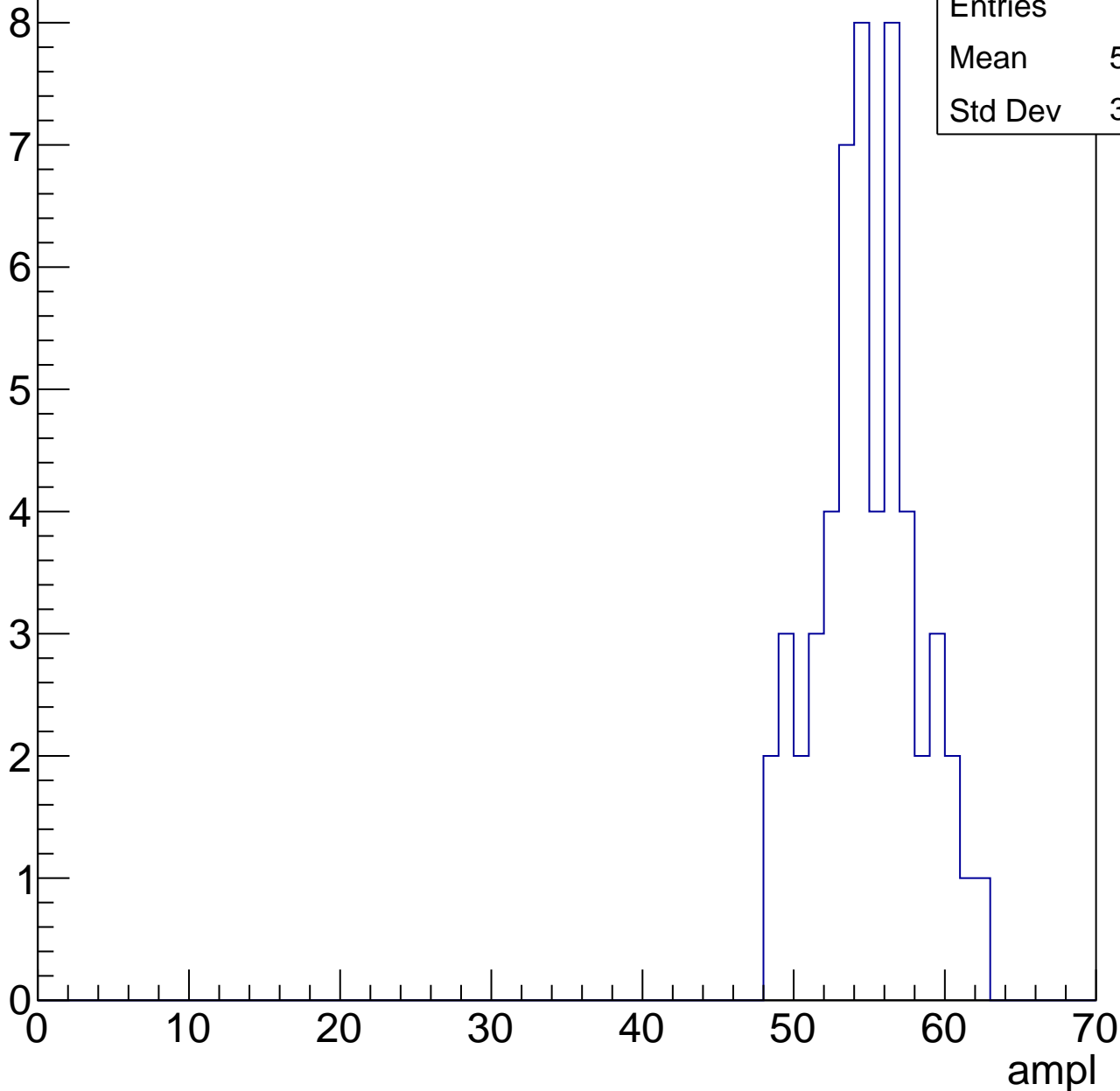


B1L102S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 54 |
| Mean | 54.43 |
| Std Dev | 3.286 |

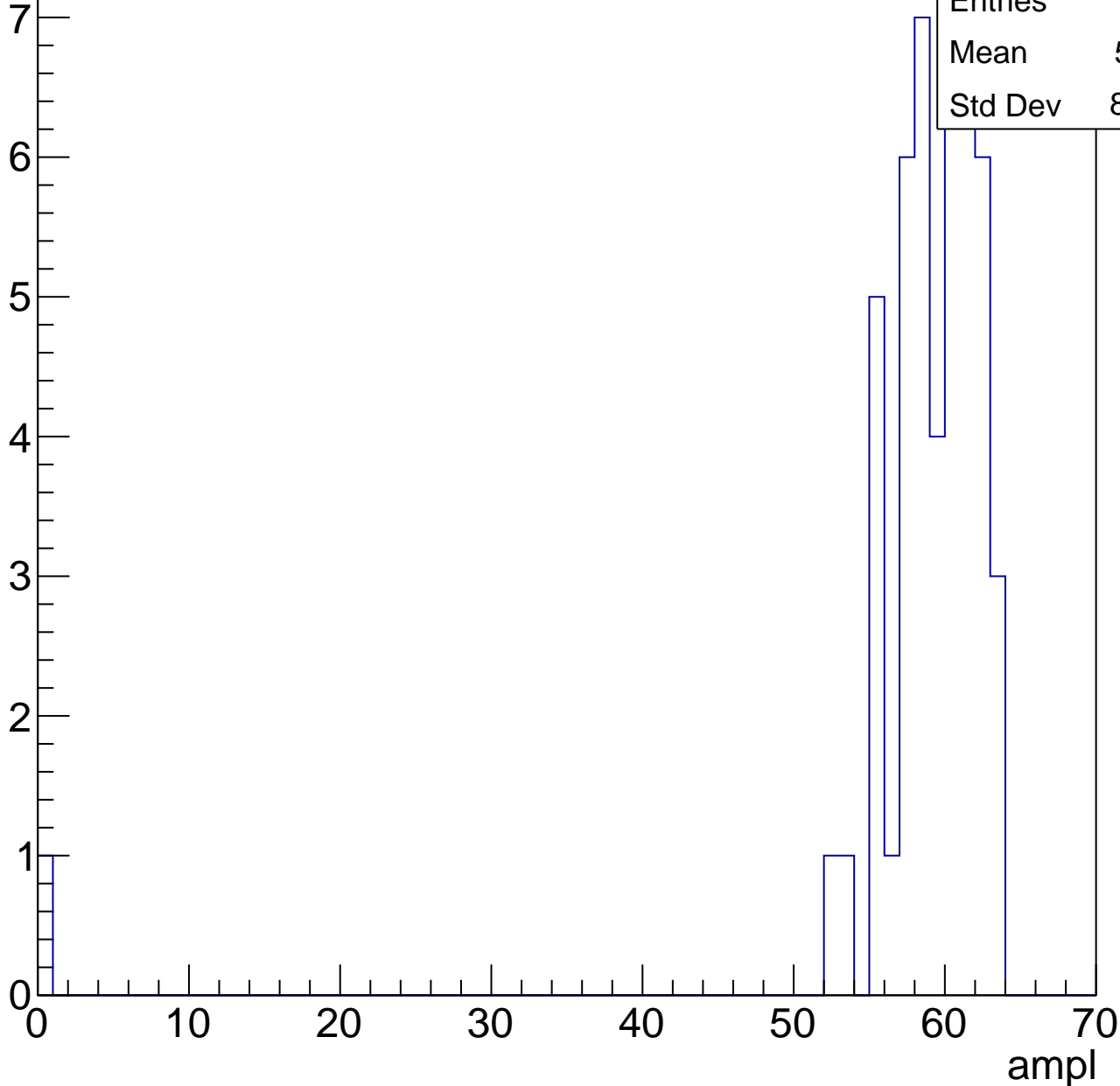


B1L102S, U15-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 49 |
| Mean | 57.71 |
| Std Dev | 8.739 |

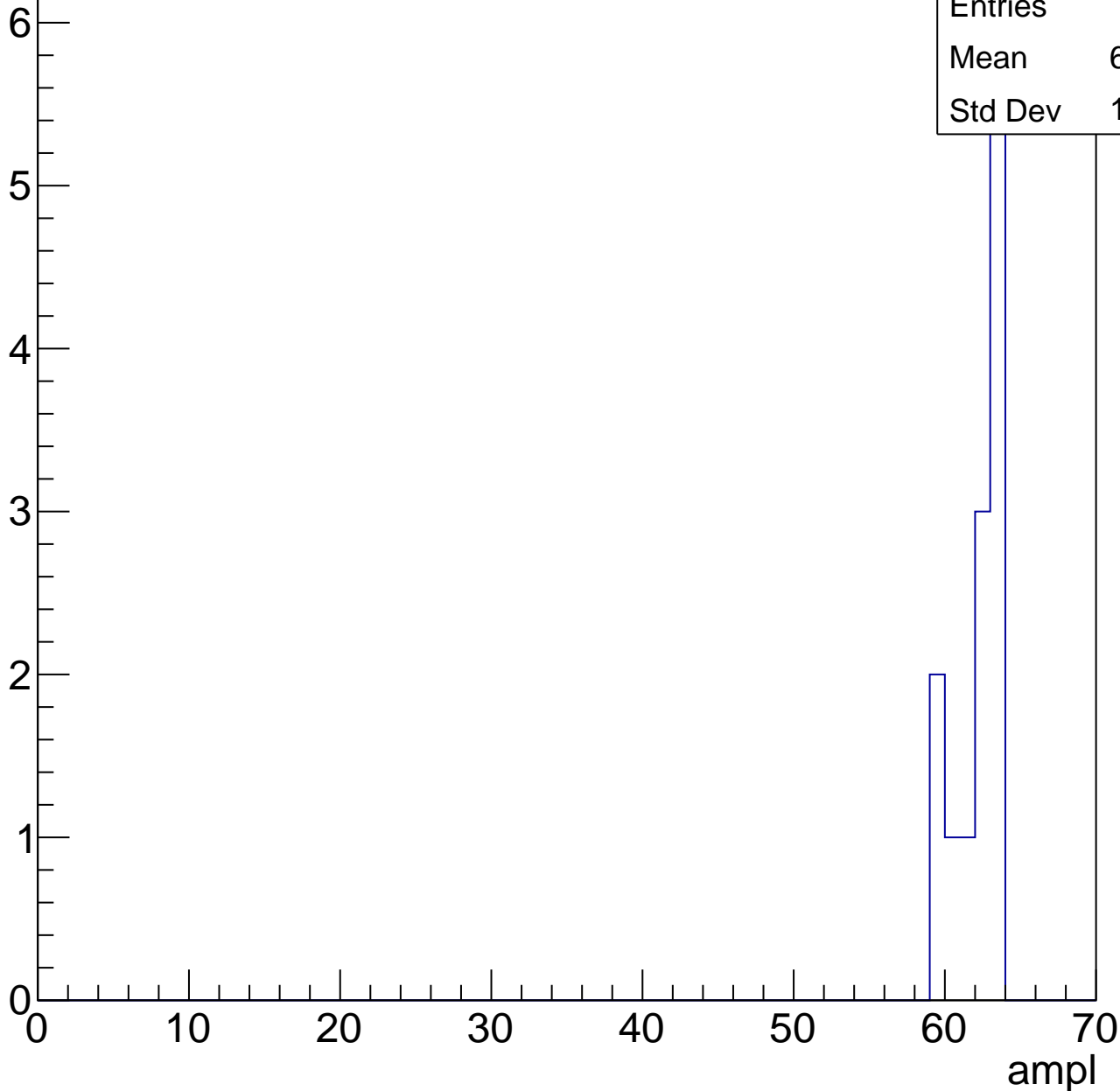


B1L102S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 13 |
| Mean | 61.77 |
| Std Dev | 1.476 |



B1L102S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

62

Std Dev

1

B1L102S, U15-ch126, adc0

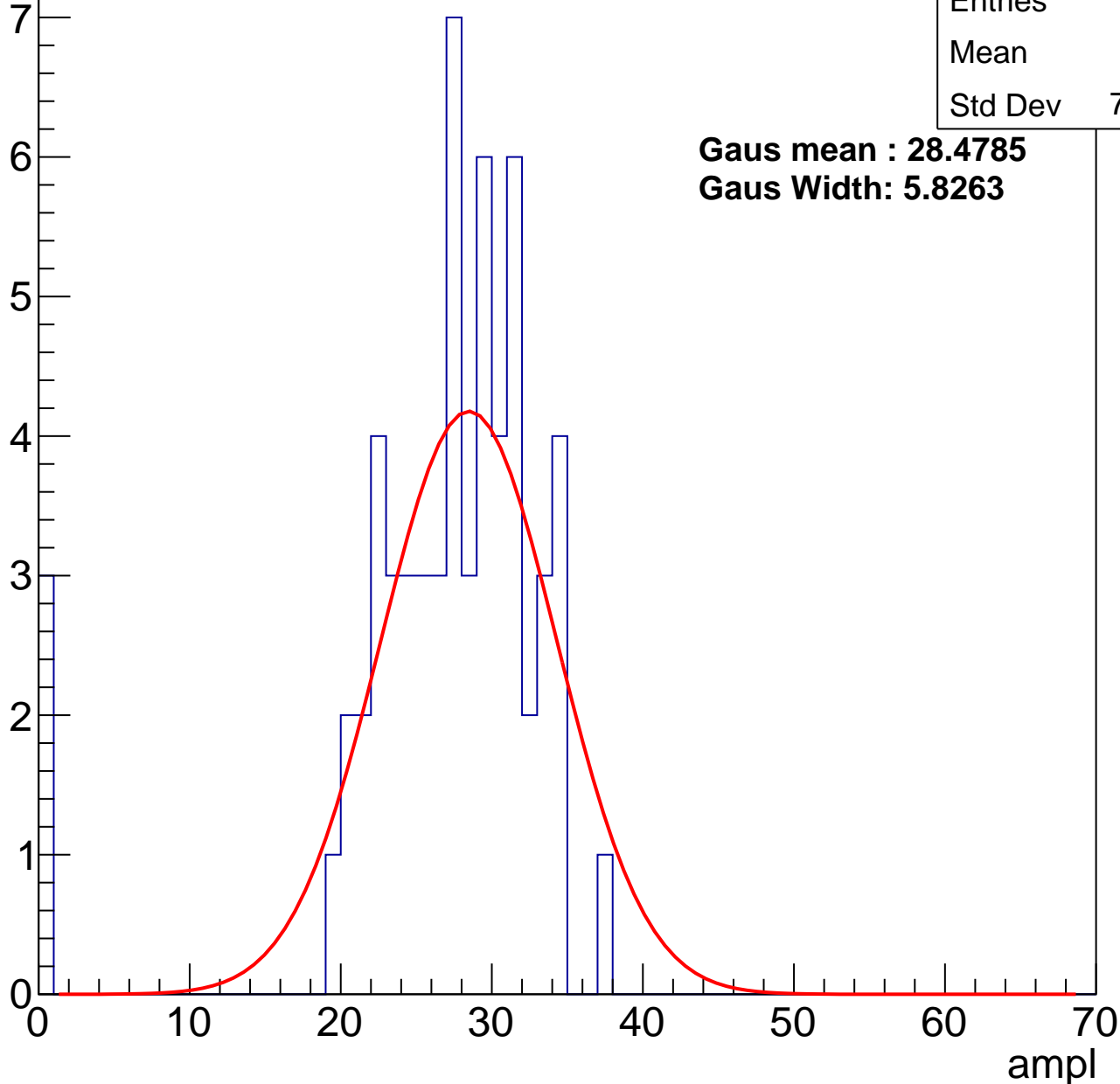
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 60 |
| Mean | 26.2 |
| Std Dev | 7.287 |

Gaus mean : 28.4785

Gaus Width: 5.8263



B1L102S, U15-ch126, adc1

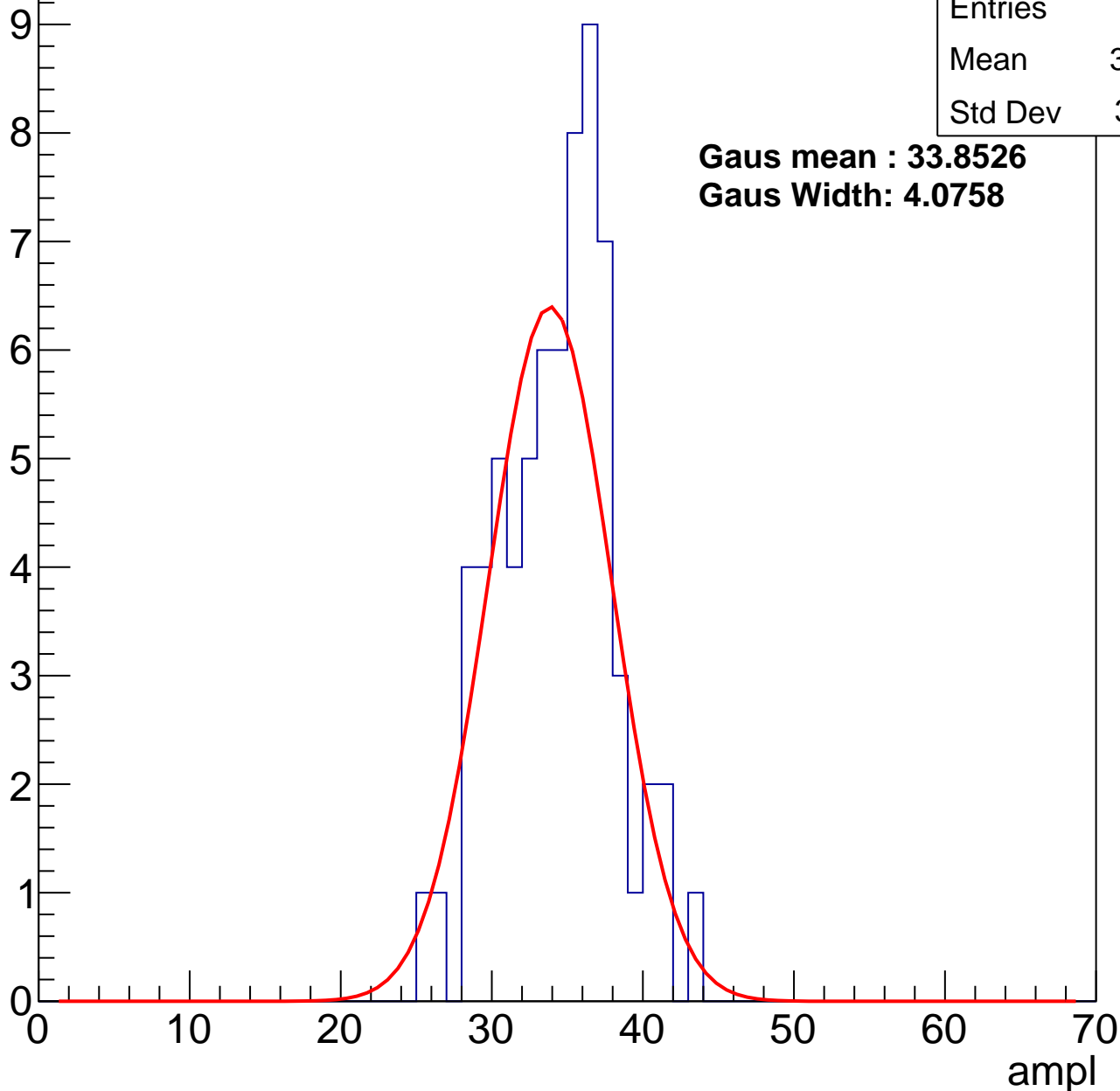
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 69 |
| Mean | 33.86 |
| Std Dev | 3.731 |

Gaus mean : 33.8526

Gaus Width: 4.0758



B1L102S, U15-ch126, adc2

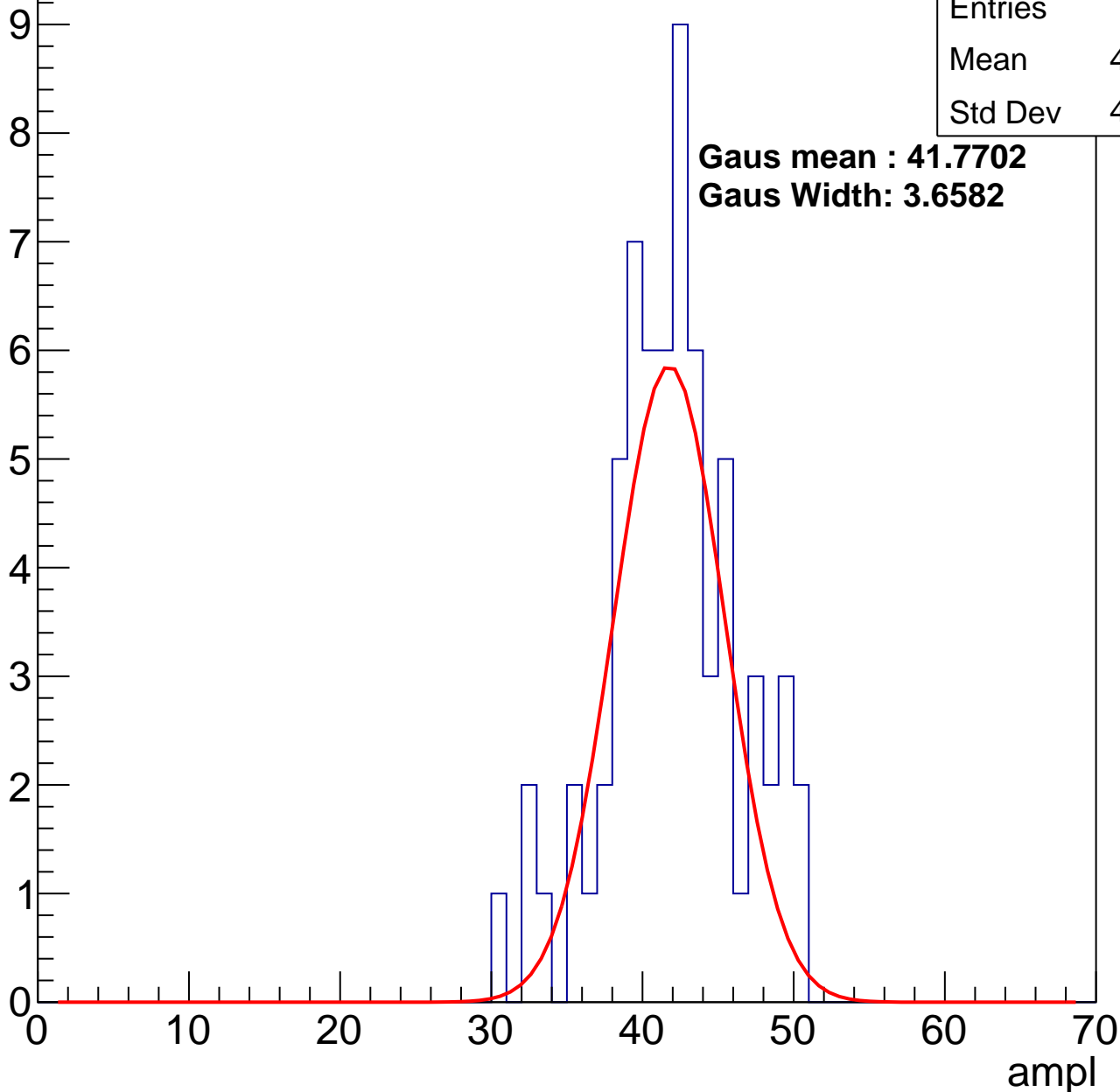
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 41.48 |
| Std Dev | 4.345 |

Gaus mean : 41.7702

Gaus Width: 3.6582

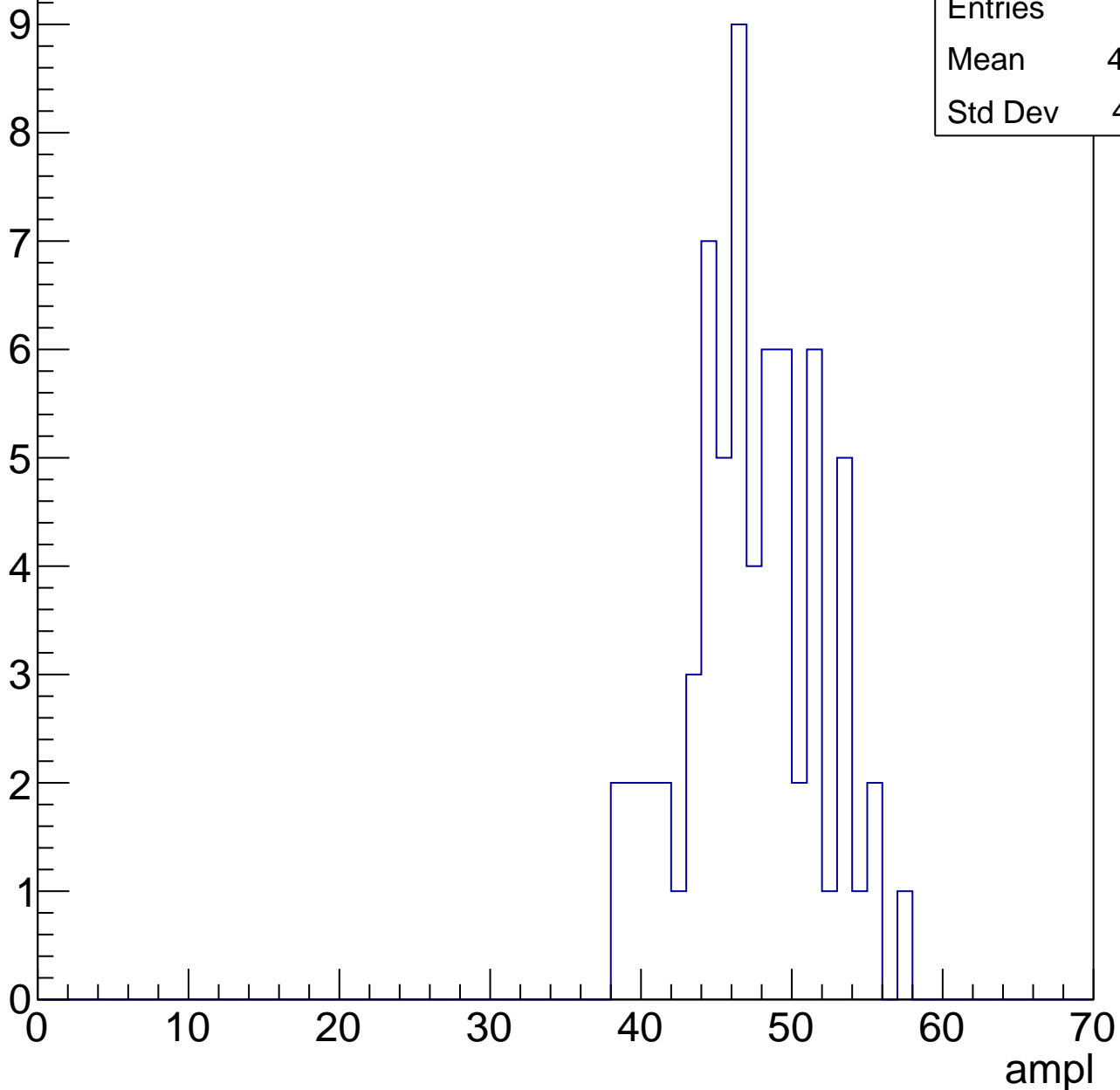


B1L102S, U15-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 67 |
| Mean | 46.99 |
| Std Dev | 4.321 |

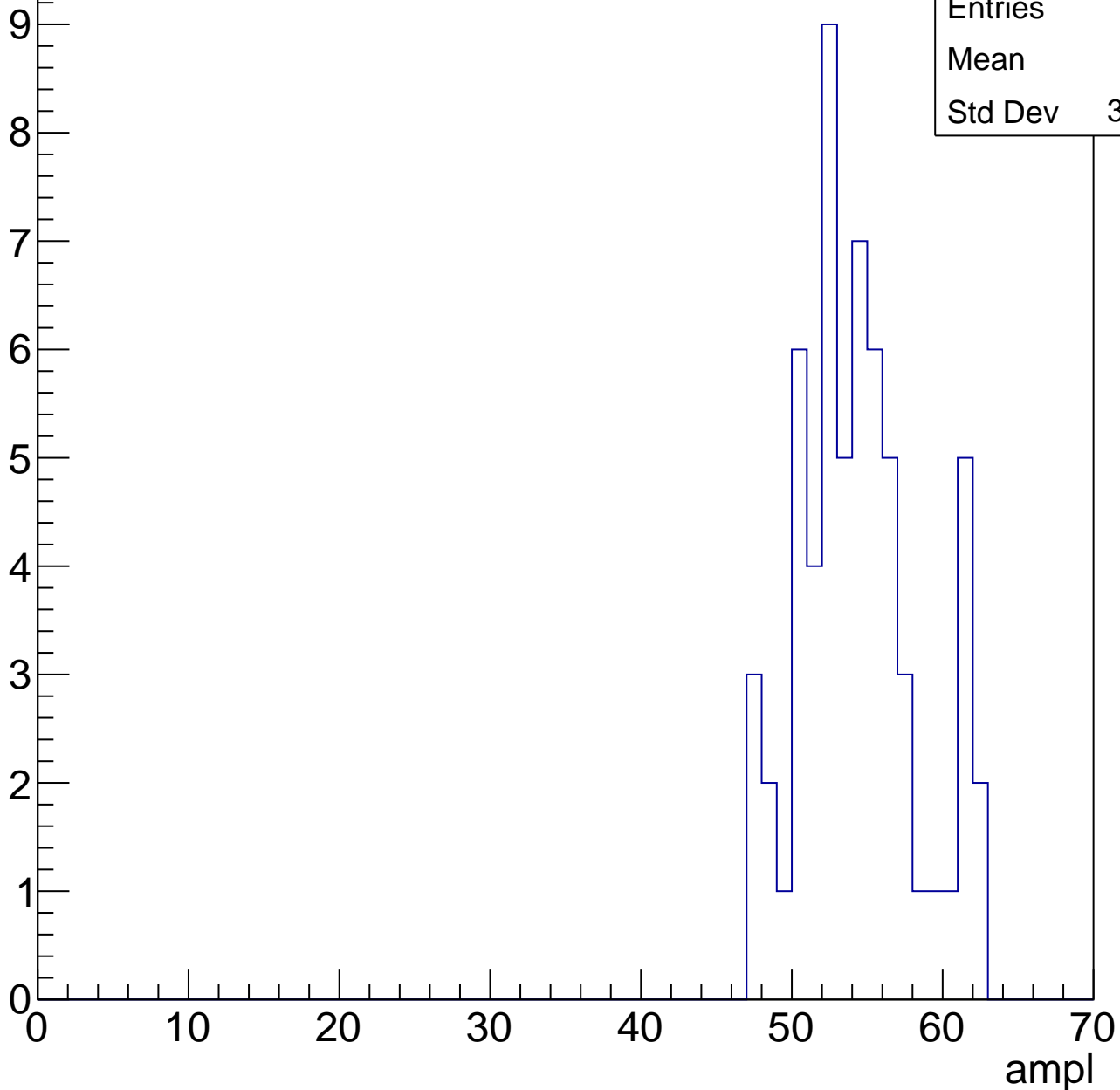


B1L102S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 61 |
| Mean | 53.9 |
| Std Dev | 3.882 |

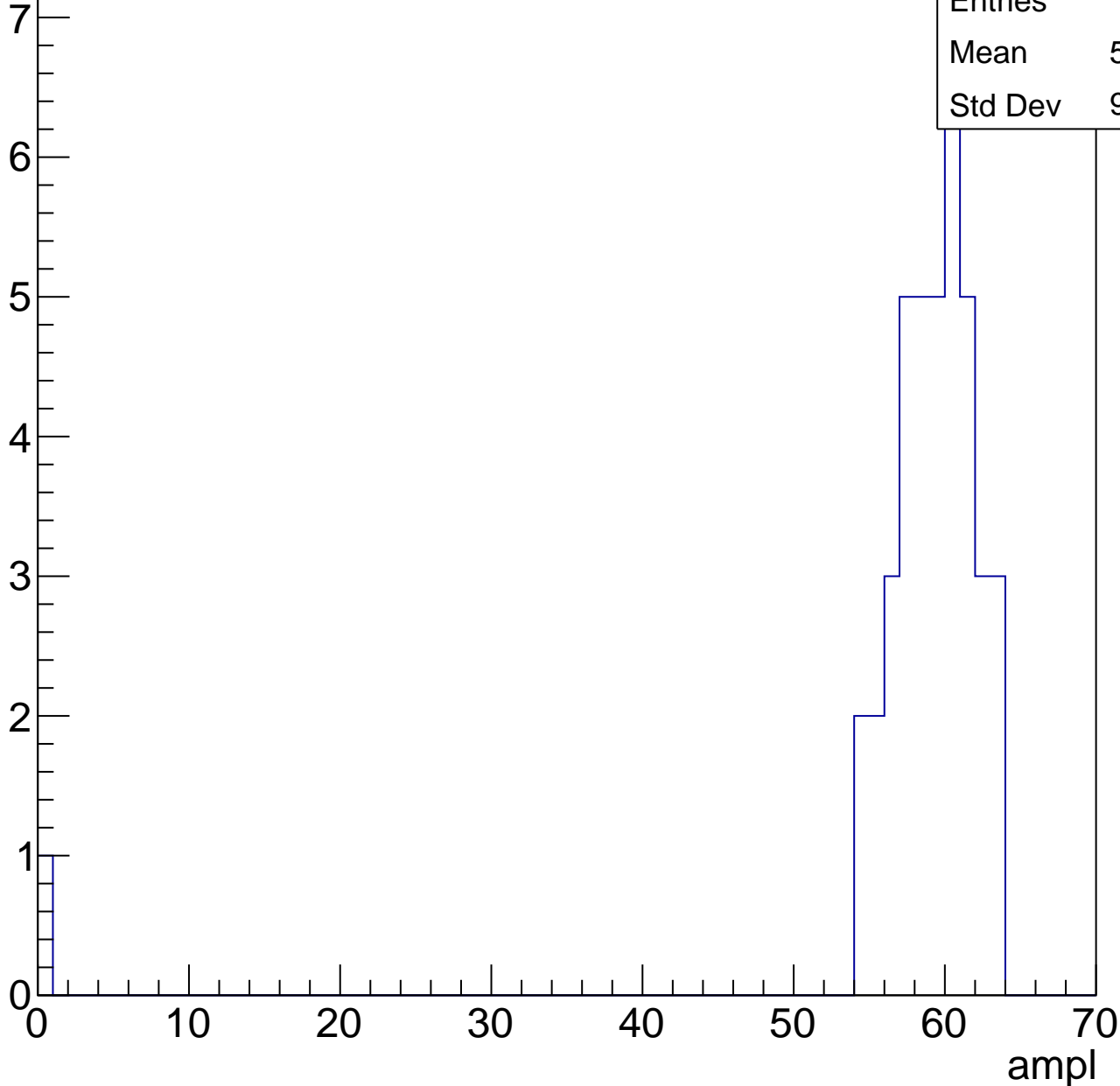


B1L102S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 41 |
| Mean | 57.46 |
| Std Dev | 9.397 |

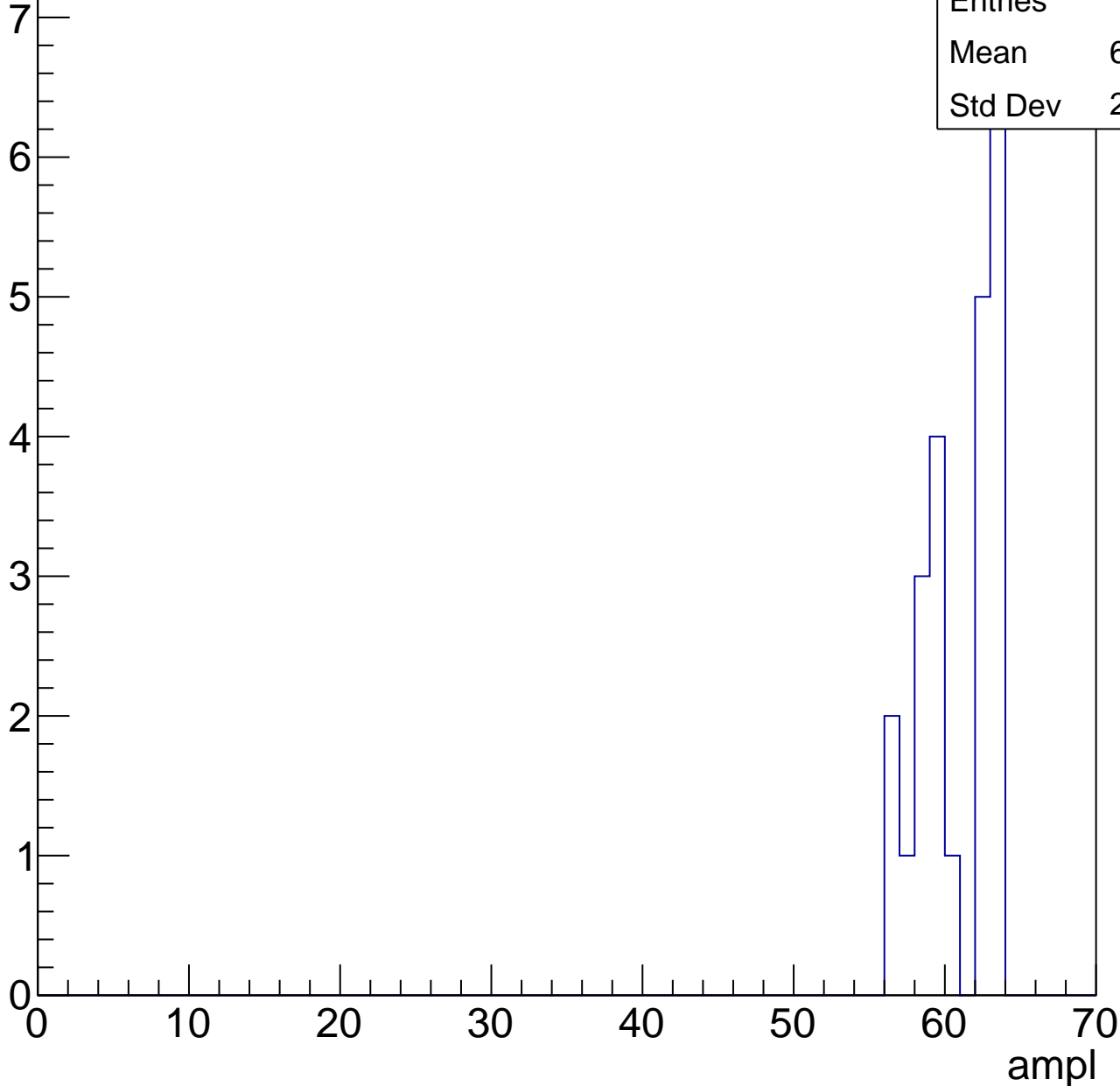


B1L102S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

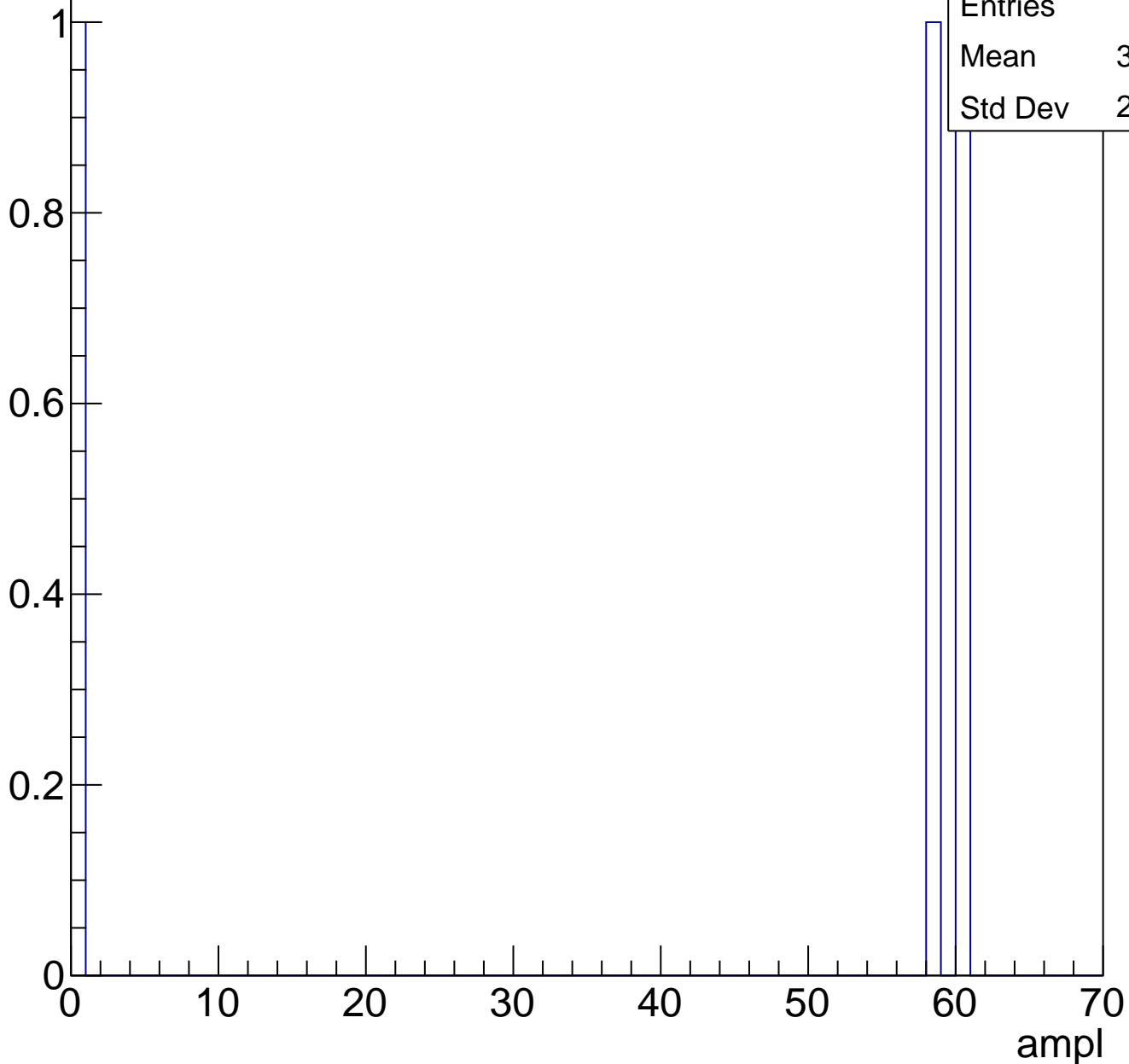
| | |
|---------|-------|
| Entries | 23 |
| Mean | 60.43 |
| Std Dev | 2.429 |



B1L102S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch127, adc0

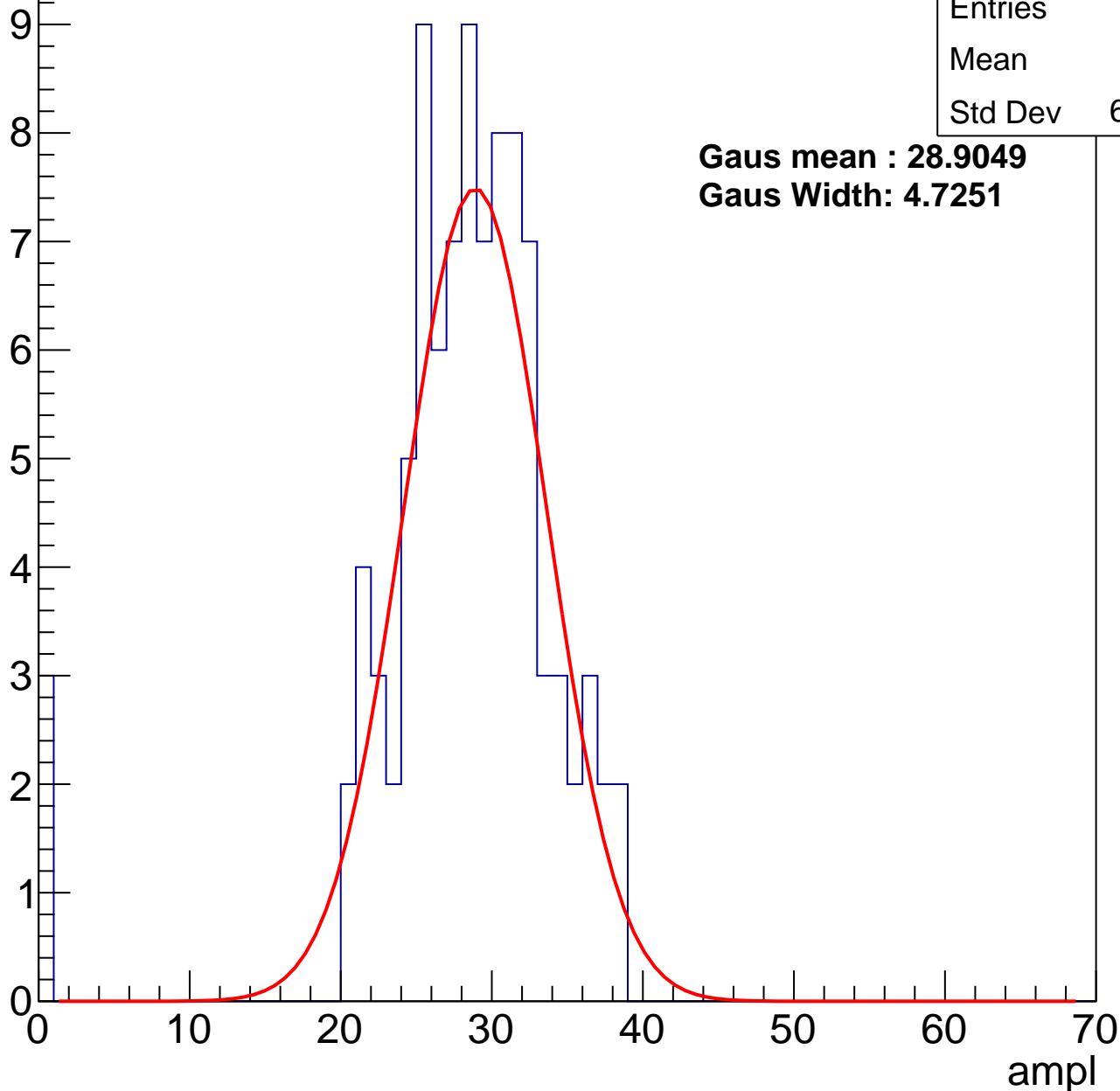
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 95 |
| Mean | 27.6 |
| Std Dev | 6.544 |

Gaus mean : 28.9049

Gaus Width: 4.7251



B1L102S, U15-ch127, adc1

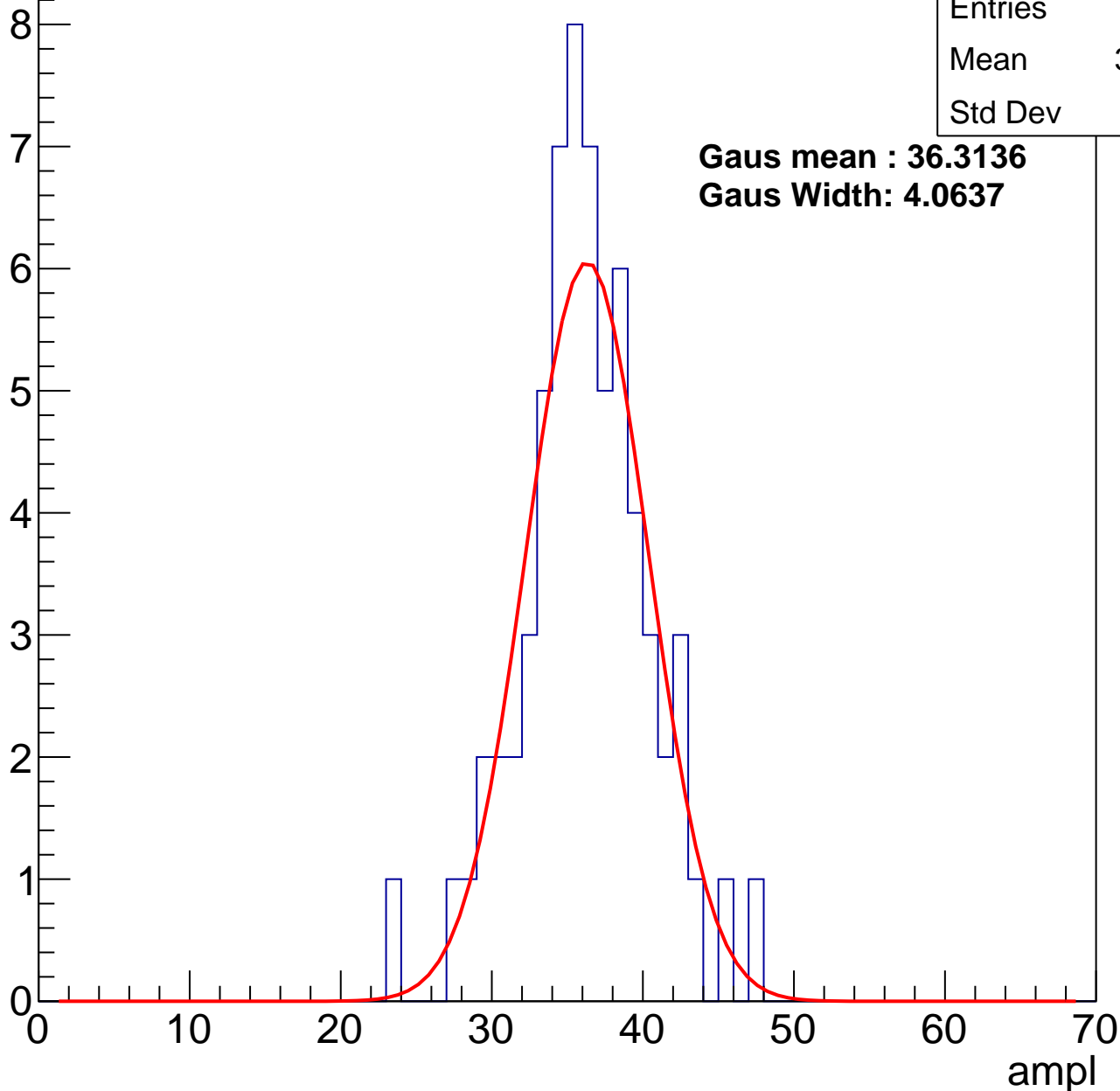
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 65 |
| Mean | 35.71 |
| Std Dev | 4.26 |

Gaus mean : 36.3136

Gaus Width: 4.0637



B1L102S, U15-ch127, adc2

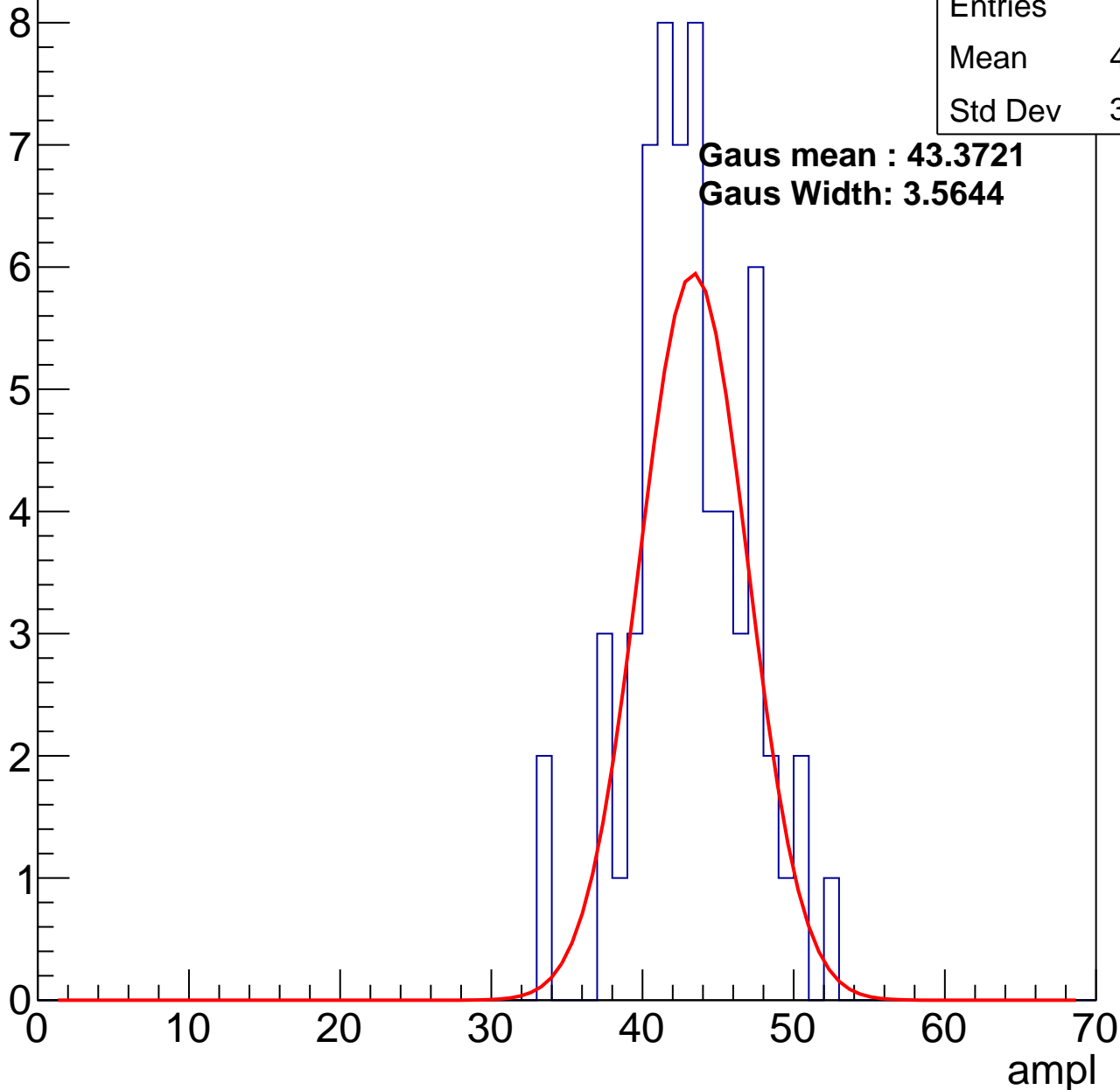
calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 62 |
| Mean | 42.76 |
| Std Dev | 3.783 |

Gaus mean : 43.3721

Gaus Width: 3.5644

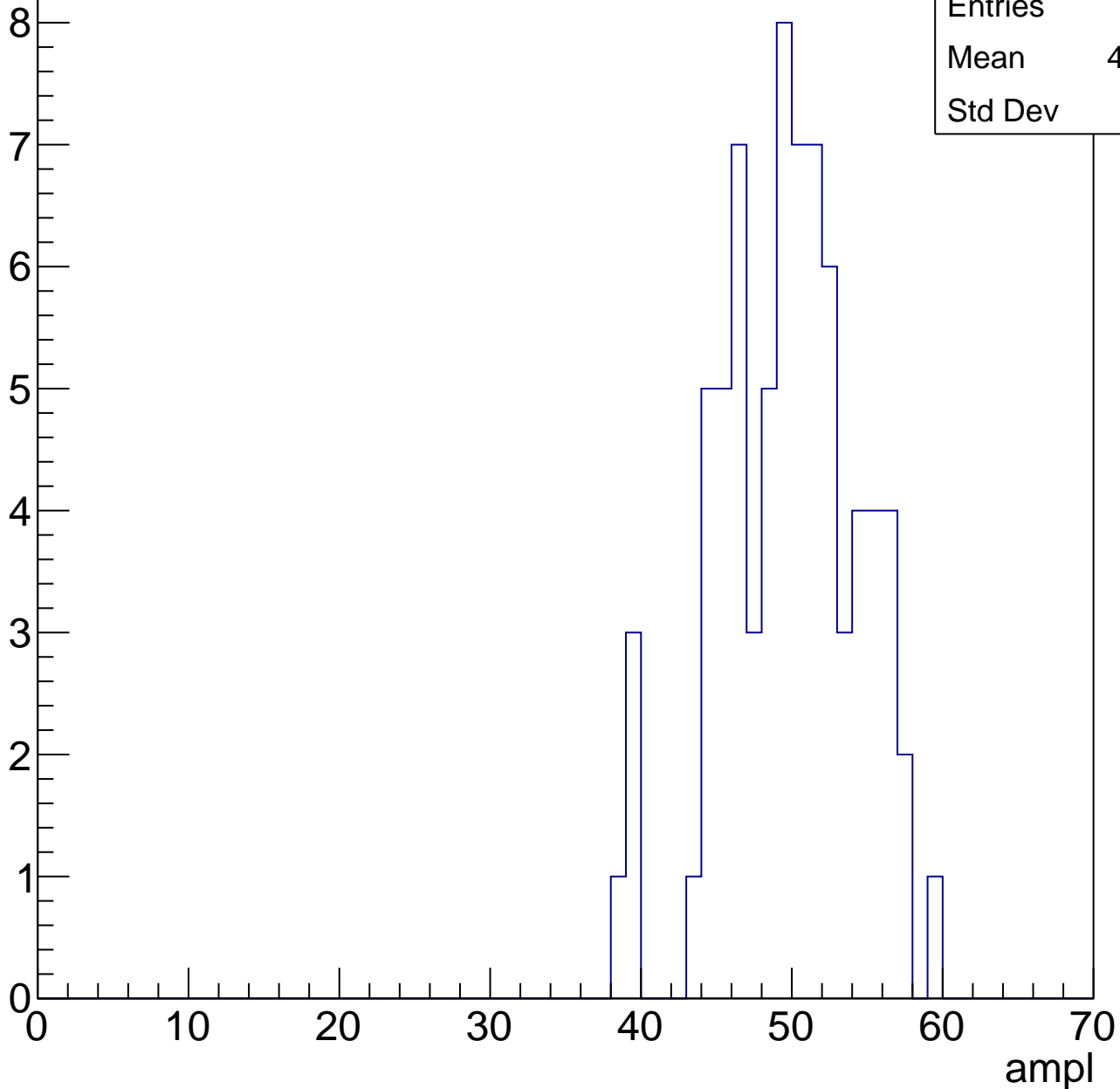


B1L102S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 76 |
| Mean | 49.33 |
| Std Dev | 4.5 |

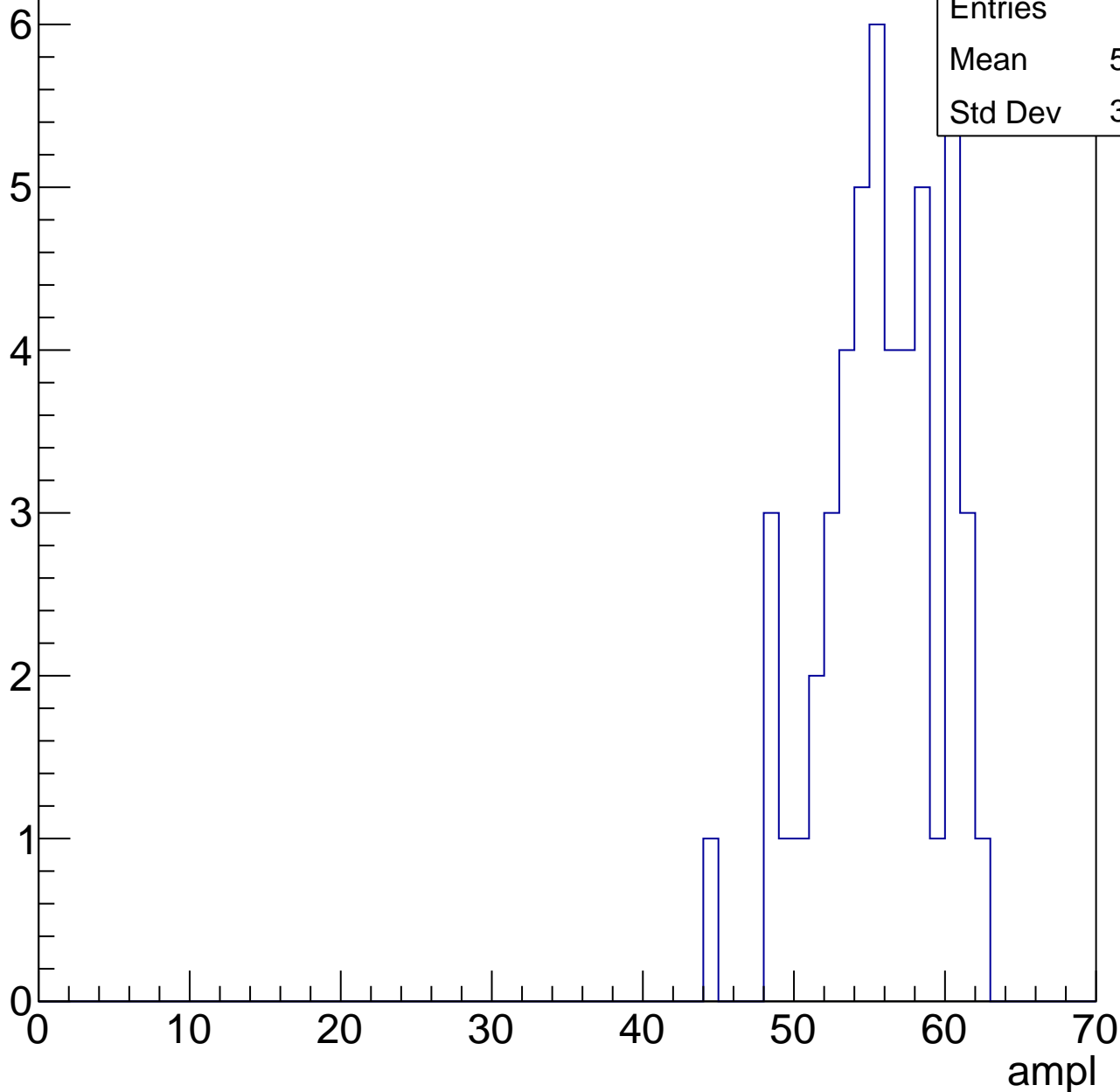


B1L102S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

| | |
|---------|-------|
| Entries | 50 |
| Mean | 55.26 |
| Std Dev | 3.984 |

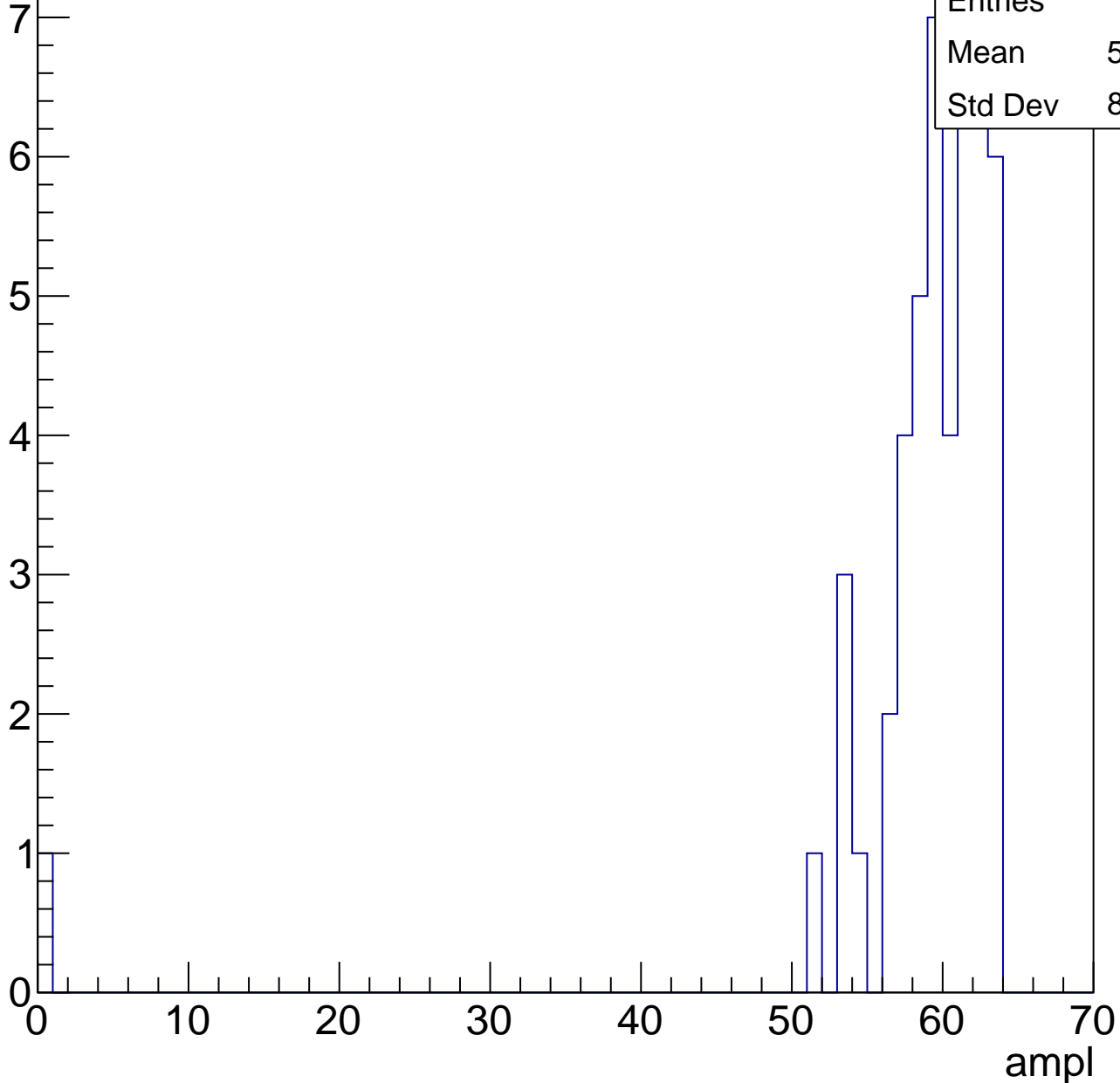


B1L102S, U15-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

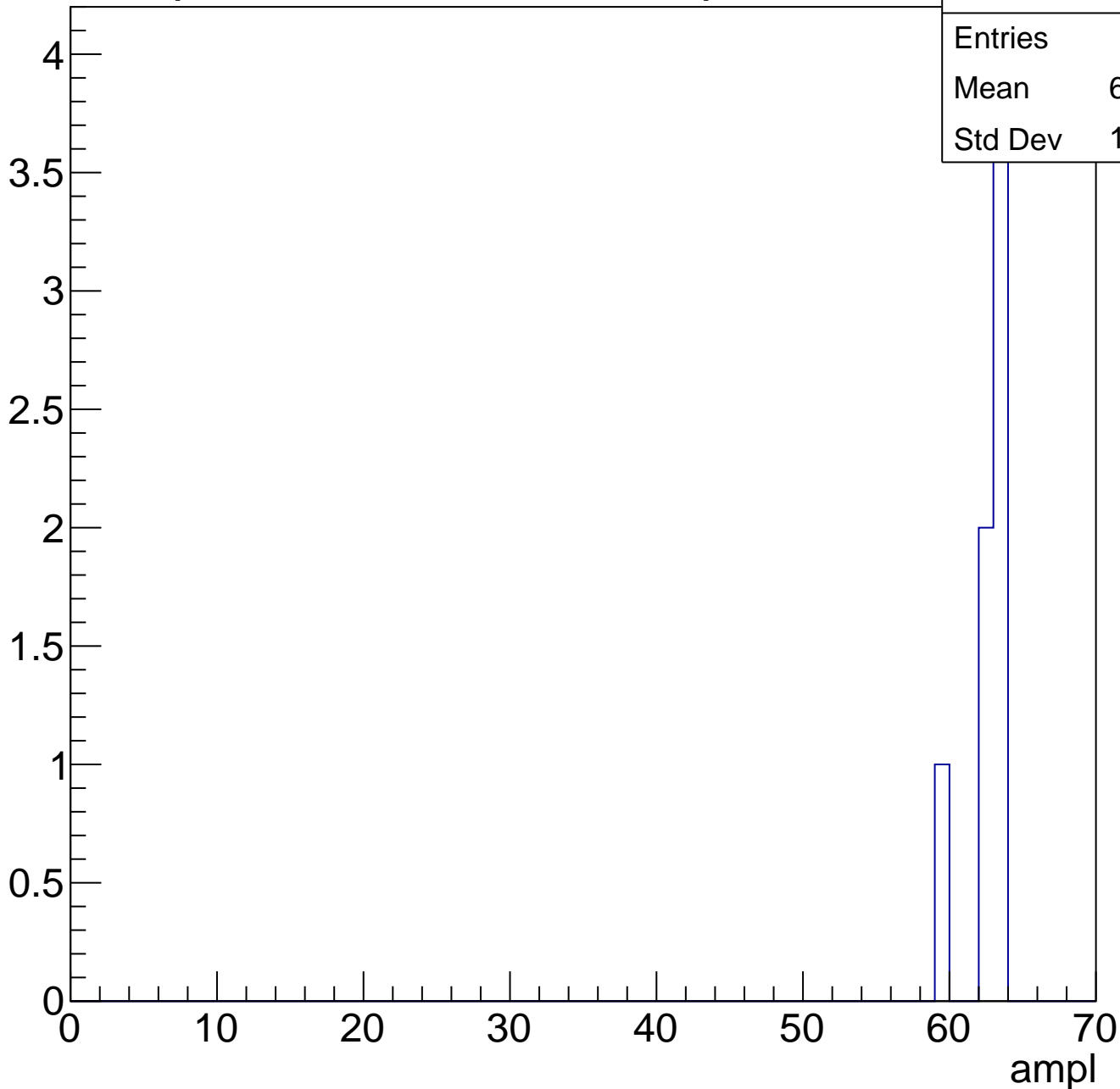
| | |
|---------|-------|
| Entries | 48 |
| Mean | 58.04 |
| Std Dev | 8.972 |



B1L102S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



| | |
|---------|---|
| Entries | 1 |
| Mean | 0 |
| Std Dev | 0 |