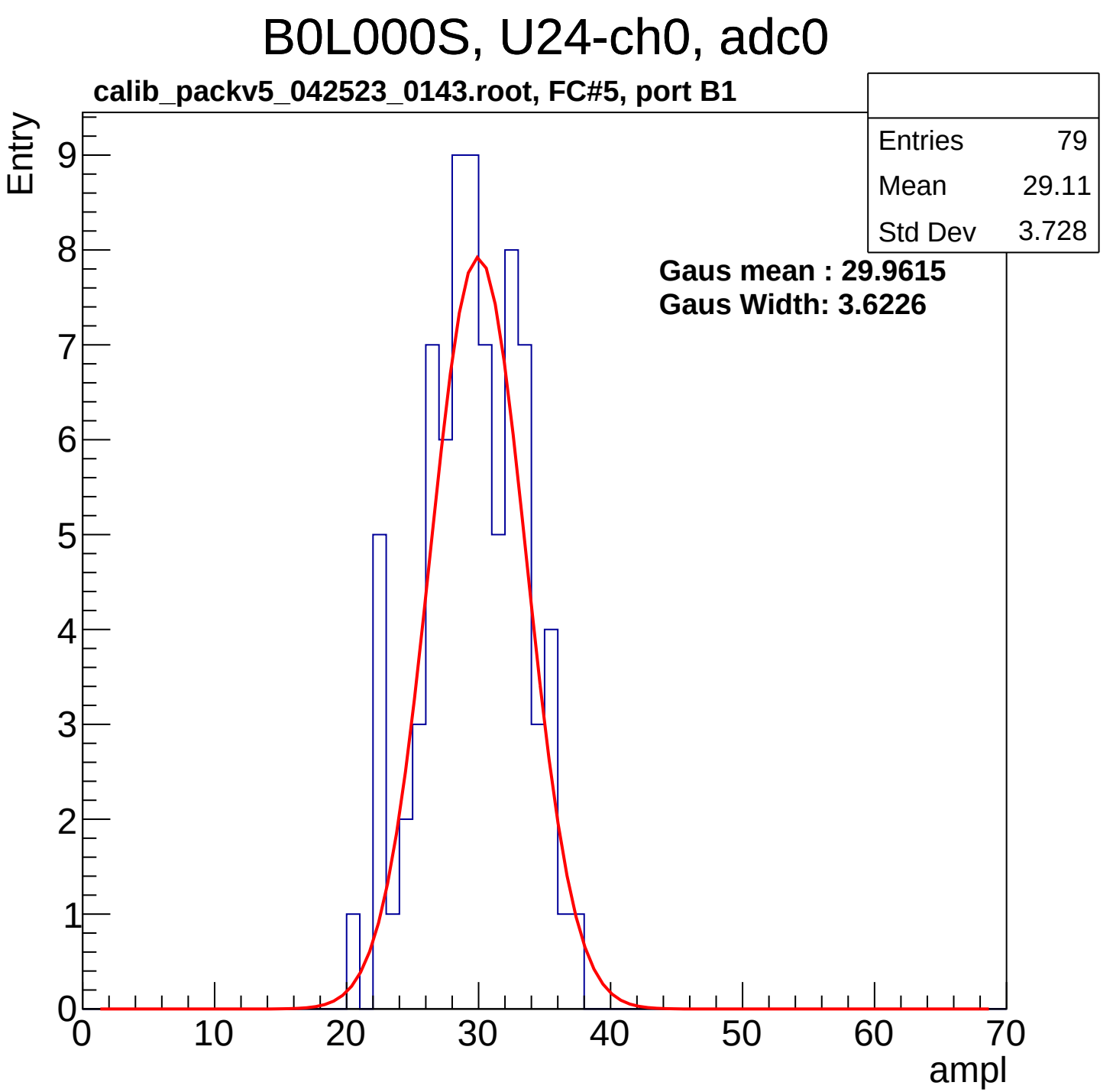
B0L000S, U24-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 29.11
Std Dev 3.728
Gaus mean : 29.9615
Gaus Width: 3.6226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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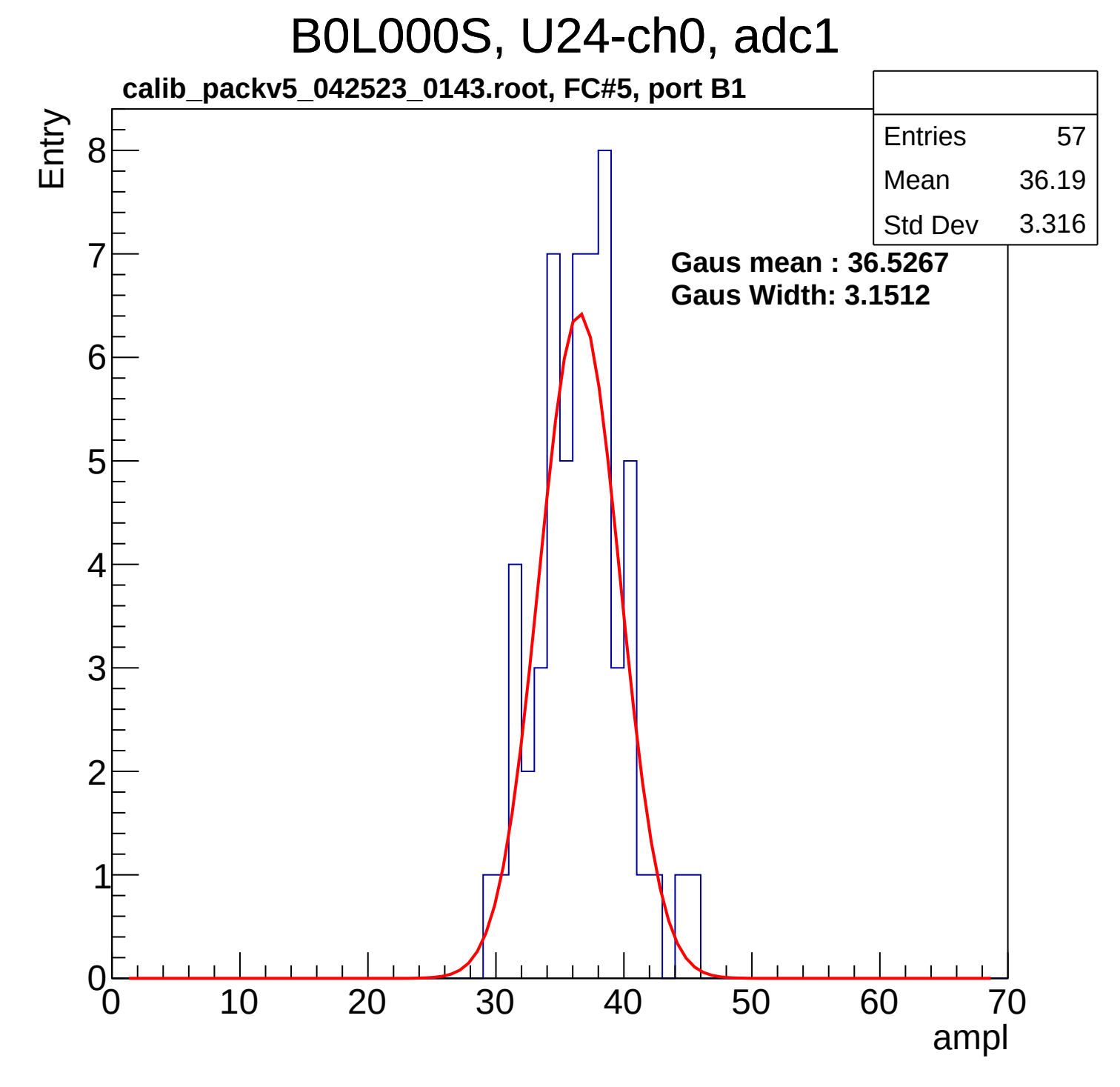
B0L000S, U24-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.19
Std Dev 3.316
Gaus mean : 36.5267
Gaus Width: 3.1512
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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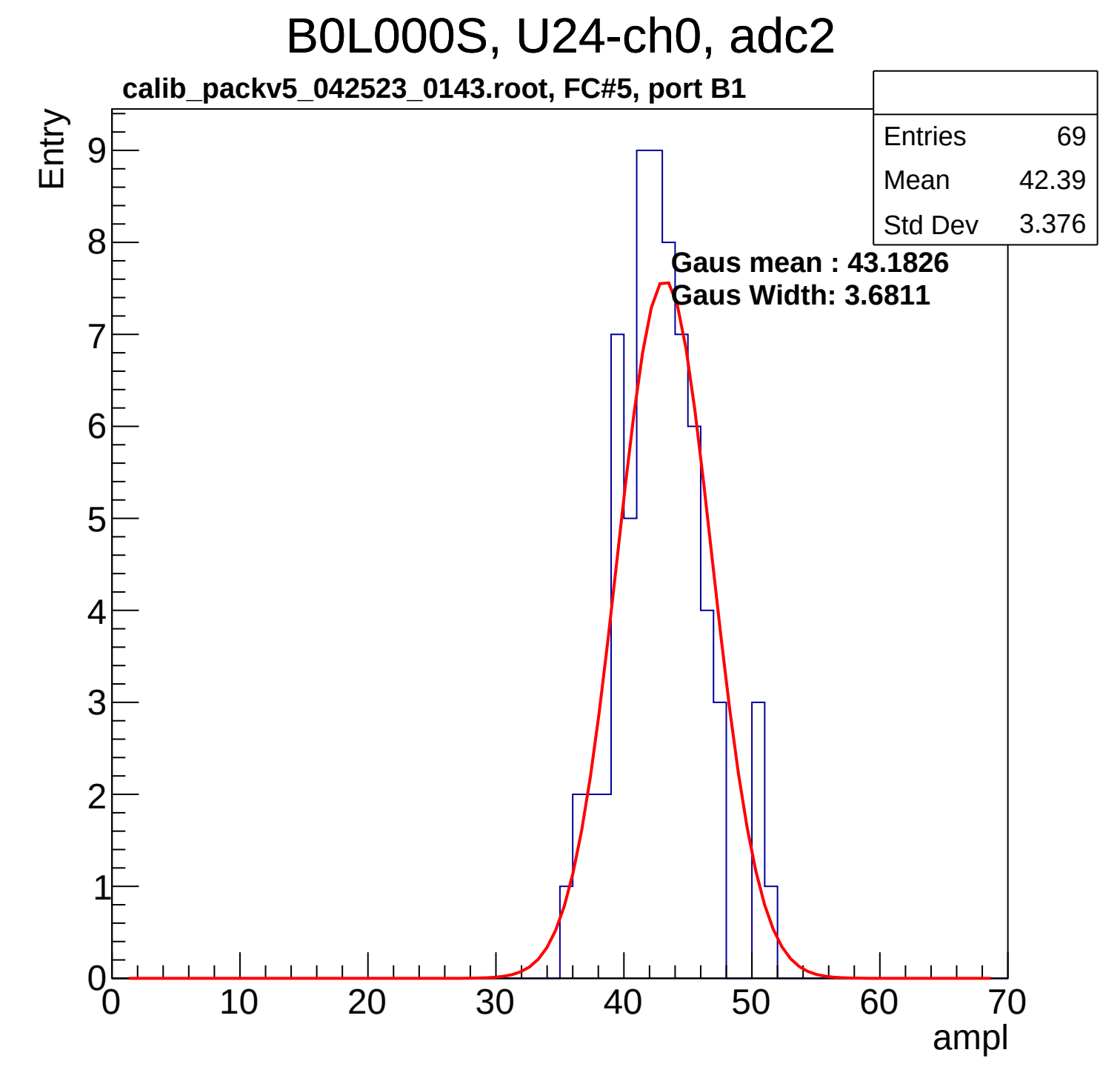
B0L000S, U24-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.39
Std Dev 3.376
Gaus mean : 43.1826
Gaus Width: 3.6811
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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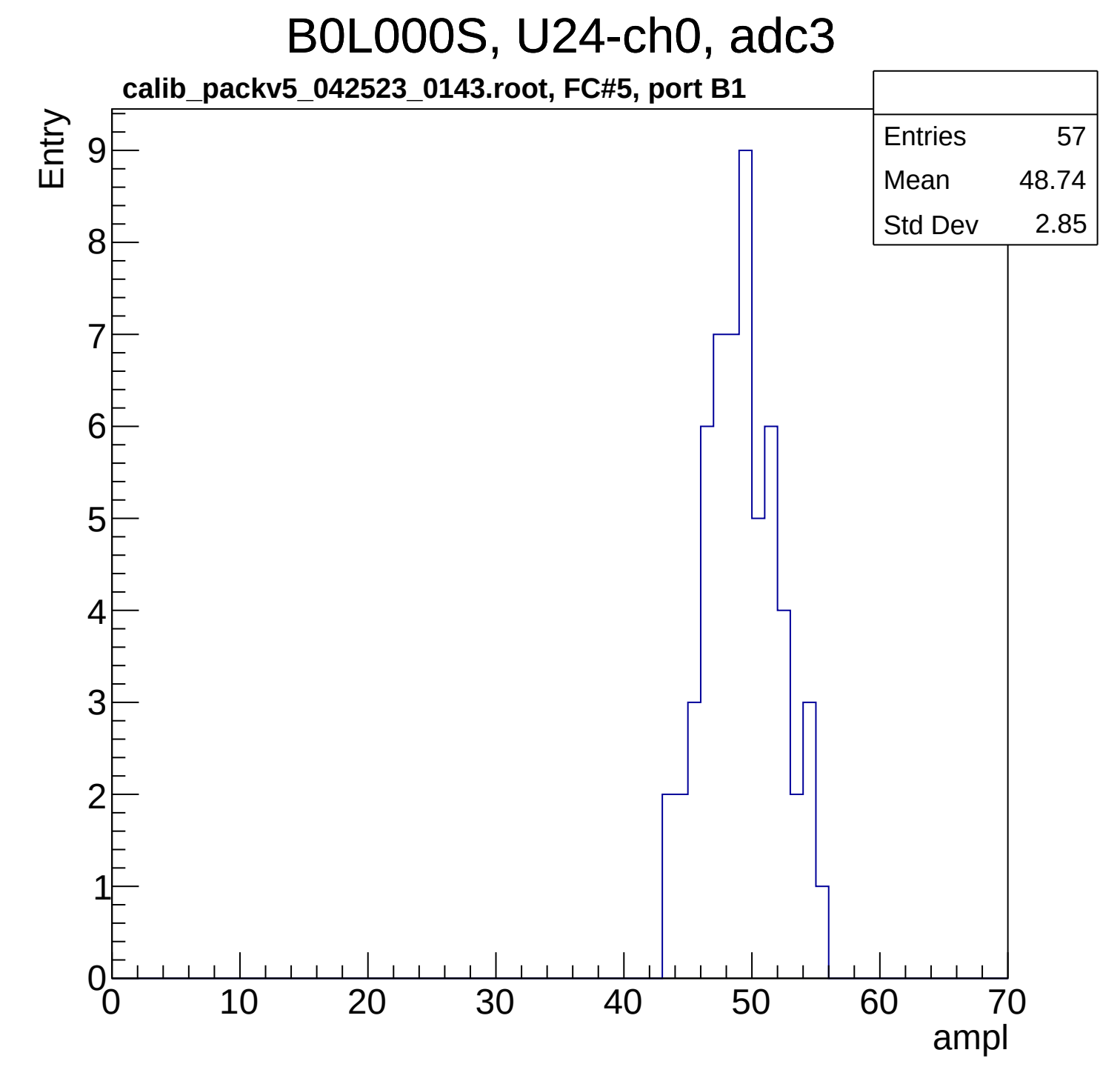
B0L000S, U24-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 48.74
Std Dev 2.85
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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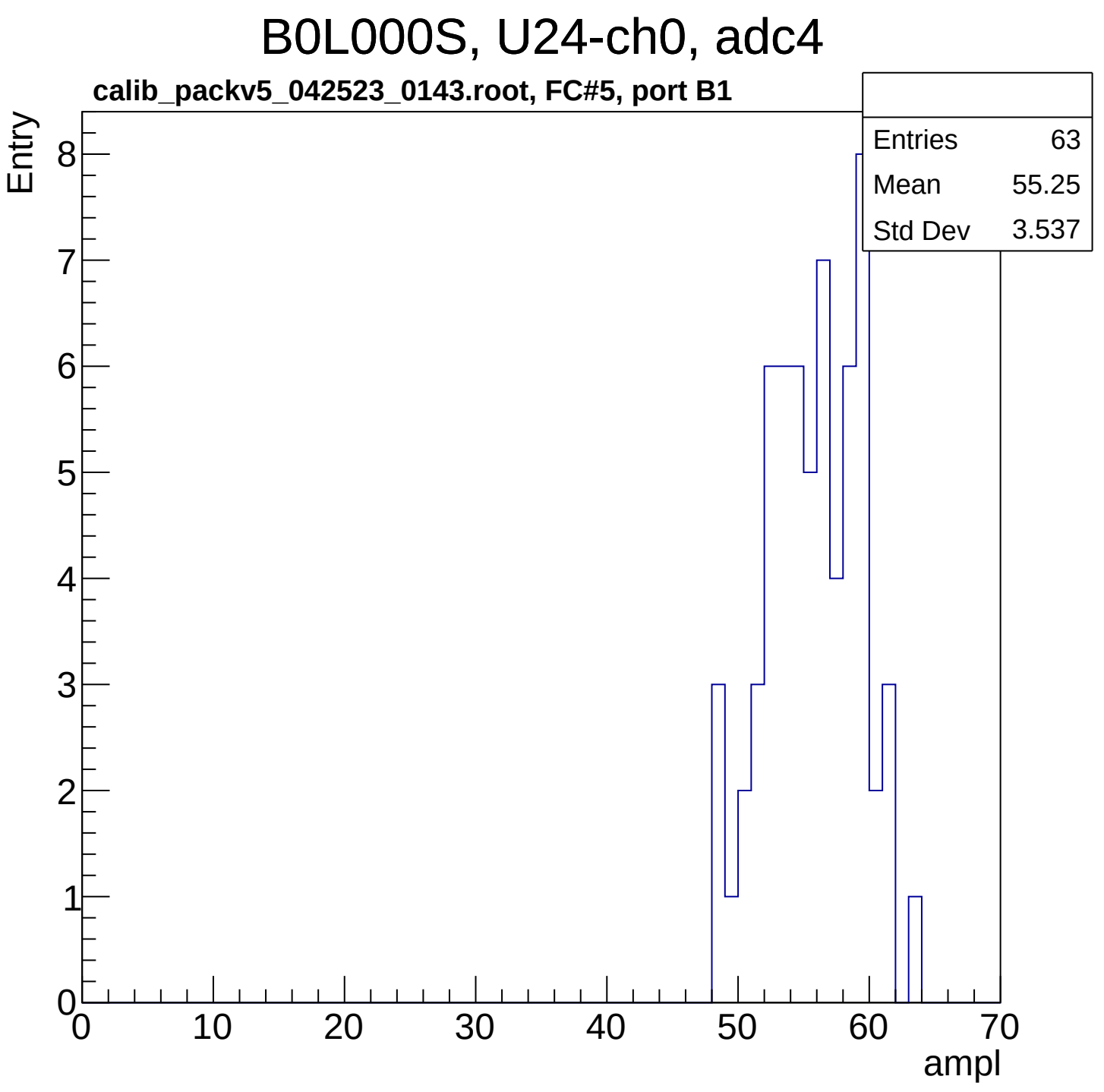
B0L000S, U24-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 55.25
Std Dev 3.537
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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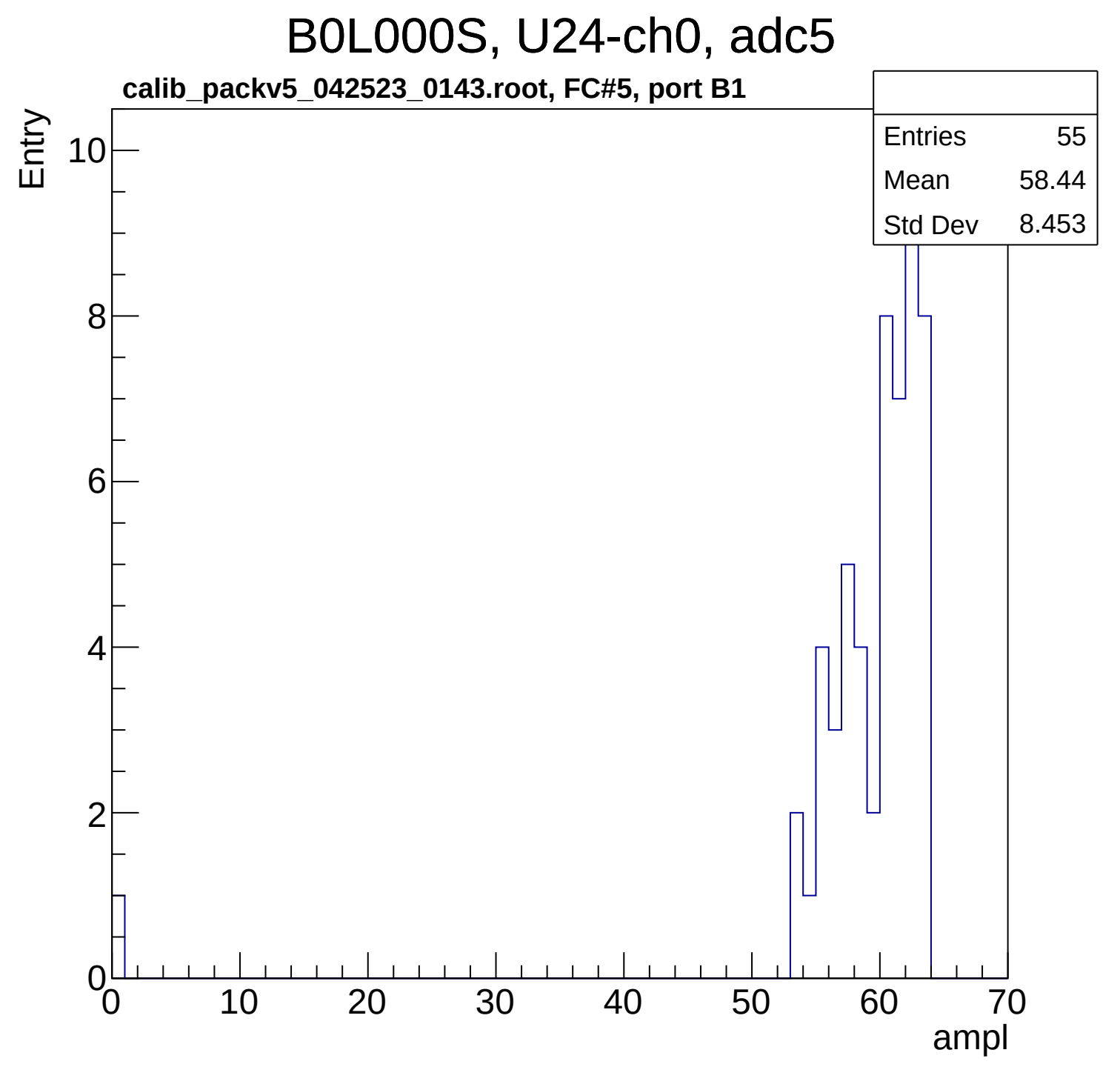
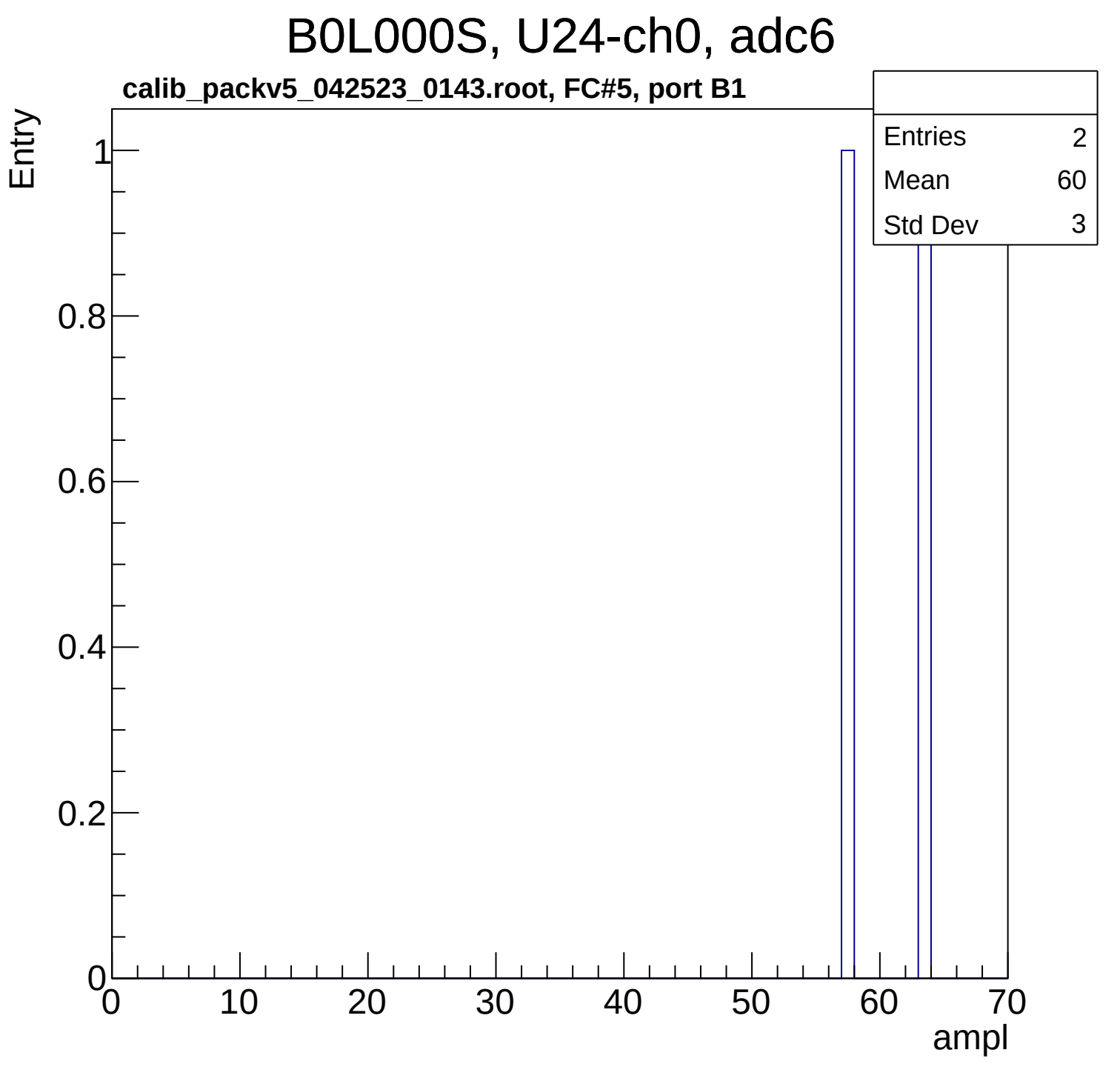

B0L000S, U24-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.44
Std Dev 8.453
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 60
Std Dev 3
Entry
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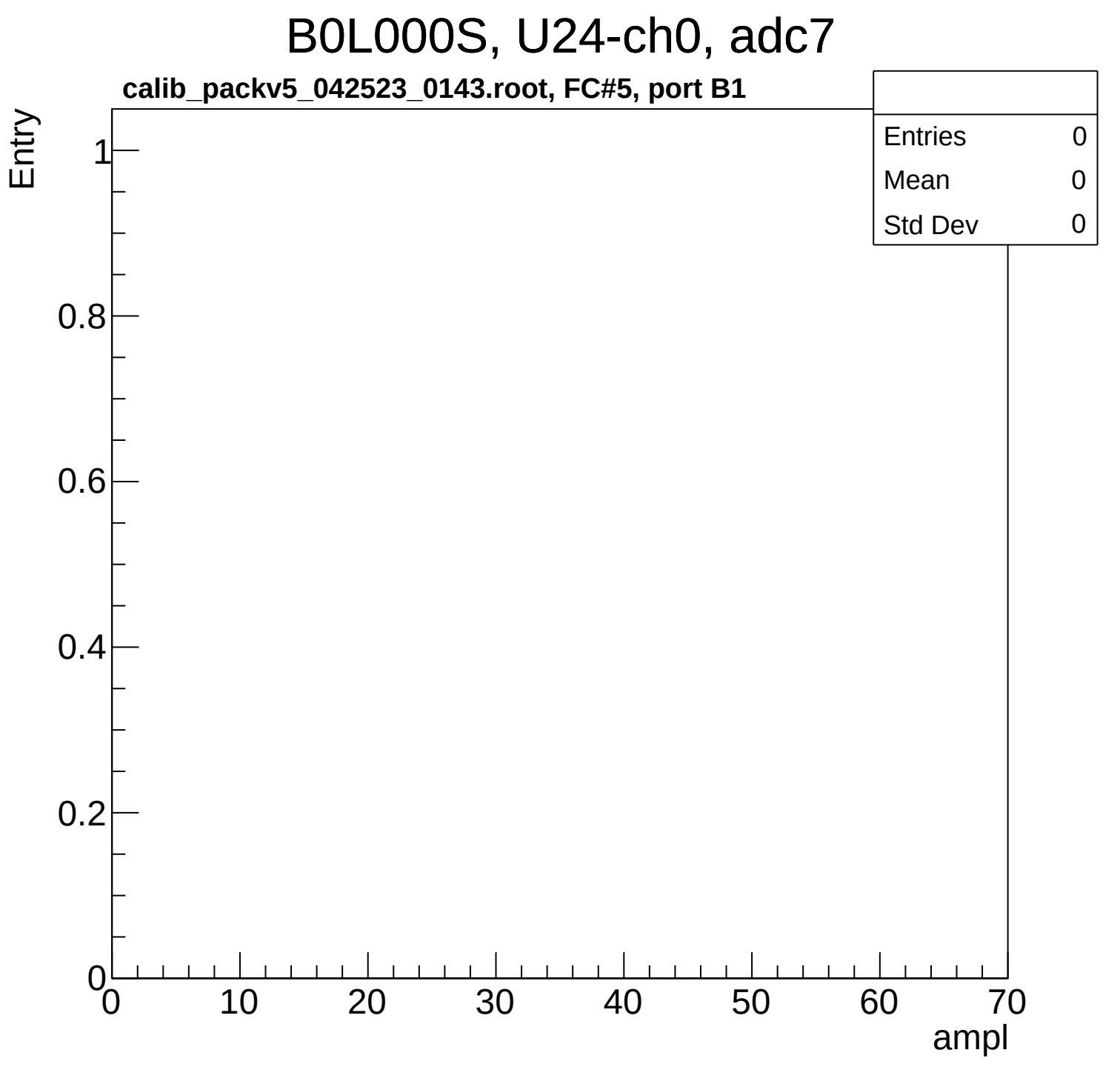
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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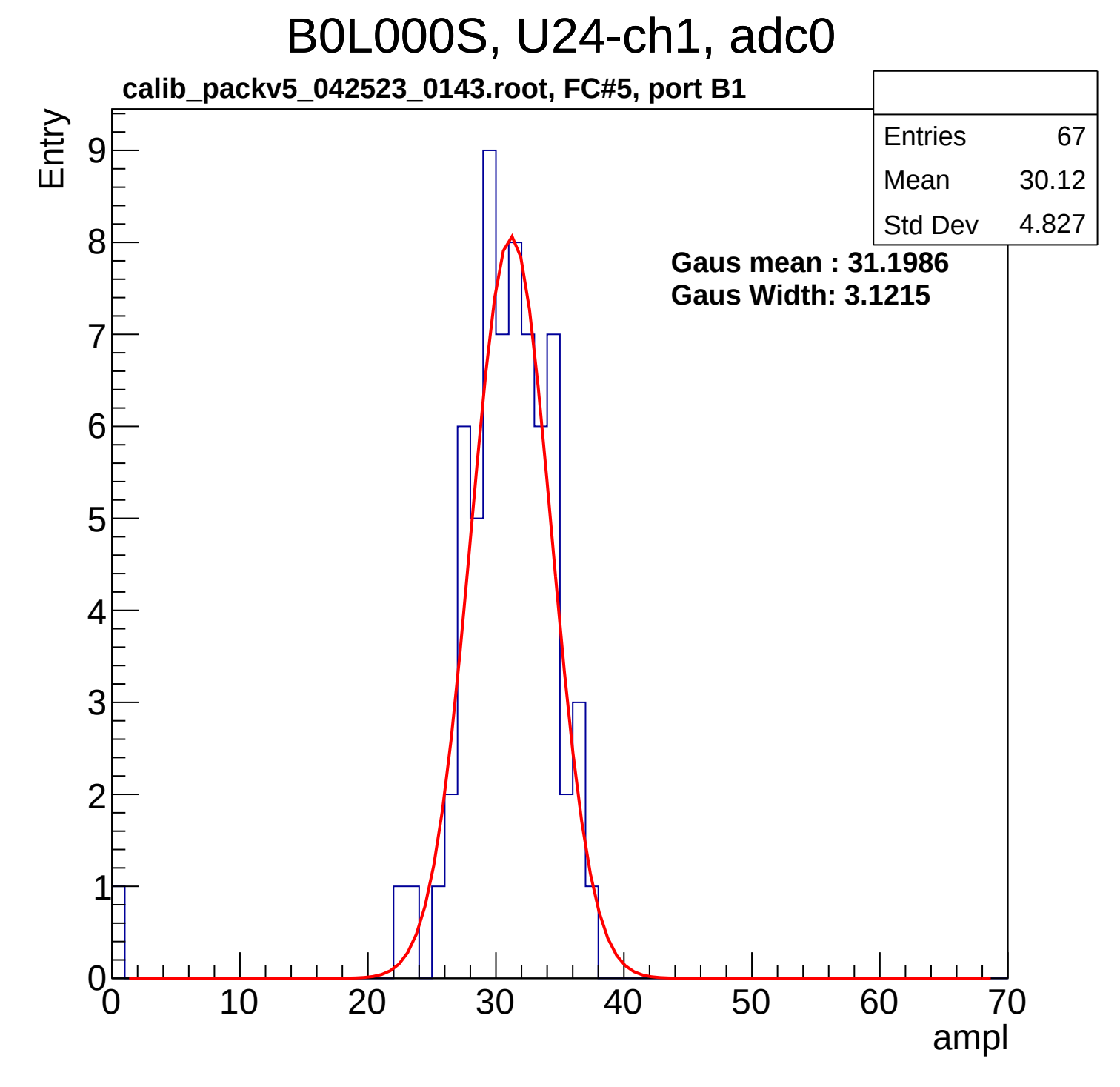
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.12
Std Dev 4.827
Gaus mean : 31.1986
Gaus Width: 3.1215
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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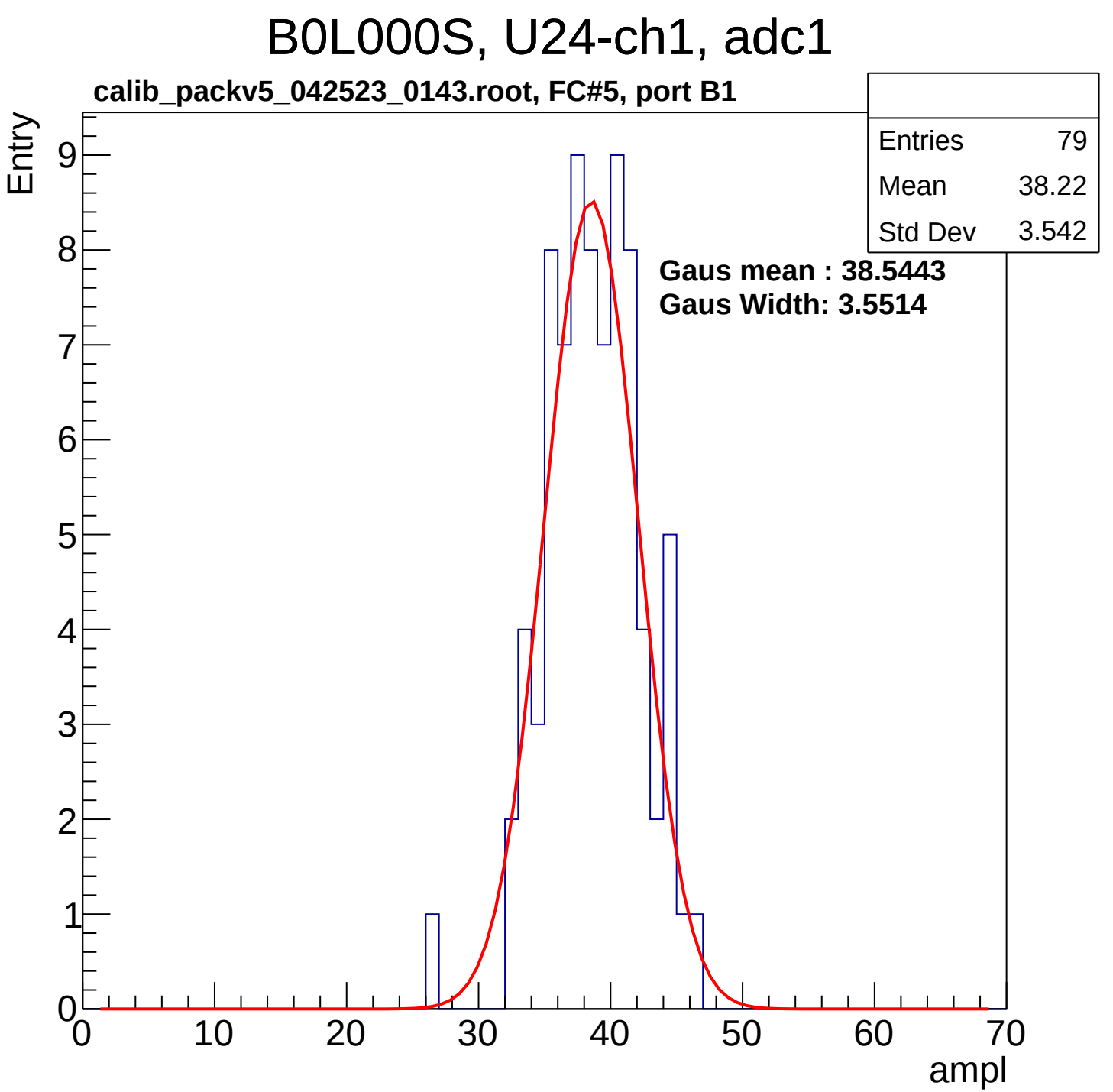
B0L000S, U24-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 38.22
Std Dev 3.542
Gaus mean : 38.5443
Gaus Width: 3.5514
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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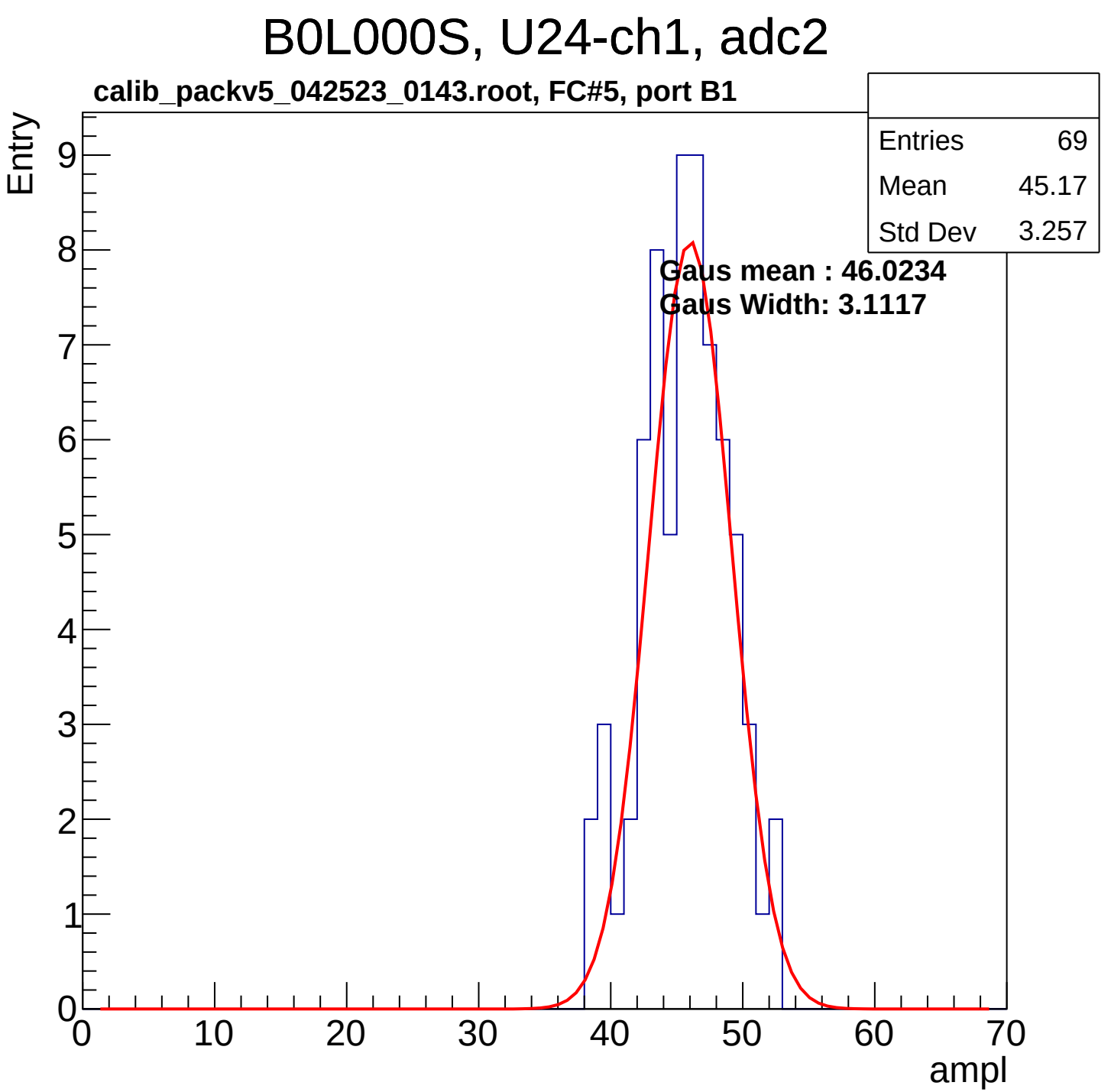
B0L000S, U24-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.17
Std Dev 3.257
Gaus mean : 46.0234
Gaus Width: 3.1117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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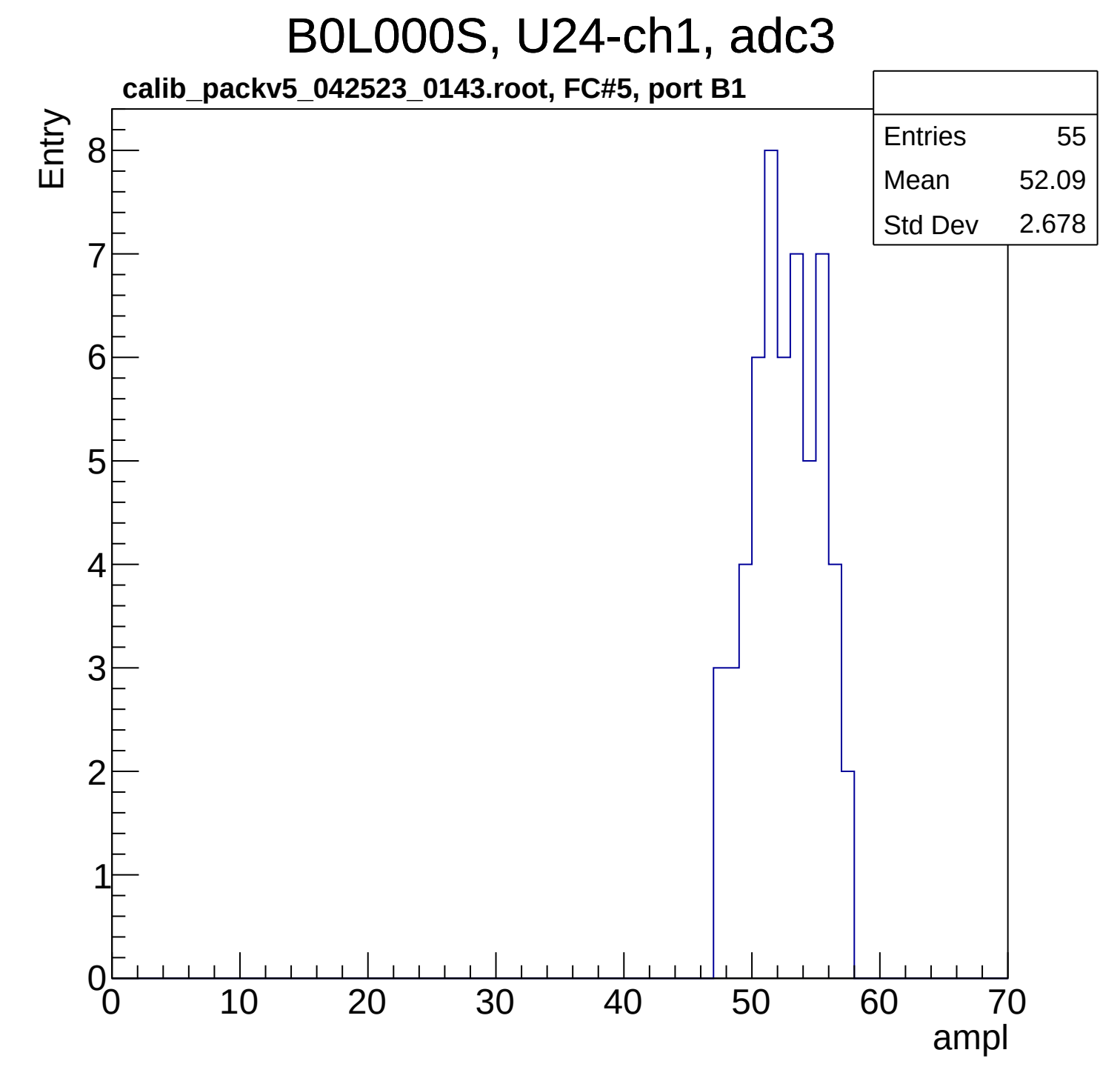
B0L000S, U24-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 52.09
Std Dev 2.678
Entry
0
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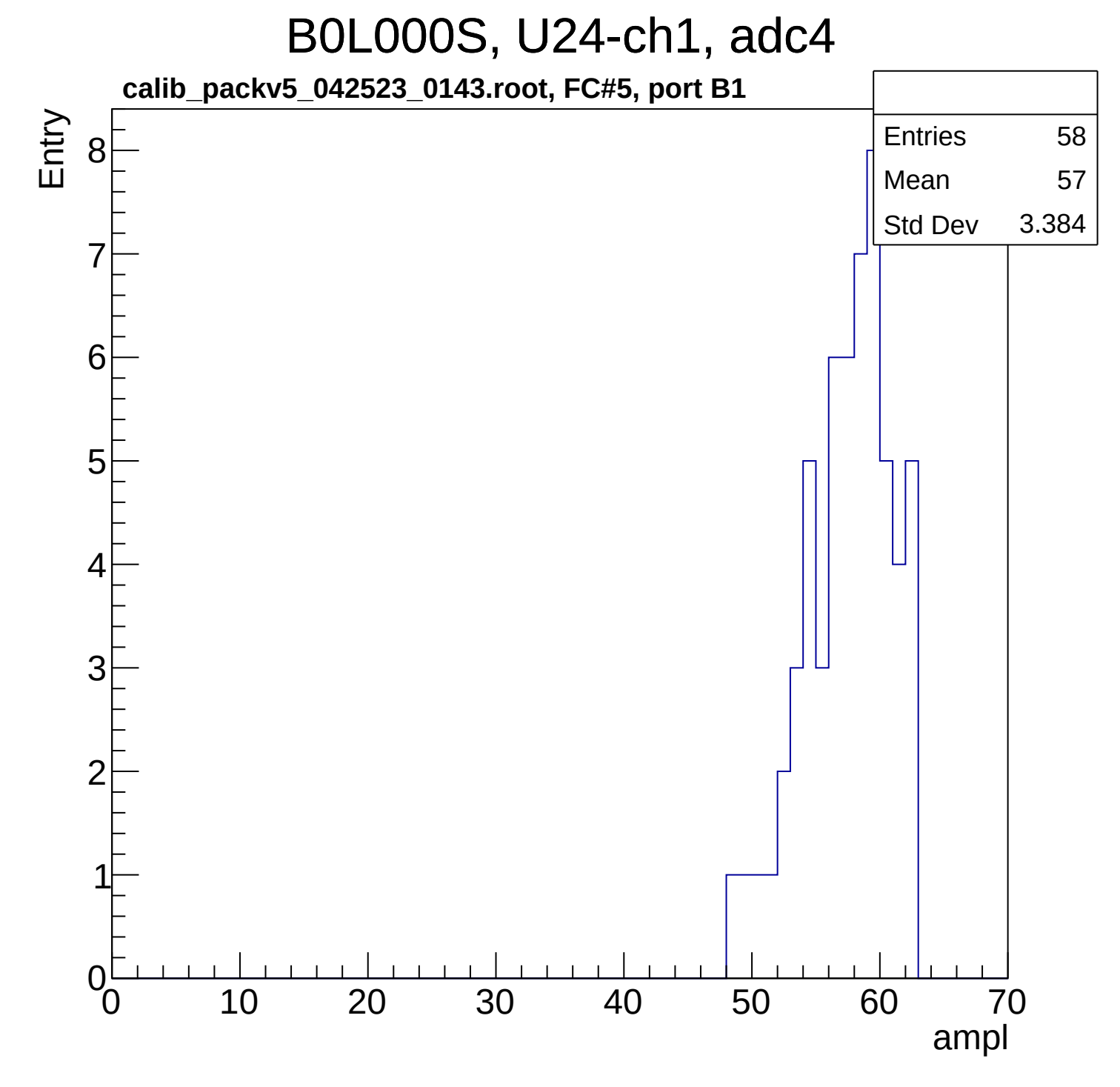
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57
Std Dev 3.384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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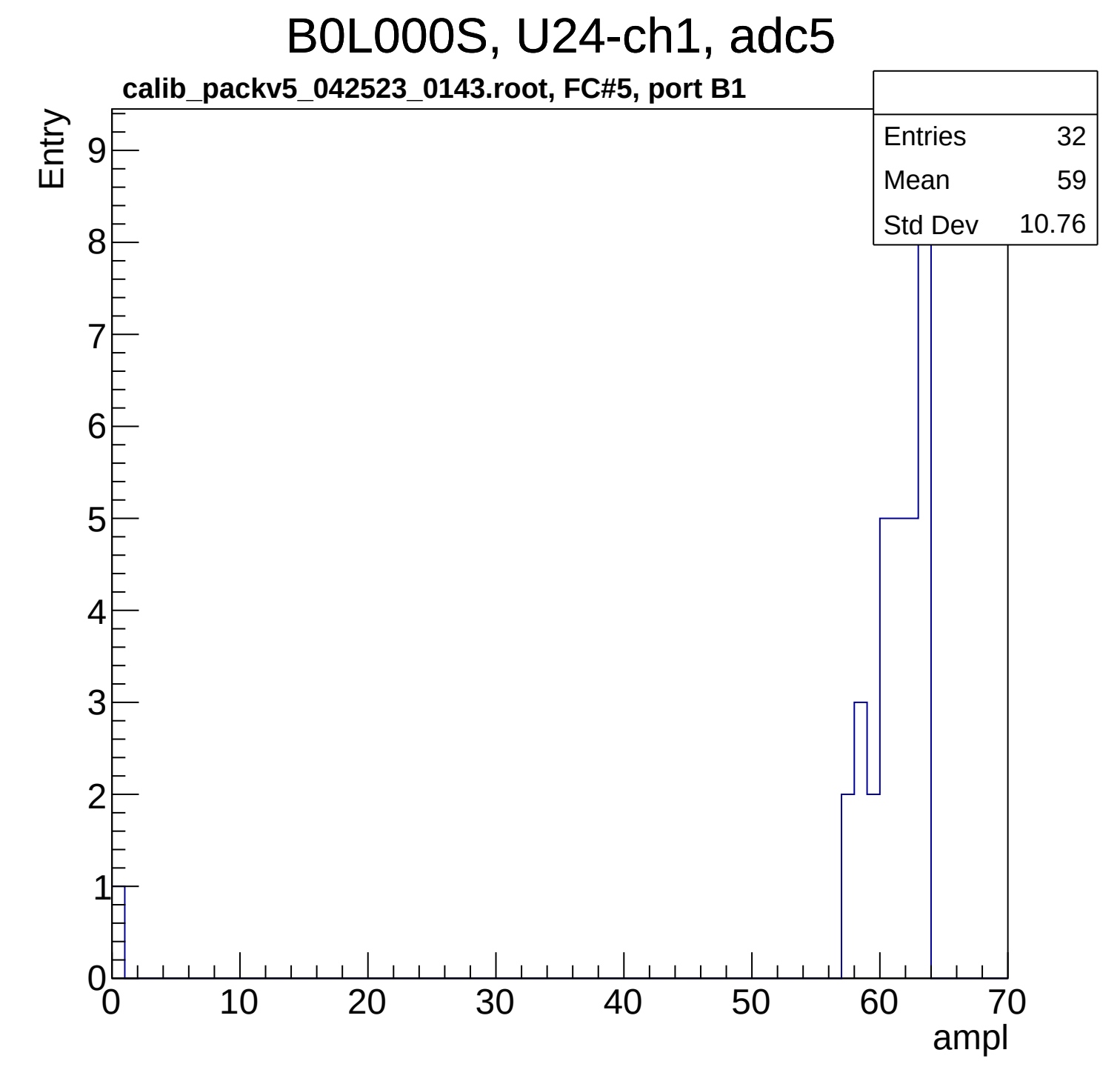
B0L000S, U24-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 59
Std Dev 10.76
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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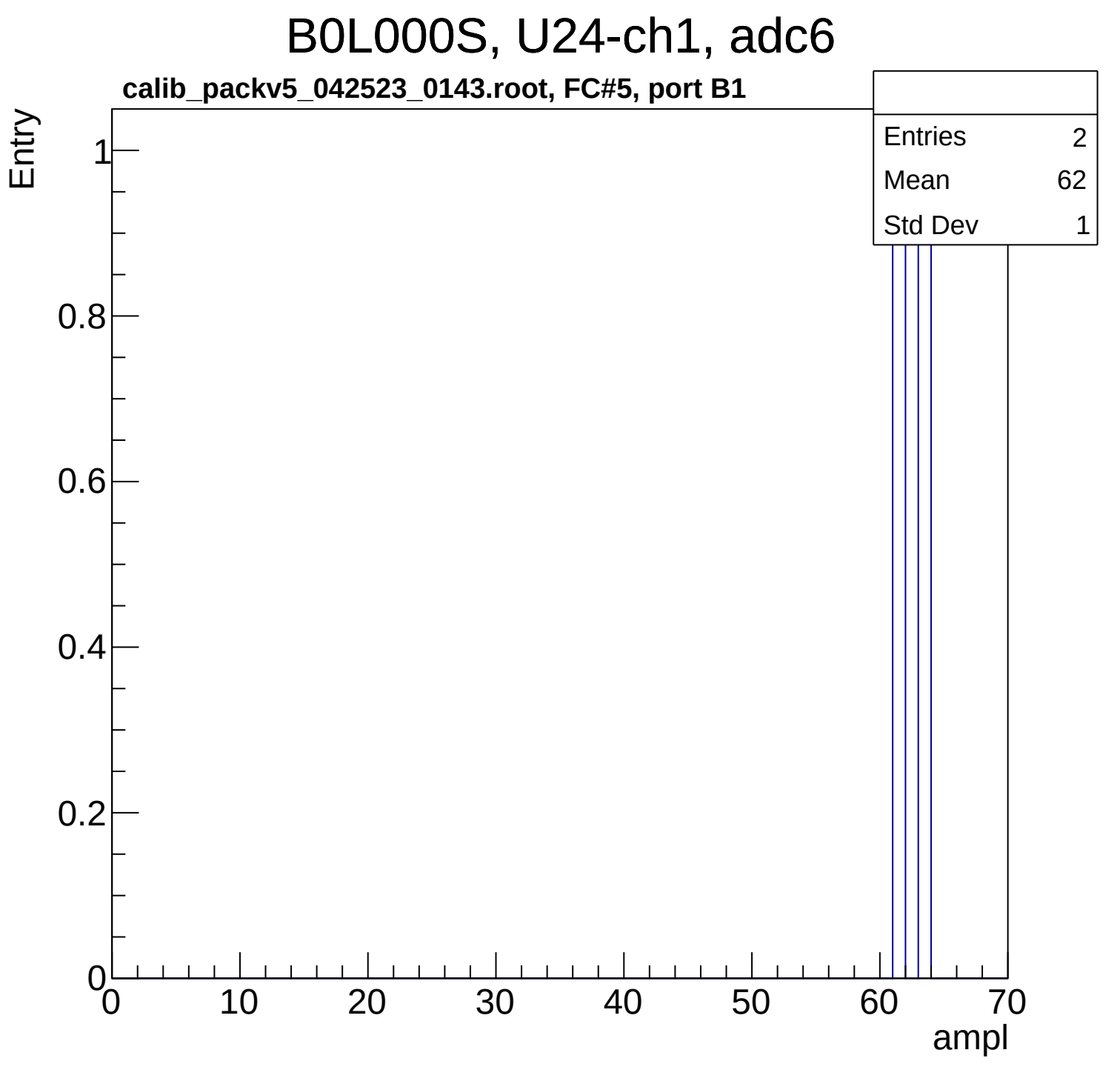
B0L000S, U24-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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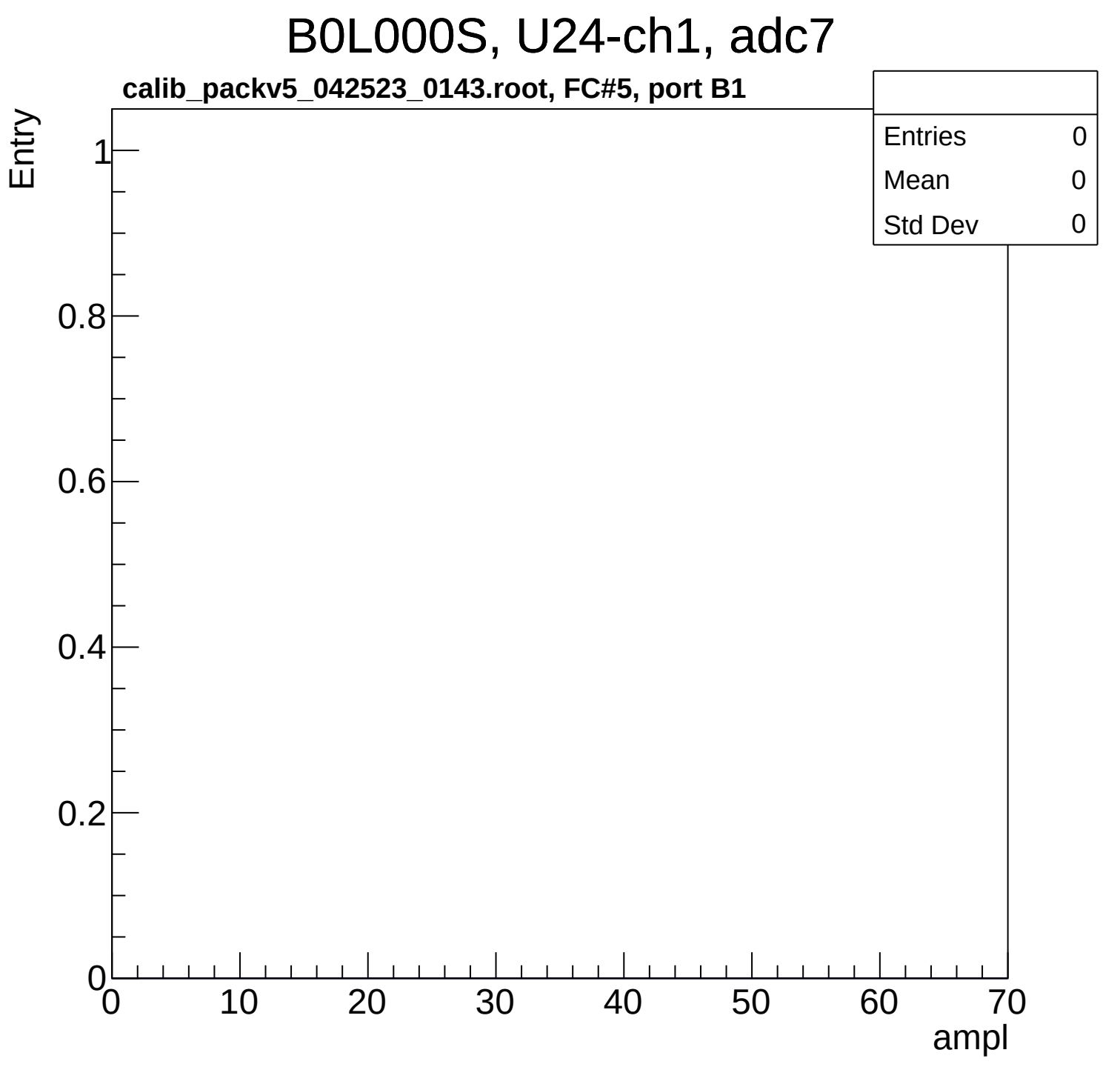
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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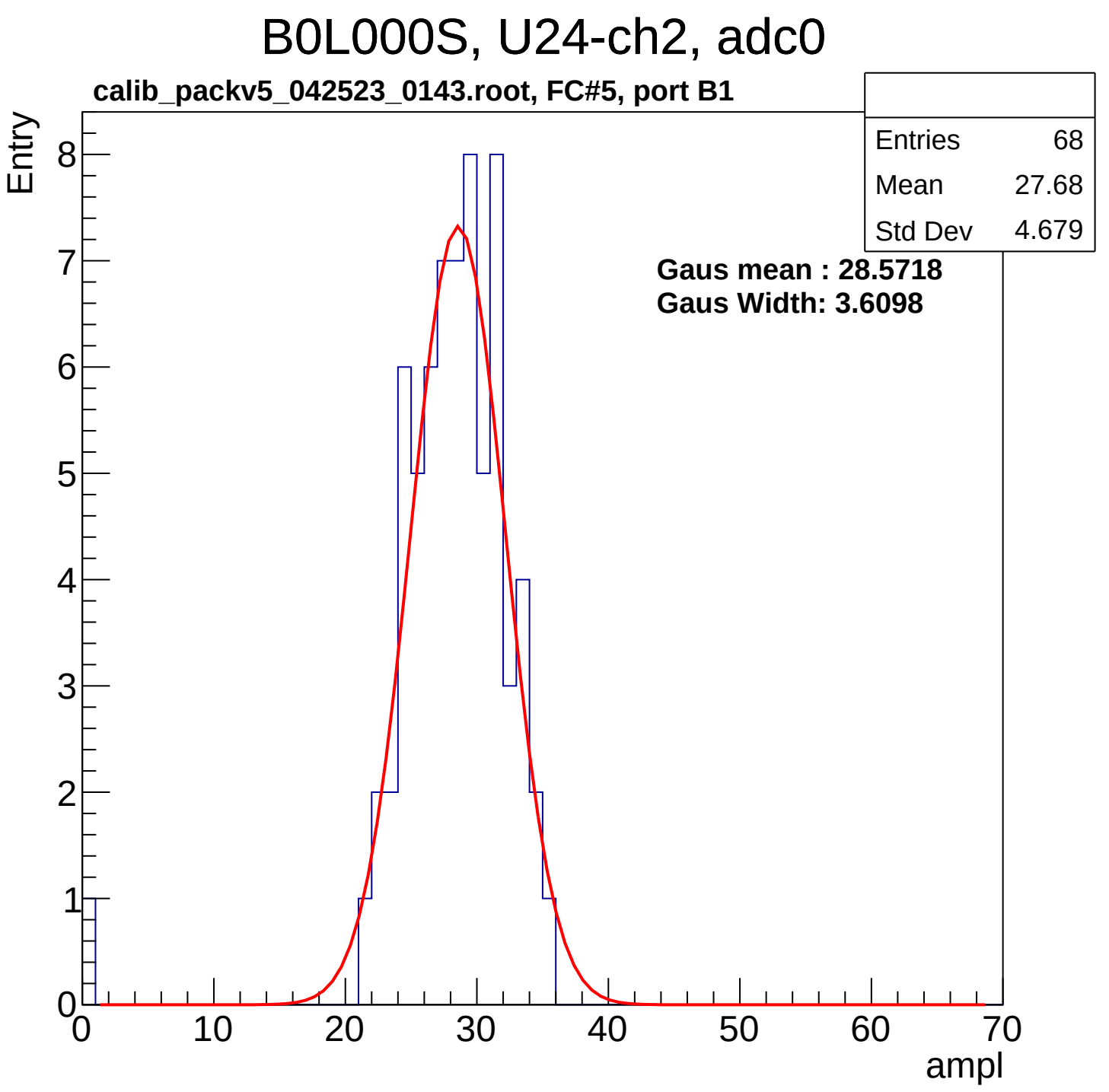
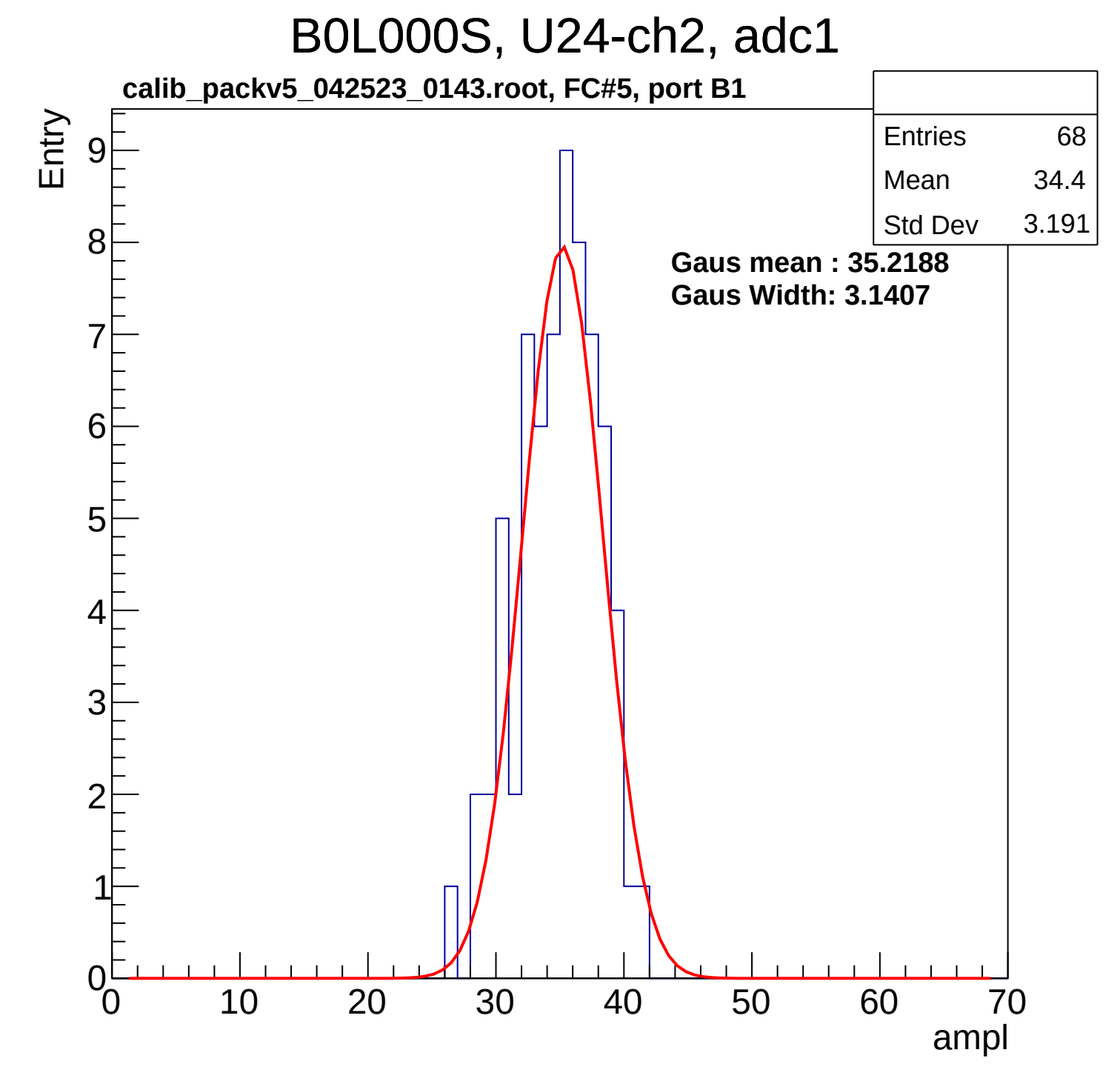
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 27.68
Std Dev 4.679
Gaus mean : 28.5718
Gaus Width: 3.6098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 34.4
Std Dev 3.191
Gaus mean : 35.2188
Gaus Width: 3.1407
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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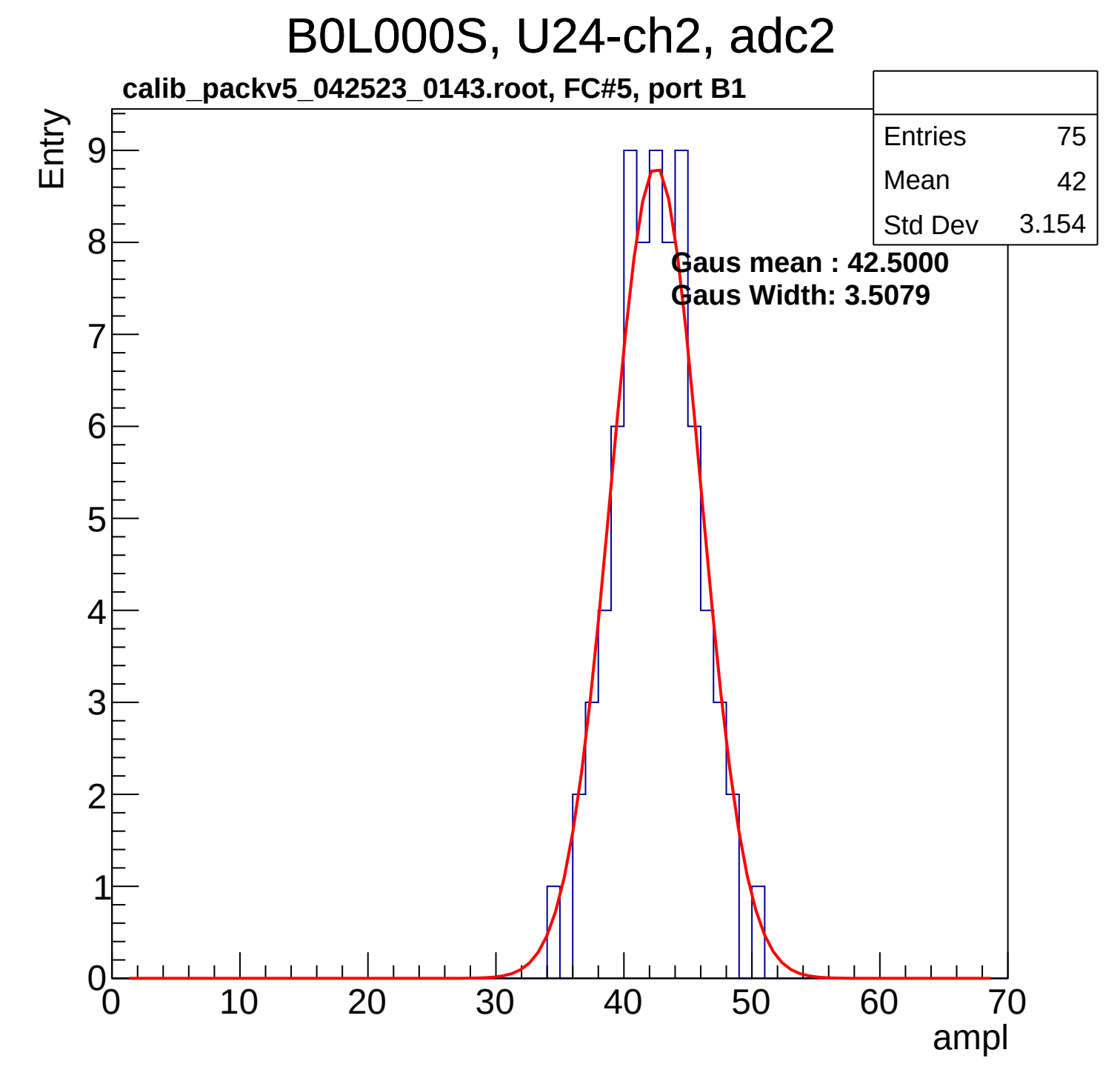
B0L000S, U24-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 42
Std Dev 3.154
Gaus mean : 42.5000
Gaus Width: 3.5079
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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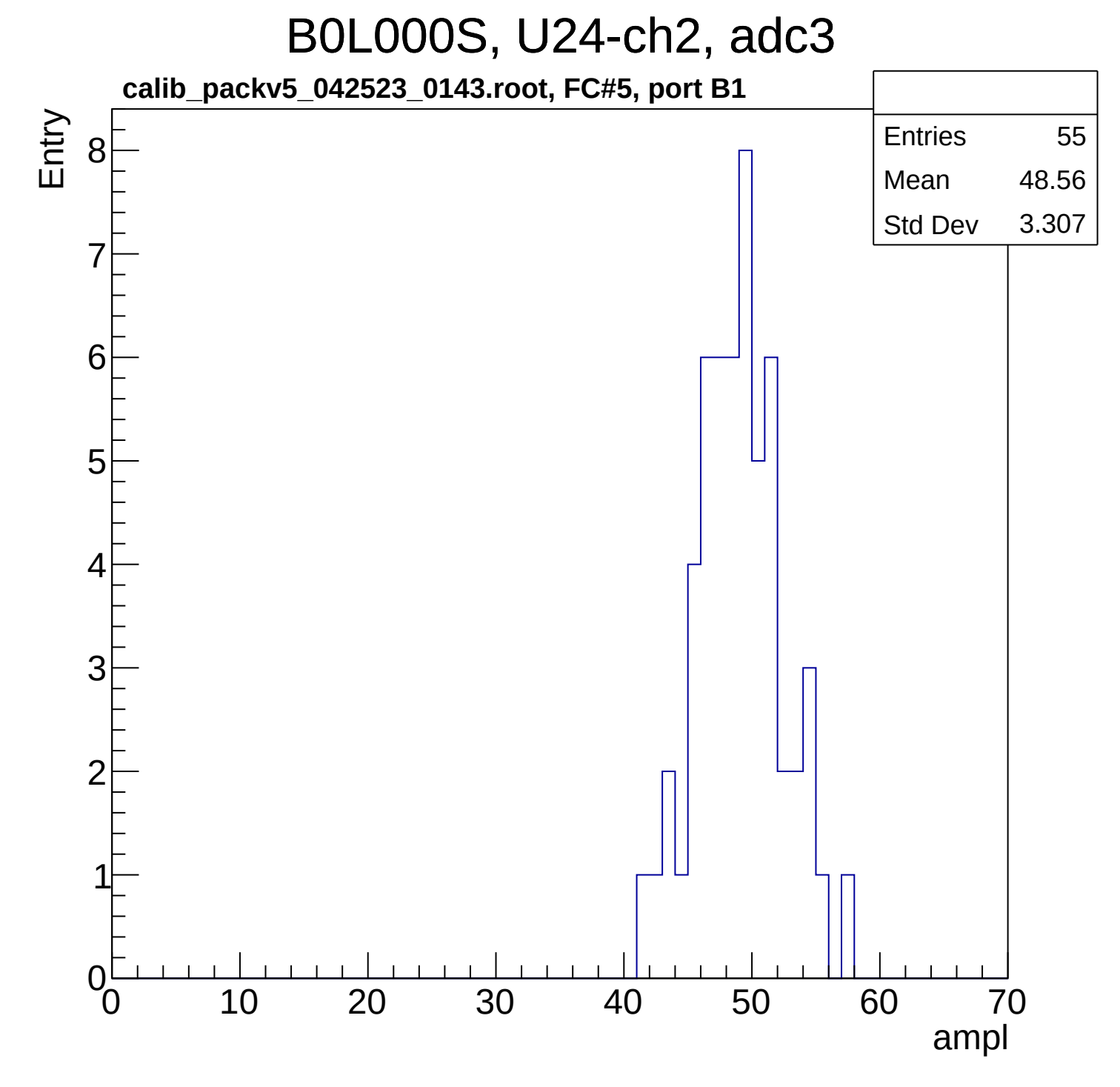
B0L000S, U24-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.56
Std Dev 3.307
Entry
ampl
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2
3
4
5
6
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0
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60
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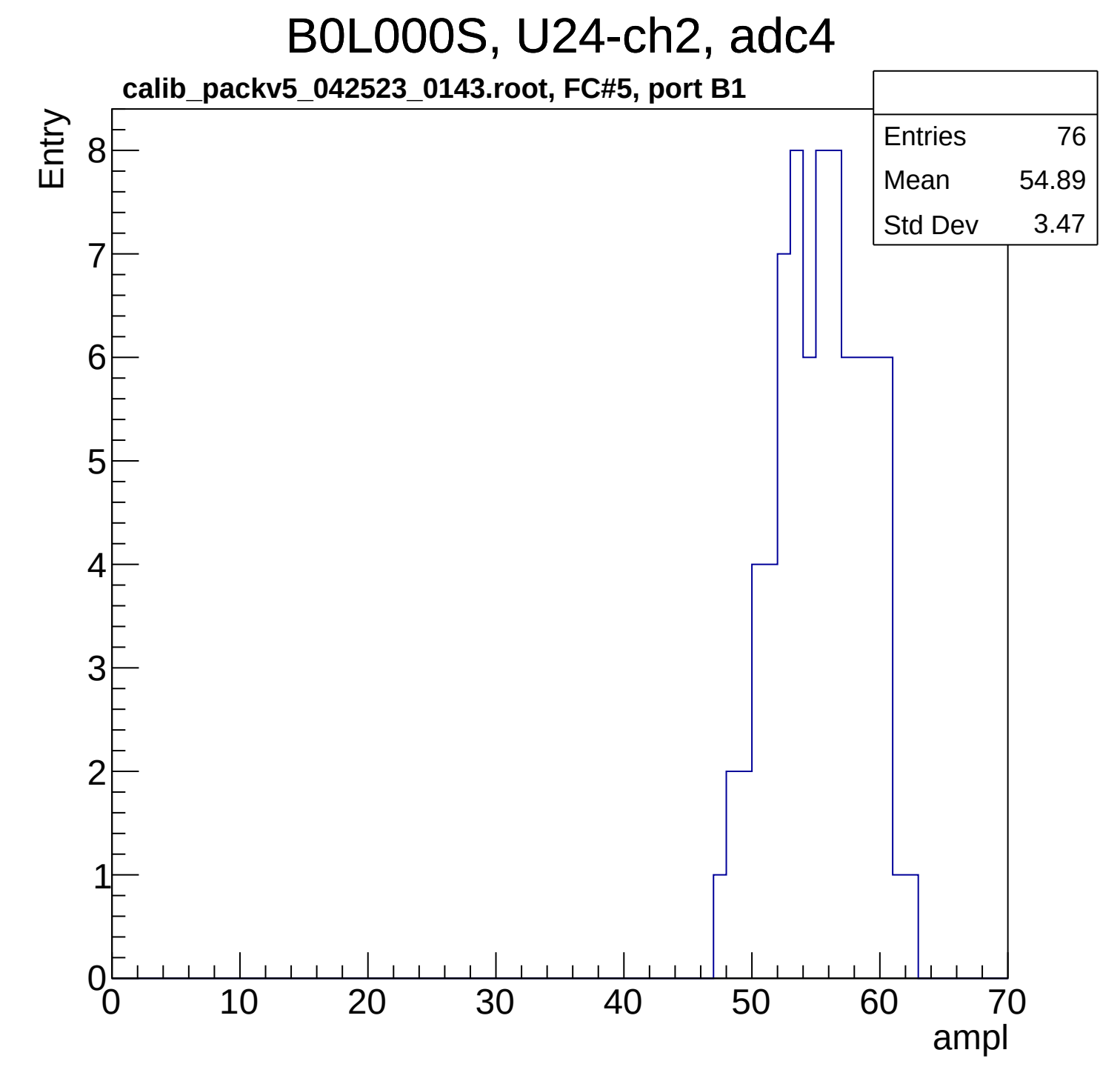
B0L000S, U24-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 54.89
Std Dev 3.47
Entry
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3
4
5
6
7
8
0
10
20
30
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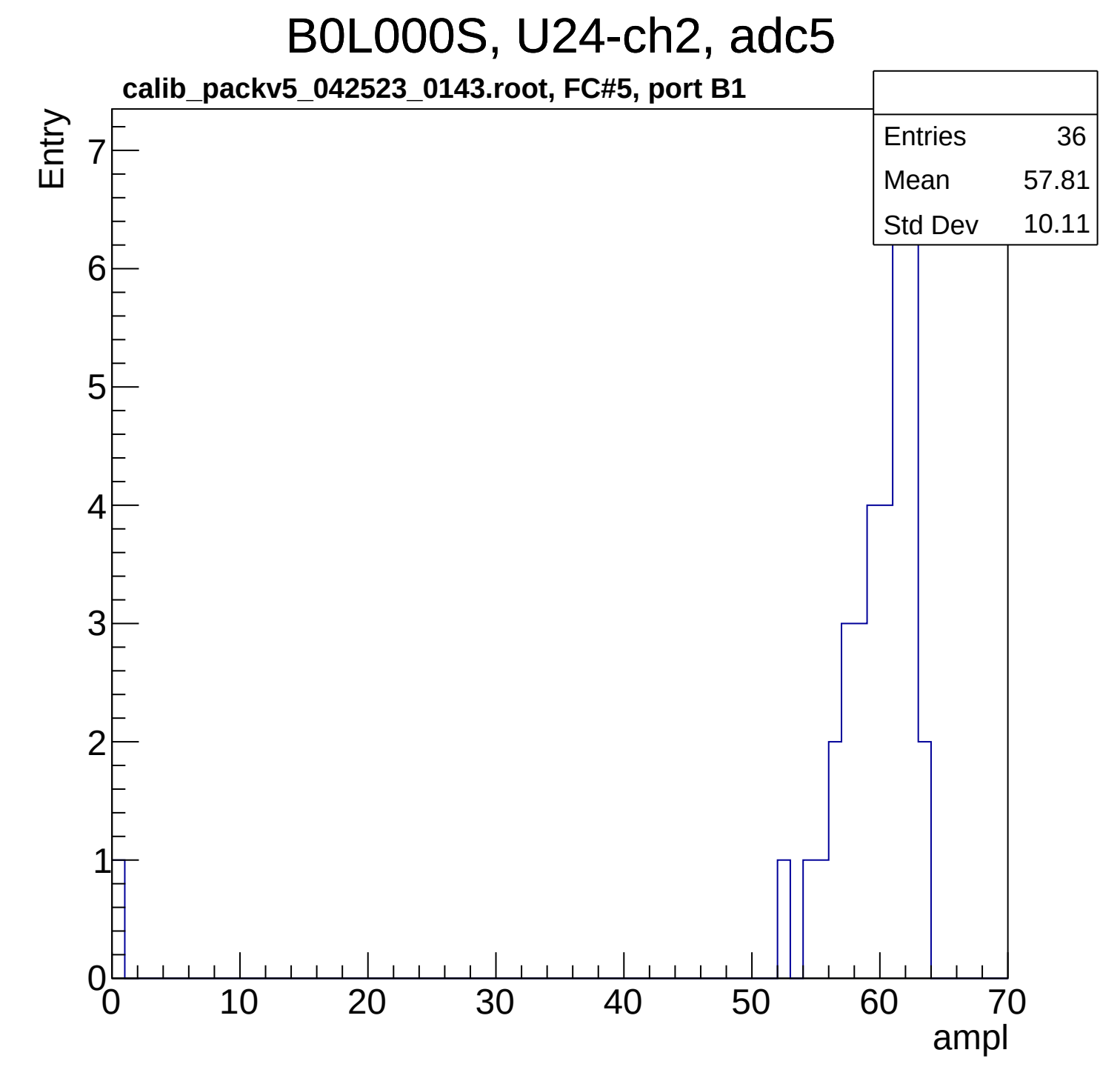
50
60
70
ampl


B0L000S, U24-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 57.81
Std Dev 10.11
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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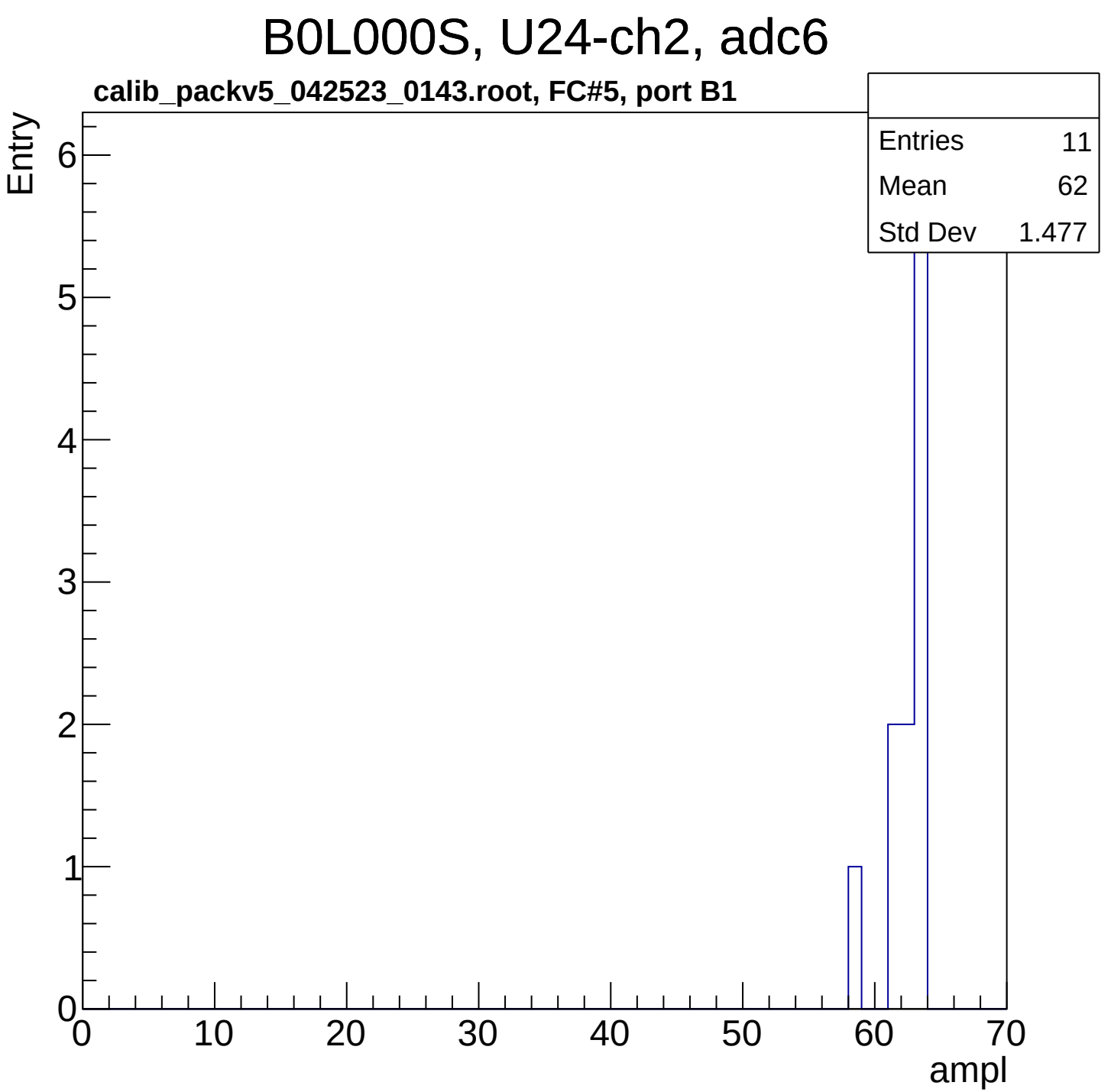
B0L000S, U24-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 62
Std Dev 1.477
Entry
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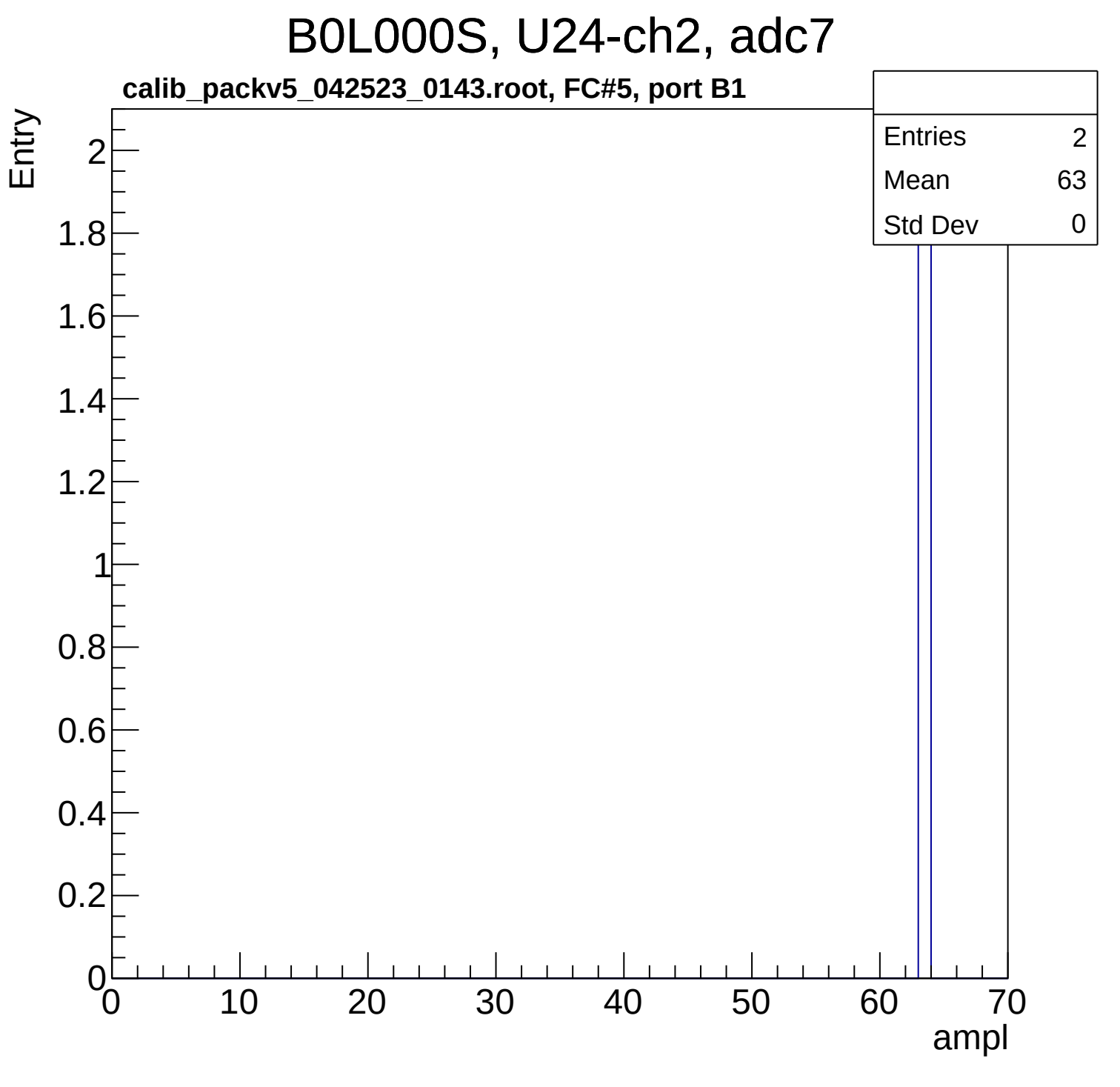
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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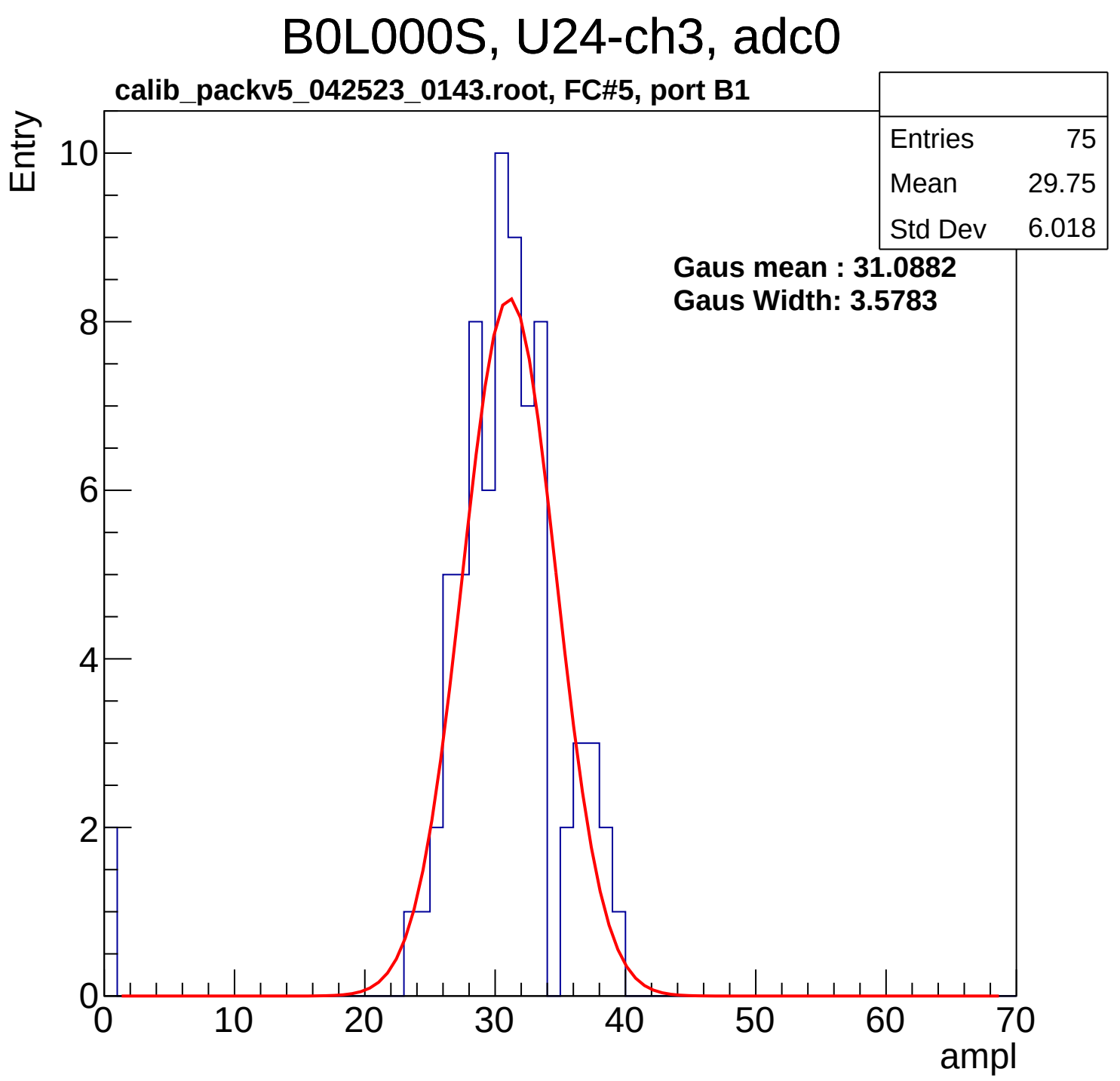
40
50
60
70


B0L000S, U24-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.75
Std Dev 6.018
Gaus mean : 31.0882
Gaus Width: 3.5783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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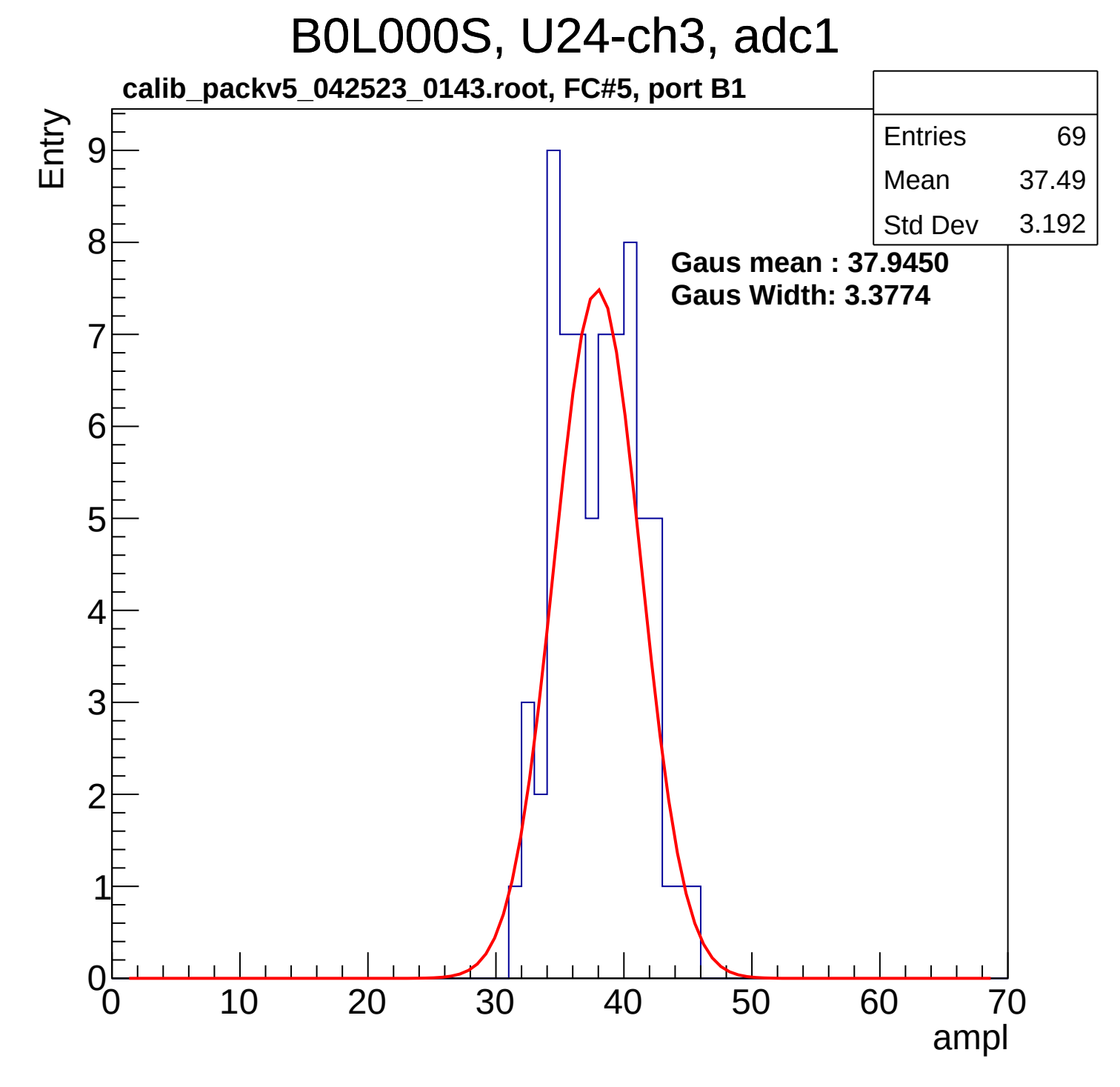
B0L000S, U24-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 37.49
Std Dev 3.192
Gaus mean : 37.9450
Gaus Width: 3.3774
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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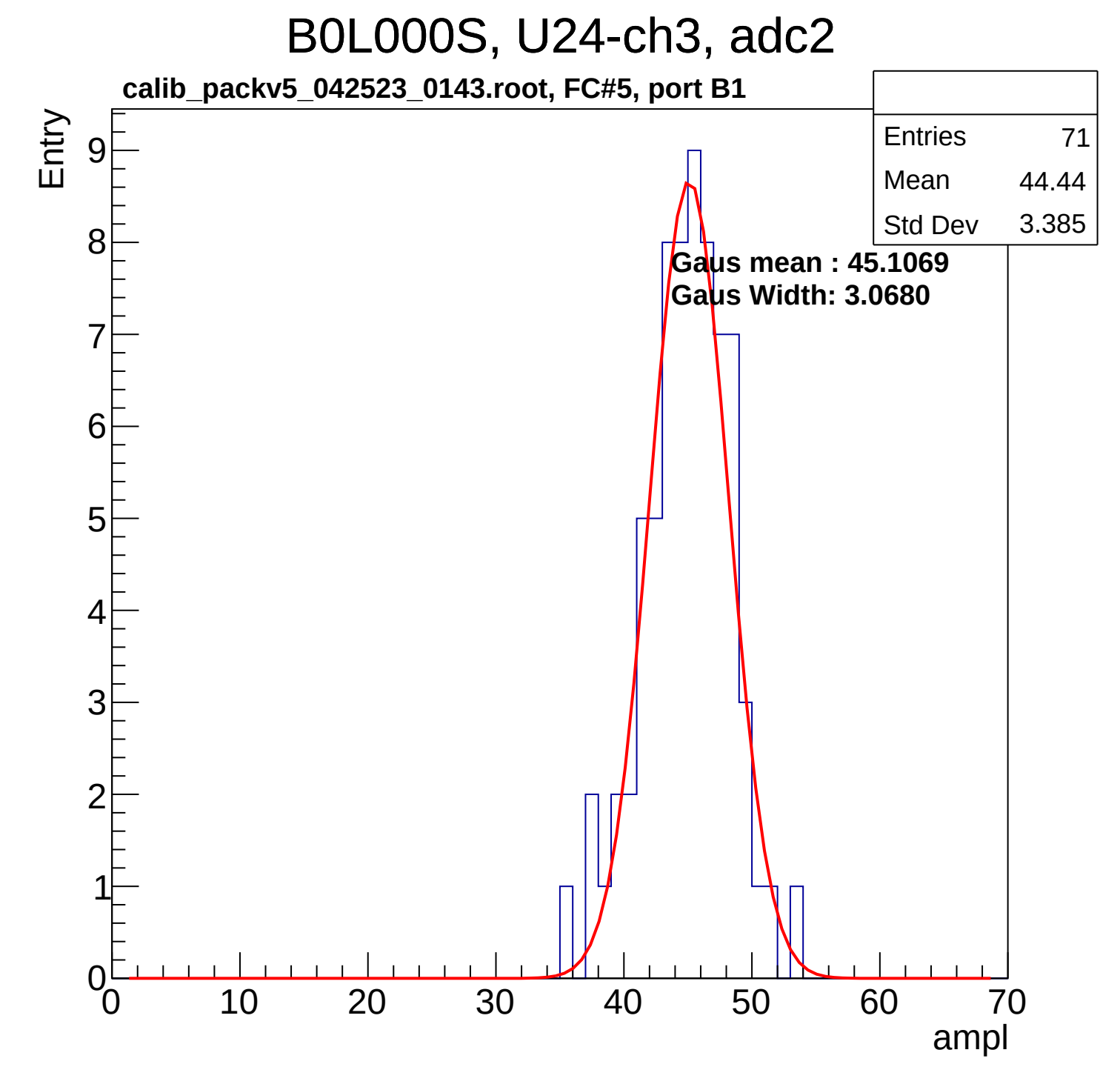
B0L000S, U24-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 44.44
Std Dev 3.385
Gaus mean : 45.1069
Gaus Width: 3.0680
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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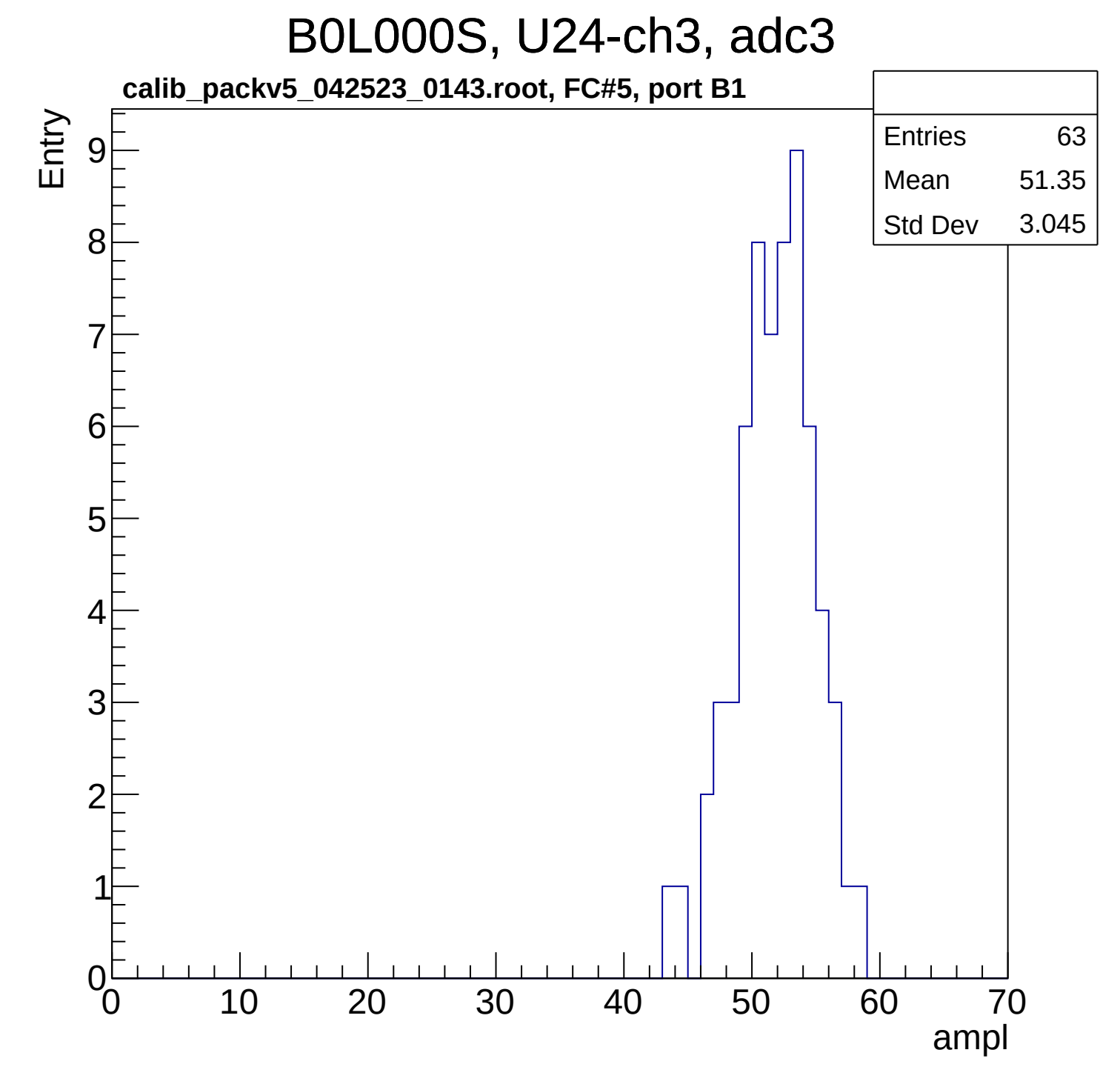
B0L000S, U24-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.35
Std Dev 3.045
Entry
ampl
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3
4
5
6
7
8
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0
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20
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60
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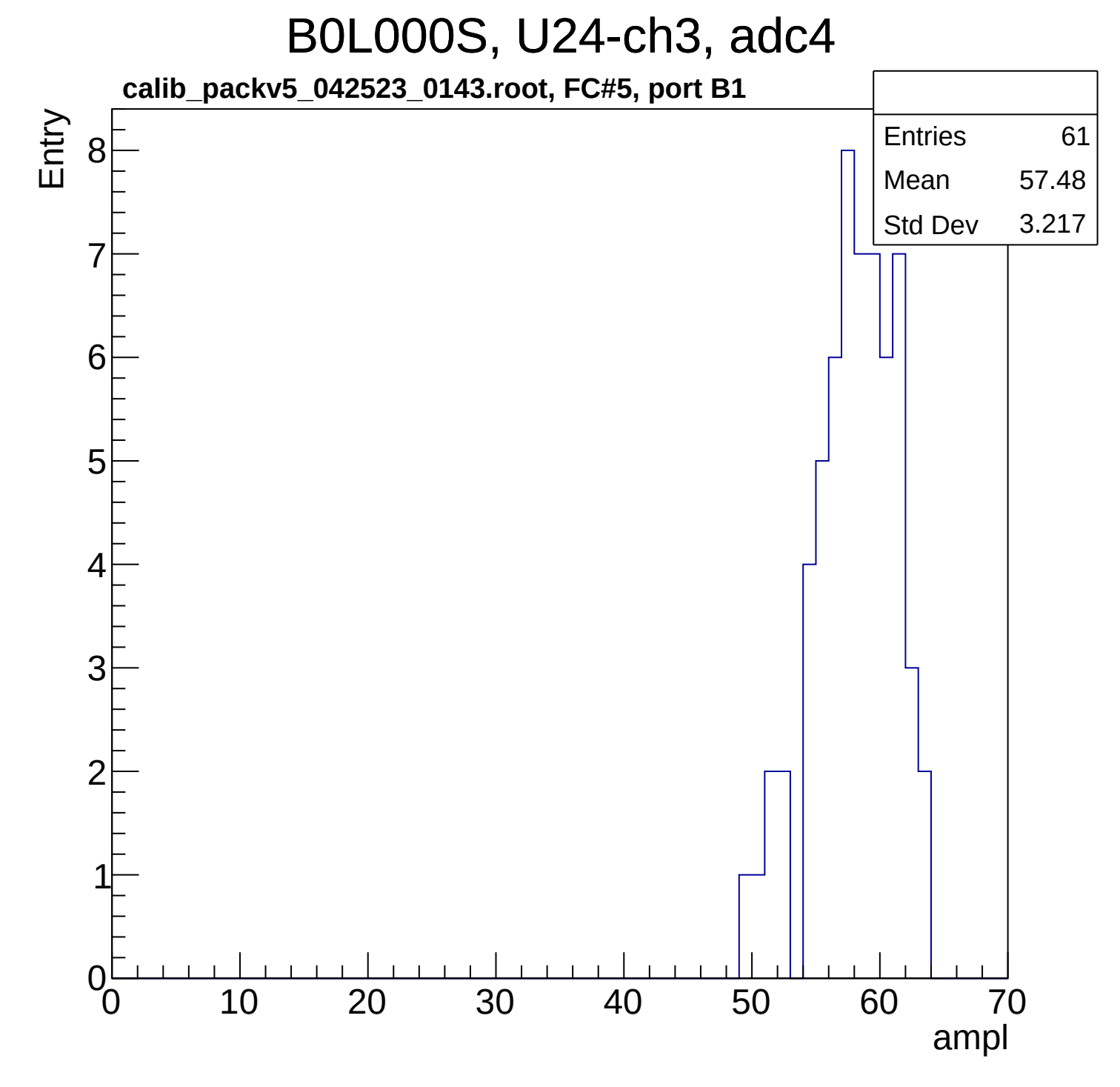
B0L000S, U24-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.48
Std Dev 3.217
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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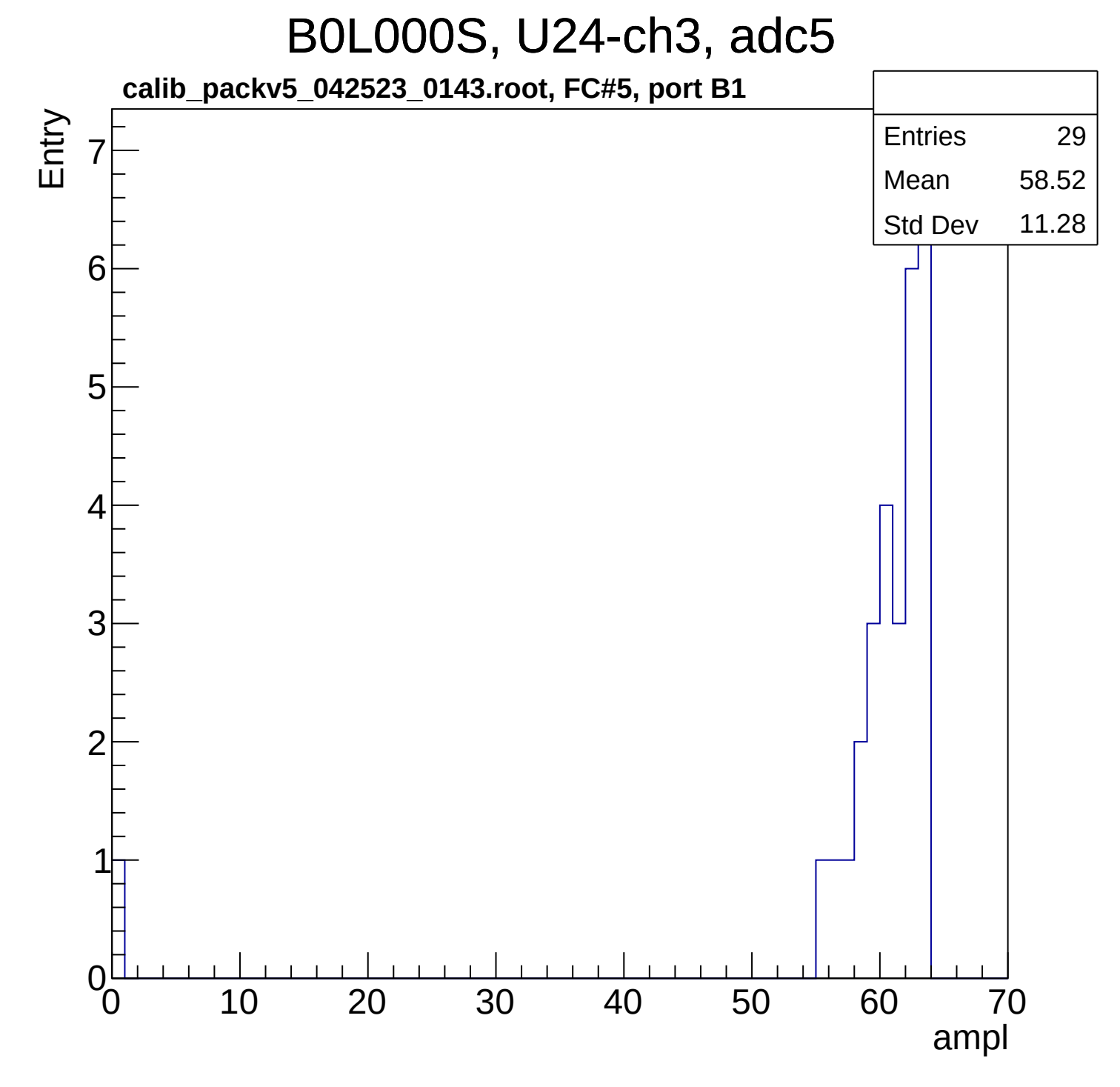
B0L000S, U24-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58.52
Std Dev 11.28
Entry
ampl
0
1
2
3
4
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7
0
10
20
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60
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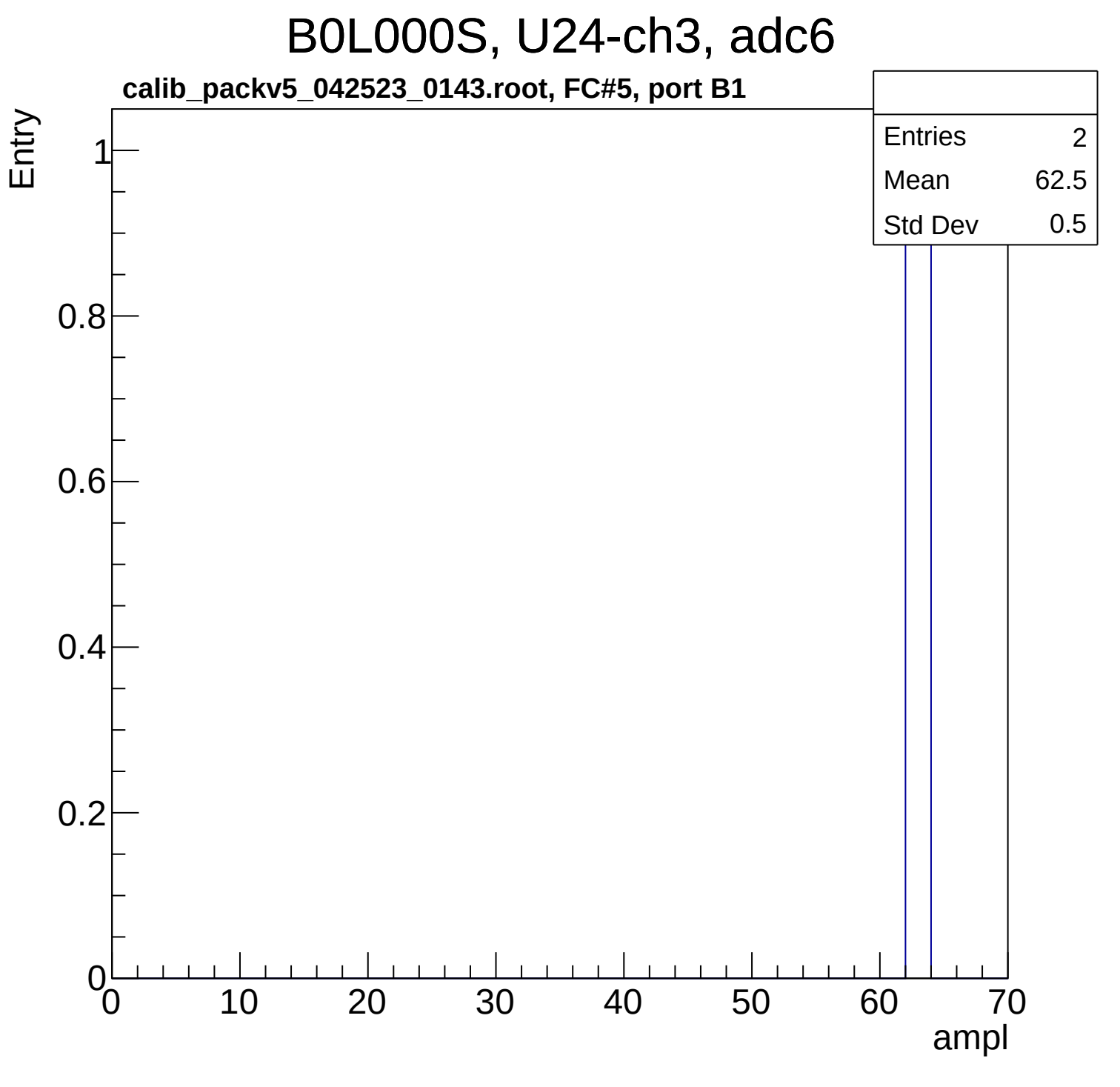
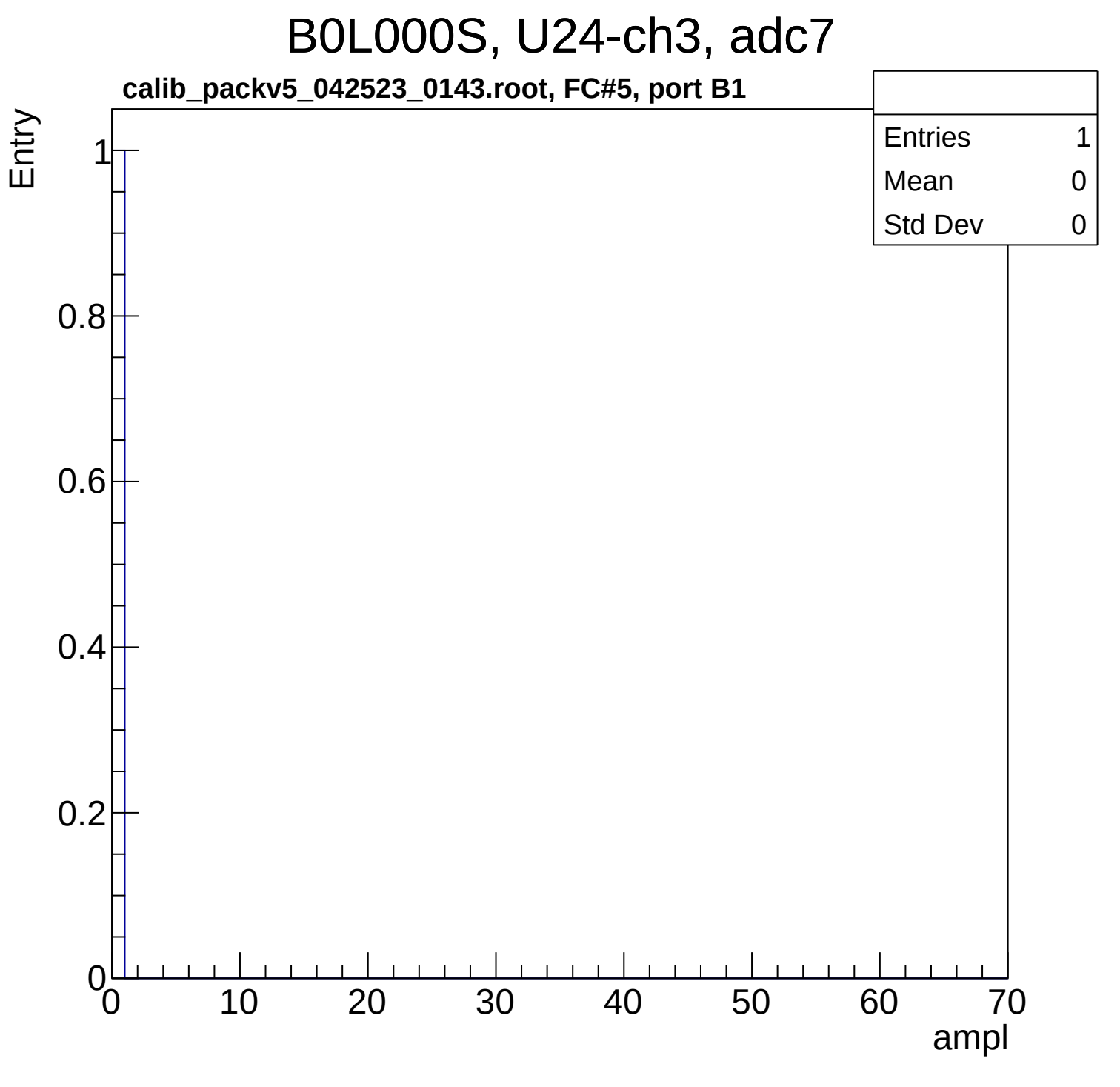

B0L000S, U24-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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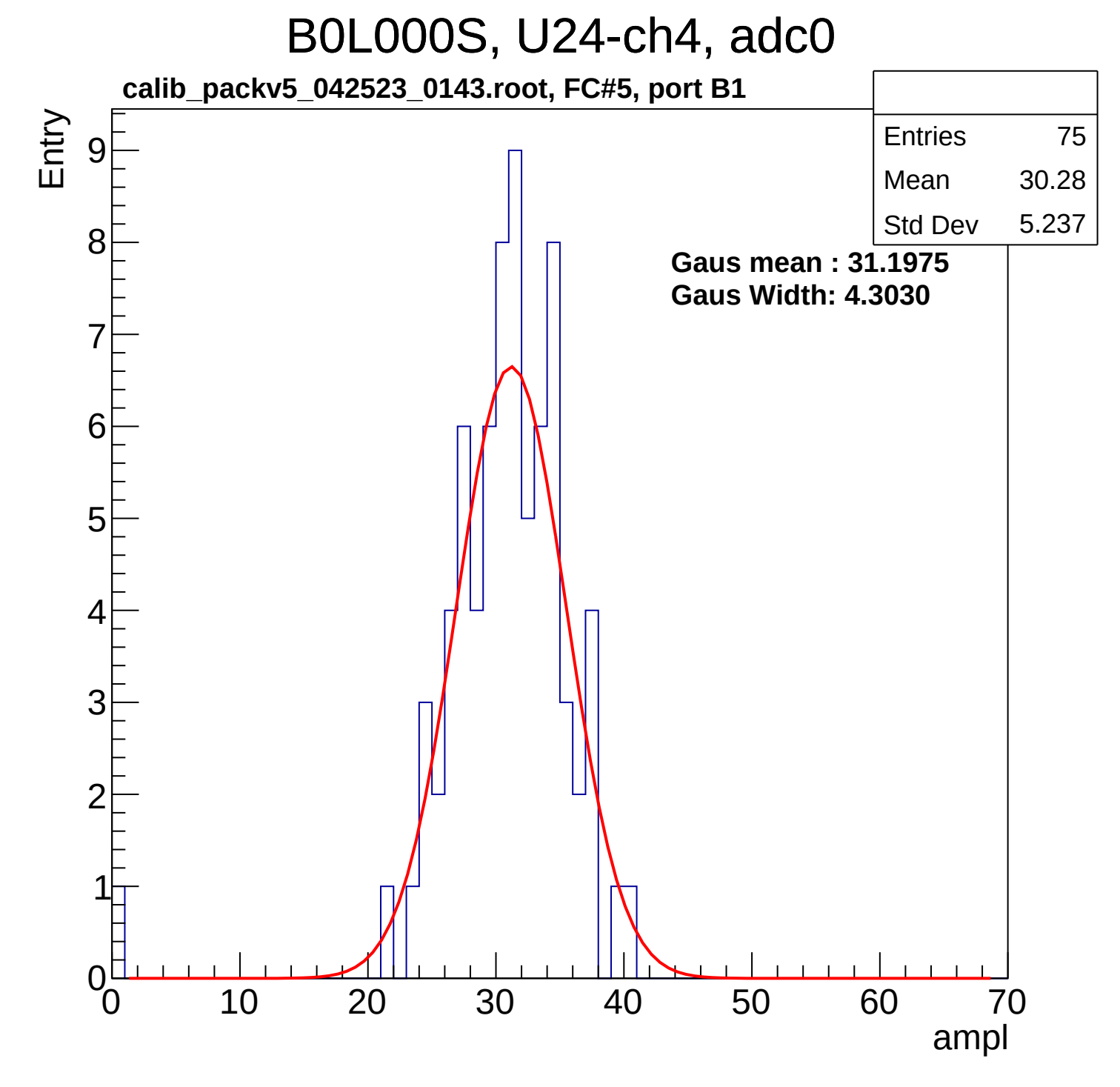
50
60
70
ampl


B0L000S, U24-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 30.28
Std Dev 5.237
Gaus mean : 31.1975
Gaus Width: 4.3030
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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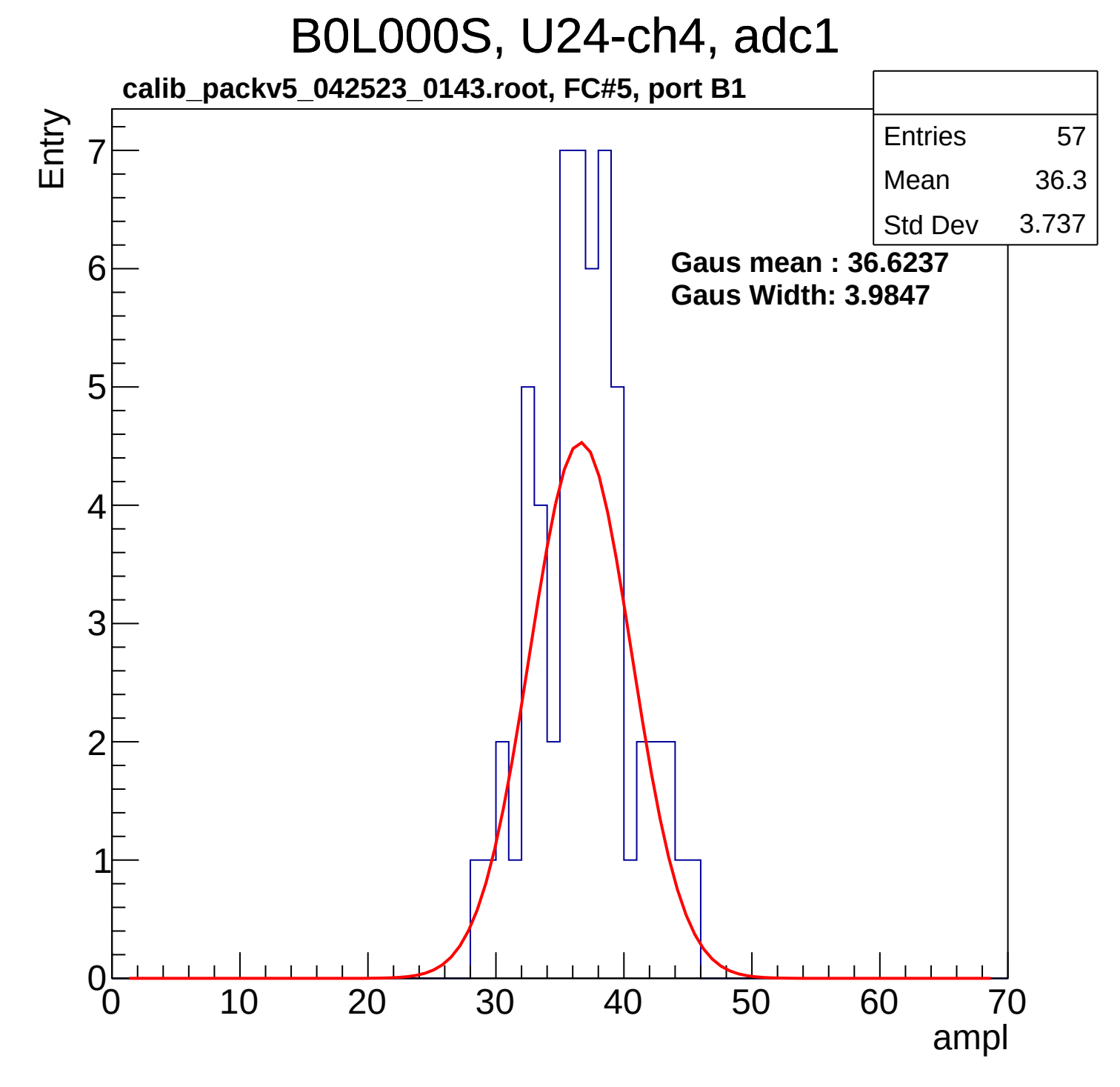
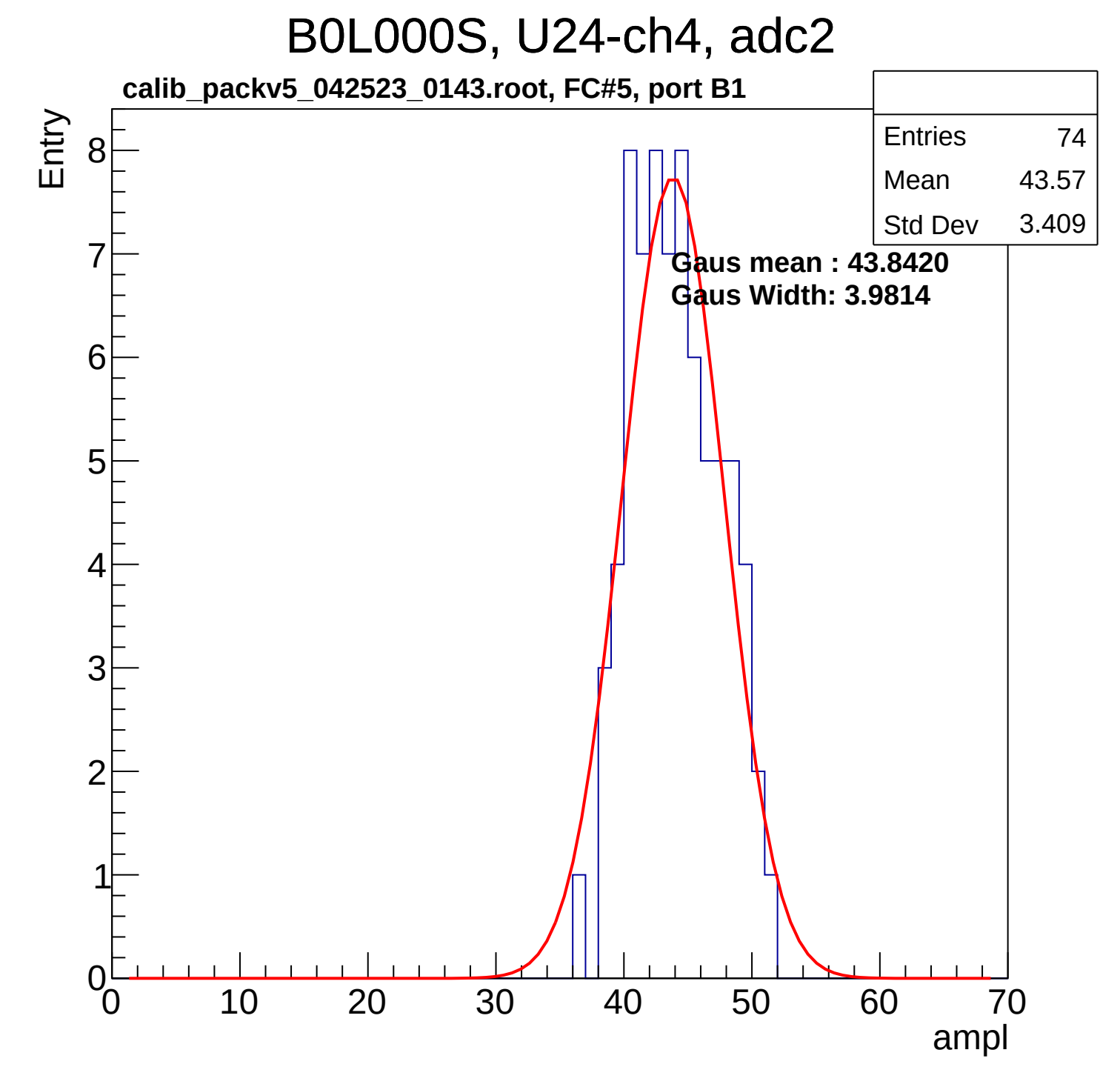

B0L000S, U24-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.3
Std Dev 3.737
Gaus mean : 36.6237
Gaus Width: 3.9847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.57
Std Dev 3.409
Gaus mean : 43.8420
Gaus Width: 3.9814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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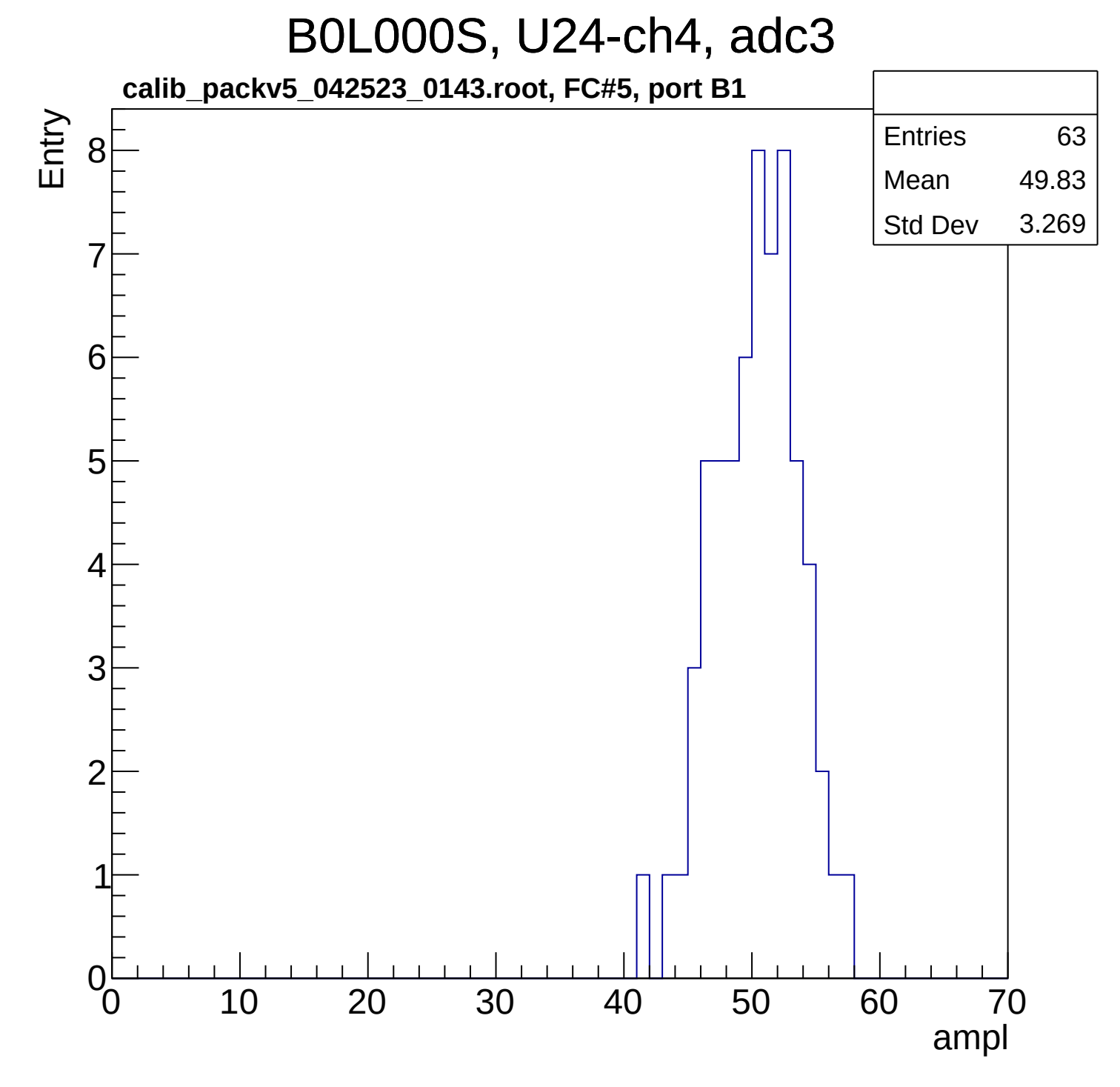
B0L000S, U24-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.83
Std Dev 3.269
Entry
ampl
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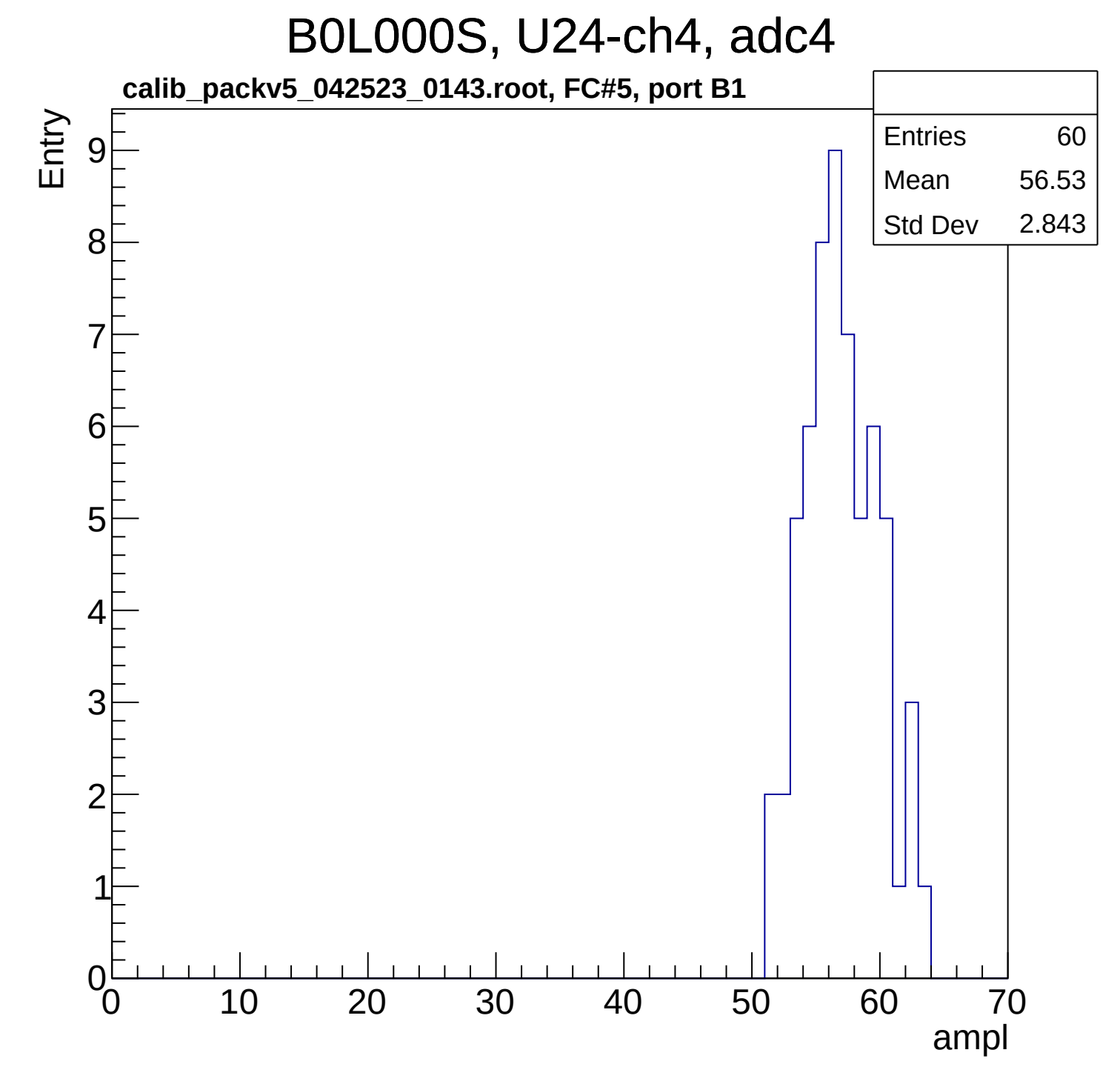
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.53
Std Dev 2.843
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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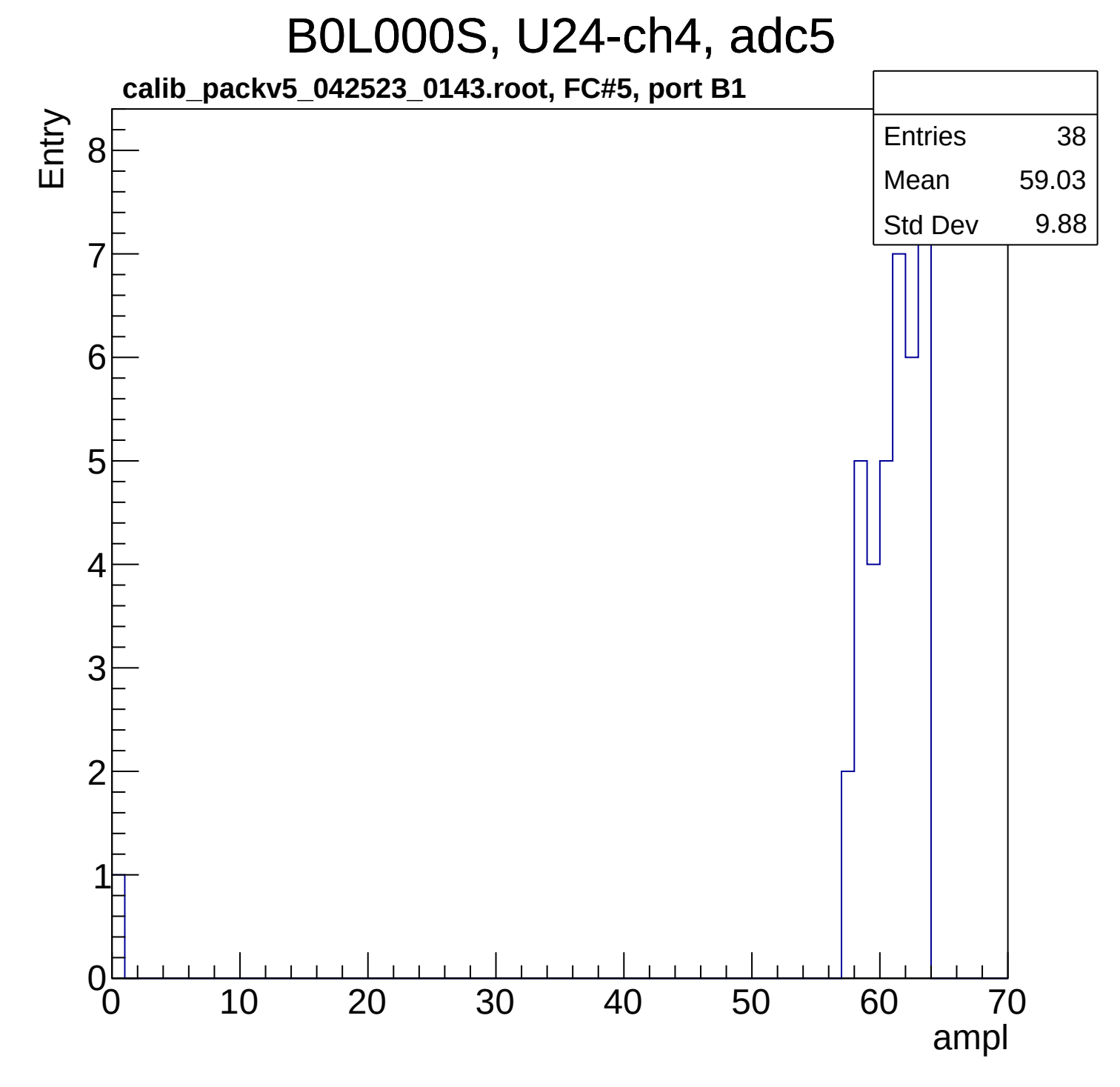
B0L000S, U24-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59.03
Std Dev 9.88
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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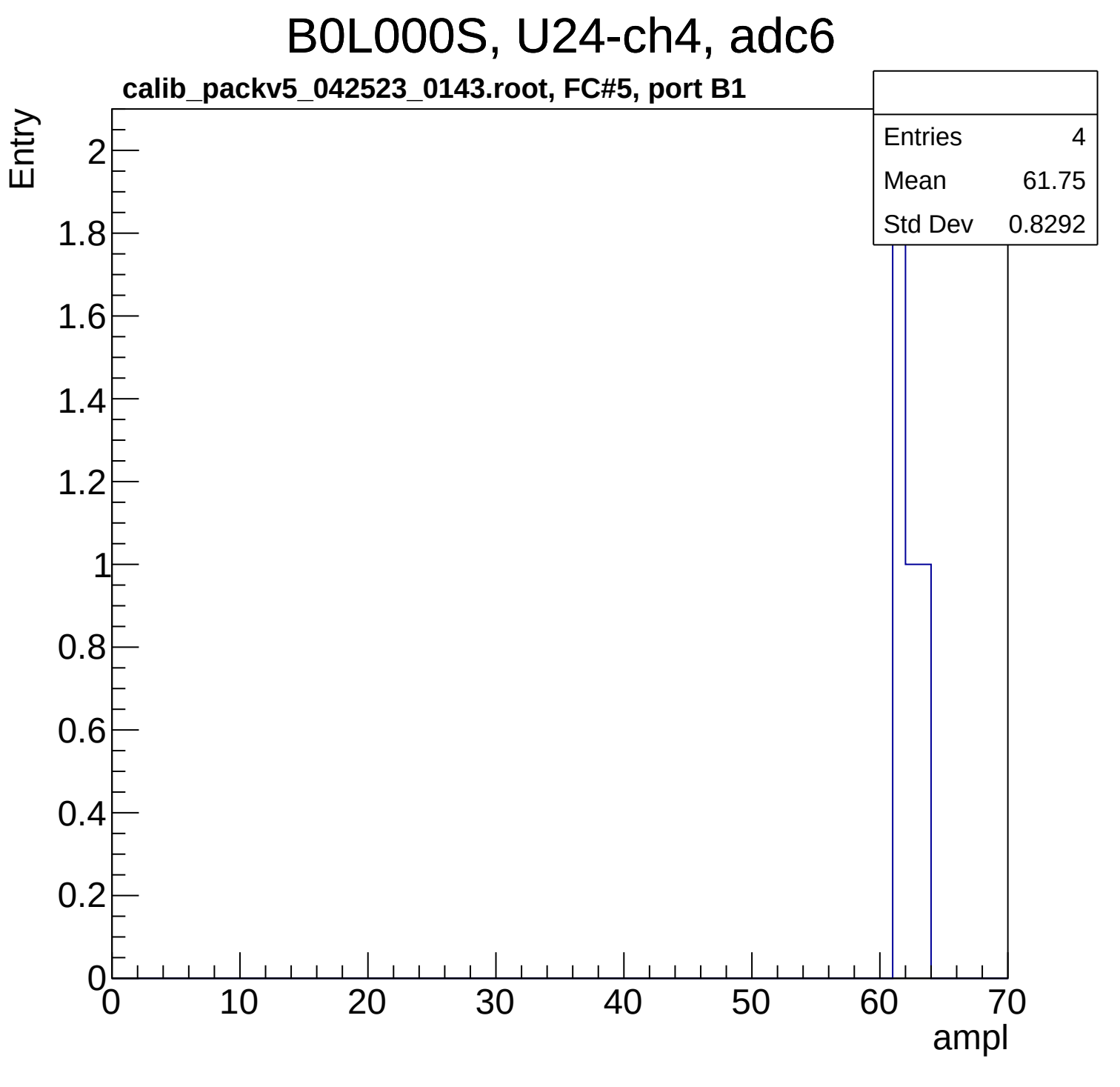
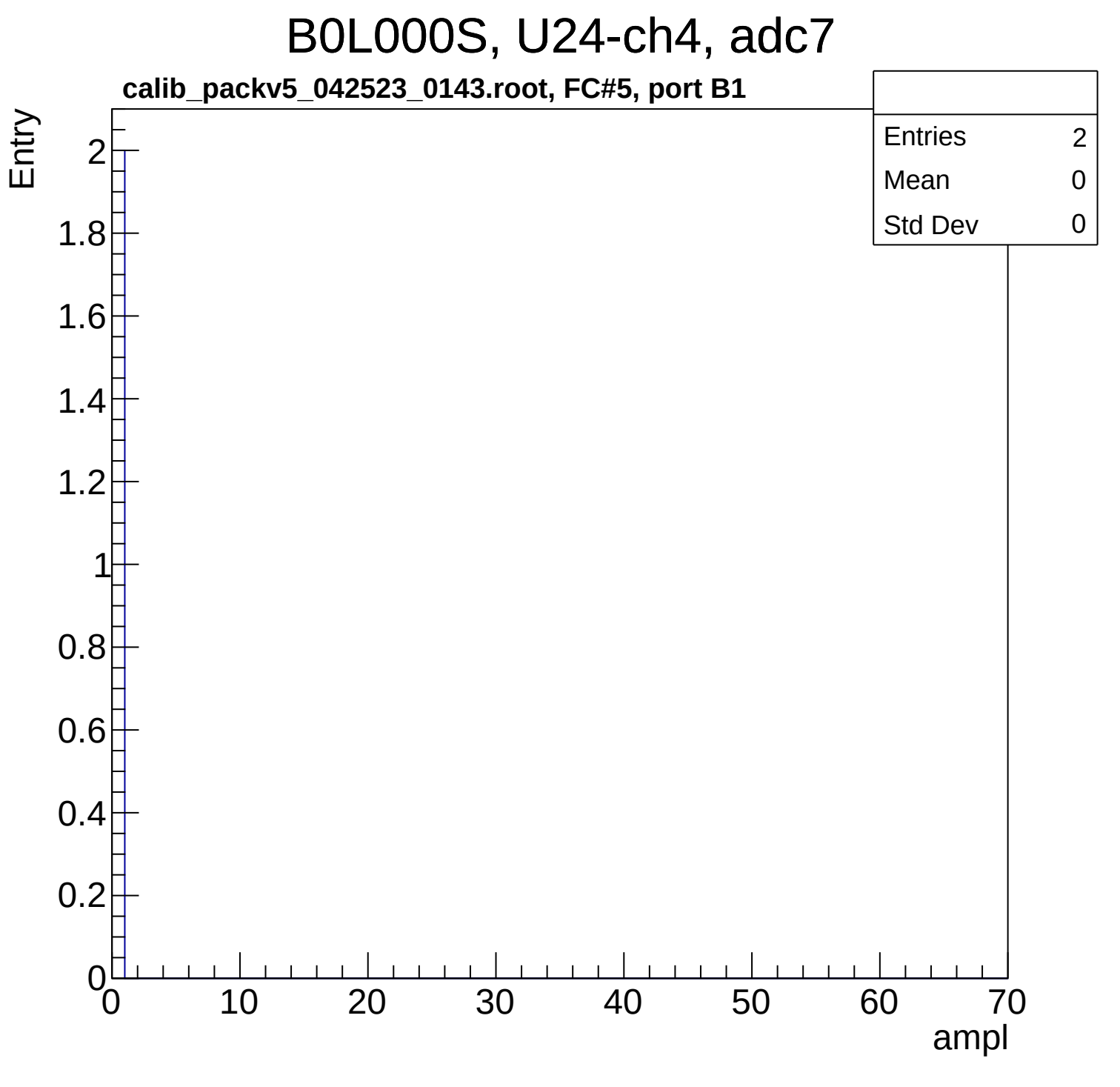
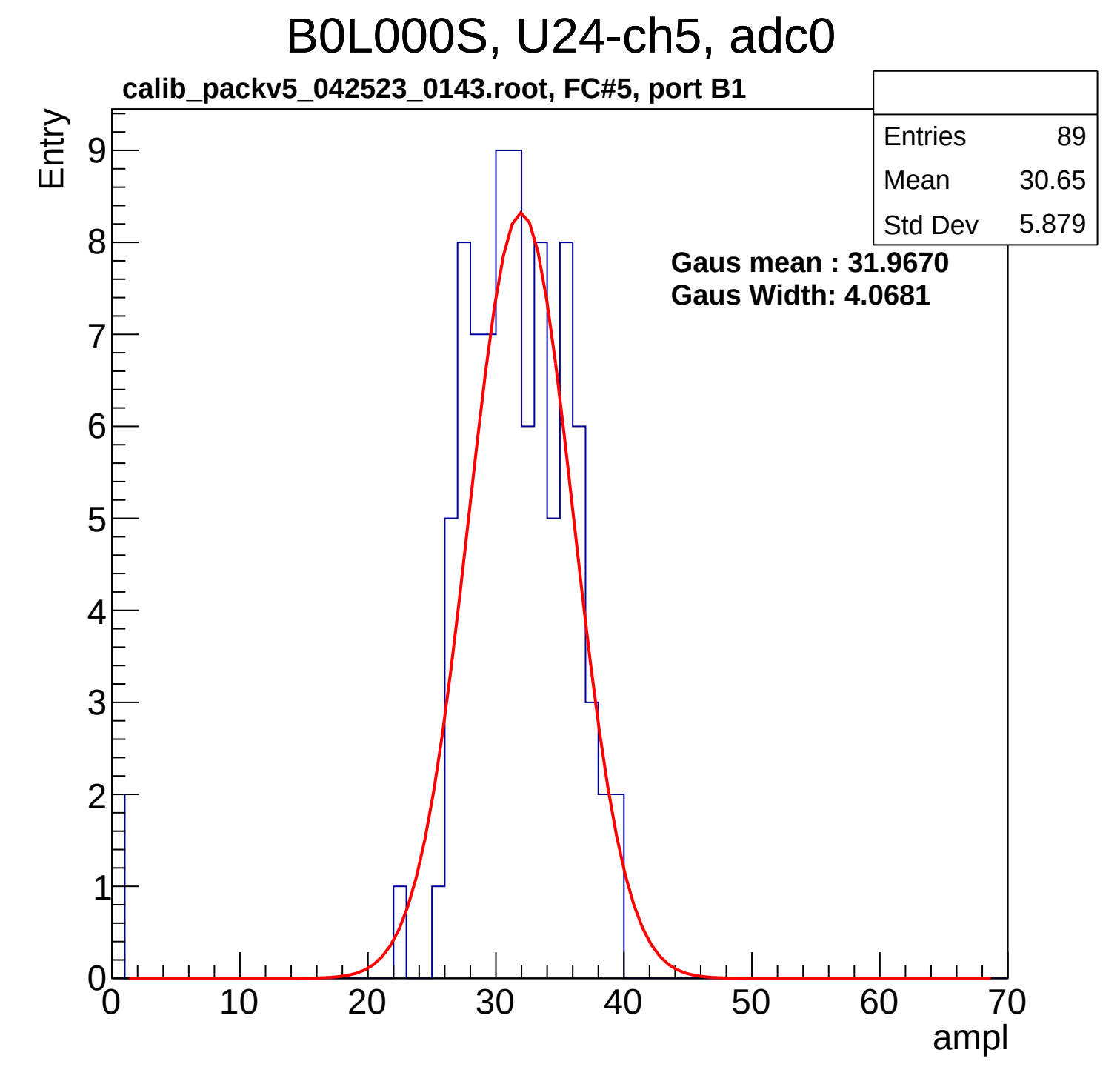

B0L000S, U24-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 89
Mean 30.65
Std Dev 5.879
Gaus mean : 31.9670
Gaus Width: 4.0681
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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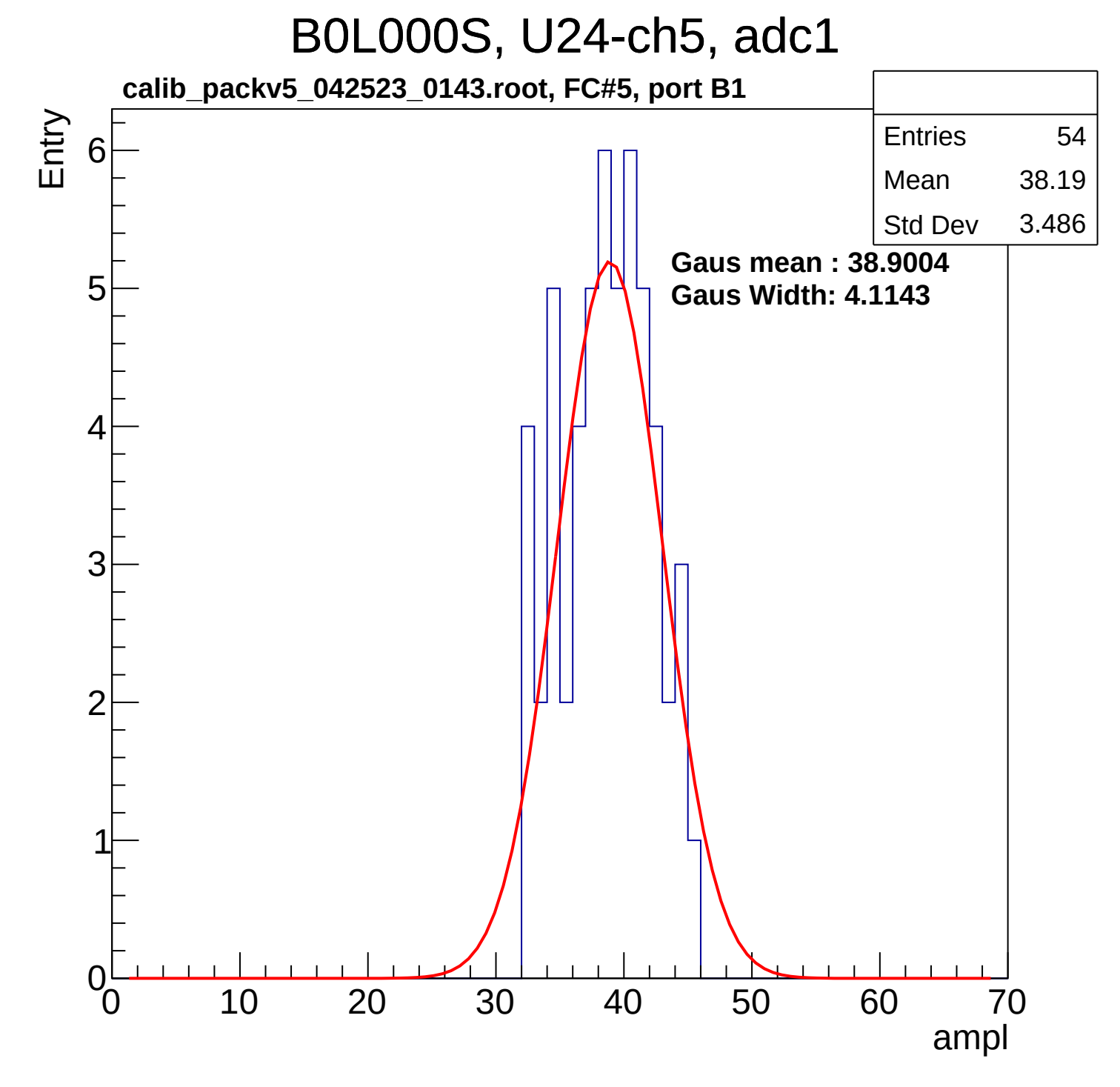
B0L000S, U24-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 38.19
Std Dev 3.486
Gaus mean : 38.9004
Gaus Width: 4.1143
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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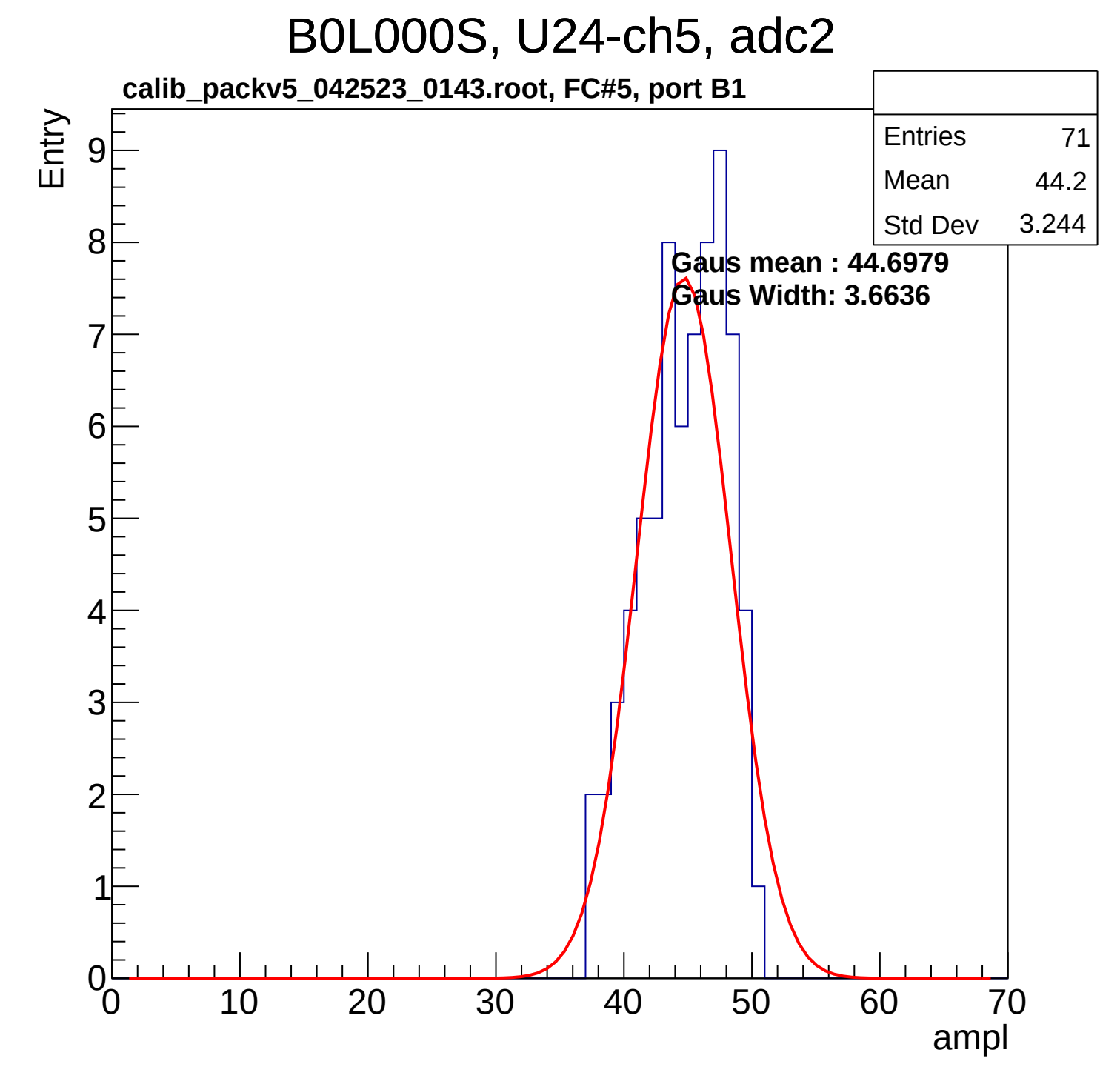
B0L000S, U24-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 44.2
Std Dev 3.244
Gaus mean : 44.6979
Gaus Width: 3.6636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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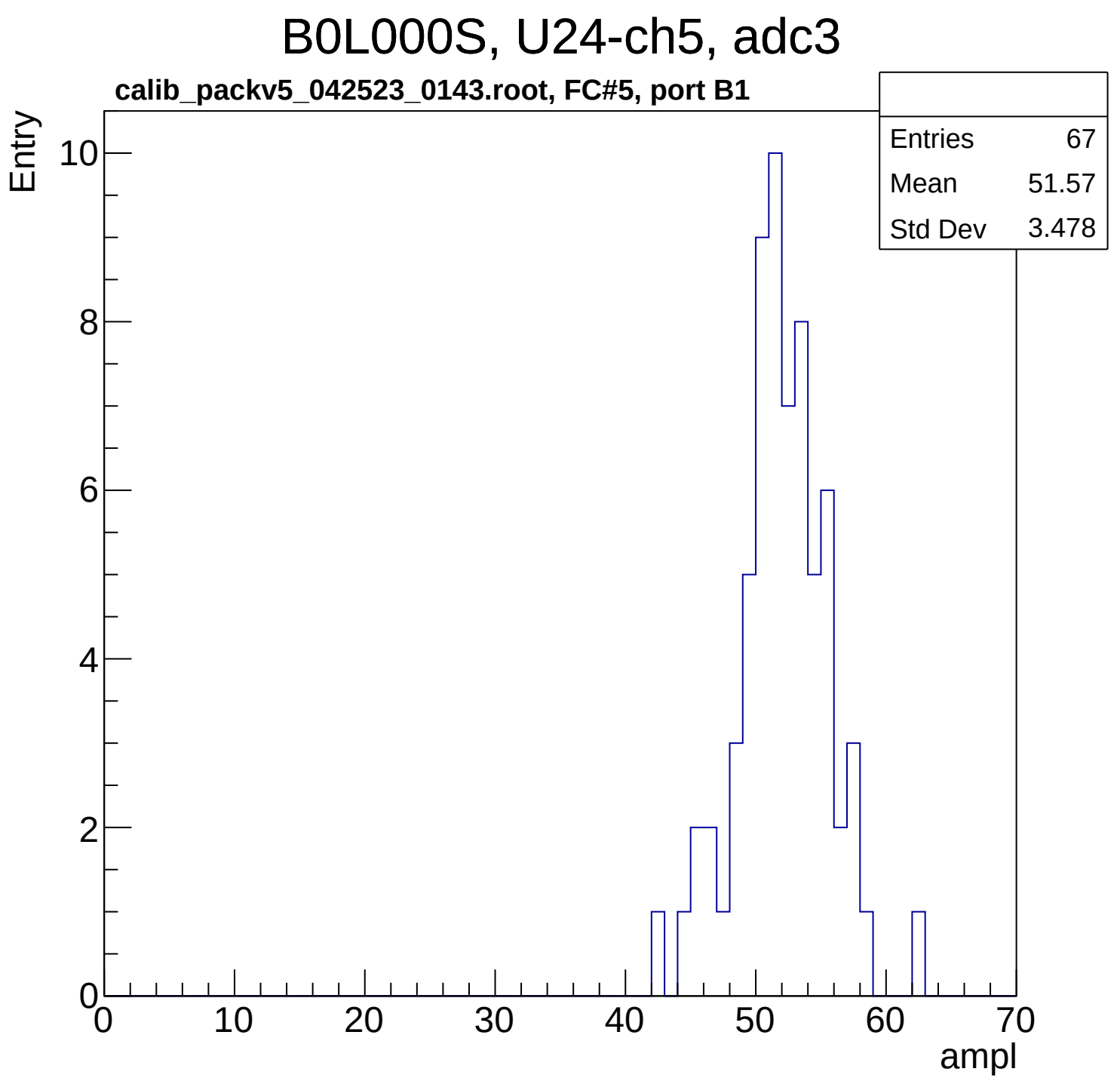
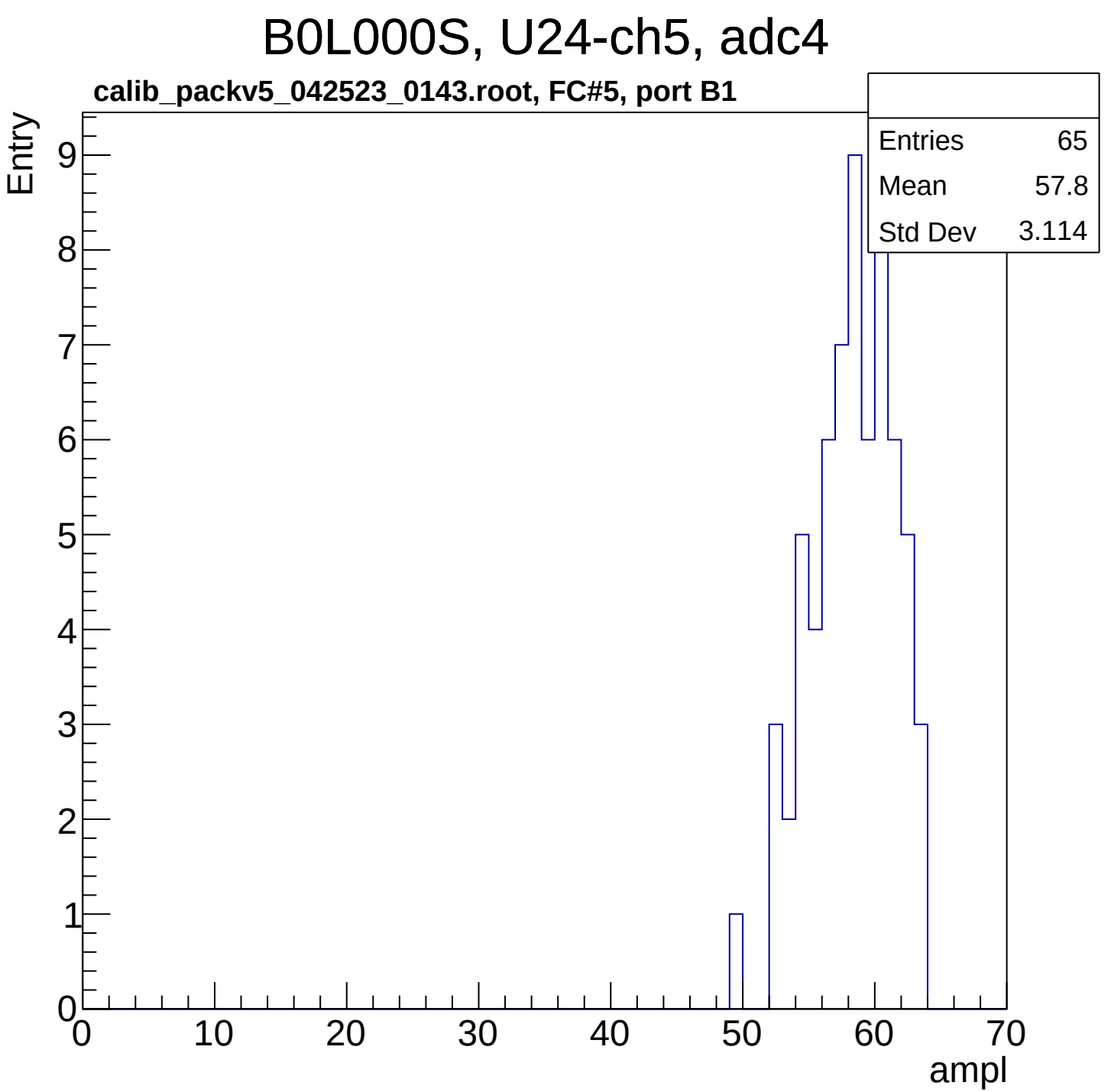
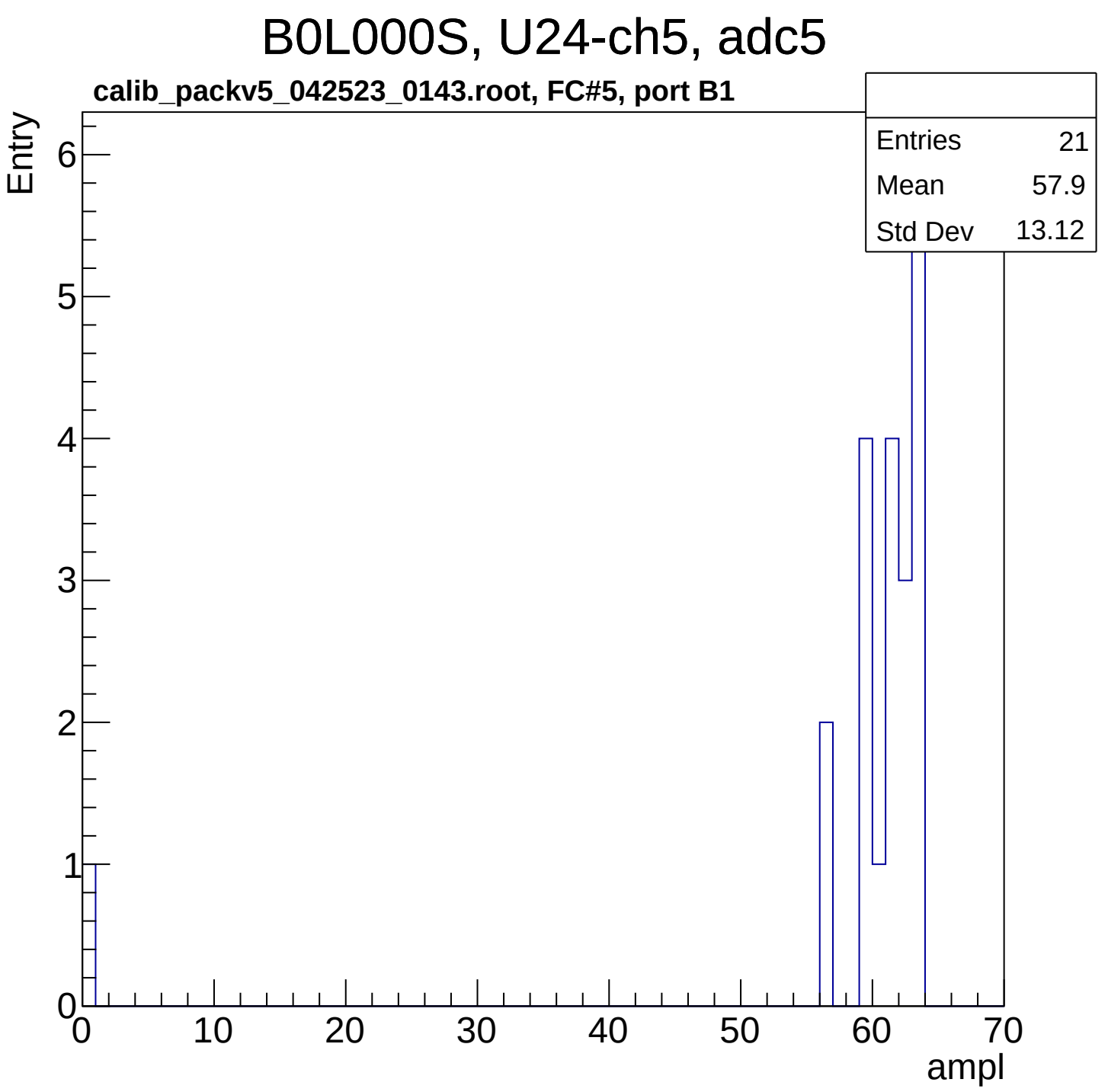
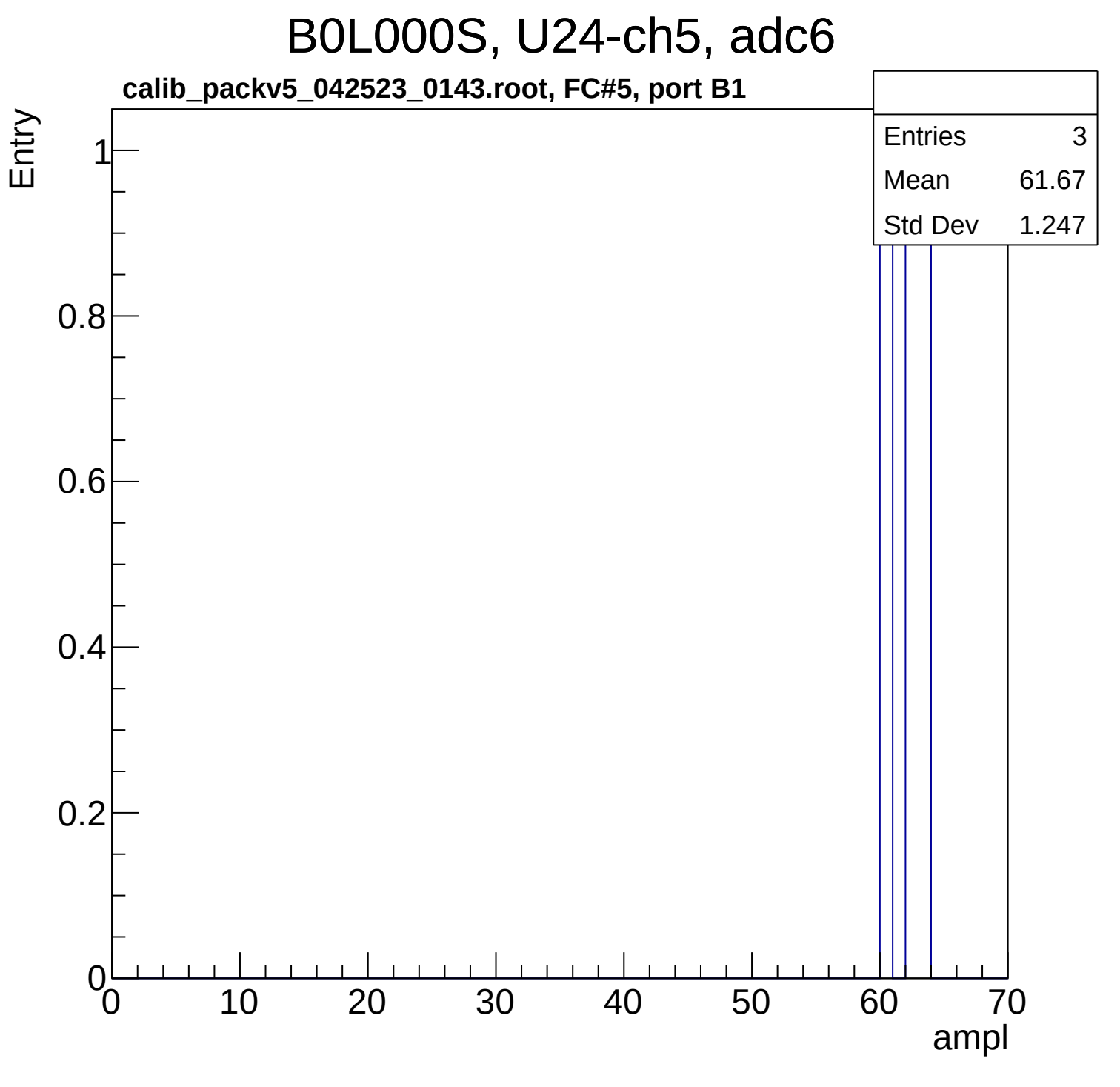
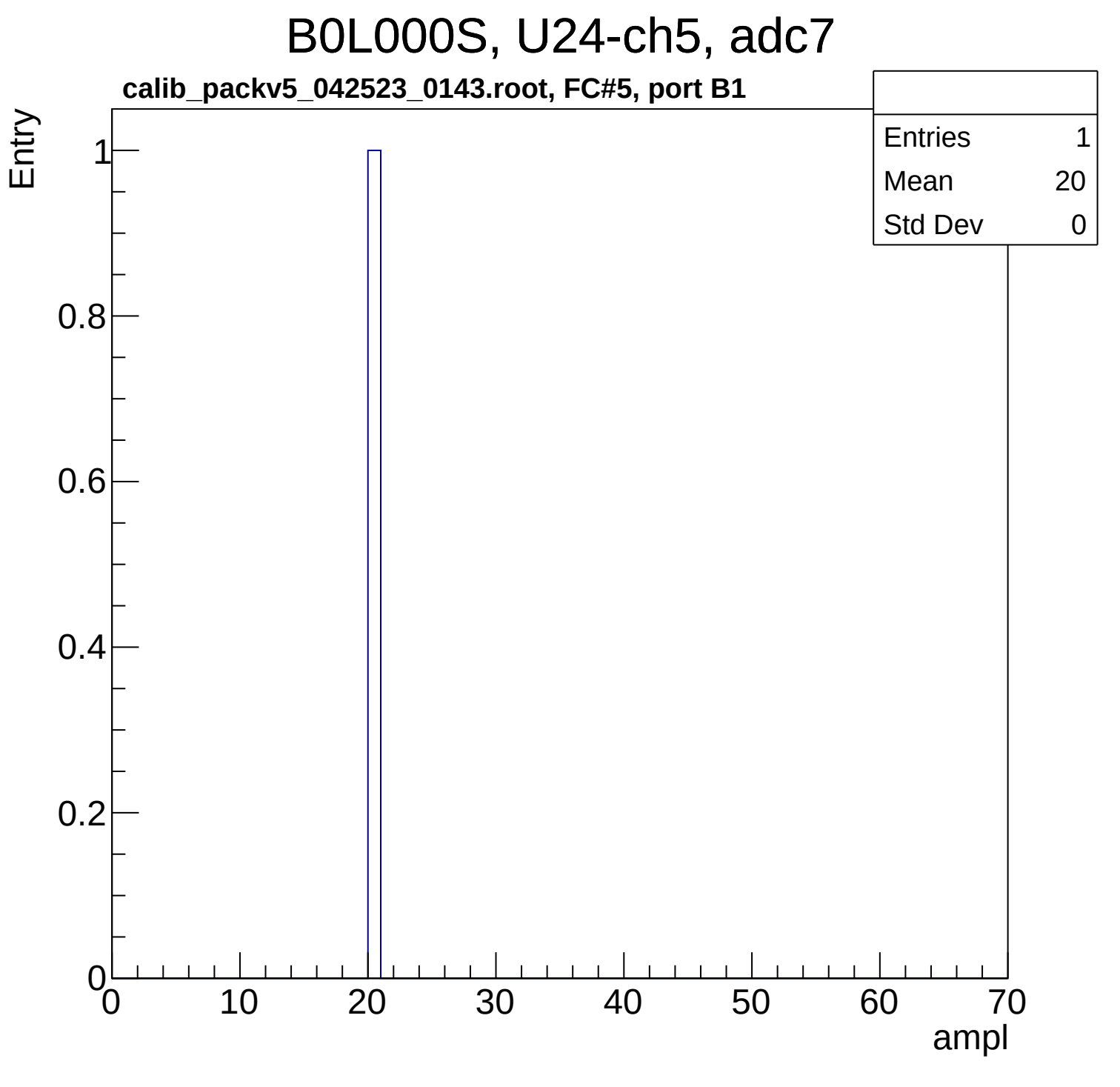
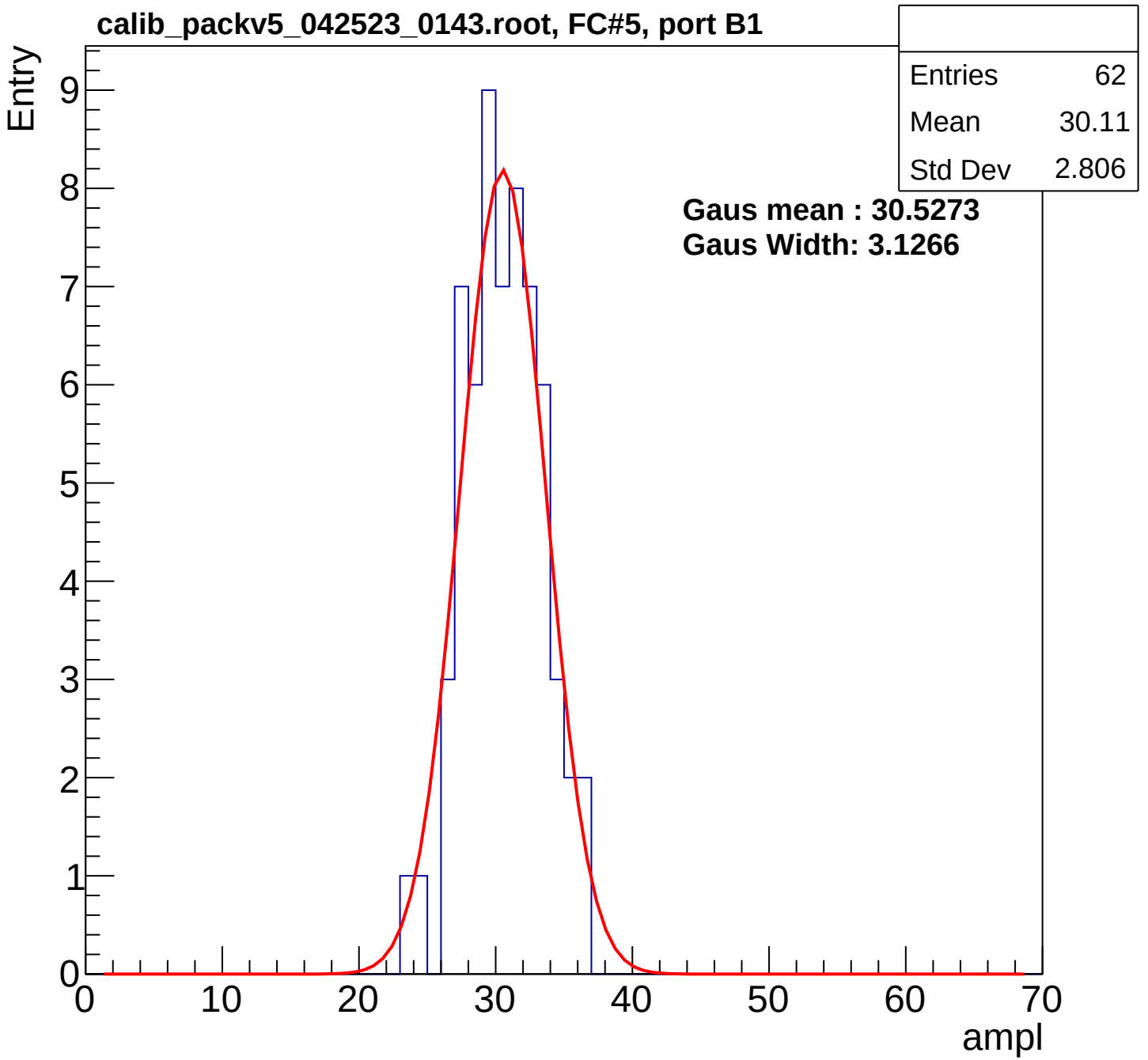

B0L000S, U24-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 51.57
Std Dev 3.478
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 57.8
Std Dev 3.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 57.9
Std Dev 13.12
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 30.11
Std Dev 2.806
Gaus mean : 30.5273
Gaus Width: 3.1266
Entry
ampl
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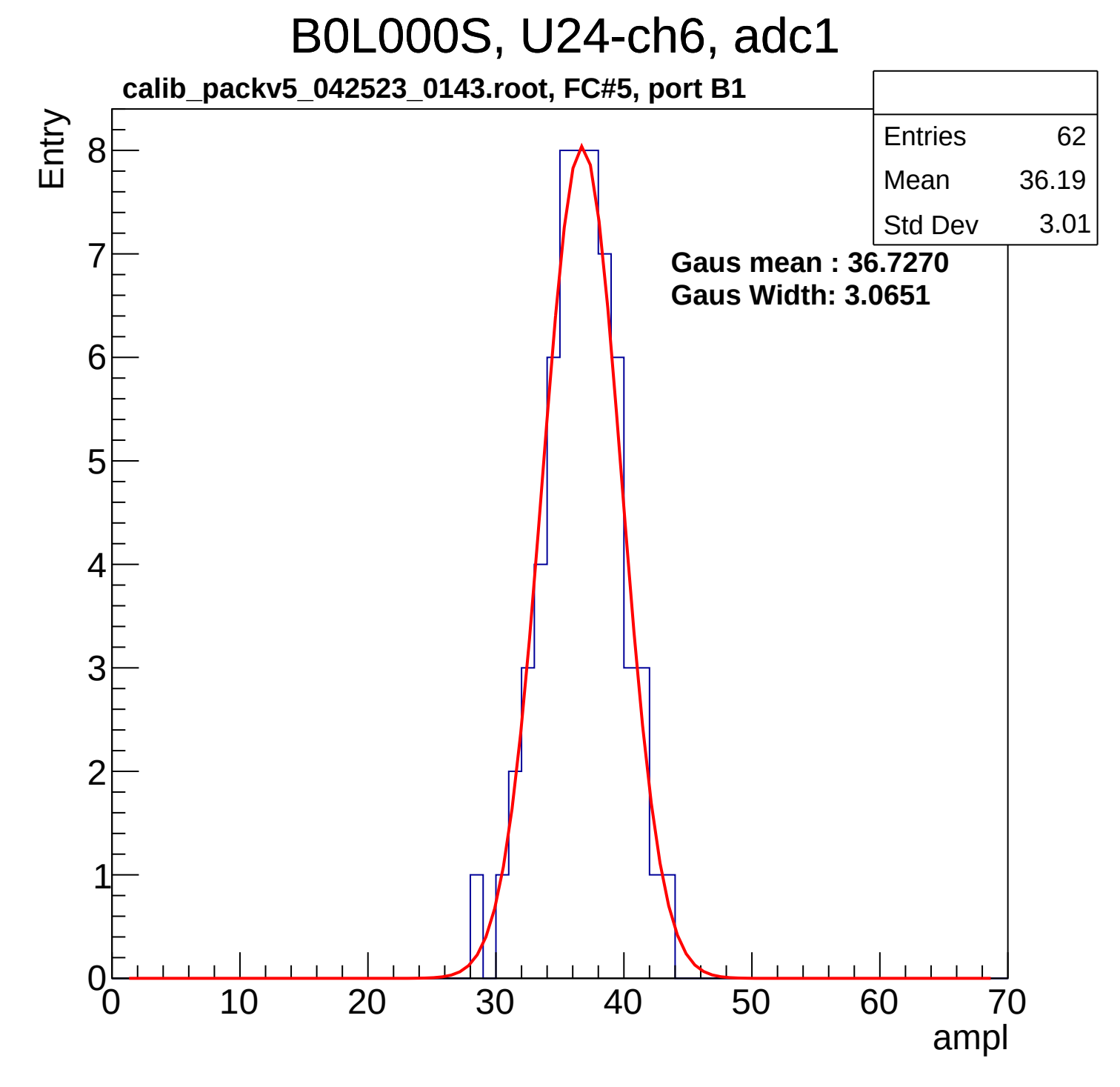
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.19
Std Dev 3.01
Gaus mean : 36.7270
Gaus Width: 3.0651
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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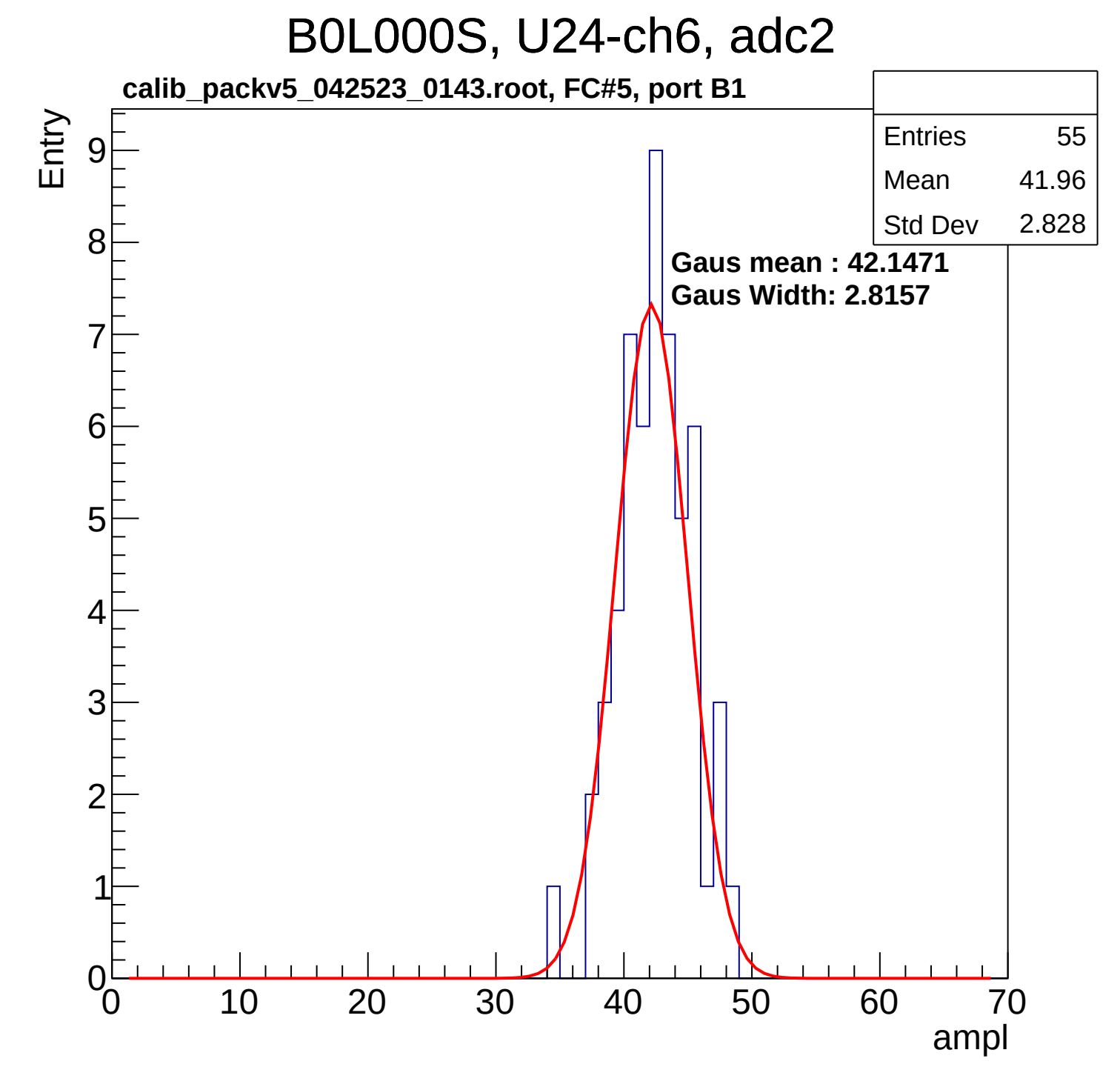
60
70


B0L000S, U24-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.96
Std Dev 2.828
Gaus mean : 42.1471
Gaus Width: 2.8157
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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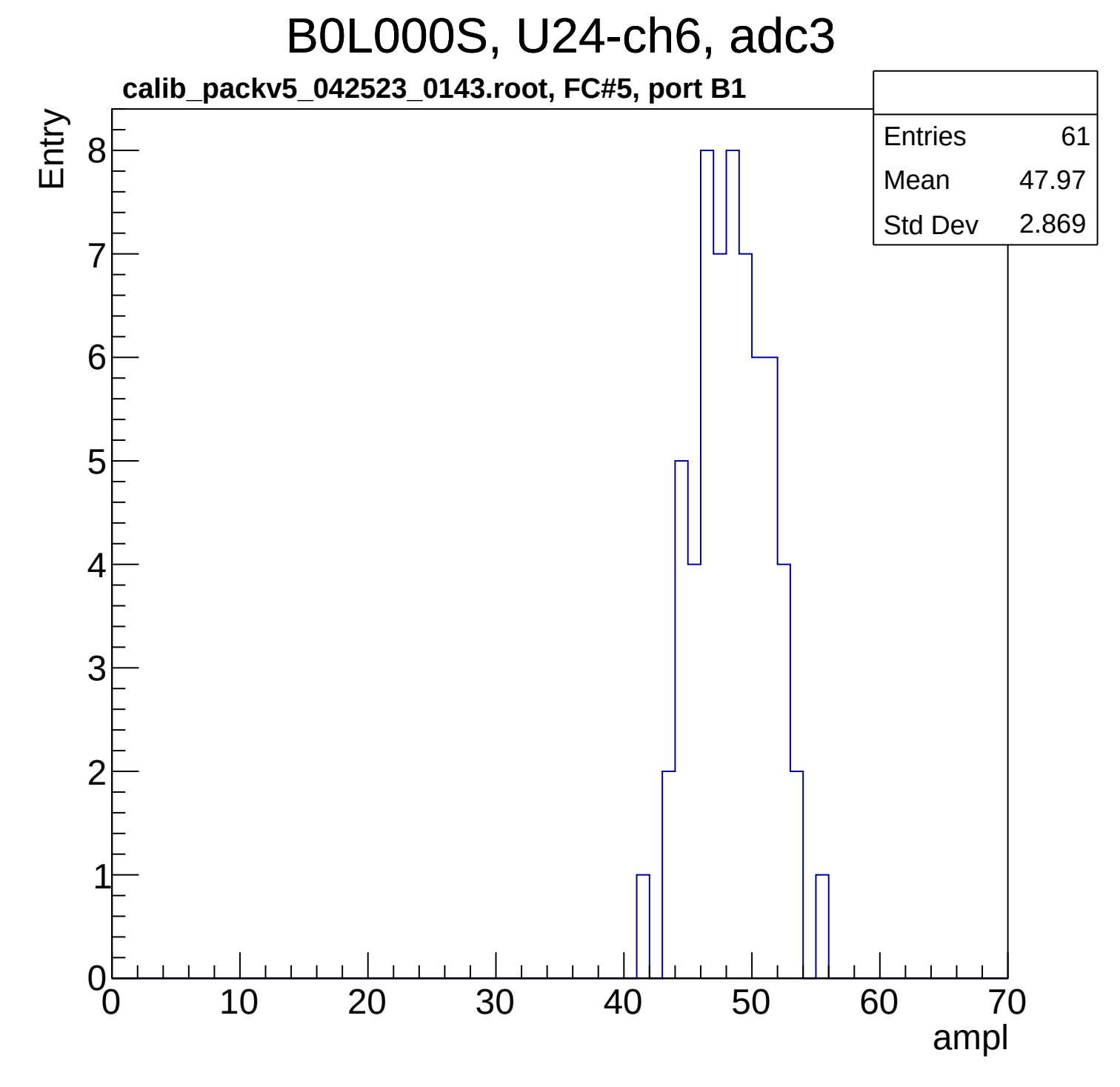
B0L000S, U24-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.97
Std Dev 2.869
Entry
ampl
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2
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4
5
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0
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20
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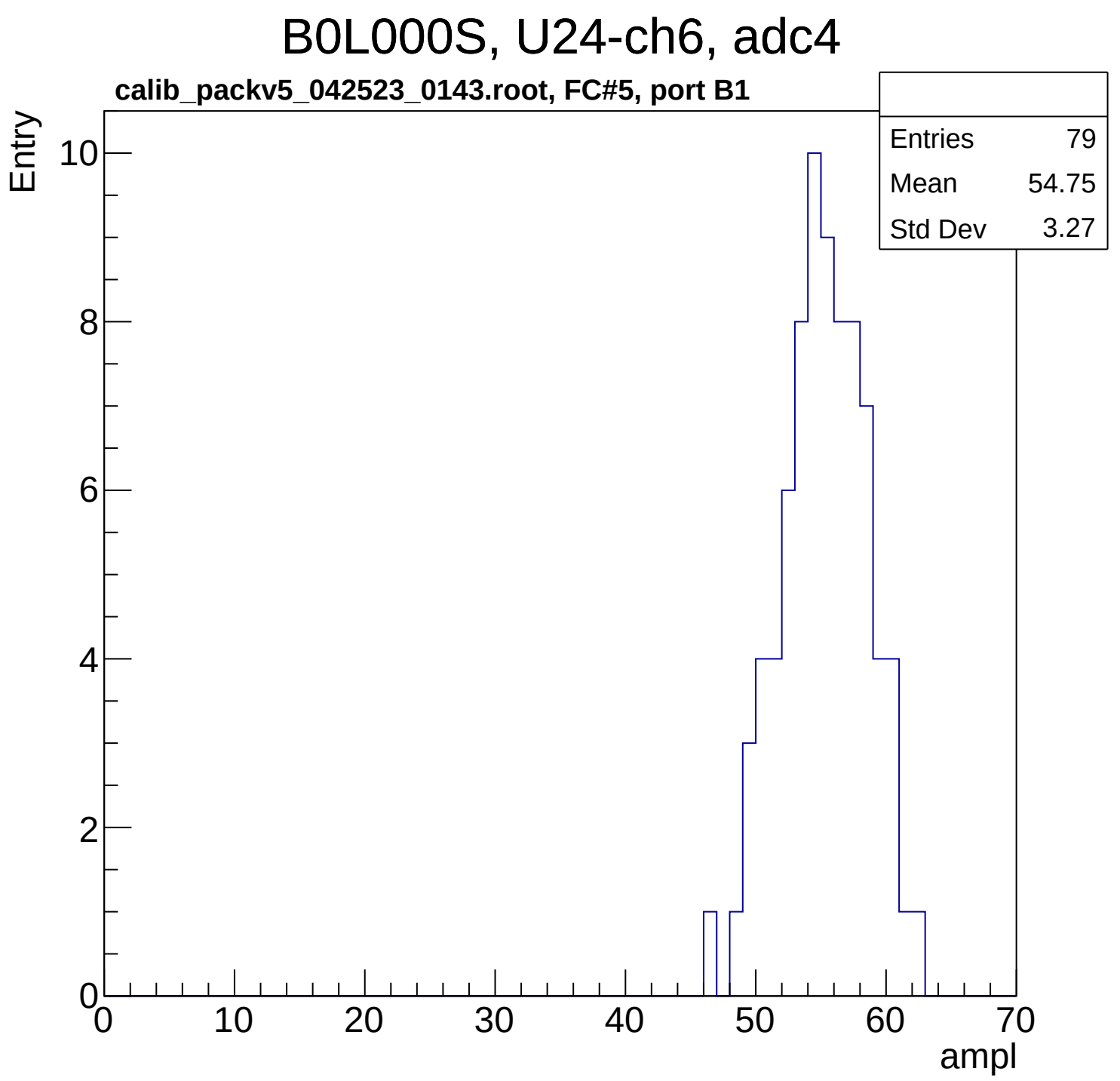
40
50
60
70


B0L000S, U24-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 54.75
Std Dev 3.27
Entry
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2
4
6
8
10
0
10
20
30
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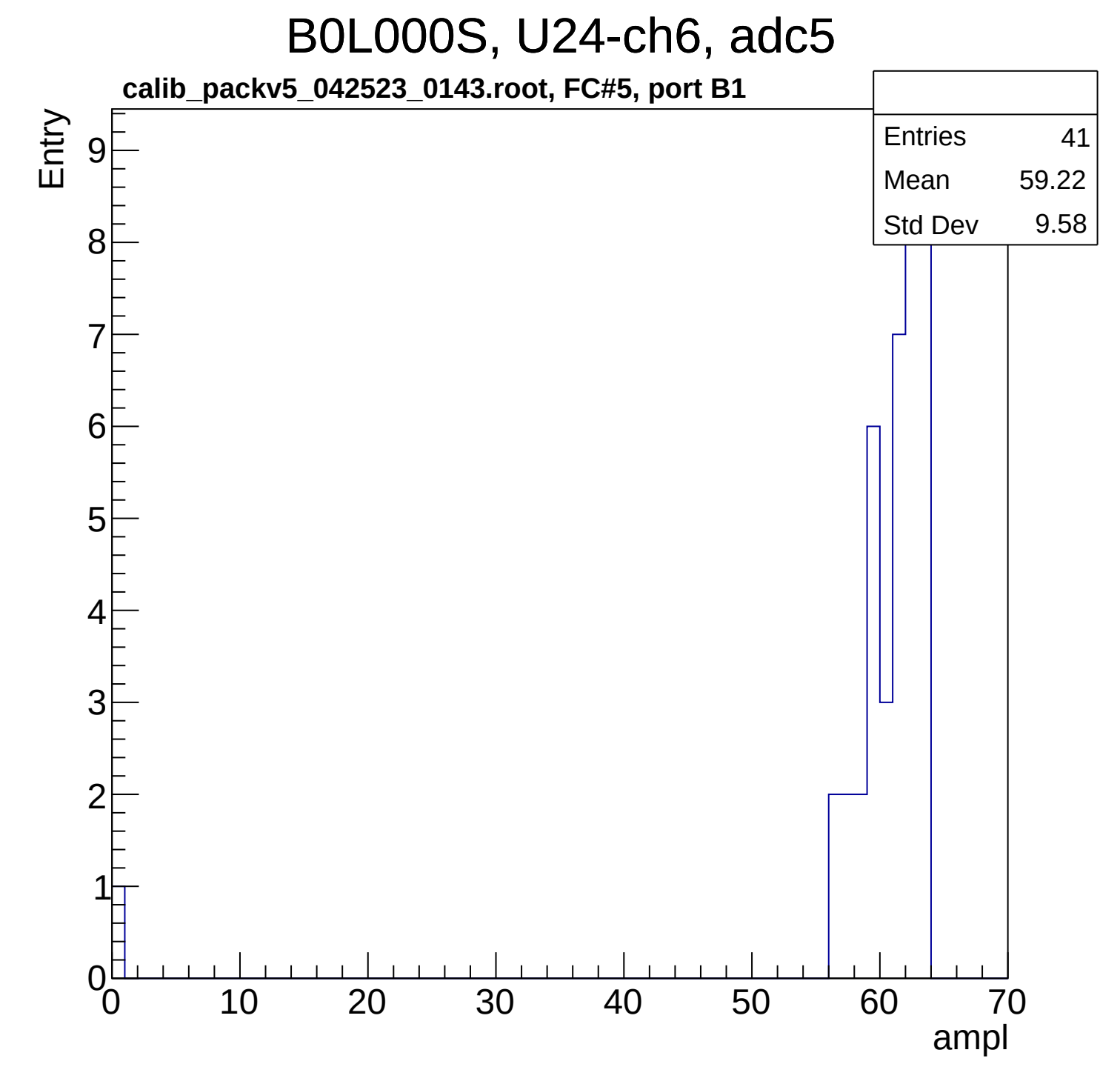
50
60
70
ampl


B0L000S, U24-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.22
Std Dev 9.58
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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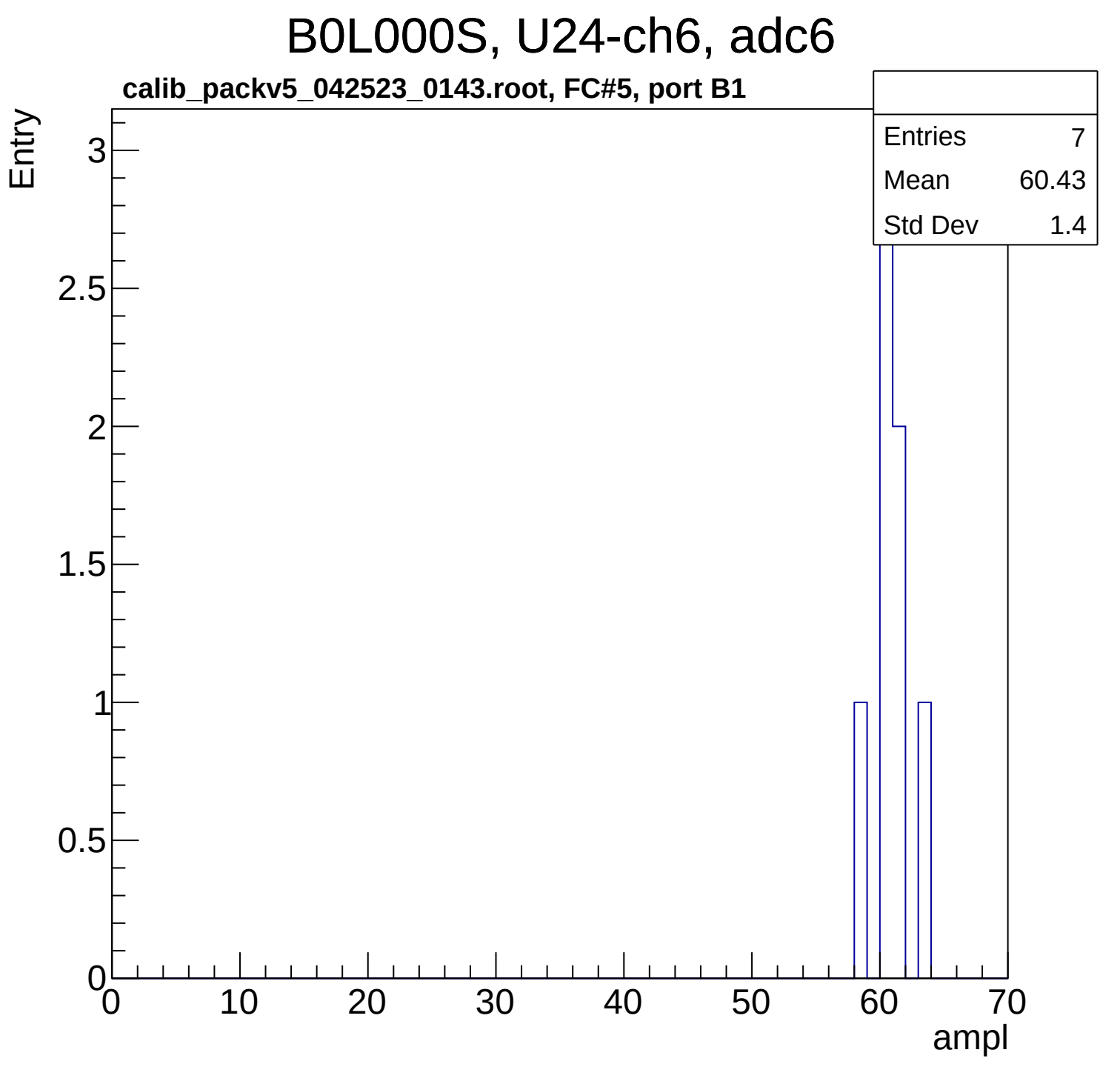
B0L000S, U24-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 60.43
Std Dev 1.4
Entry
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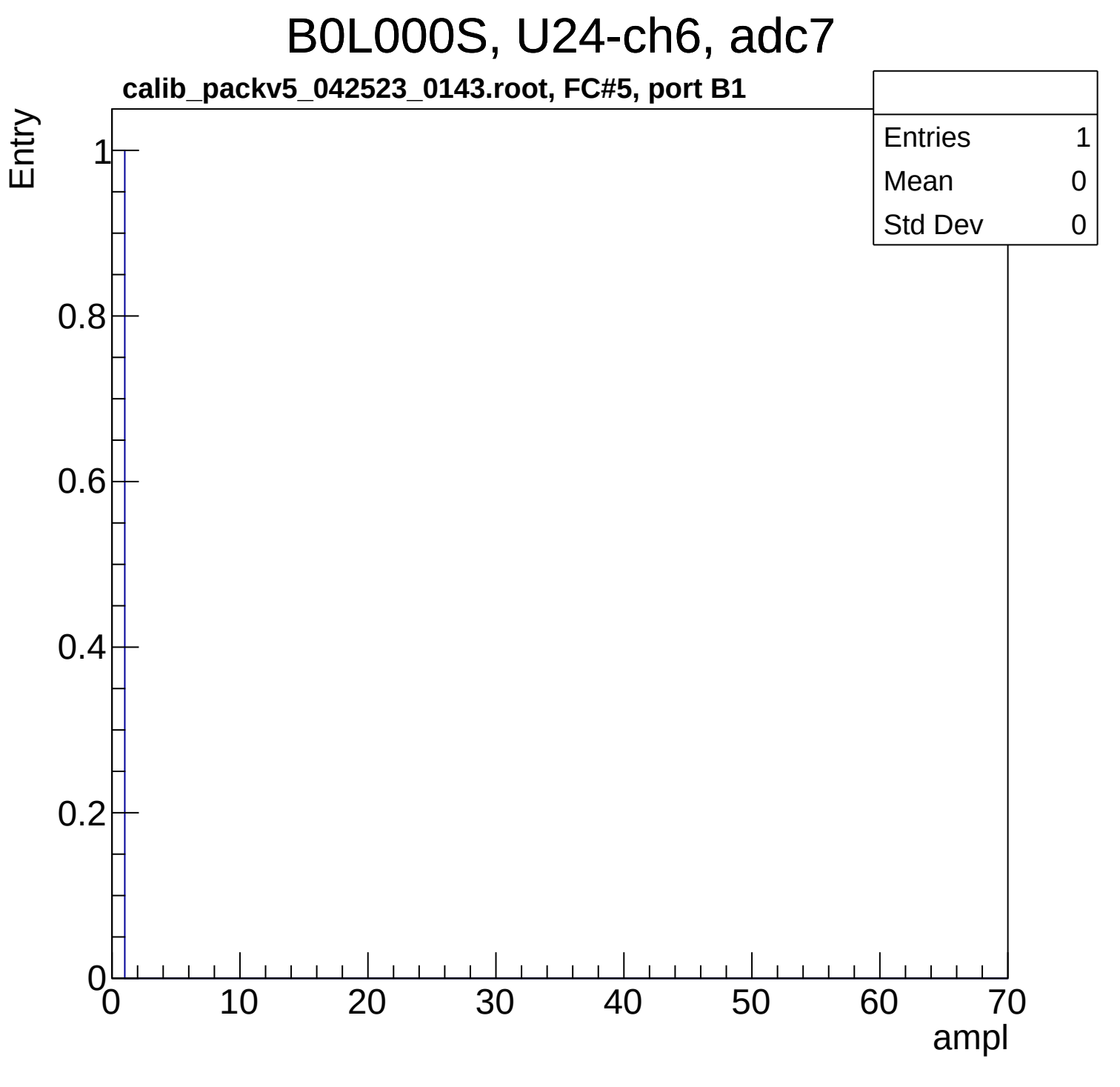
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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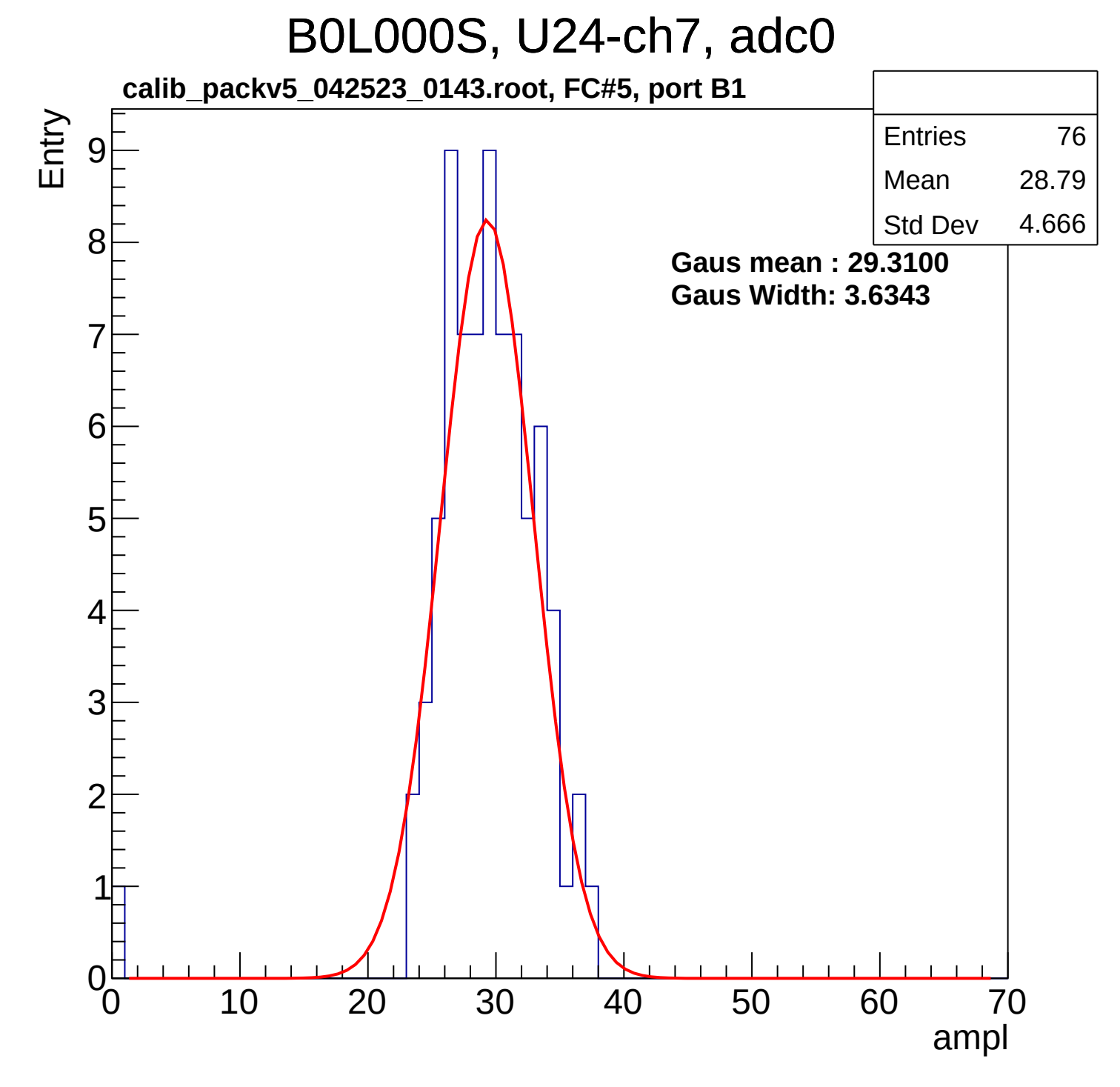
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.79
Std Dev 4.666
Gaus mean : 29.3100
Gaus Width: 3.6343
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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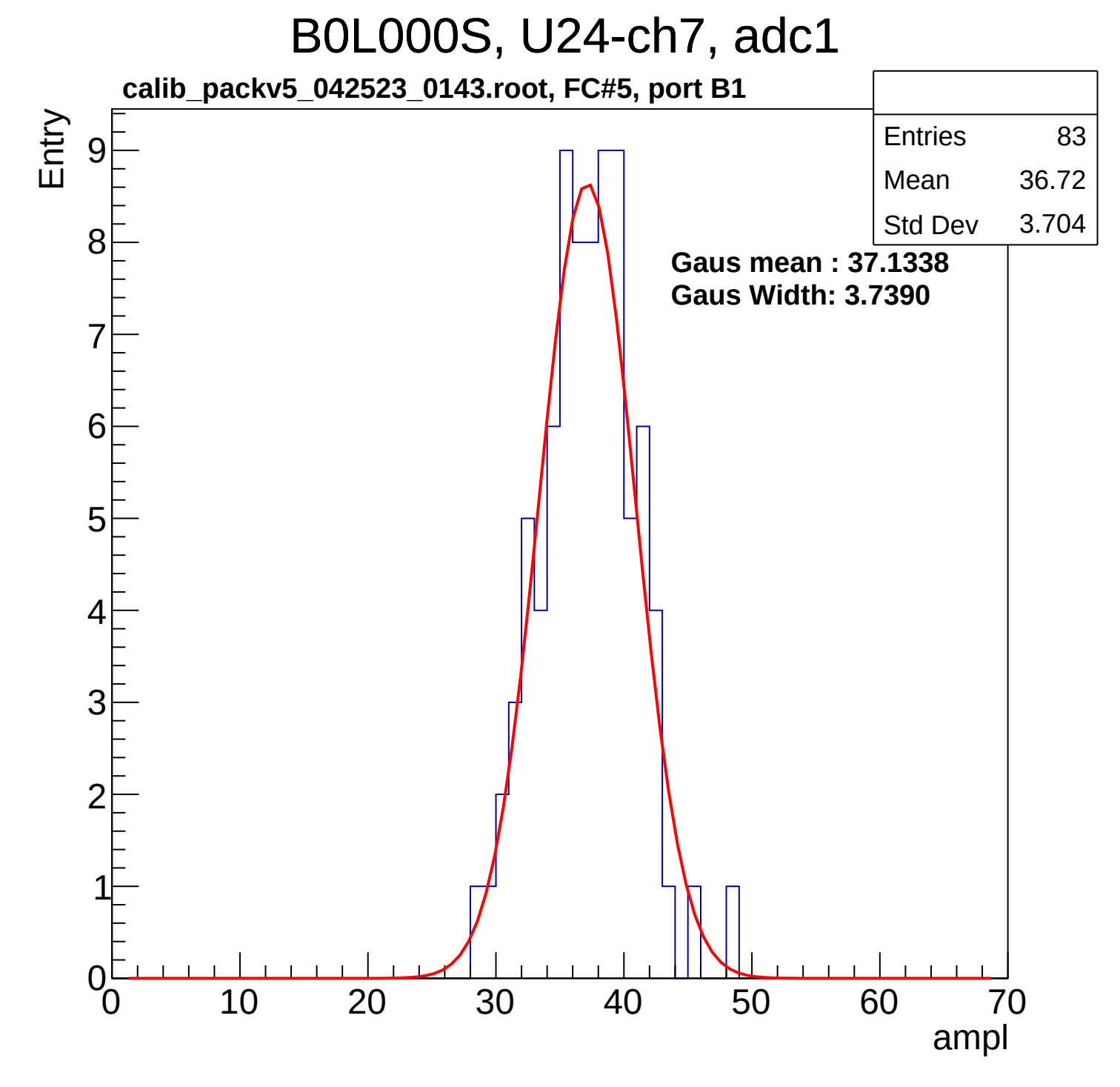
60
70


B0L000S, U24-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 36.72
Std Dev 3.704
Gaus mean : 37.1338
Gaus Width: 3.7390
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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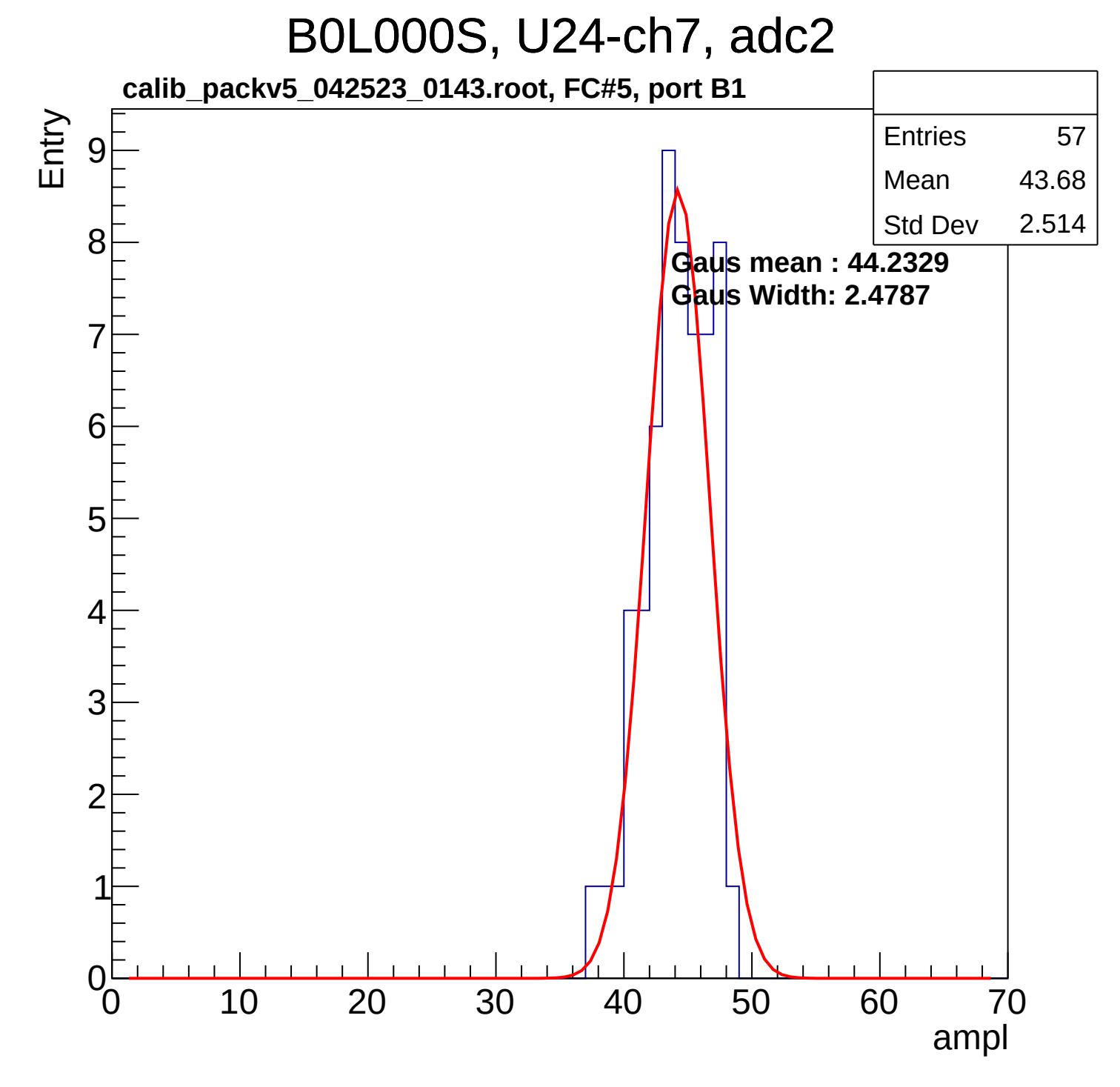
B0L000S, U24-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.68
Std Dev 2.514
Gaus mean : 44.2329
Gaus Width: 2.4787
Entry
ampl
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2
3
4
5
6
7
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0
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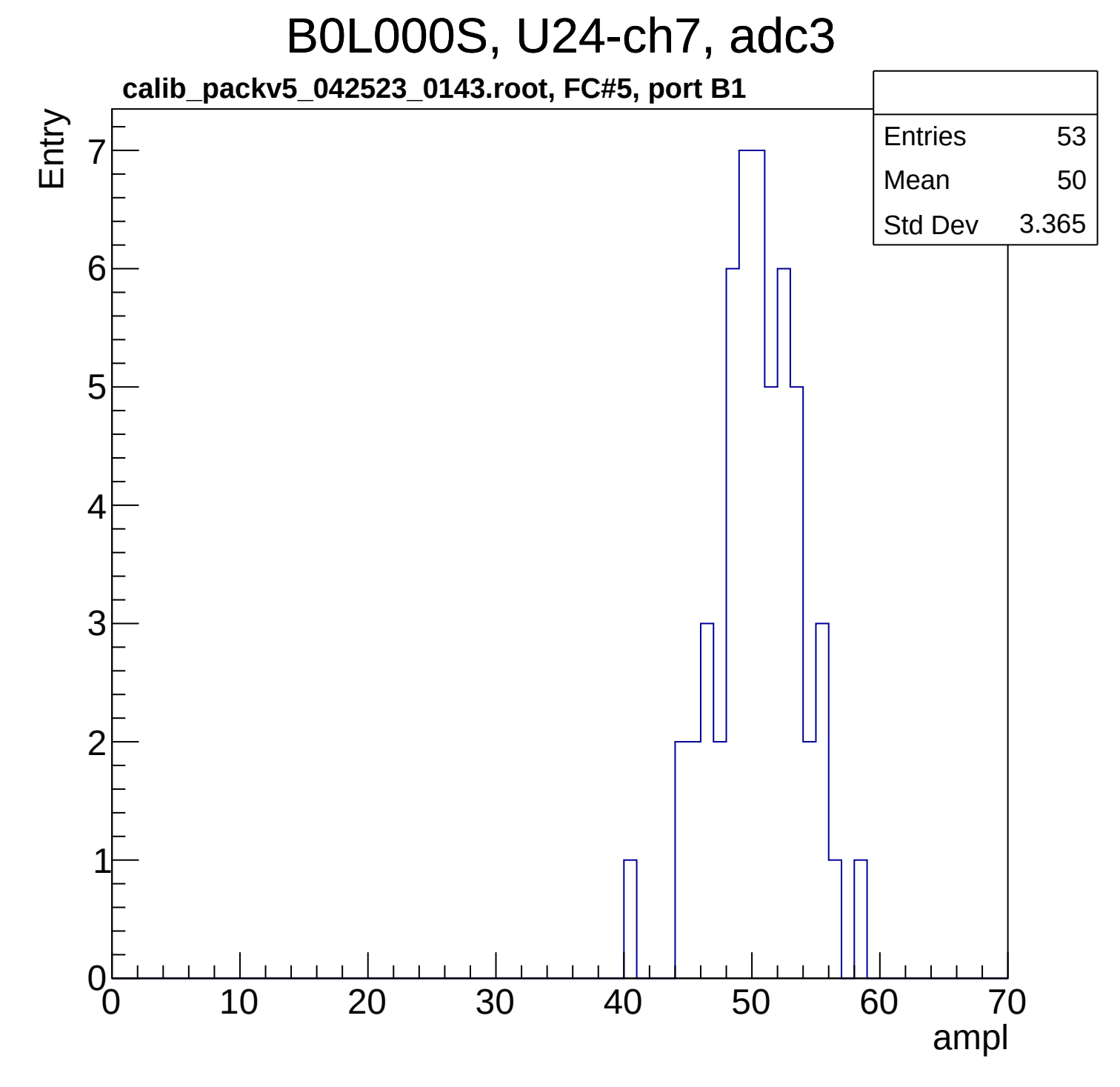
B0L000S, U24-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 50
Std Dev 3.365
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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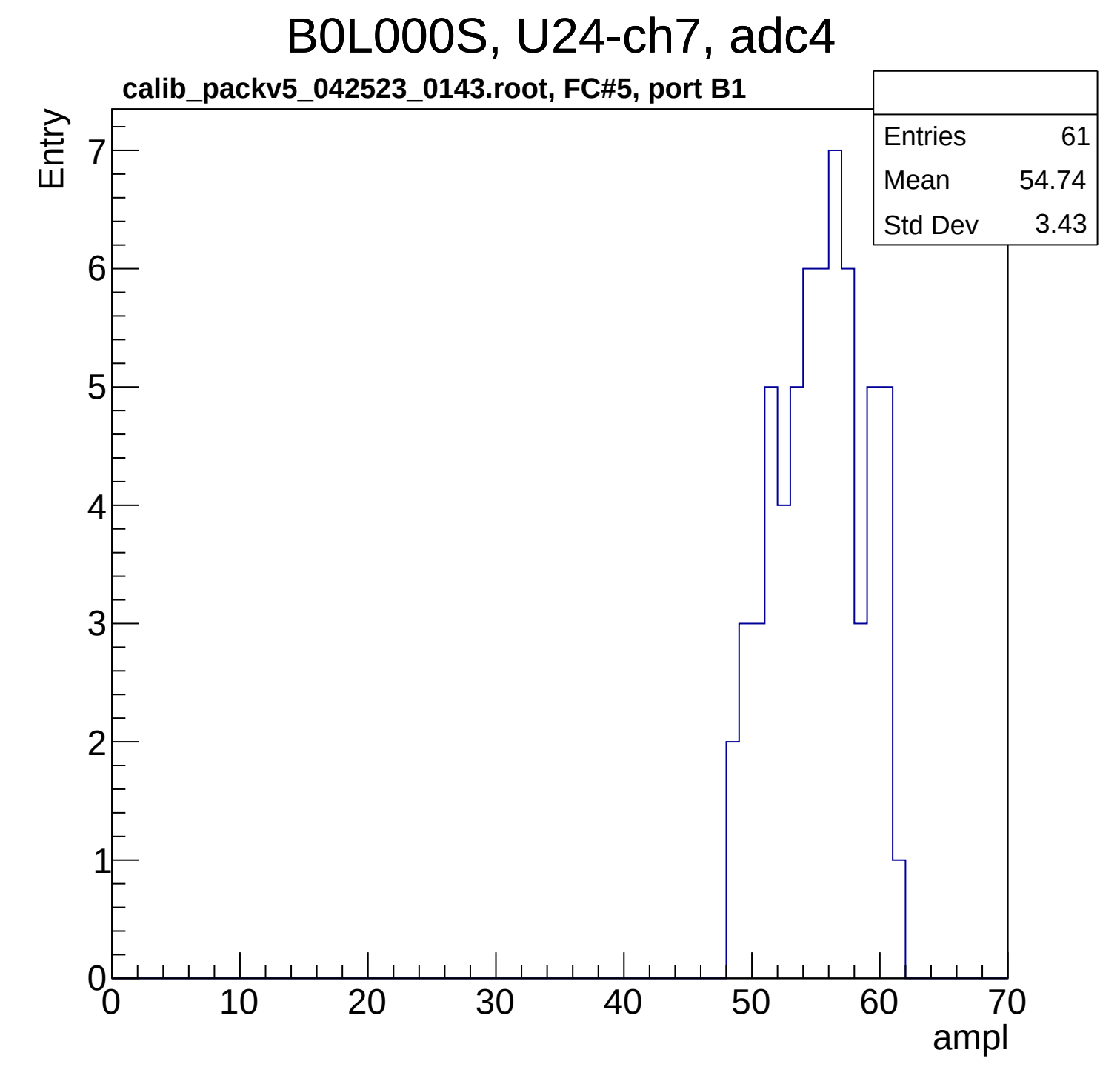
B0L000S, U24-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.74
Std Dev 3.43
Entry
ampl
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3
4
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0
10
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40
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60
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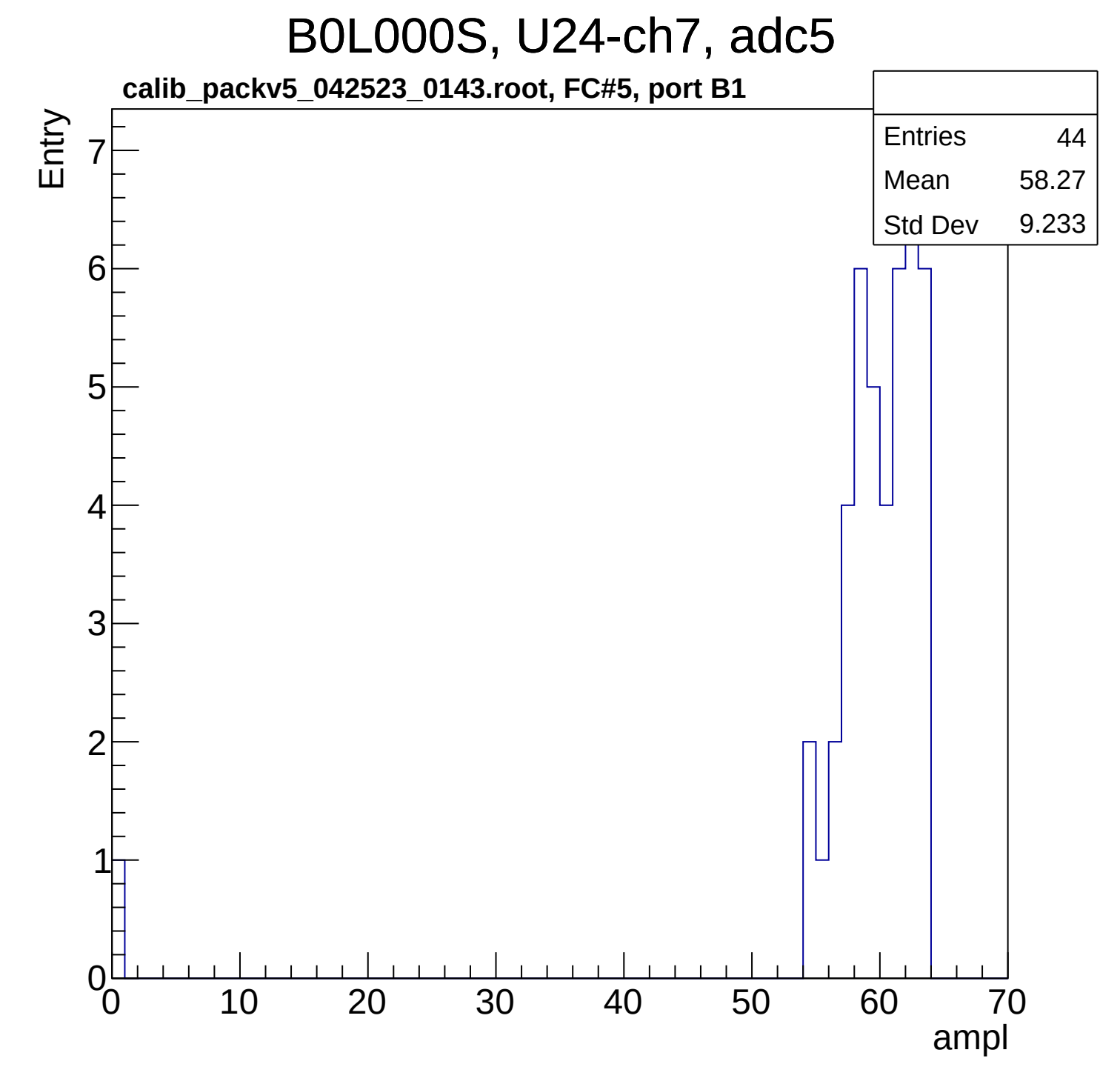
B0L000S, U24-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.27
Std Dev 9.233
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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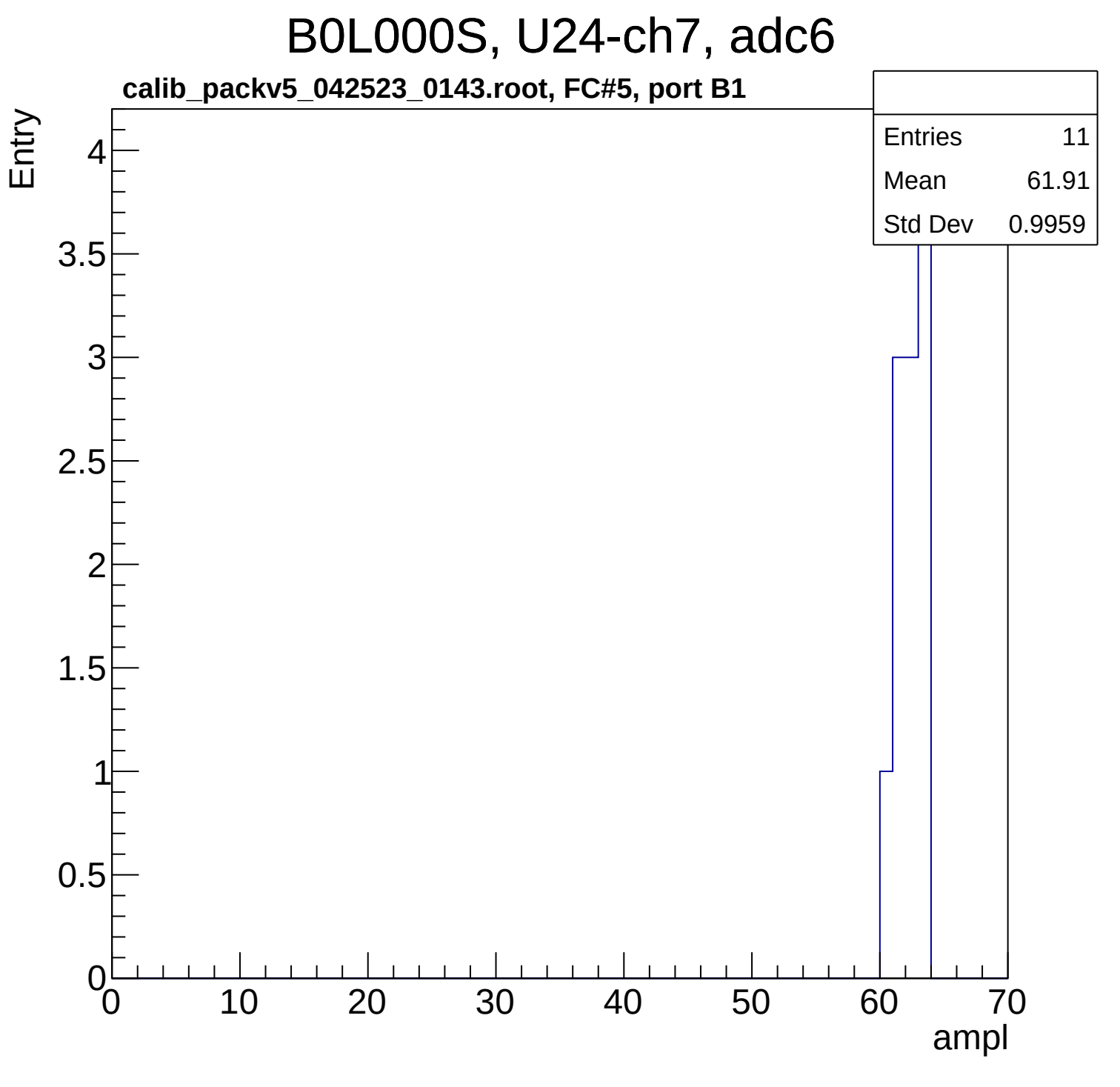
40
50
60
70


B0L000S, U24-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.91
Std Dev 0.9959
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
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0
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60
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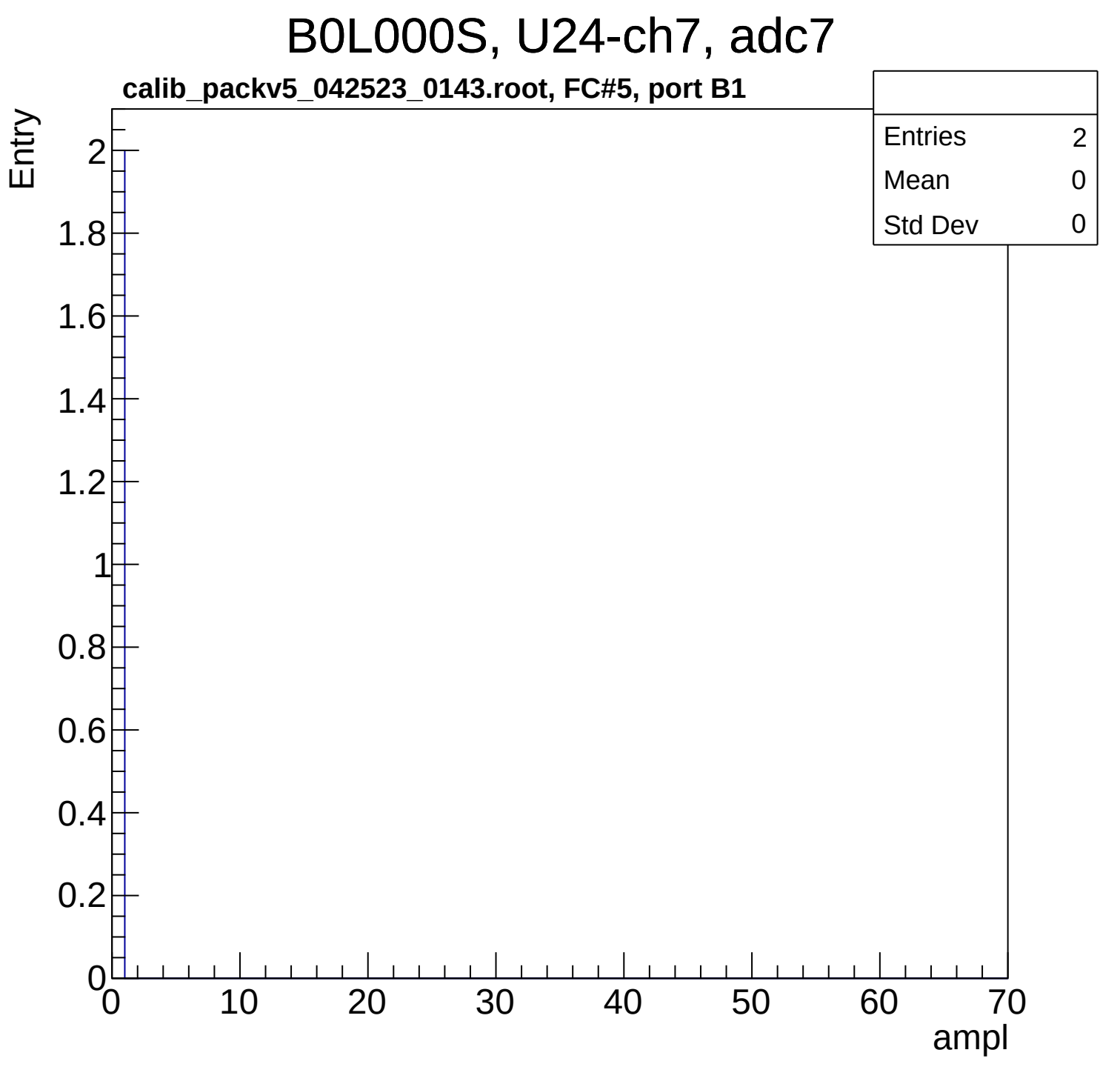
B0L000S, U24-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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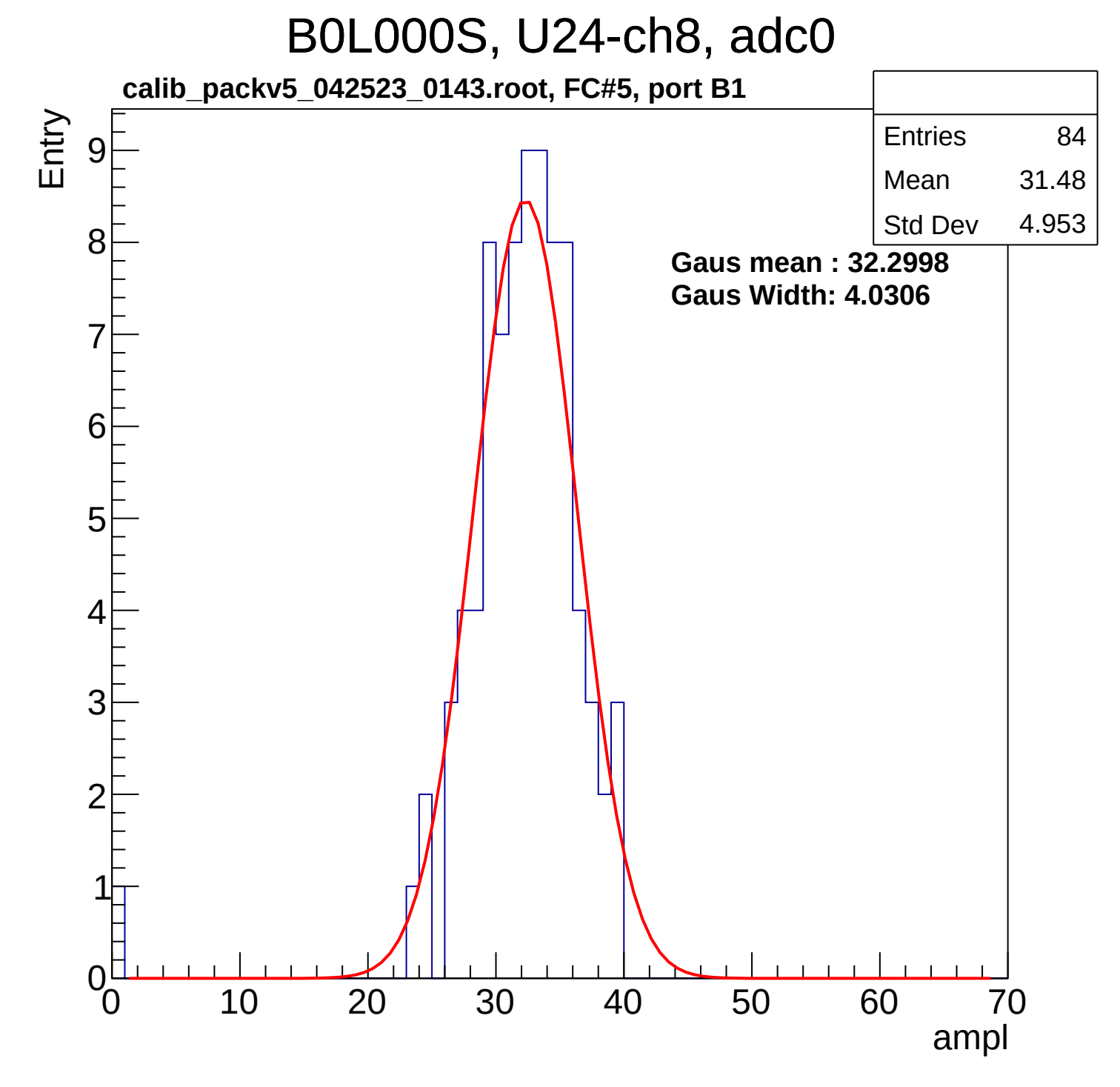
B0L000S, U24-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 31.48
Std Dev 4.953
Gaus mean : 32.2998
Gaus Width: 4.0306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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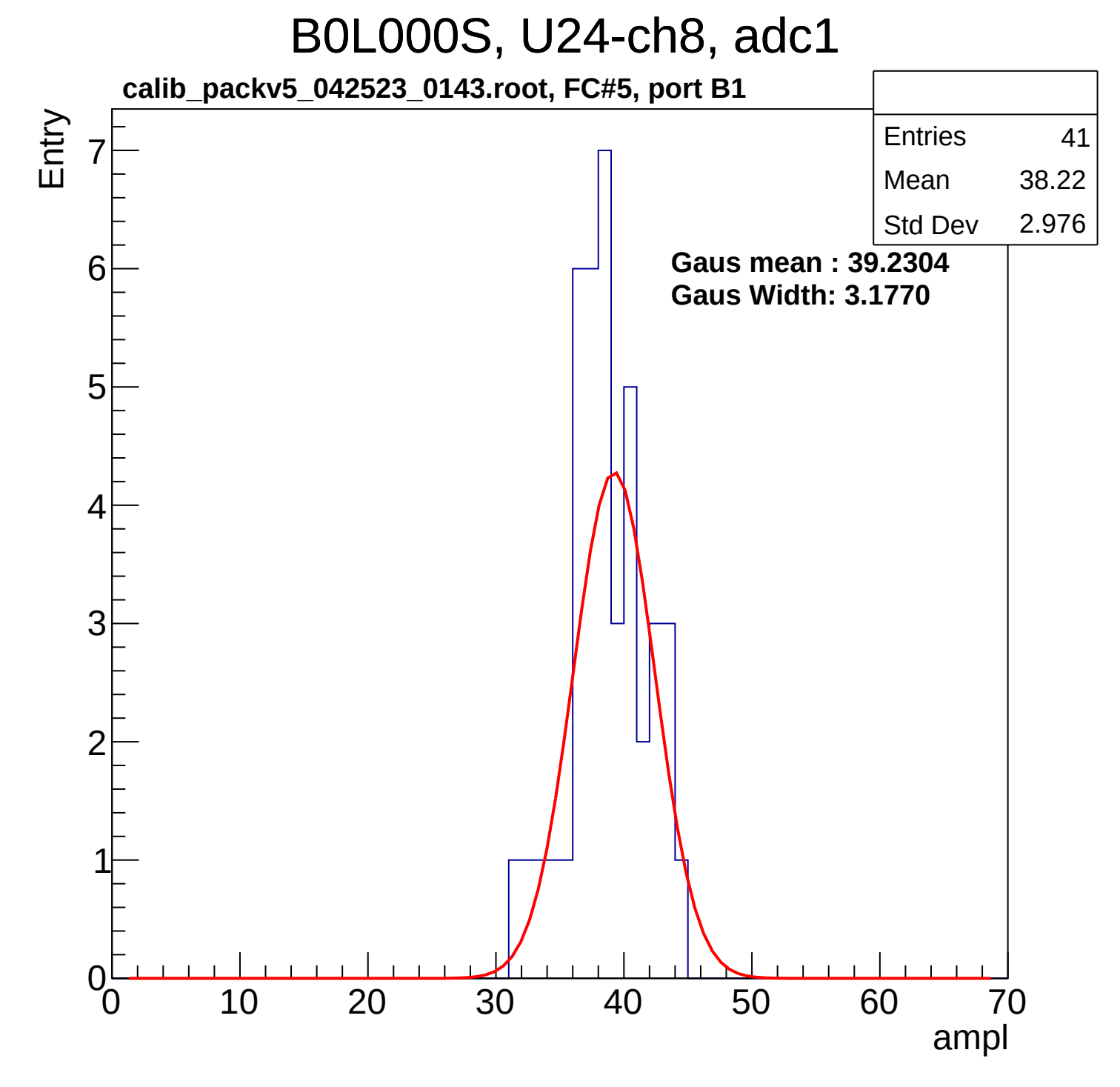
B0L000S, U24-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 38.22
Std Dev 2.976
Gaus mean : 39.2304
Gaus Width: 3.1770
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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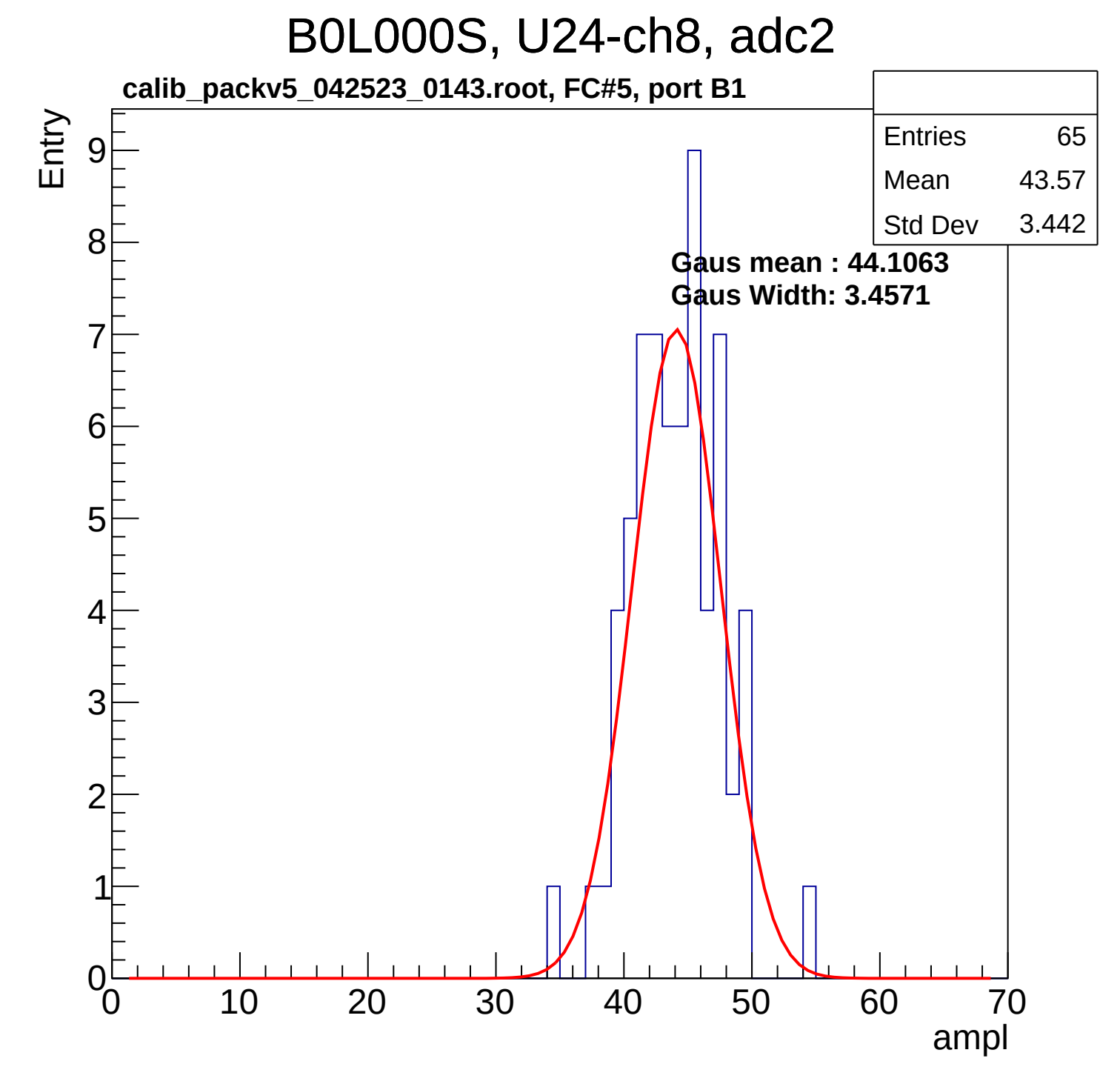
B0L000S, U24-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.57
Std Dev 3.442
Gaus mean : 44.1063
Gaus Width: 3.4571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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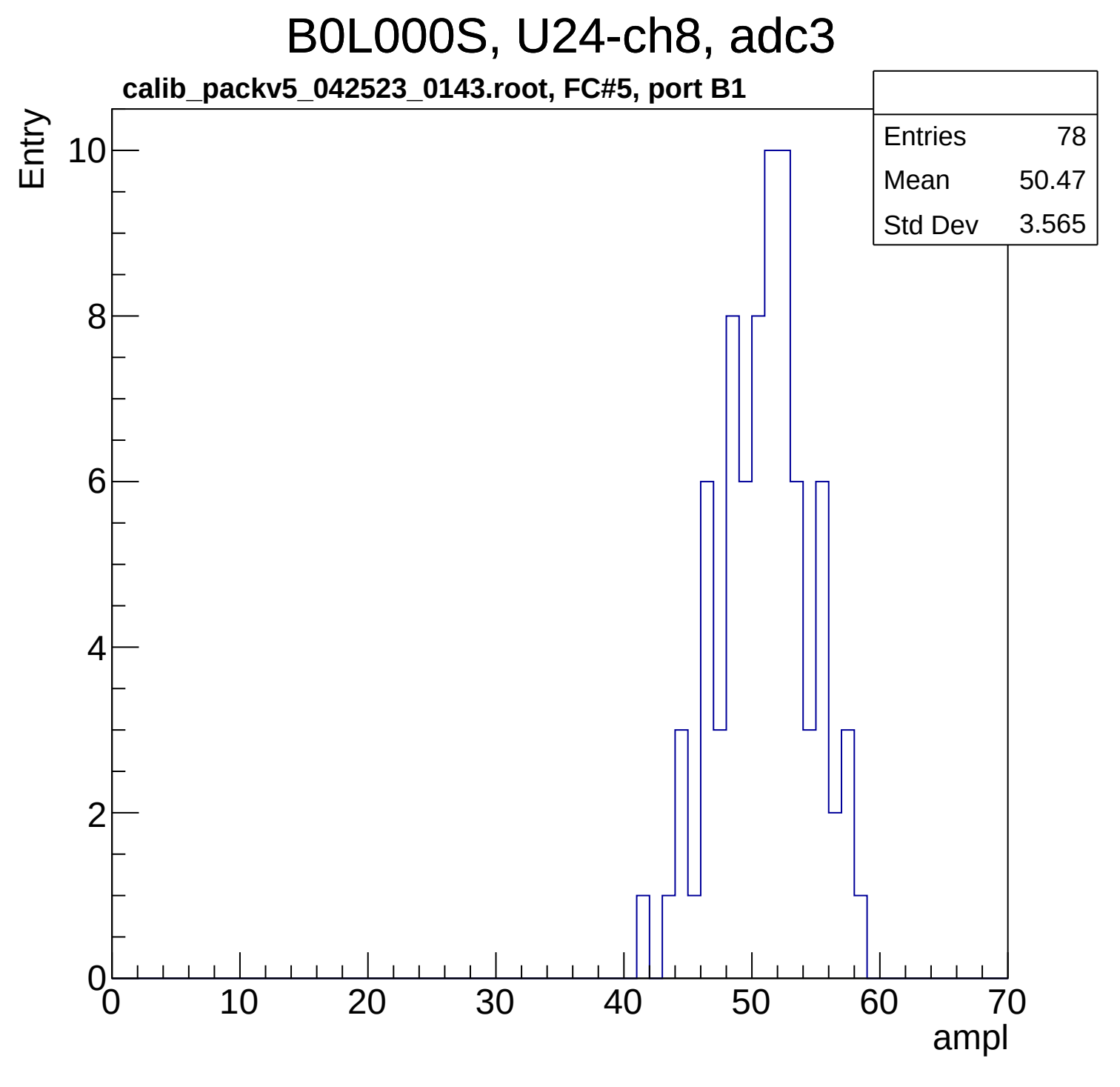
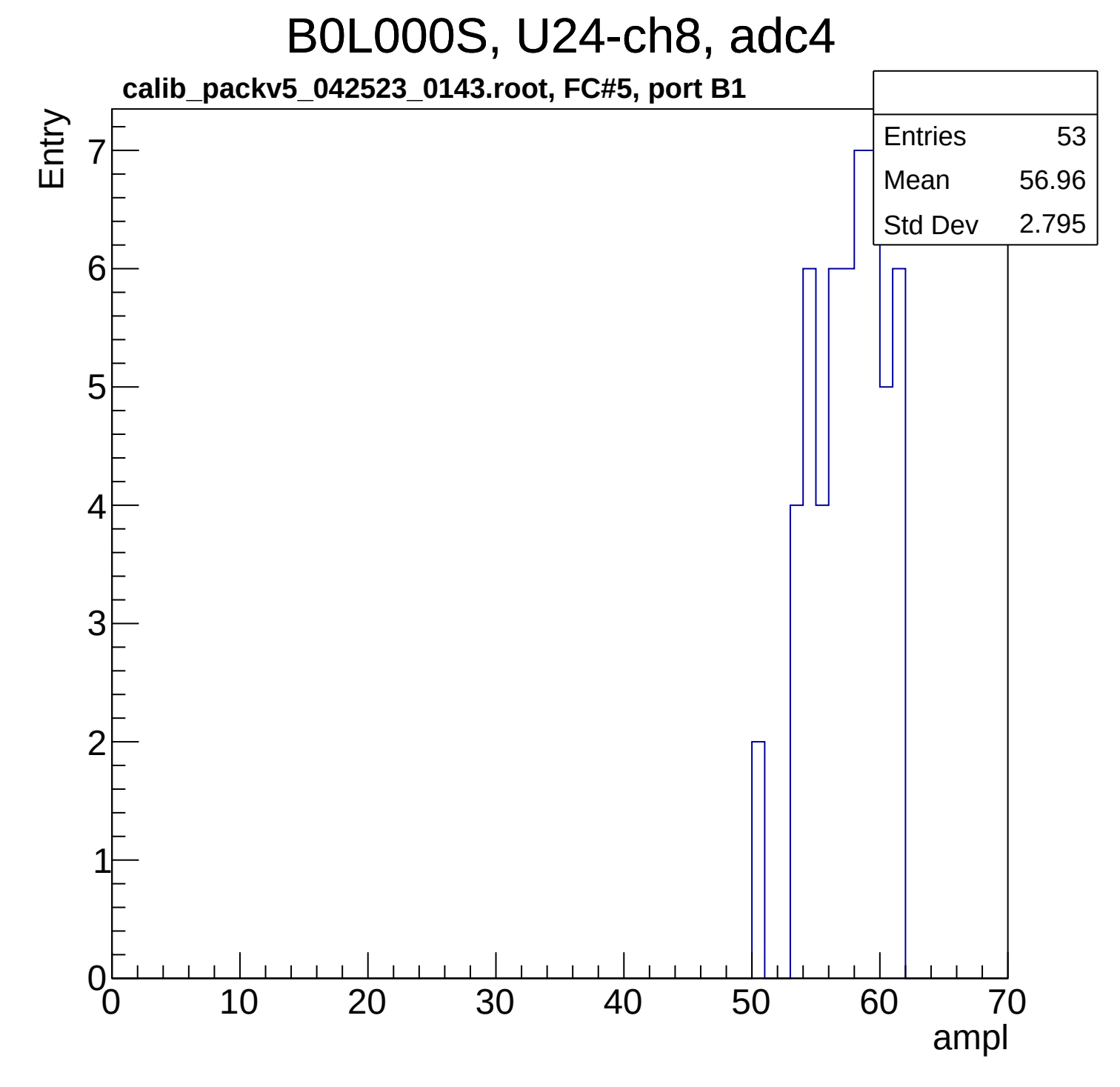

B0L000S, U24-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 50.47
Std Dev 3.565
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.96
Std Dev 2.795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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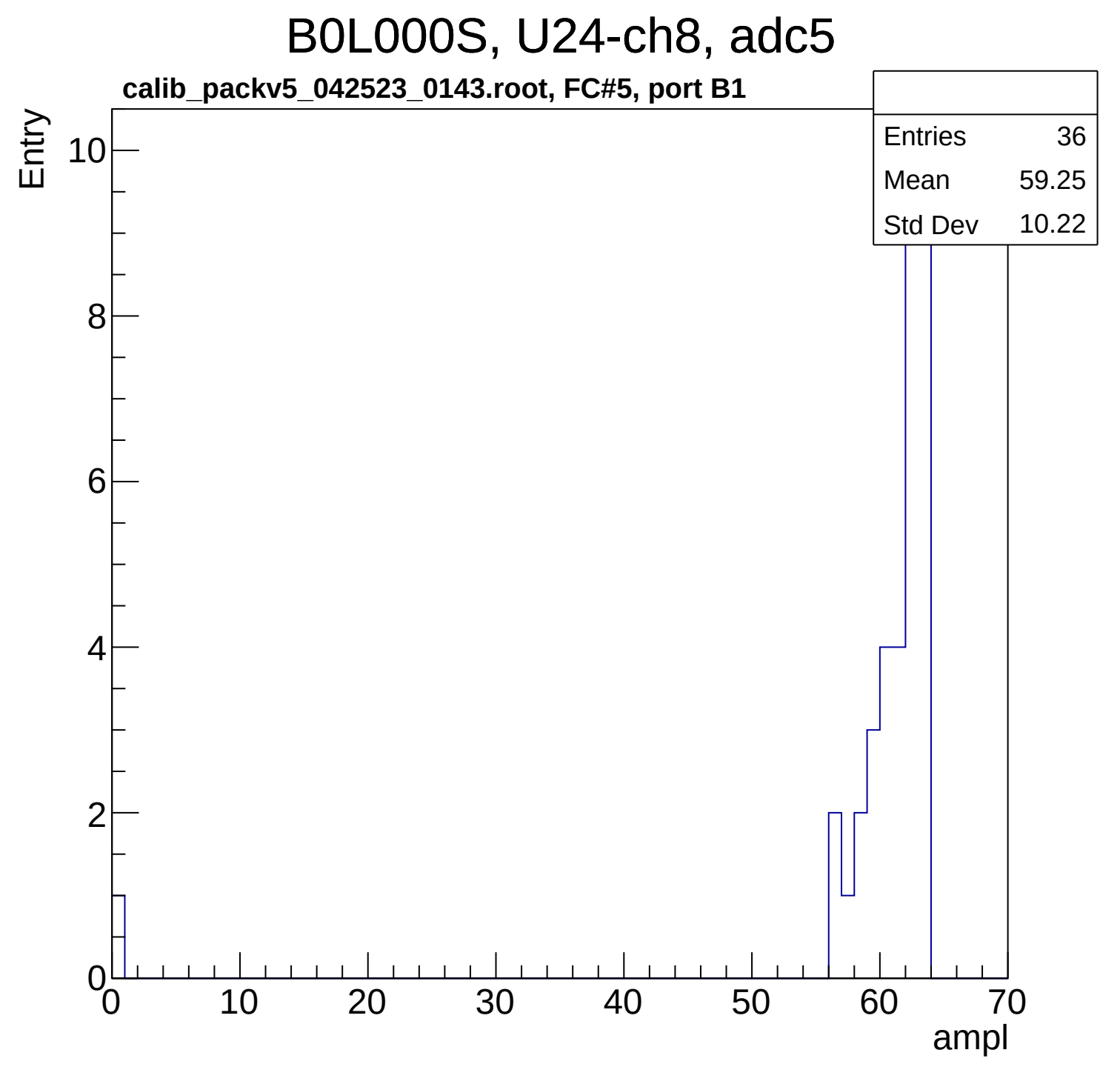
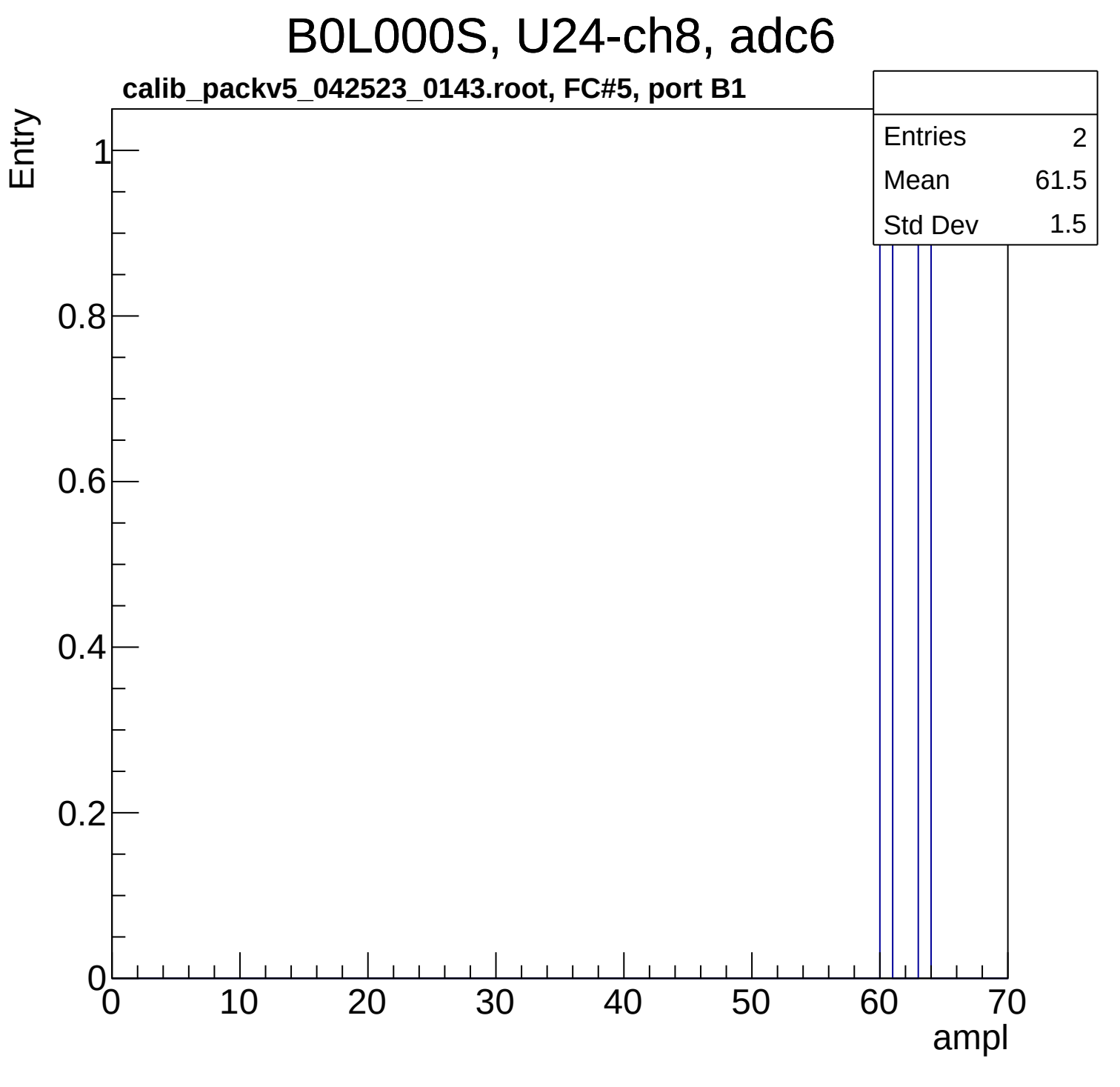

B0L000S, U24-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 59.25
Std Dev 10.22
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
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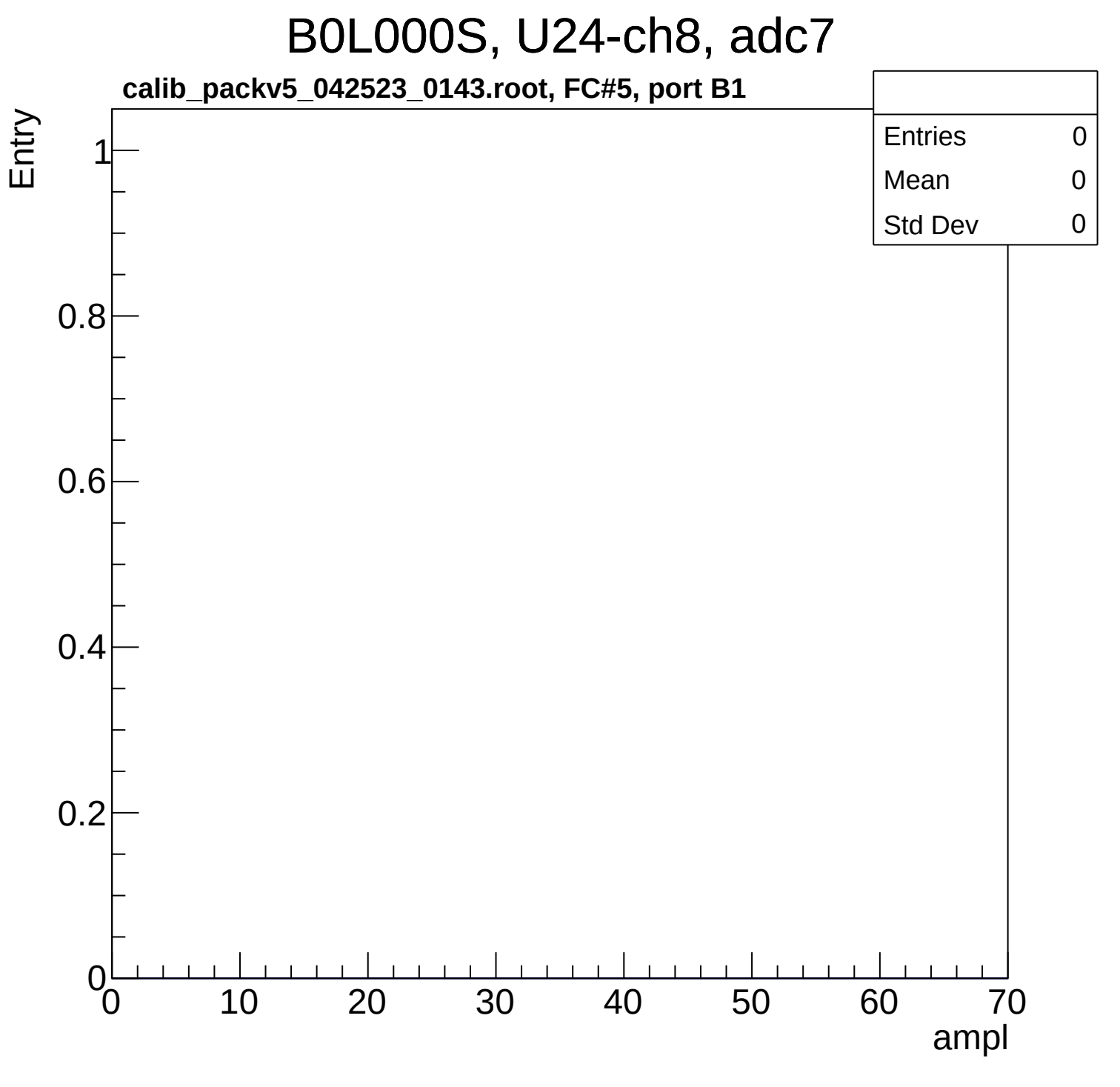
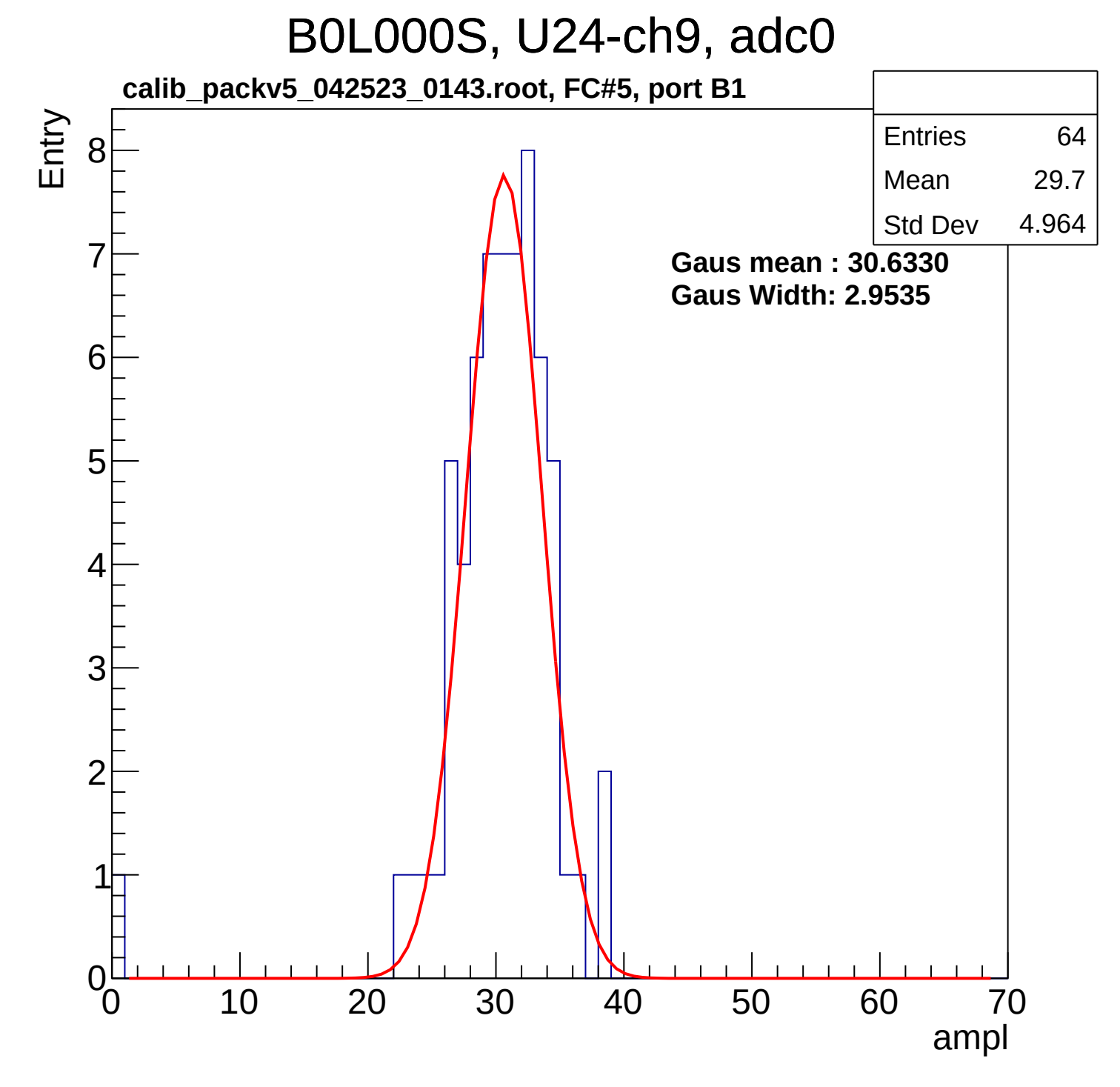
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U24-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.7
Std Dev 4.964
Gaus mean : 30.6330
Gaus Width: 2.9535
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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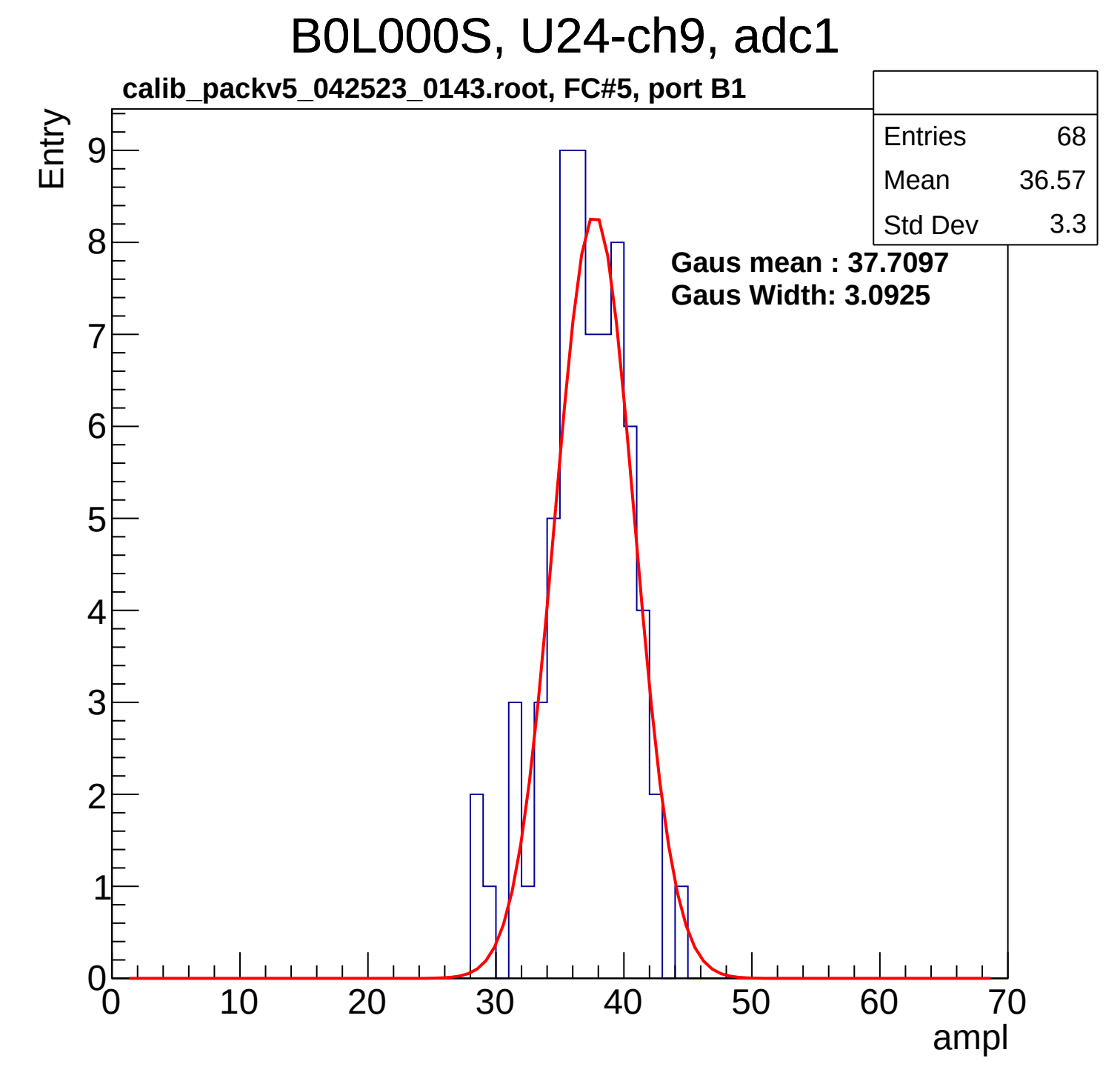
B0L000S, U24-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.57
Std Dev 3.3
Gaus mean : 37.7097
Gaus Width: 3.0925
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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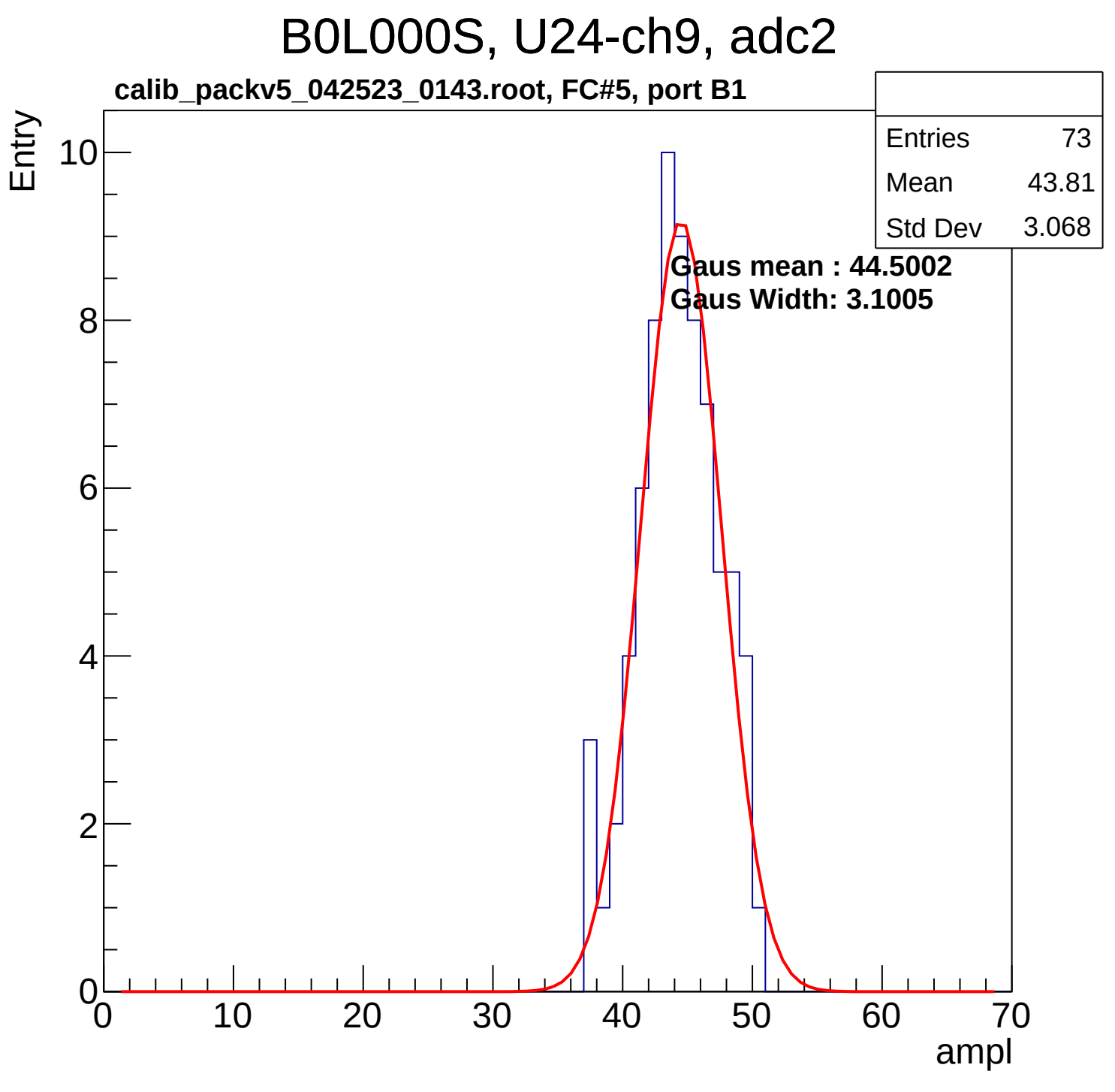
B0L000S, U24-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.81
Std Dev 3.068
Gaus mean : 44.5002
Gaus Width: 3.1005
Entry
ampl
0
2
4
6
8
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0
10
20
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40
50
60
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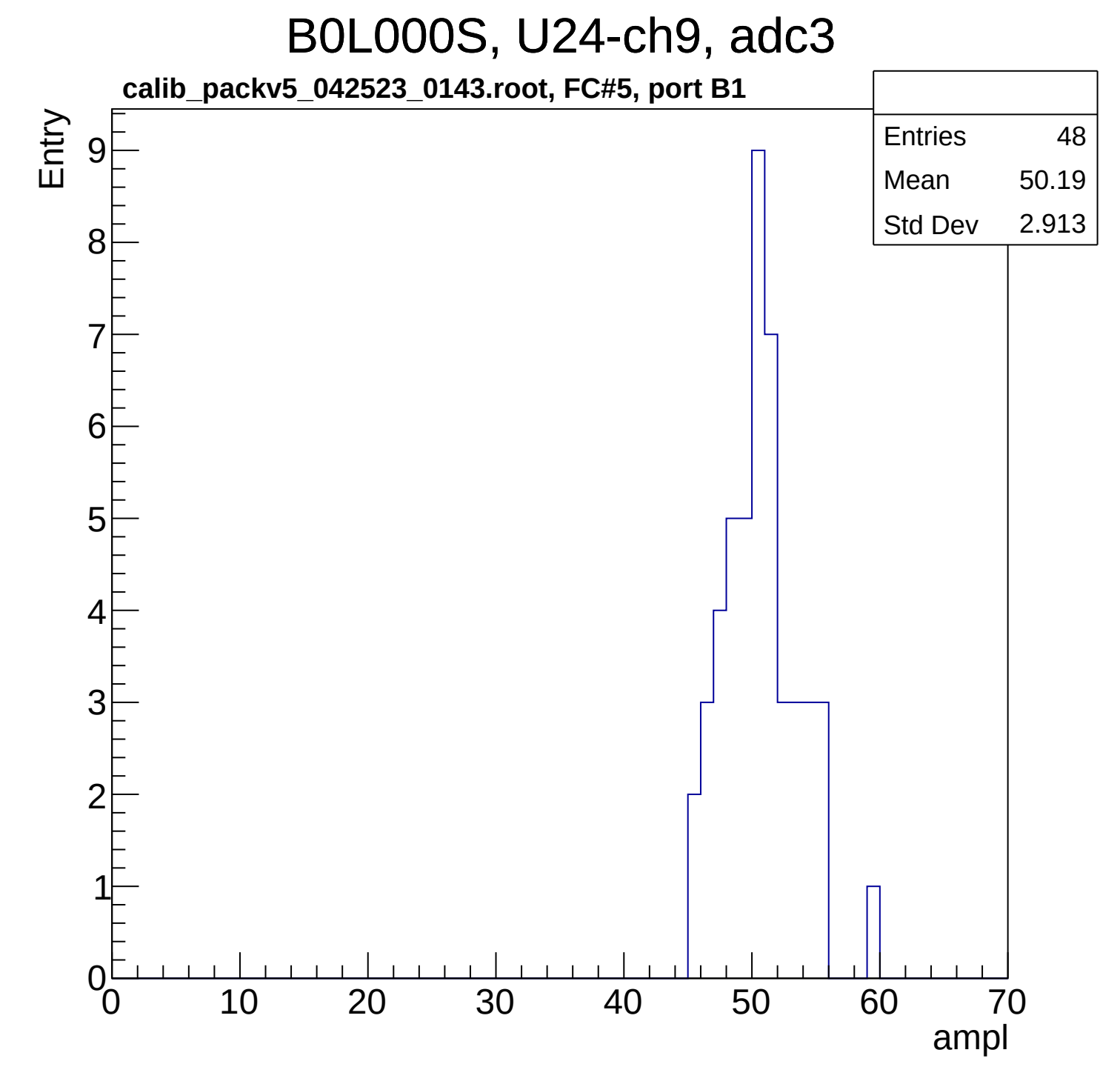
B0L000S, U24-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 50.19
Std Dev 2.913
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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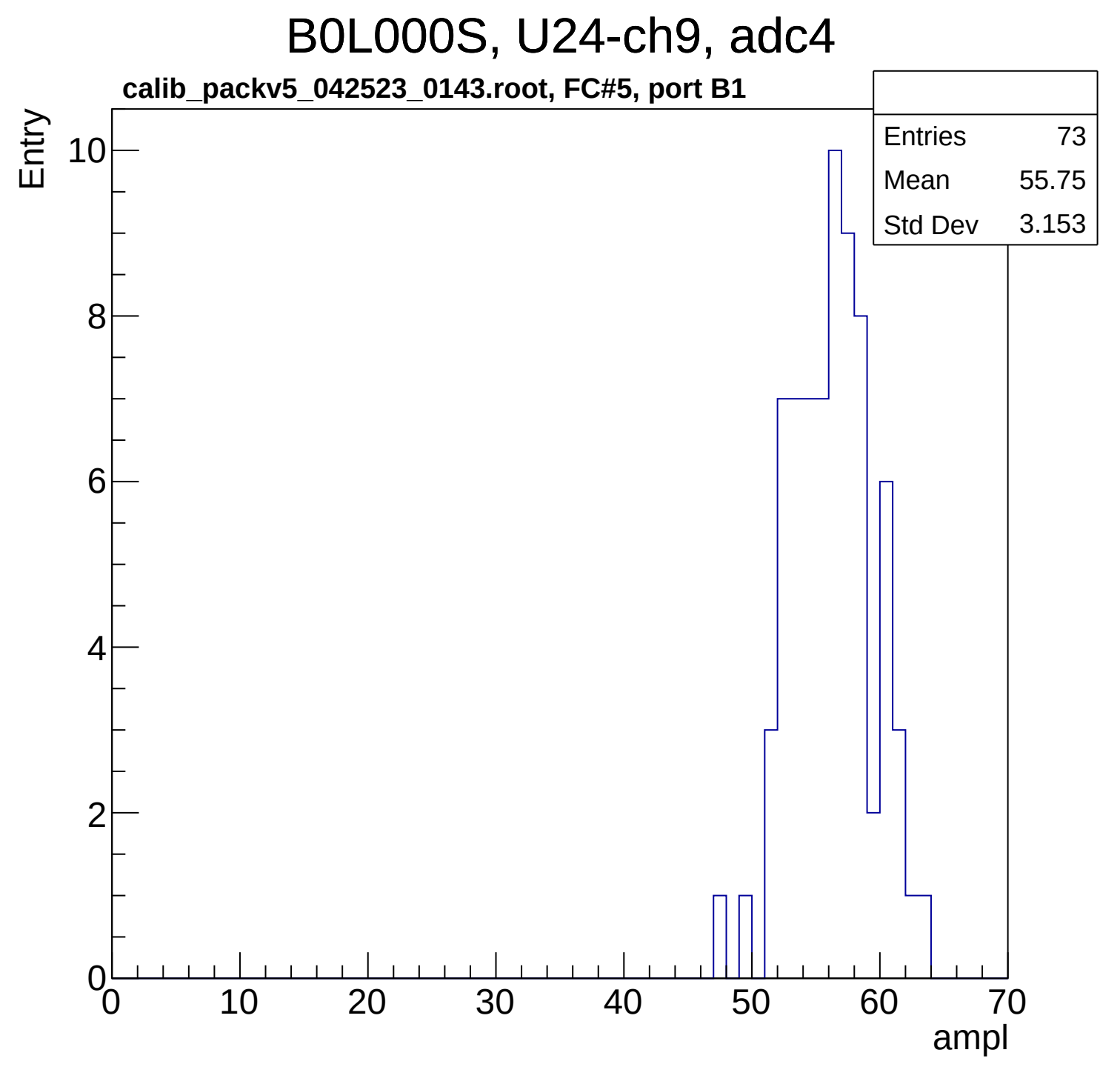
B0L000S, U24-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 55.75
Std Dev 3.153
Entry
ampl
0
2
4
6
8
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0
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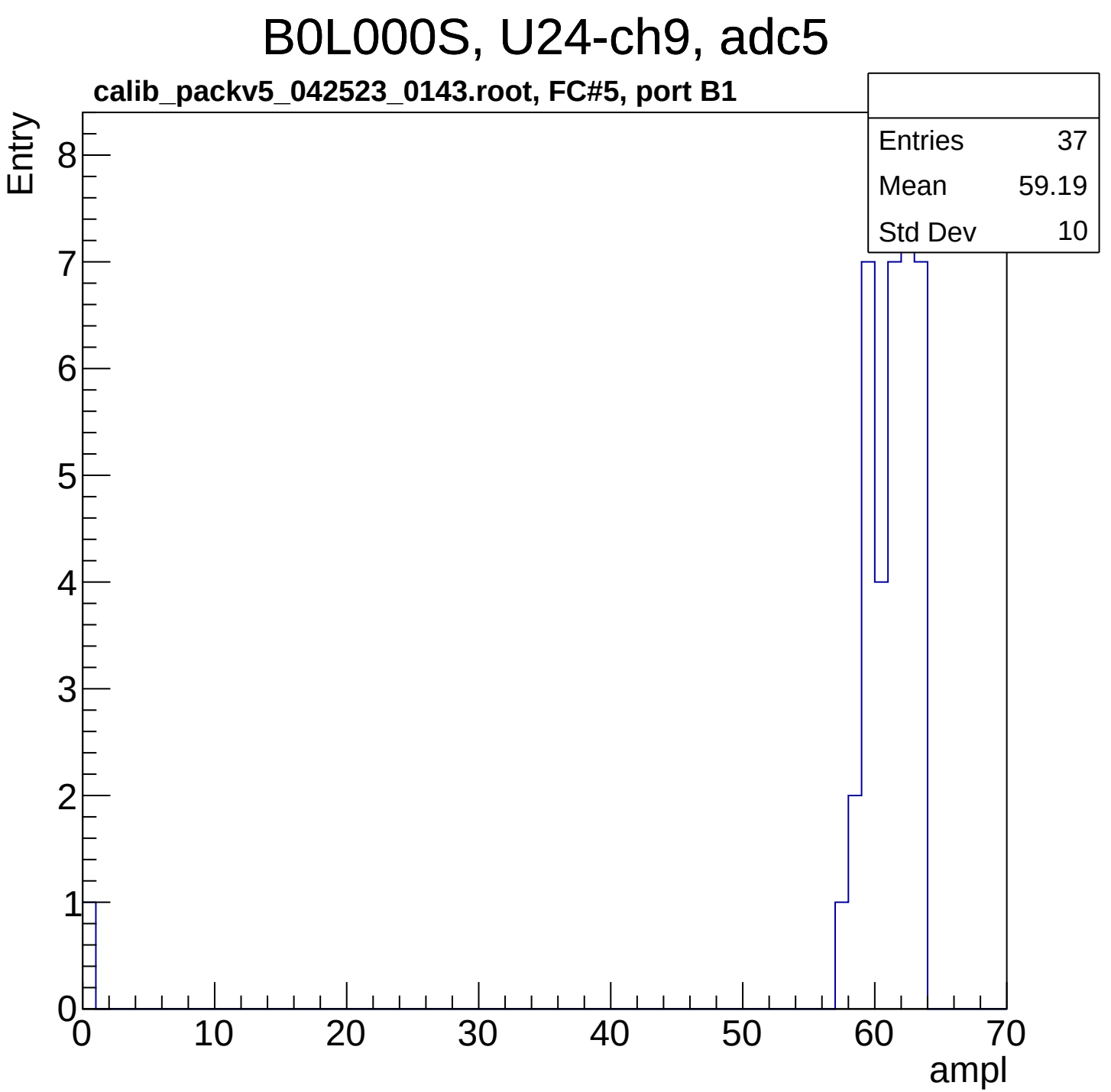
20
30
40
50
60
70


B0L000S, U24-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.19
Std Dev 10
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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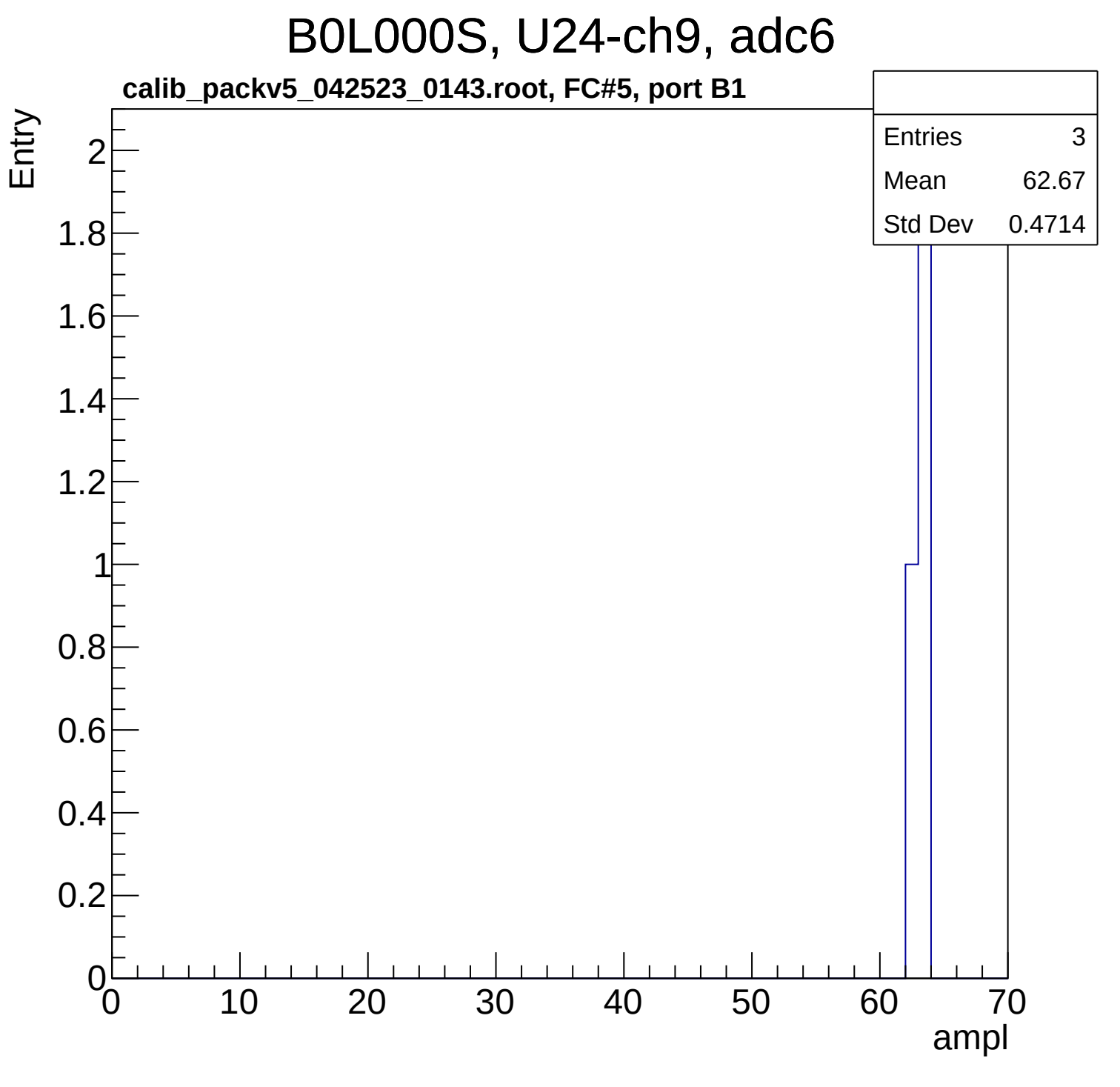
B0L000S, U24-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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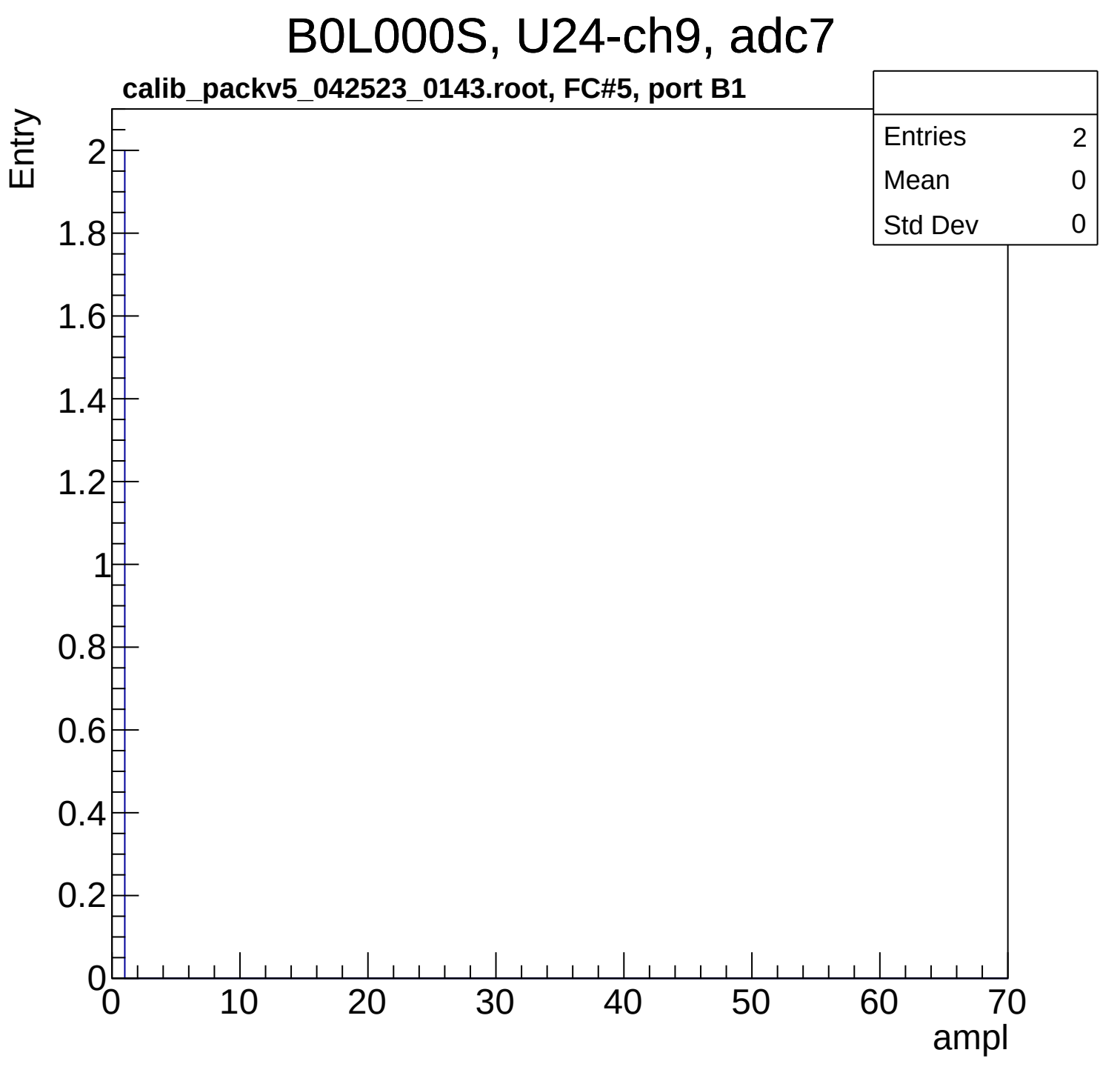
B0L000S, U24-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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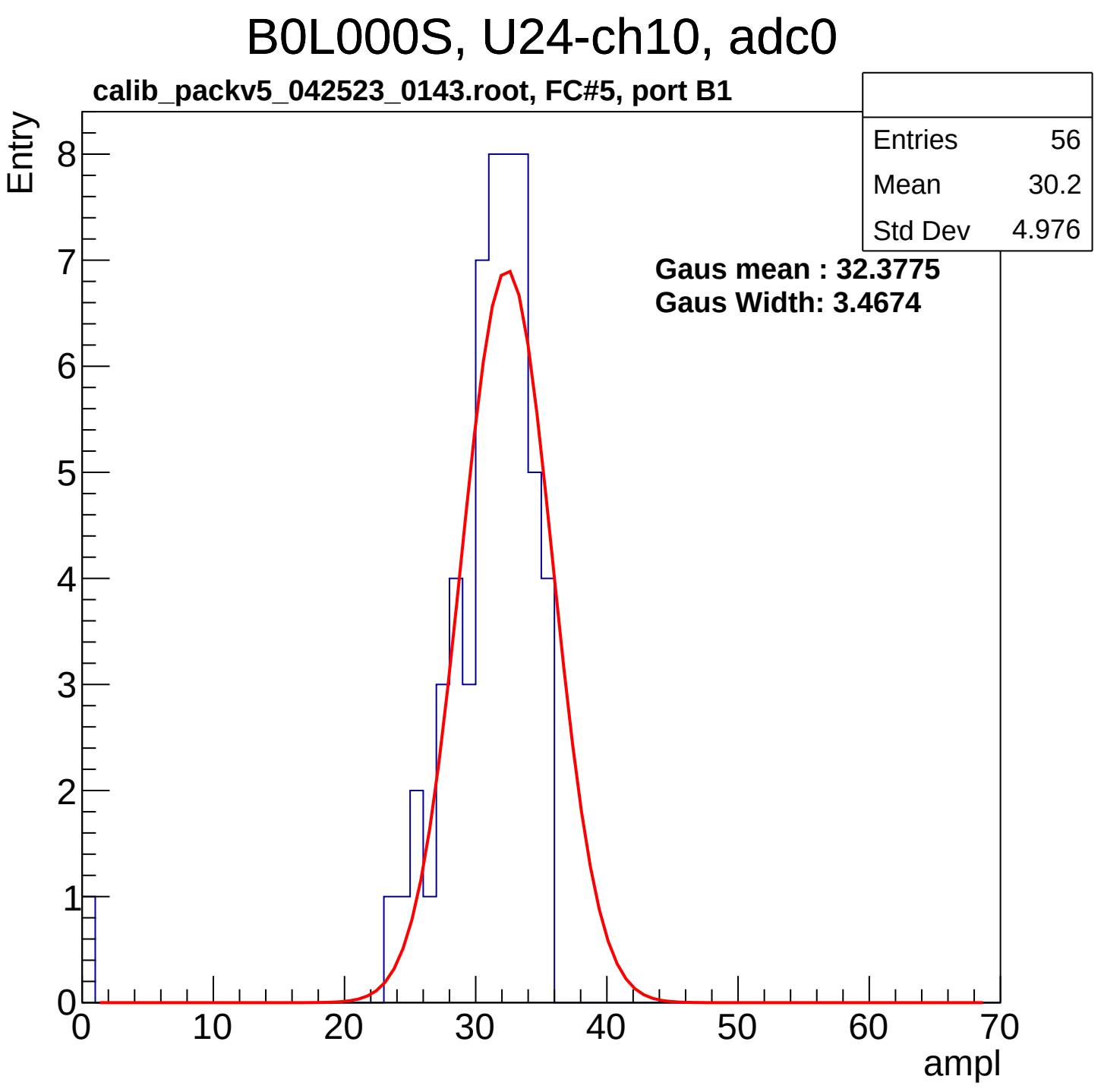
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 30.2
Std Dev 4.976
Gaus mean : 32.3775
Gaus Width: 3.4674
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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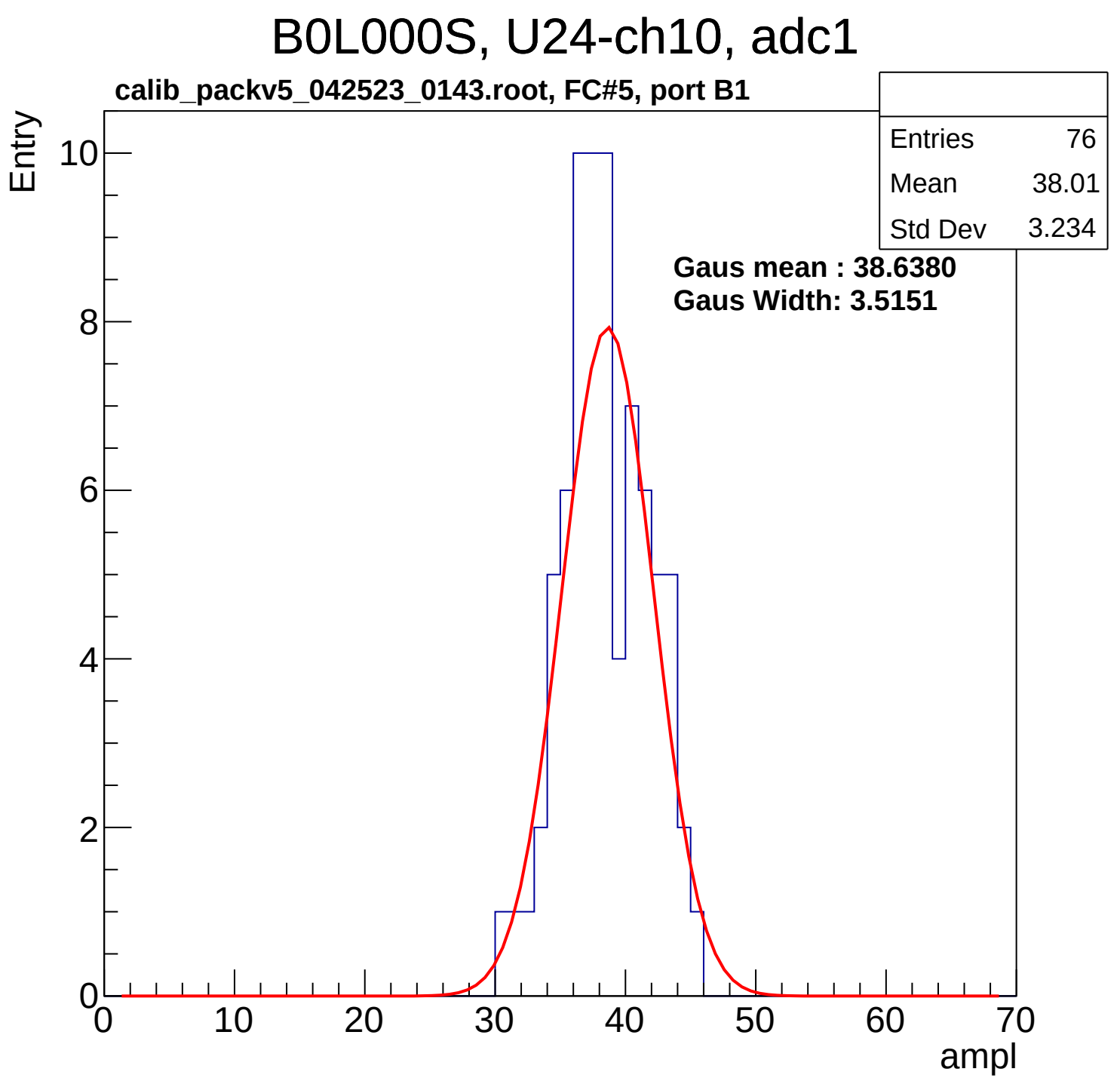
B0L000S, U24-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 38.01
Std Dev 3.234
Gaus mean : 38.6380
Gaus Width: 3.5151
Entry
ampl
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4
6
8
10
0
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20
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40
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60
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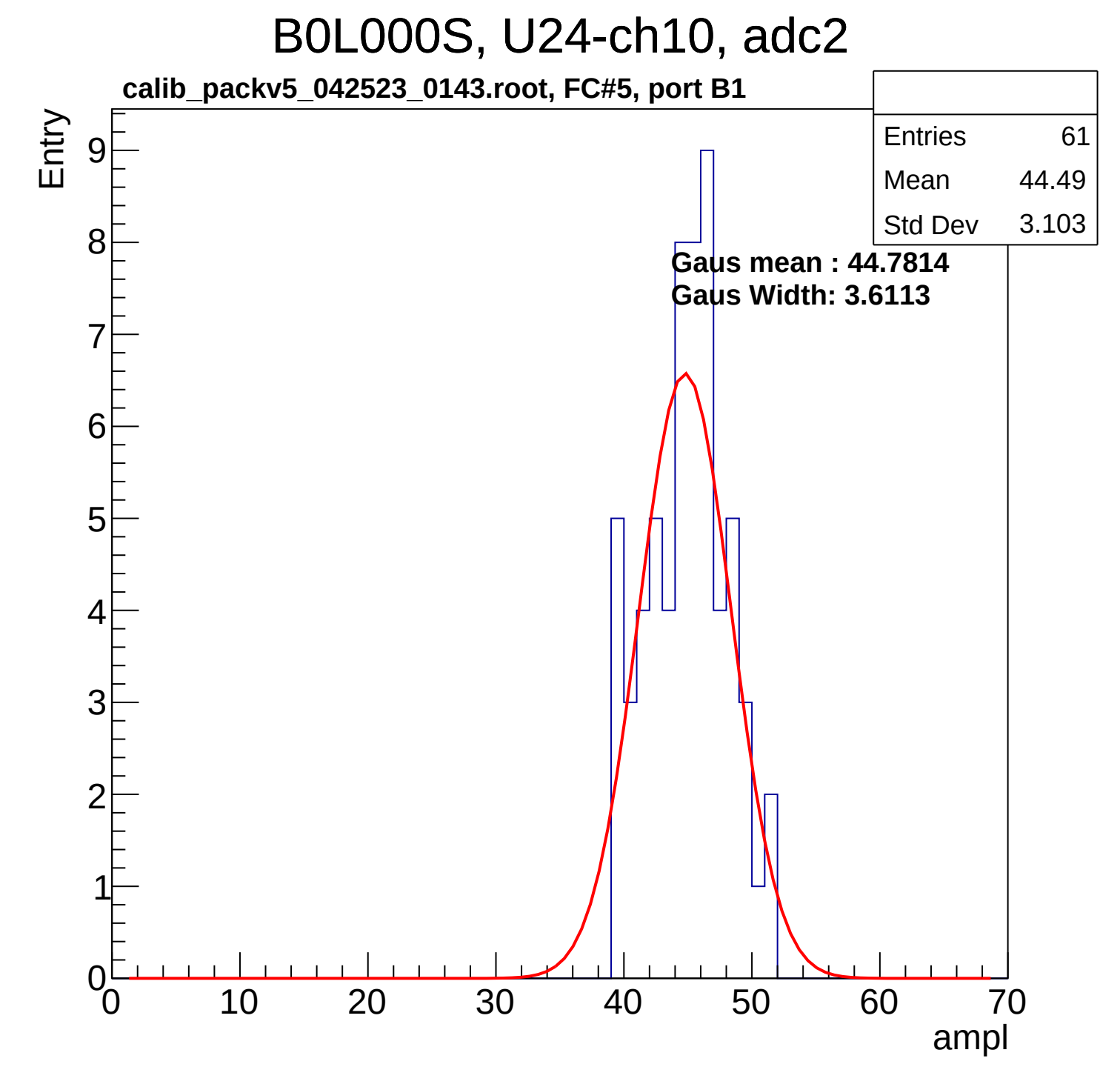
B0L000S, U24-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 44.49
Std Dev 3.103
Gaus mean : 44.7814
Gaus Width: 3.6113
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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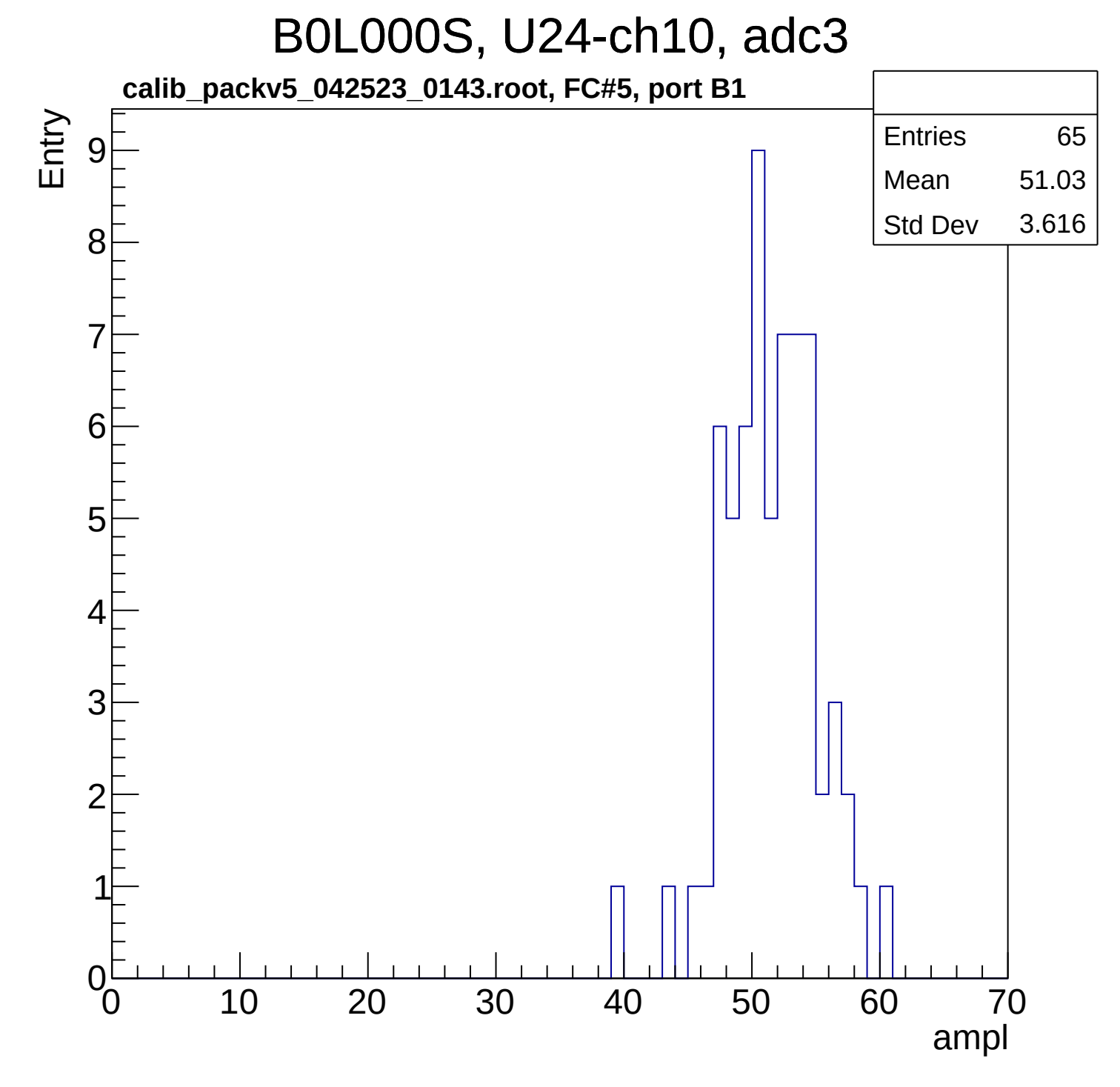
B0L000S, U24-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 51.03
Std Dev 3.616
Entry
ampl
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4
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6
7
8
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0
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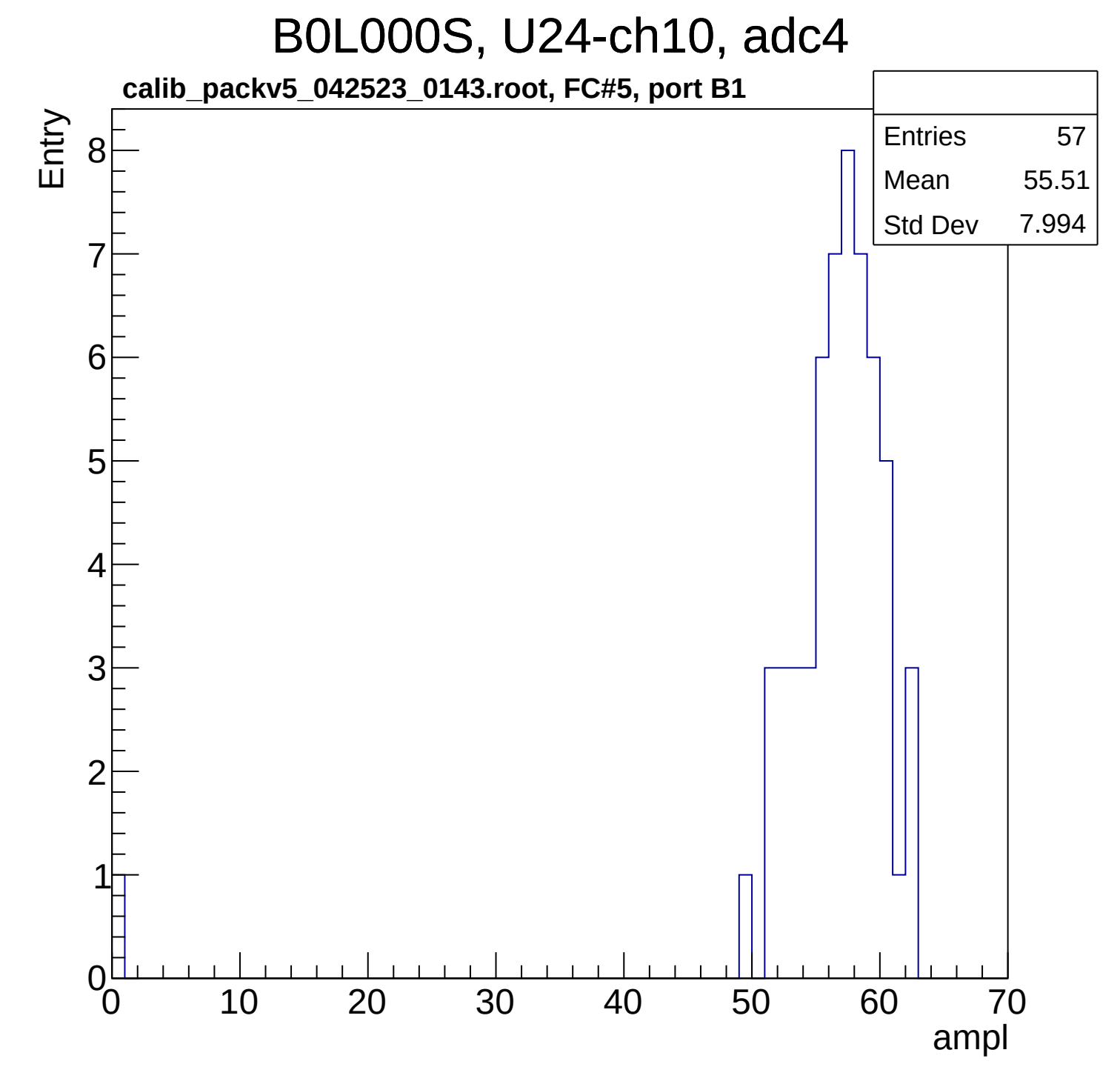
B0L000S, U24-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.51
Std Dev 7.994
Entry
ampl
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4
5
6
7
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0
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60
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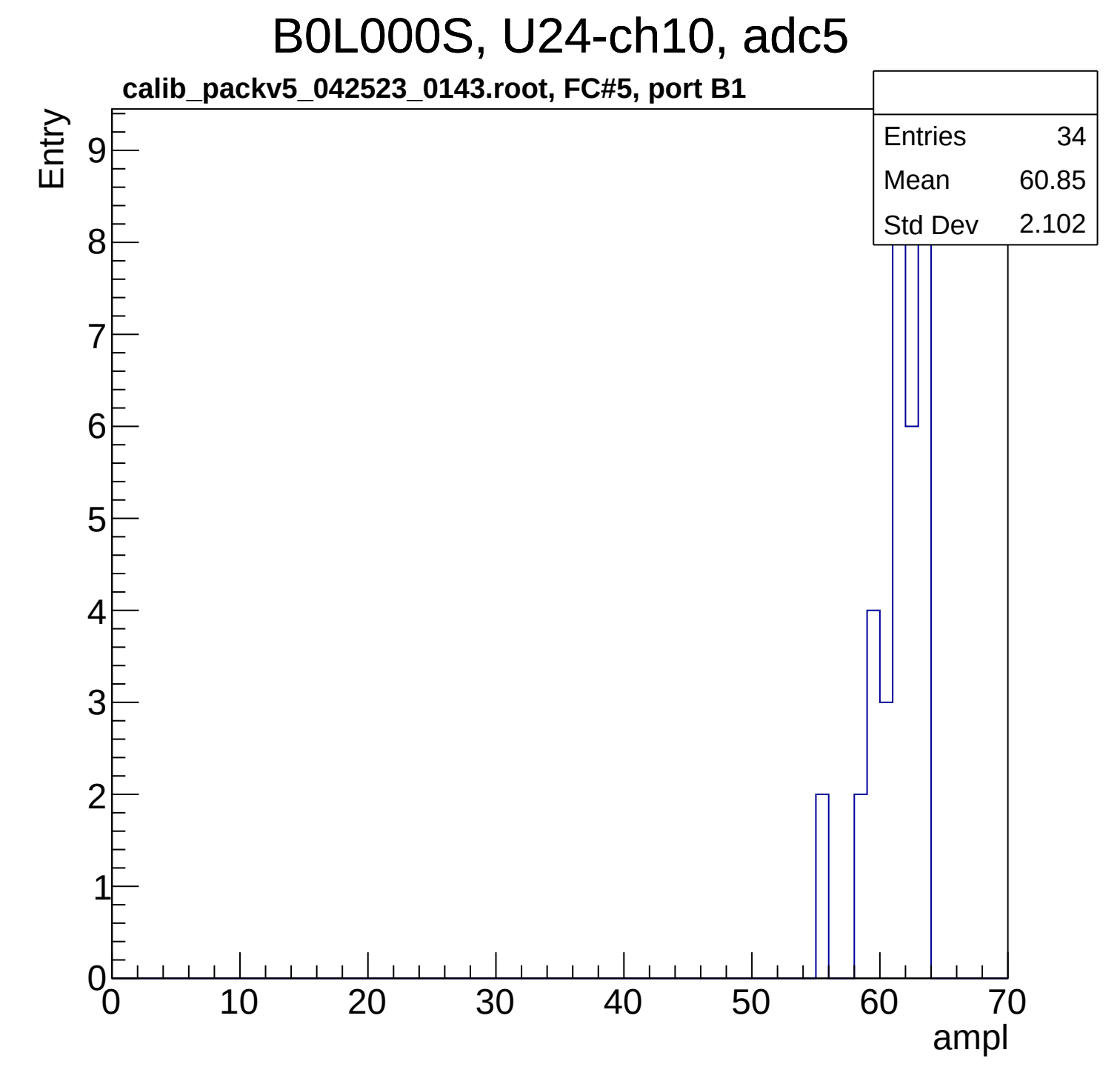
B0L000S, U24-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 60.85
Std Dev 2.102
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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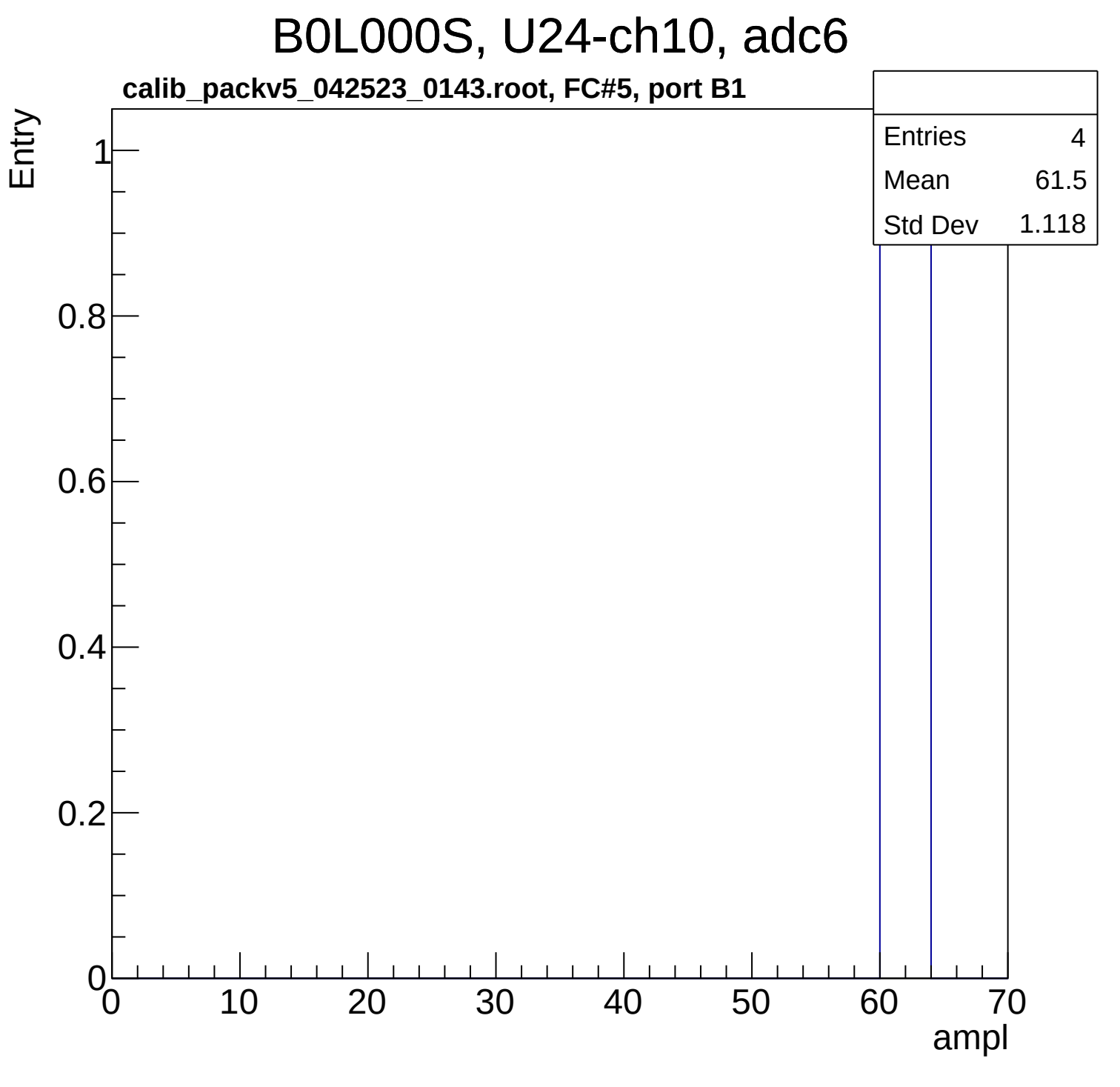
B0L000S, U24-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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0.6
0.4
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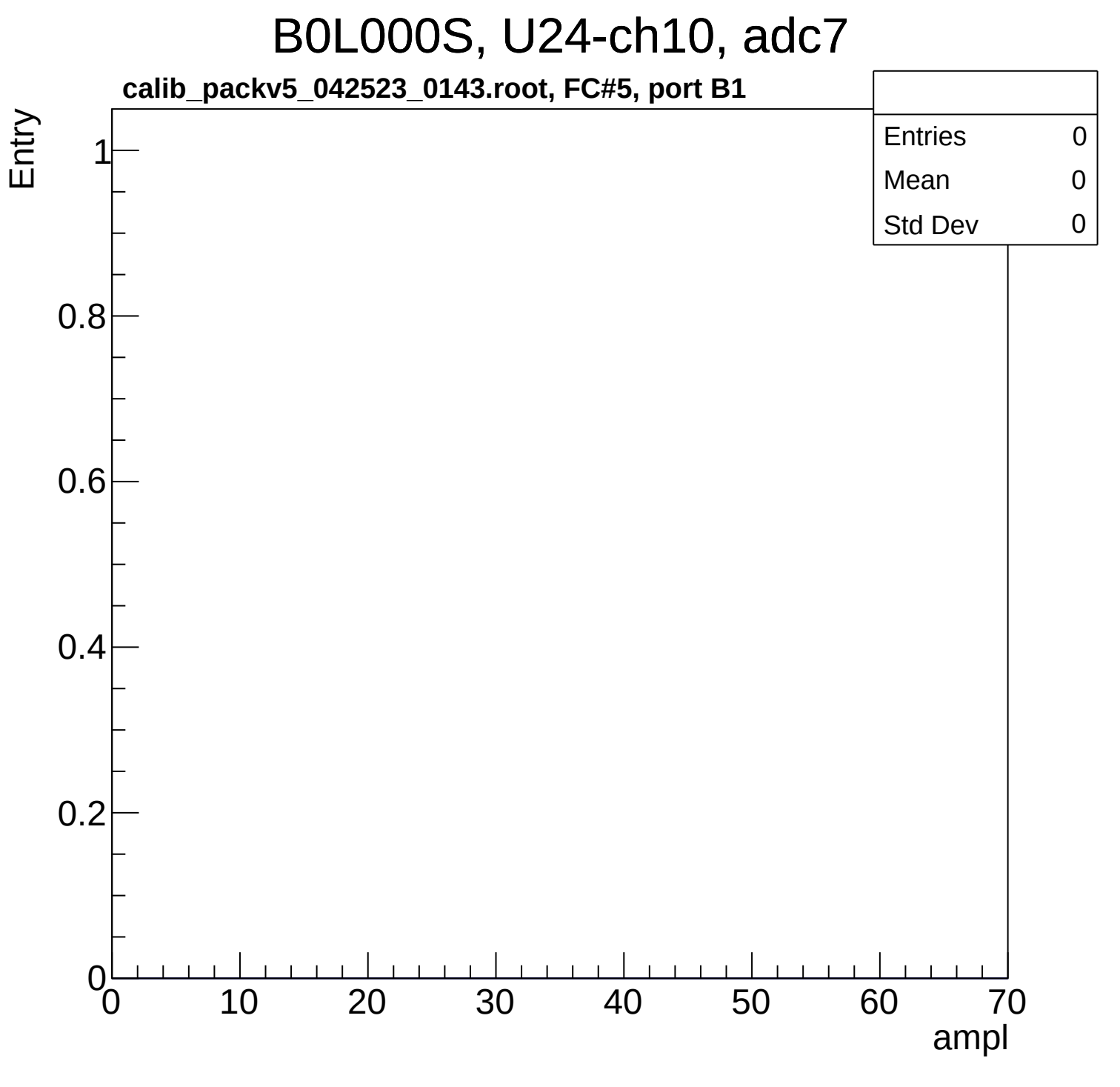
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
10
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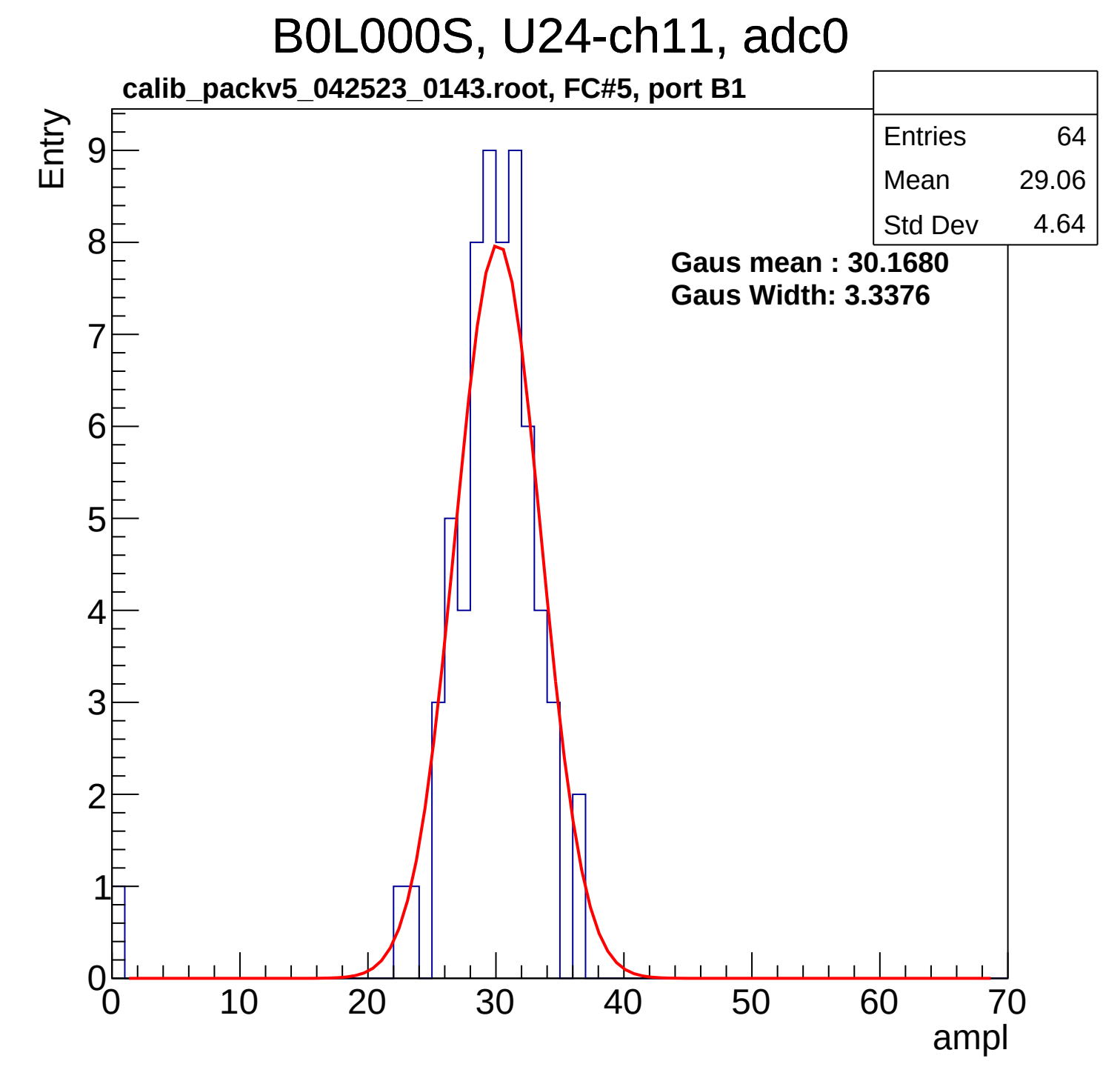
40
50
60
70
ampl


B0L000S, U24-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.06
Std Dev 4.64
Gaus mean : 30.1680
Gaus Width: 3.3376
Entry
ampl
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3
4
5
6
7
8
9
0
10
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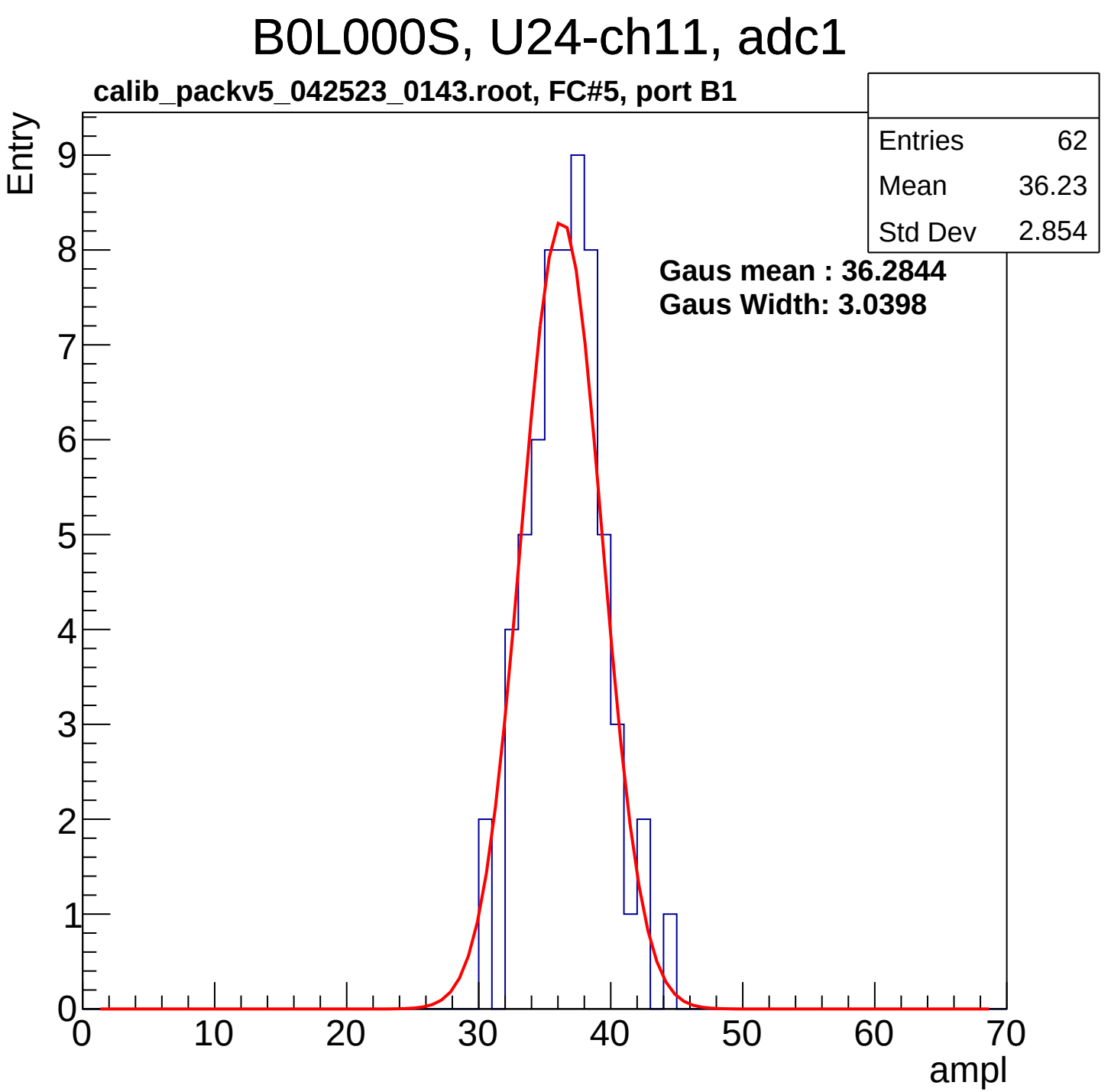
B0L000S, U24-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.23
Std Dev 2.854
Gaus mean : 36.2844
Gaus Width: 3.0398
Entry
ampl
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3
4
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6
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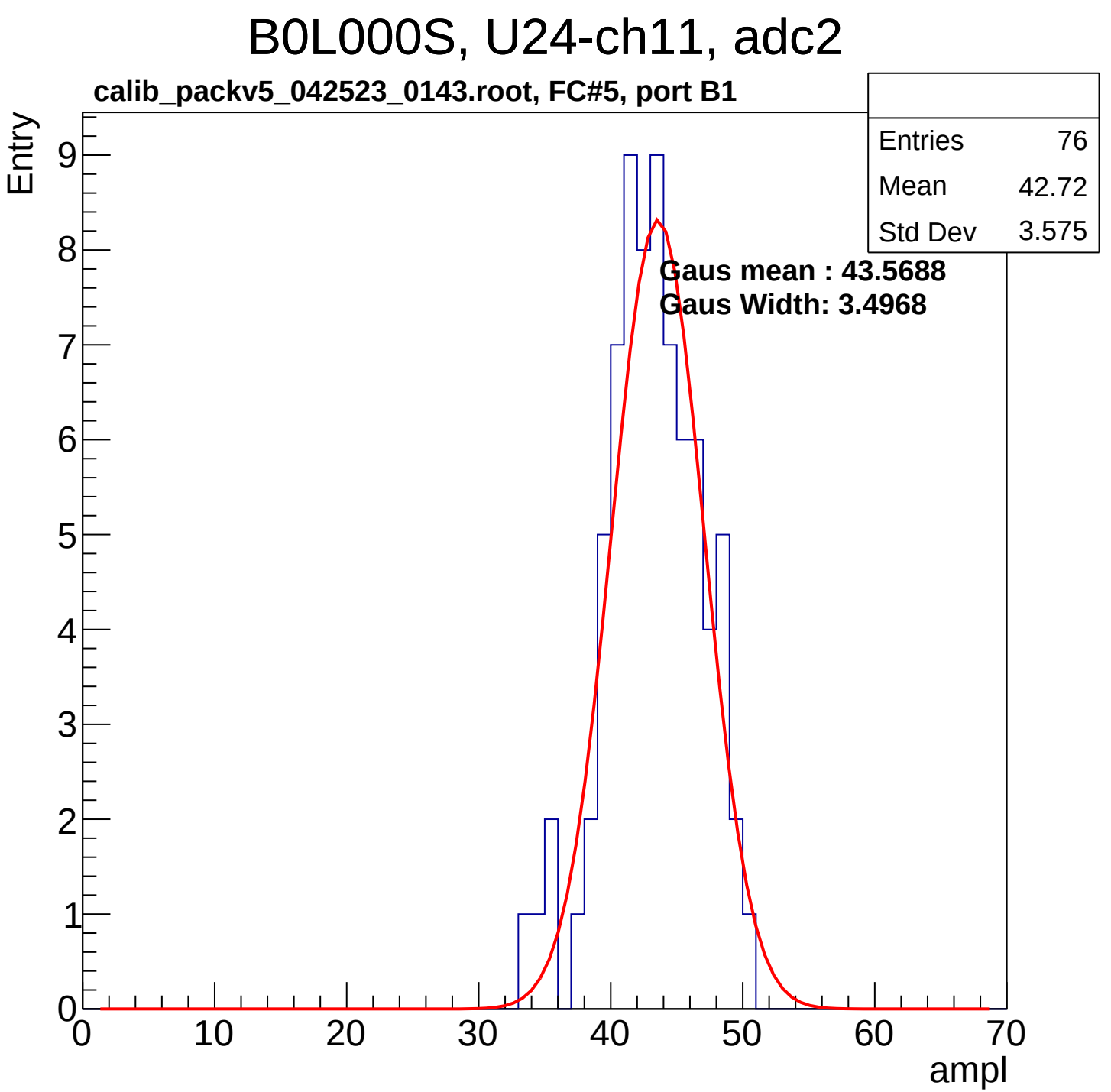
B0L000S, U24-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 42.72
Std Dev 3.575
Gaus mean : 43.5688
Gaus Width: 3.4968
Entry
ampl
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2
3
4
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6
7
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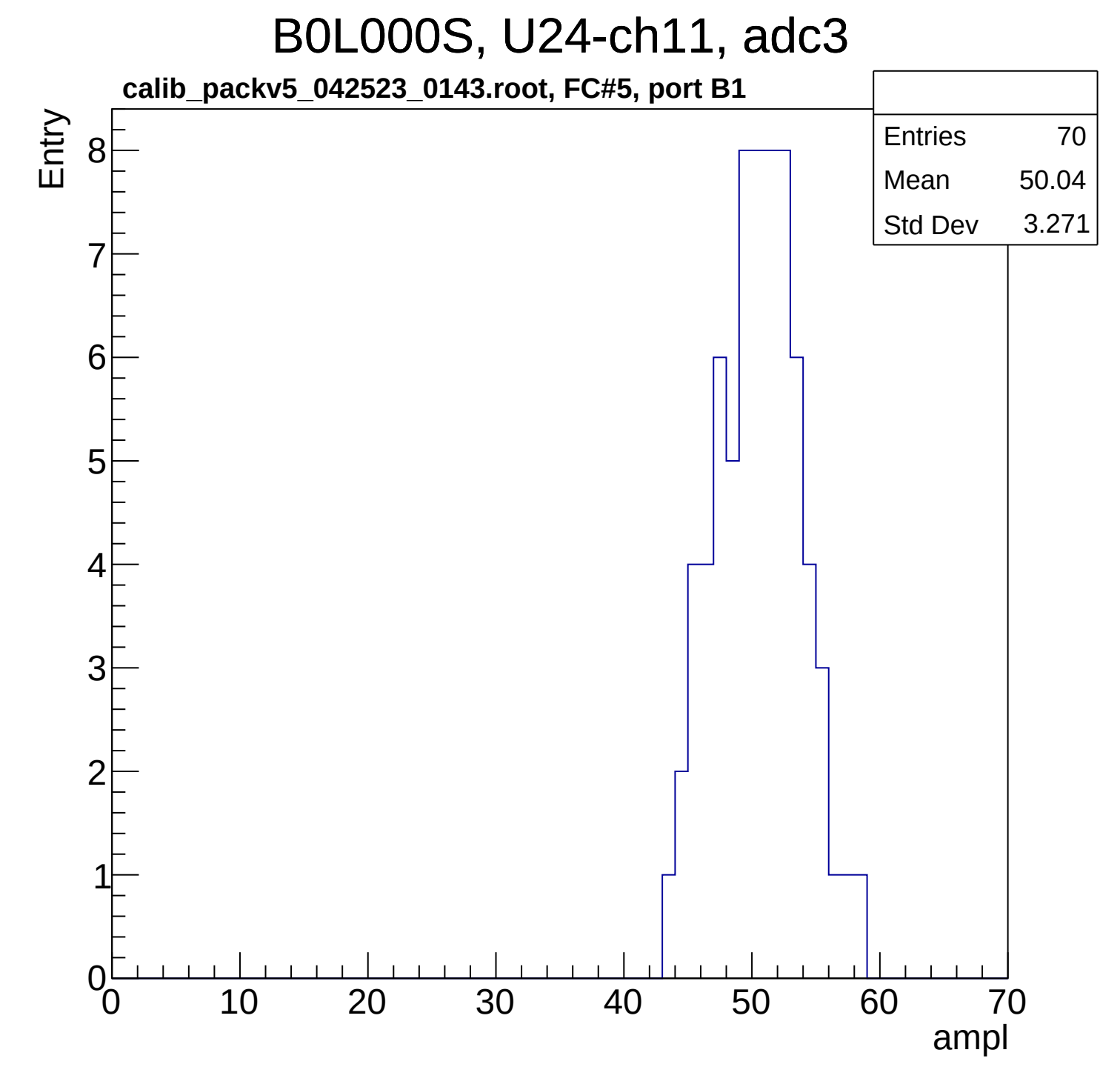
B0L000S, U24-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 50.04
Std Dev 3.271
Entry
ampl
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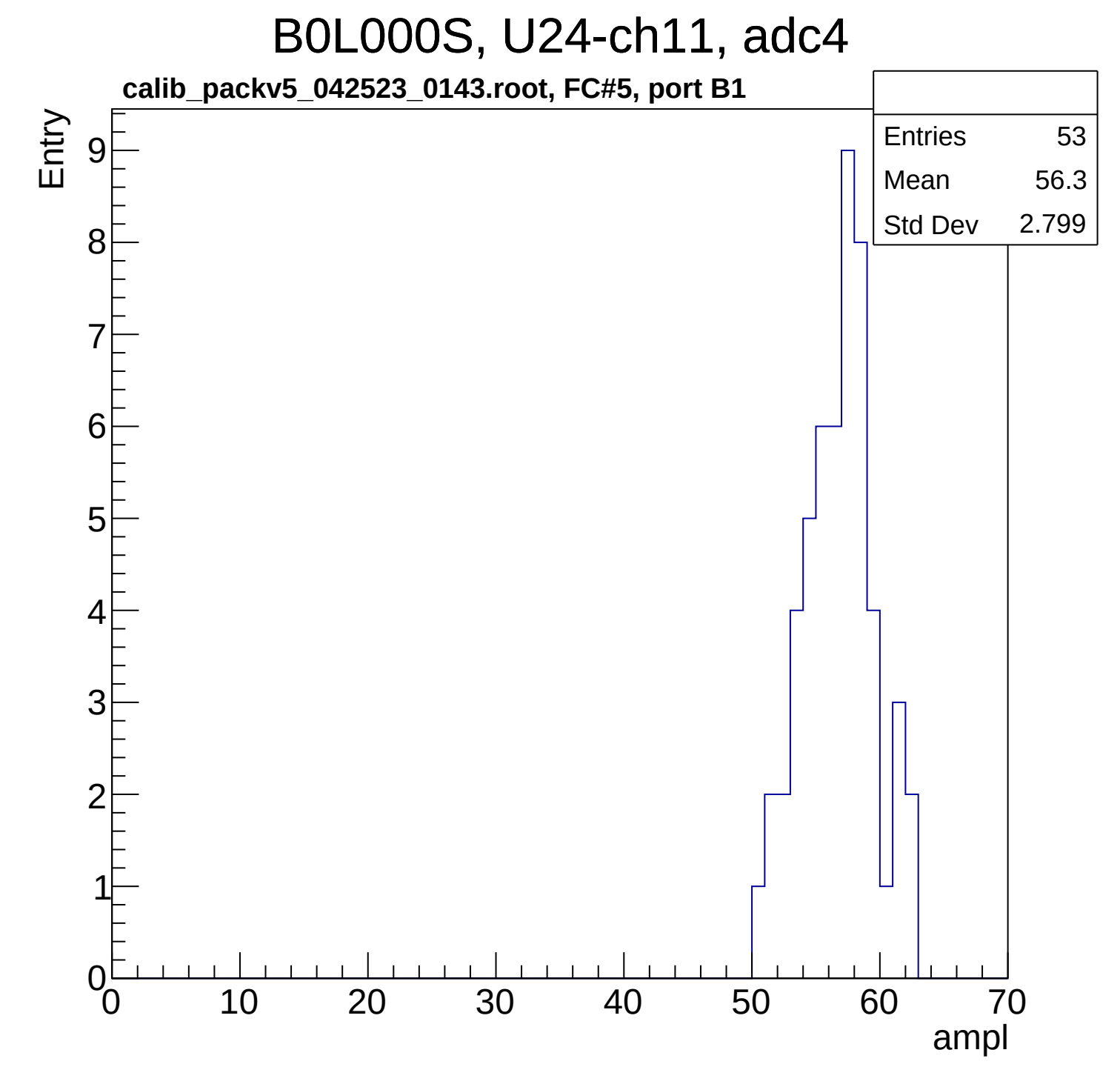
B0L000S, U24-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.3
Std Dev 2.799
Entry
ampl
0
1
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6
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0
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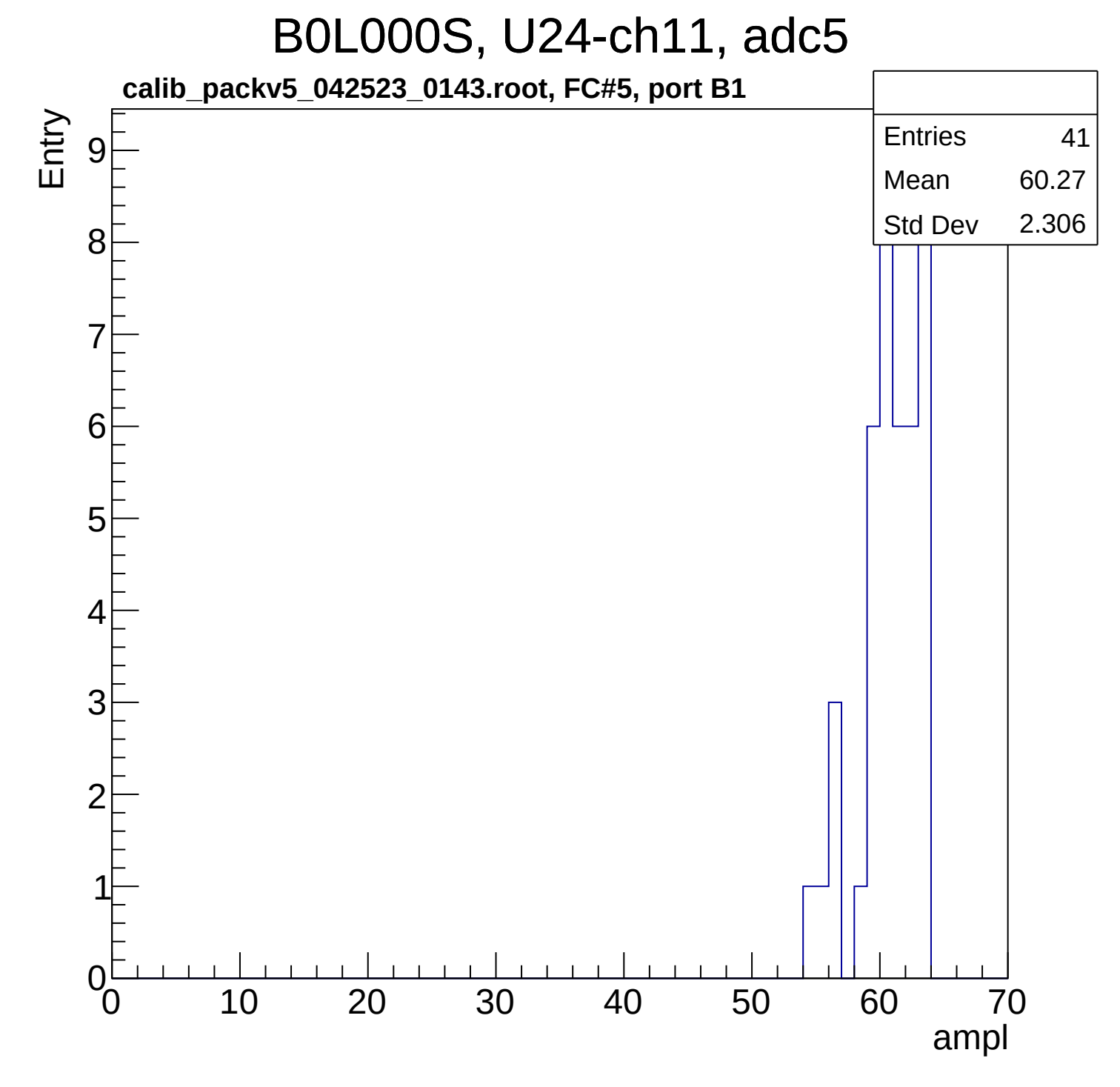
B0L000S, U24-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.27
Std Dev 2.306
Entry
ampl
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2
3
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6
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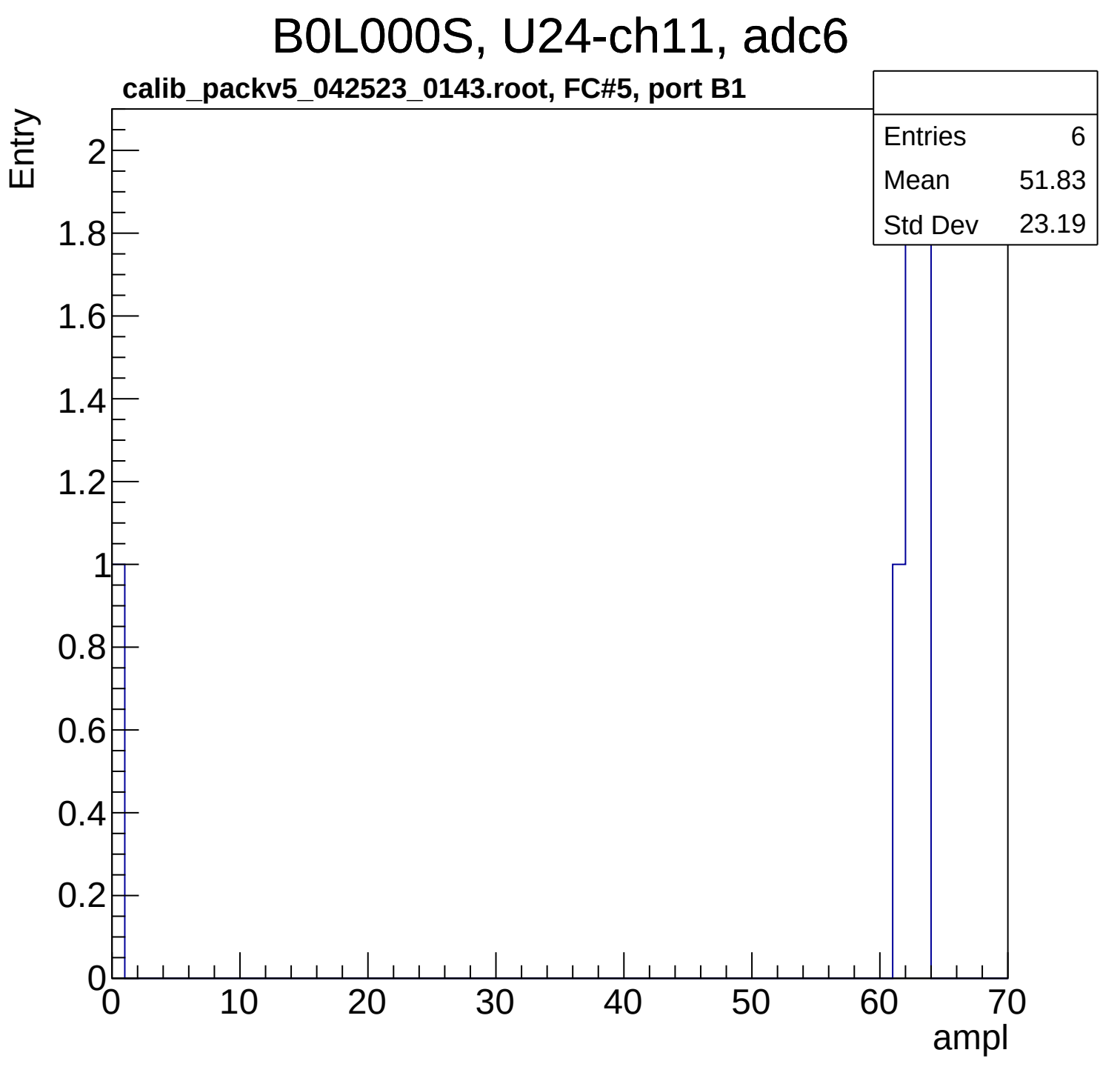
B0L000S, U24-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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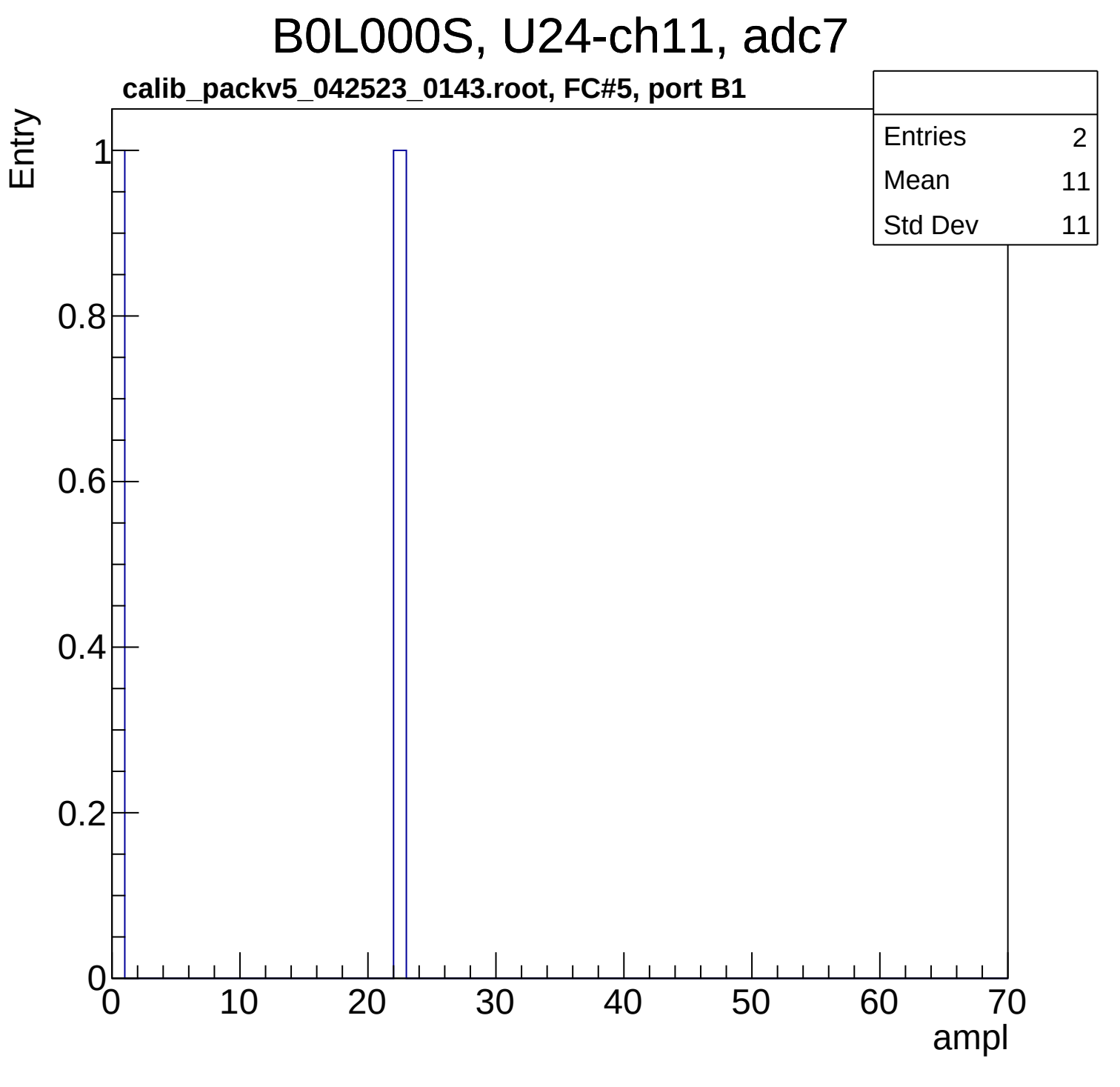
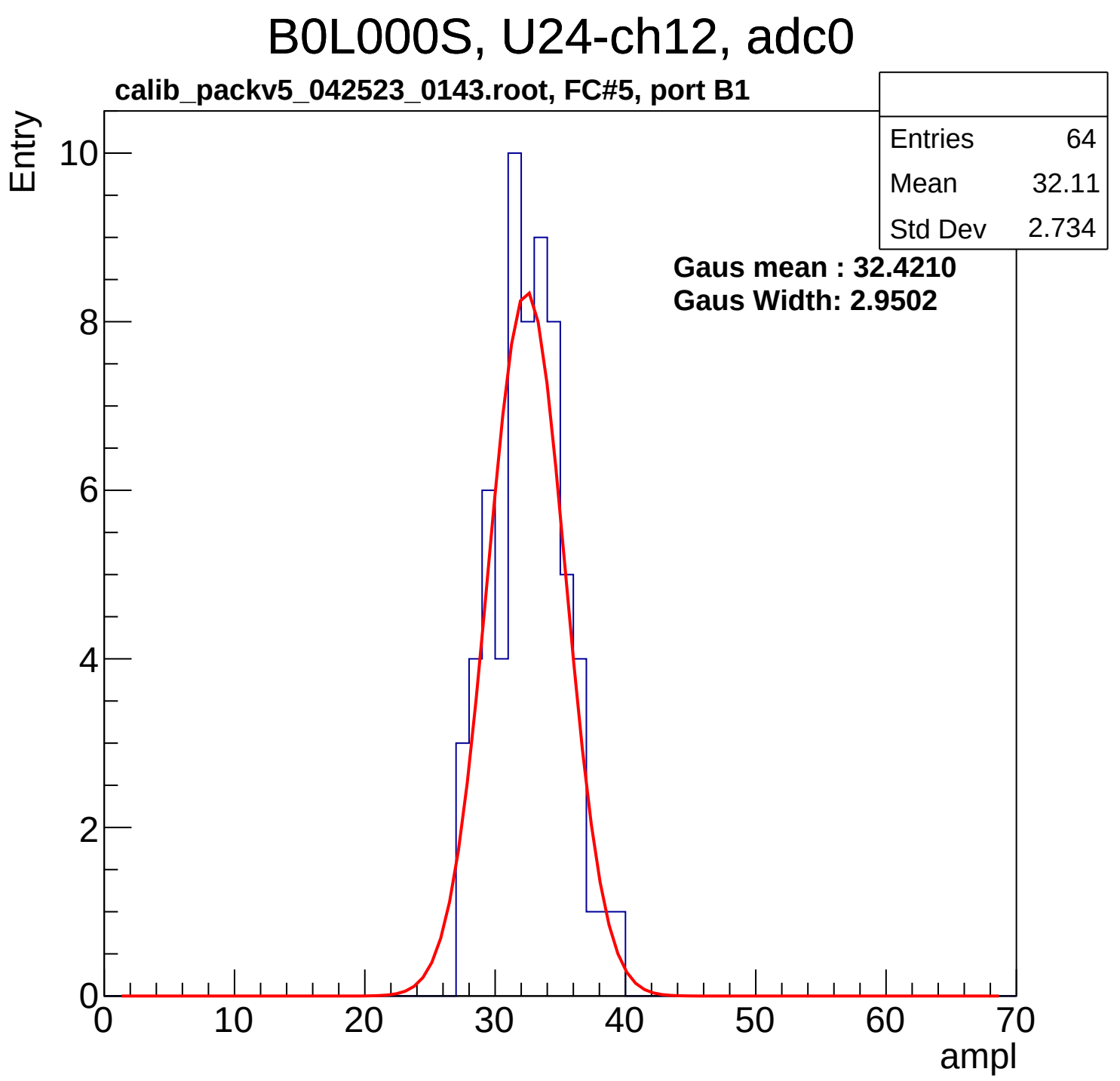

B0L000S, U24-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 32.11
Std Dev 2.734
Gaus mean : 32.4210
Gaus Width: 2.9502
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
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60
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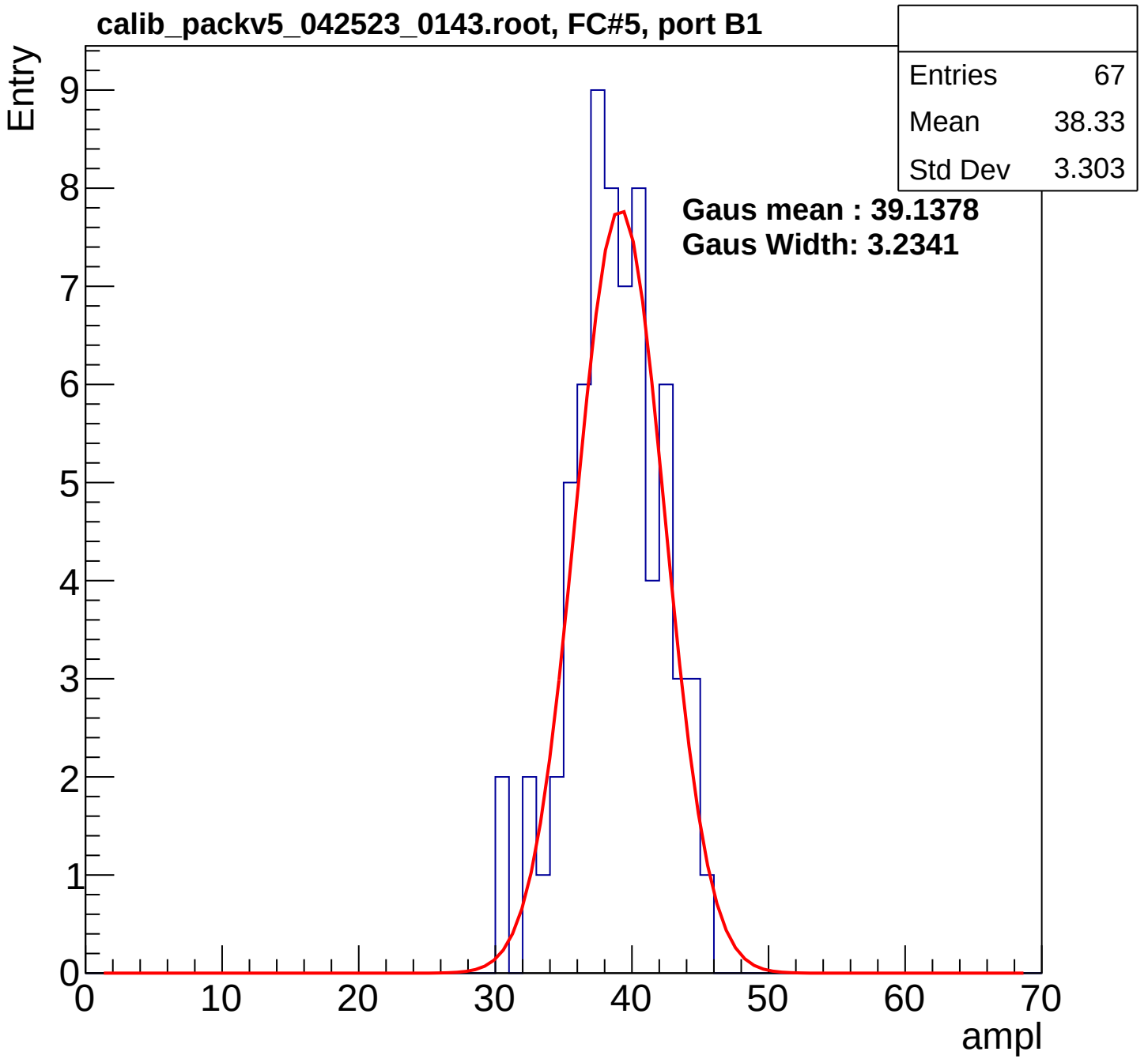
B0L000S, U24-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 38.33
Std Dev 3.303
Gaus mean : 39.1378
Gaus Width: 3.2341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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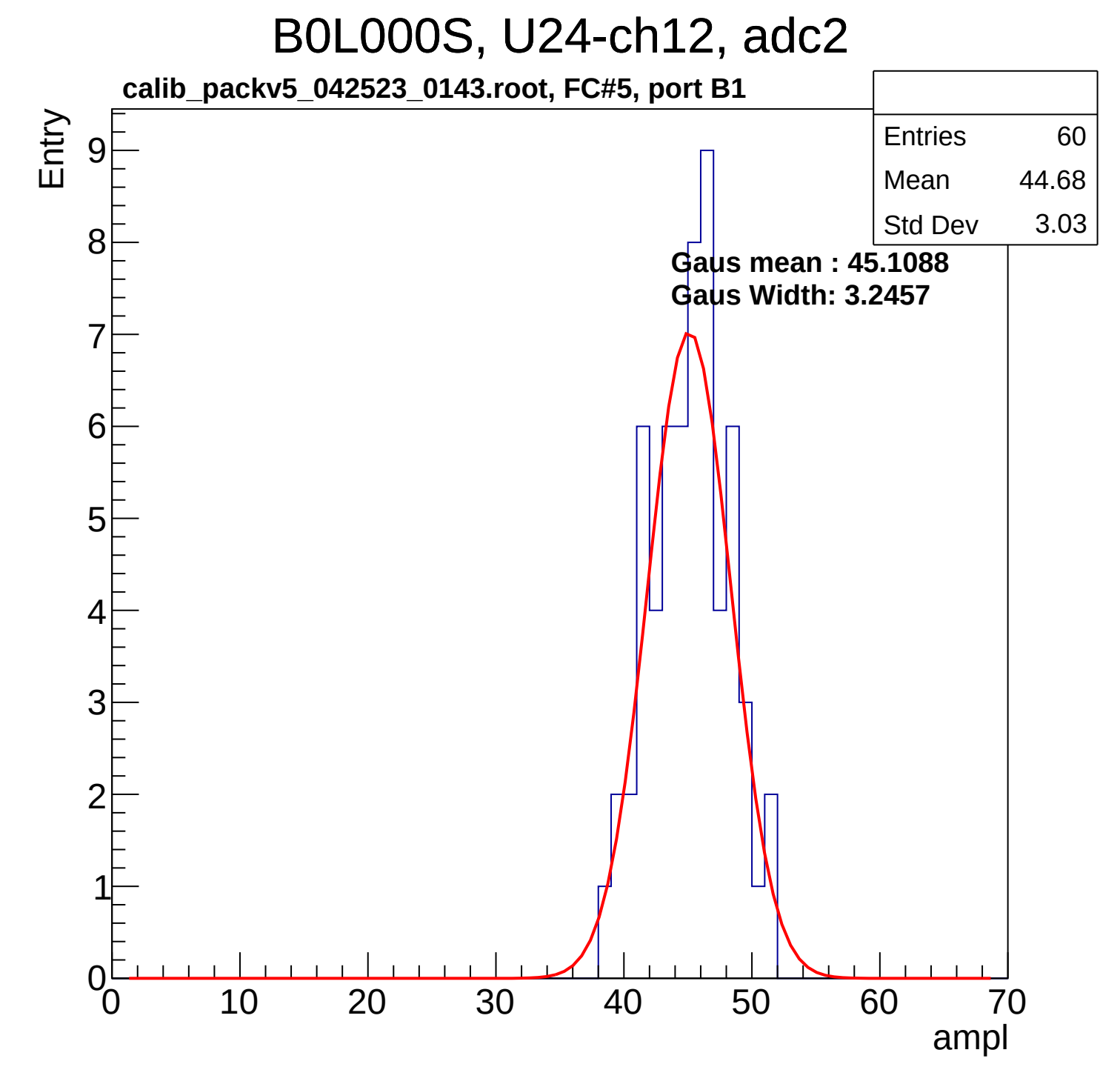
B0L000S, U24-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 44.68
Std Dev 3.03
Gaus mean : 45.1088
Gaus Width: 3.2457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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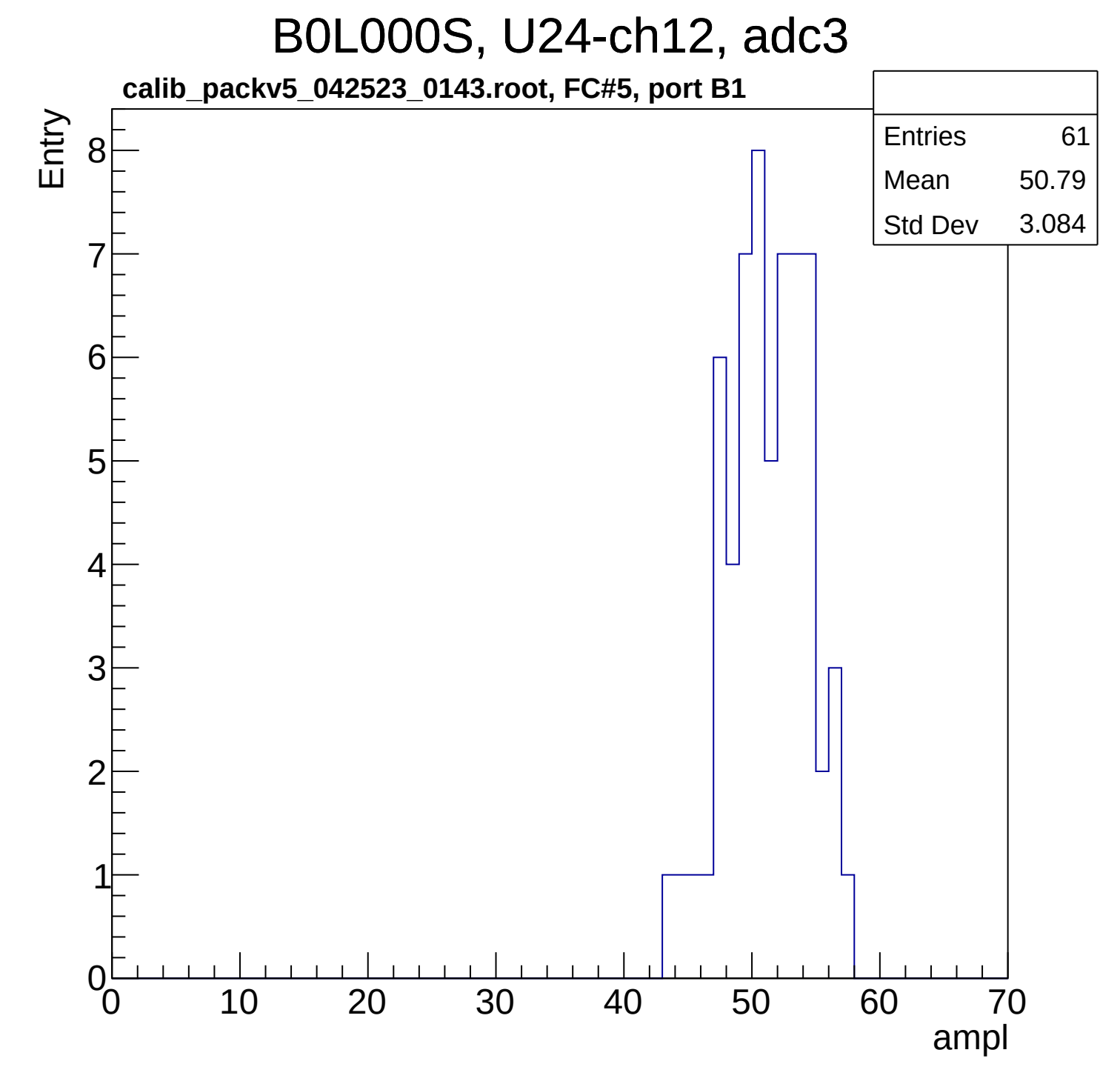
B0L000S, U24-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.79
Std Dev 3.084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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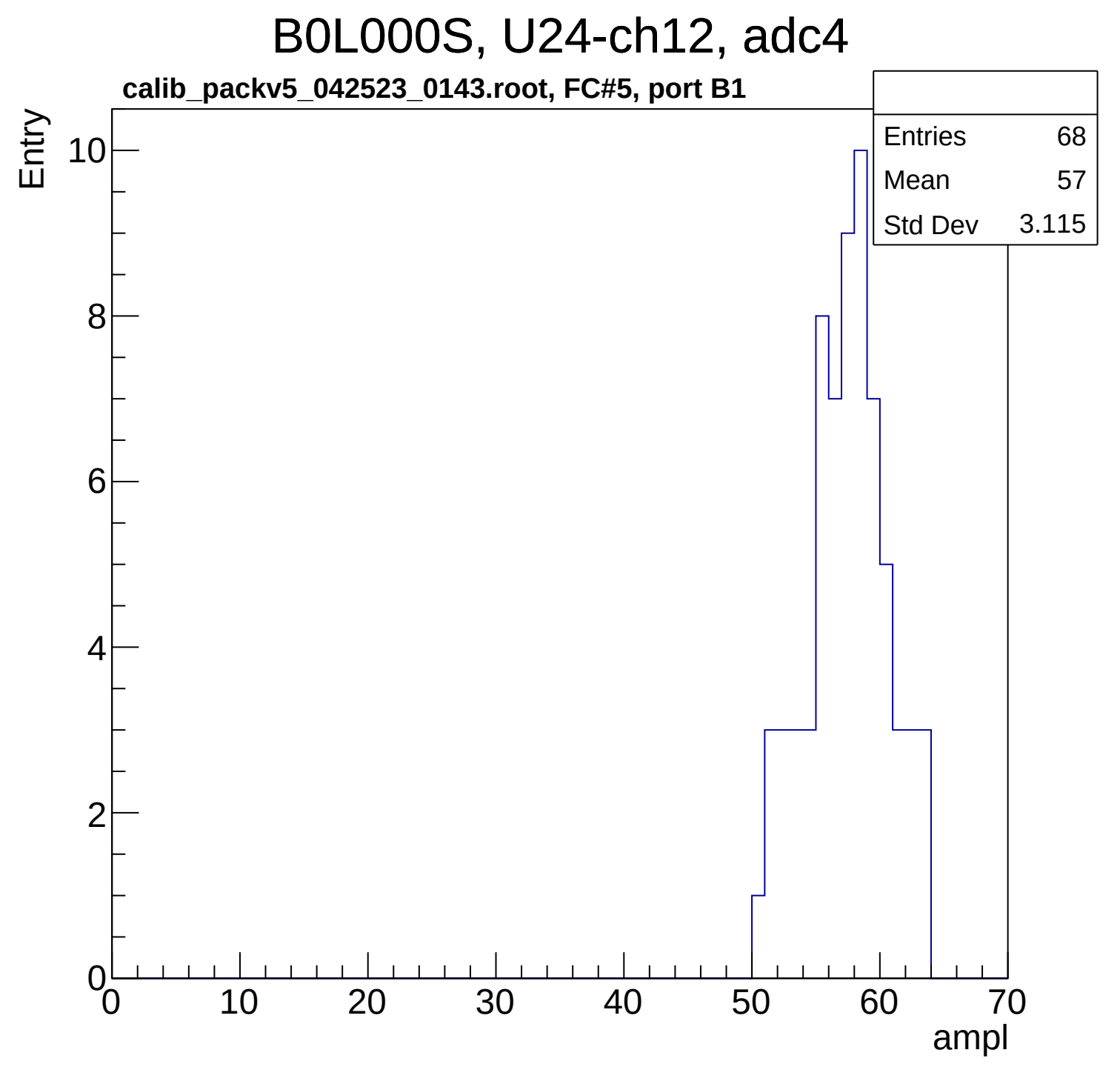
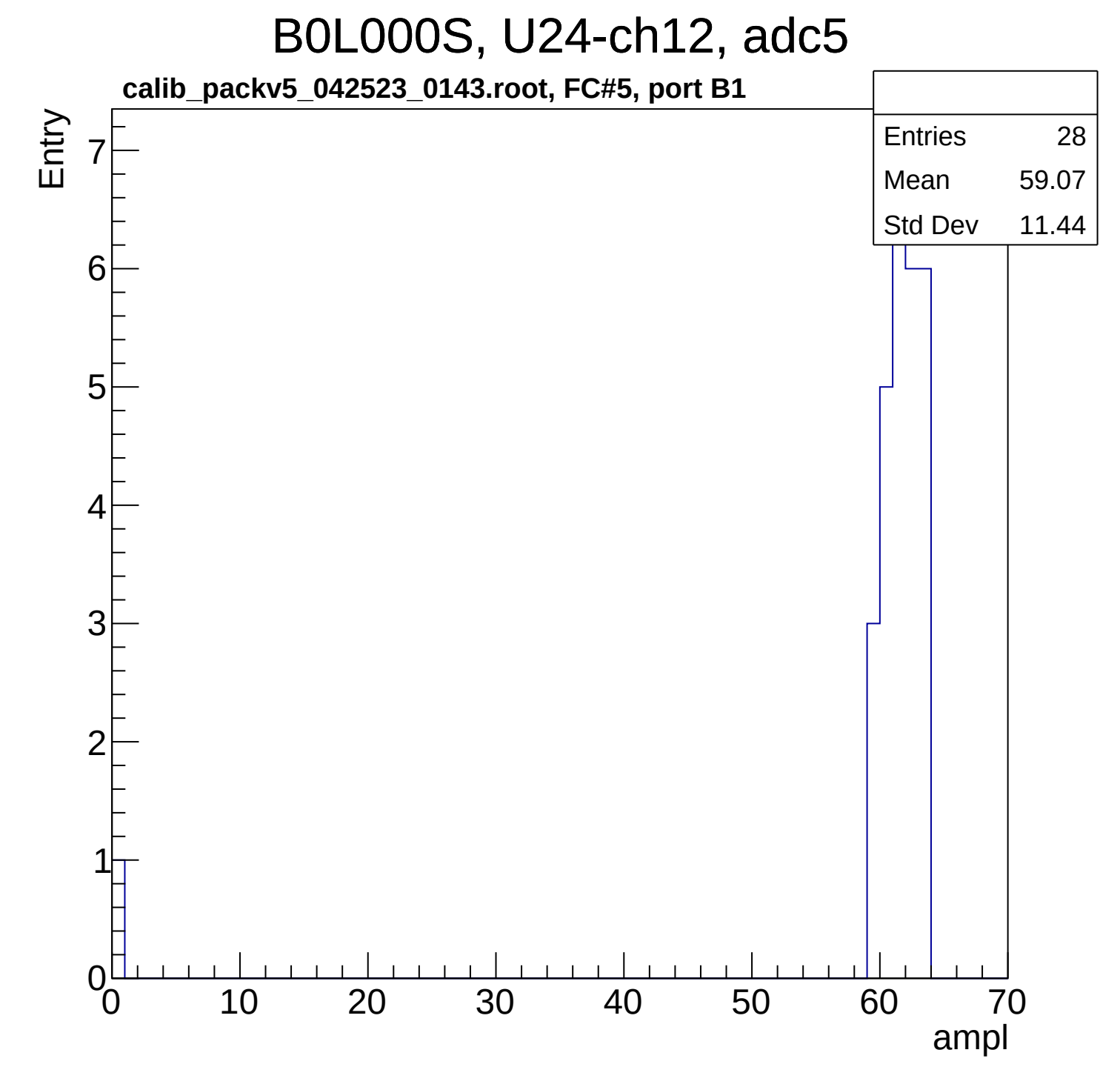

B0L000S, U24-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 57
Std Dev 3.115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 59.07
Std Dev 11.44
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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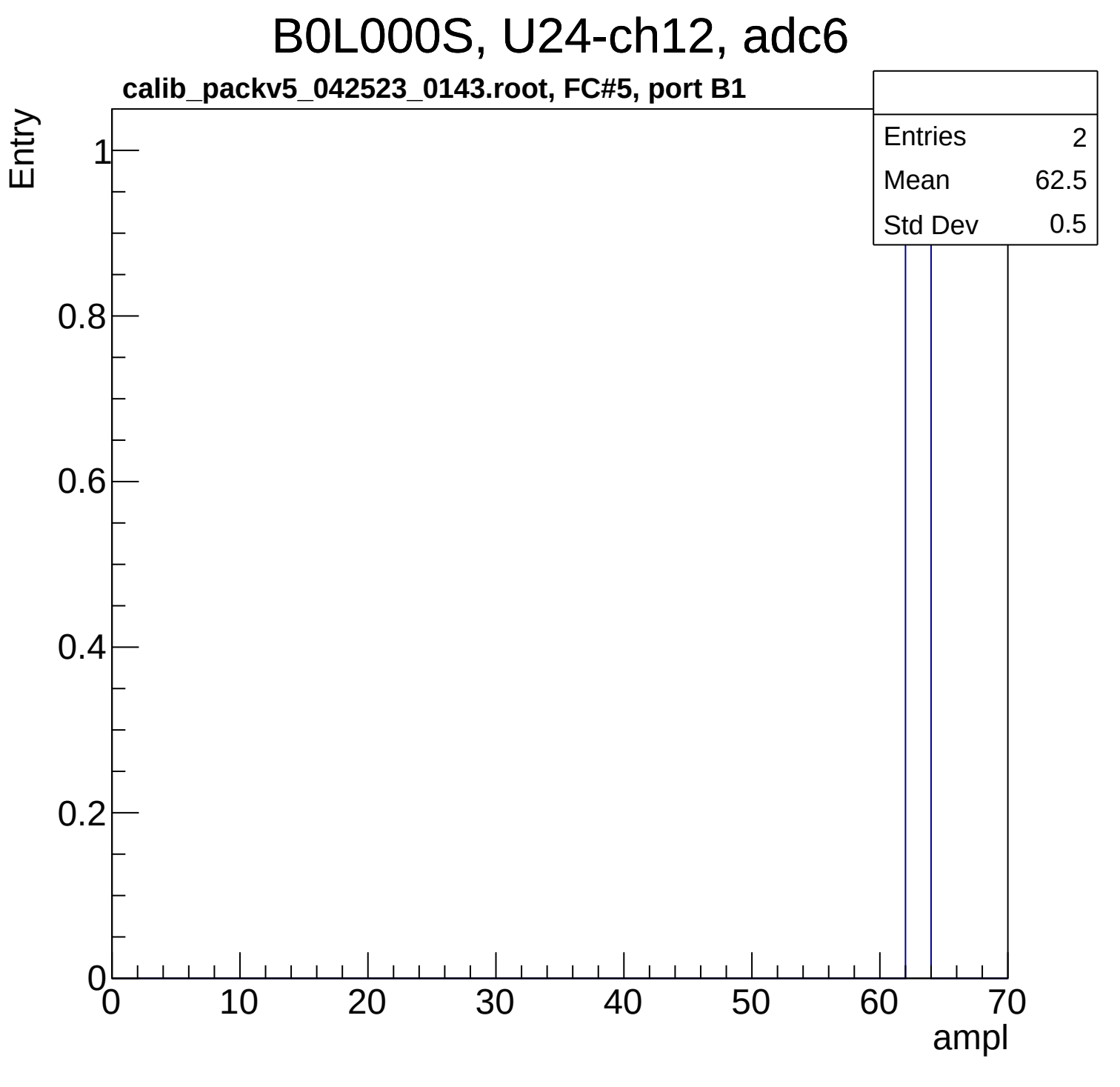
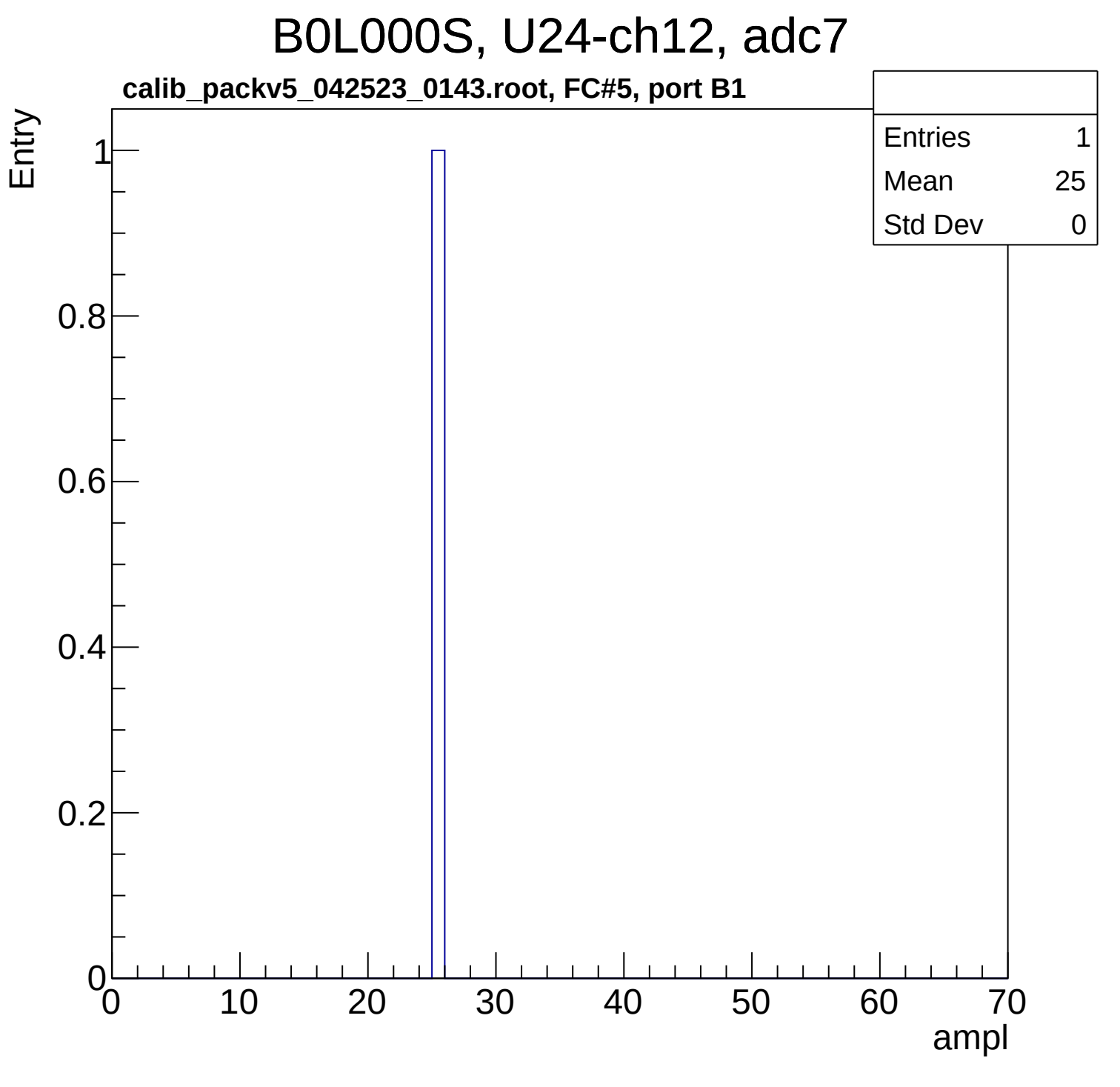

B0L000S, U24-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 25
Std Dev 0
Entry
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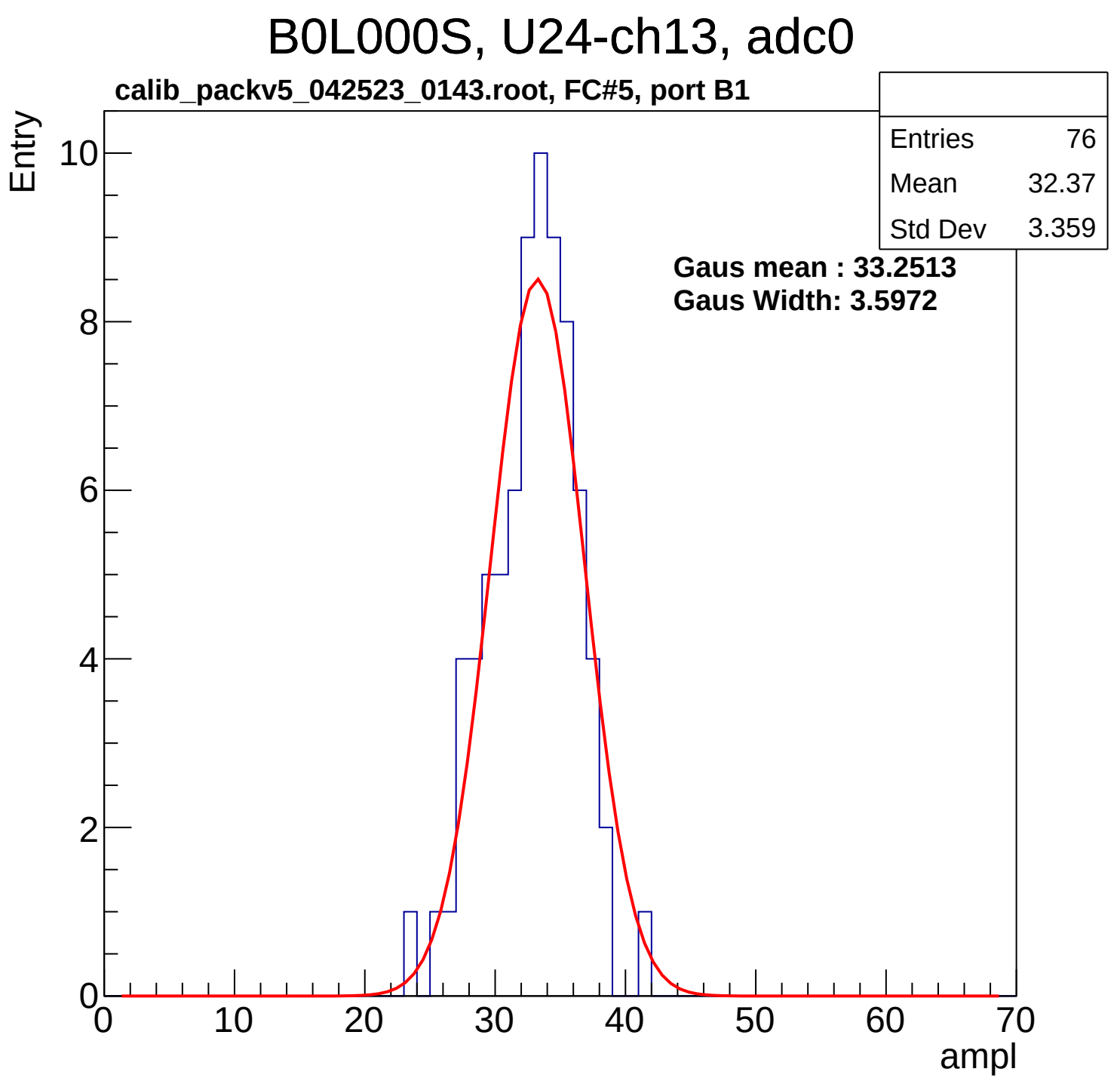
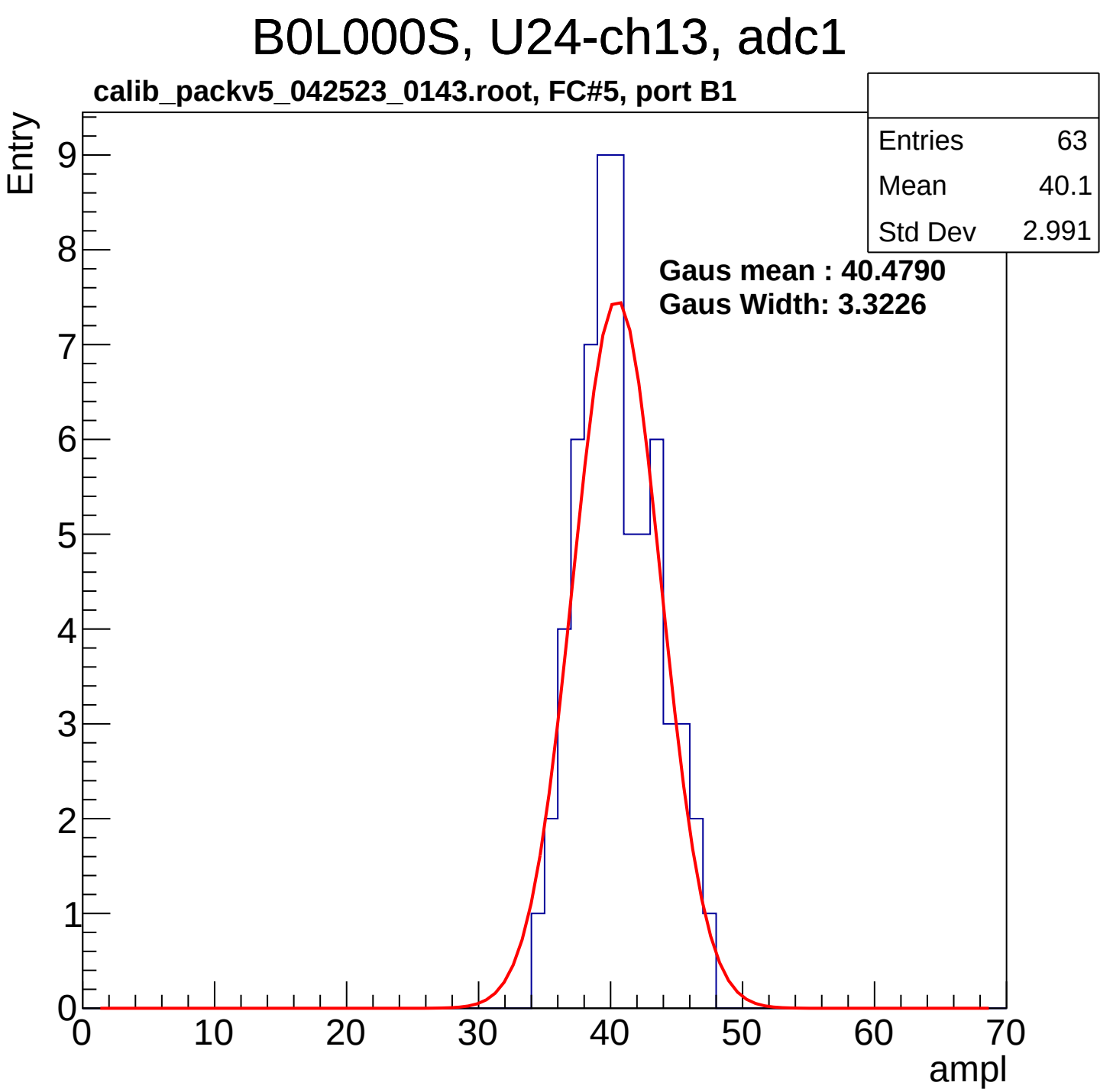
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 32.37
Std Dev 3.359
Gaus mean : 33.2513
Gaus Width: 3.5972
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 40.1
Std Dev 2.991
Gaus mean : 40.4790
Gaus Width: 3.3226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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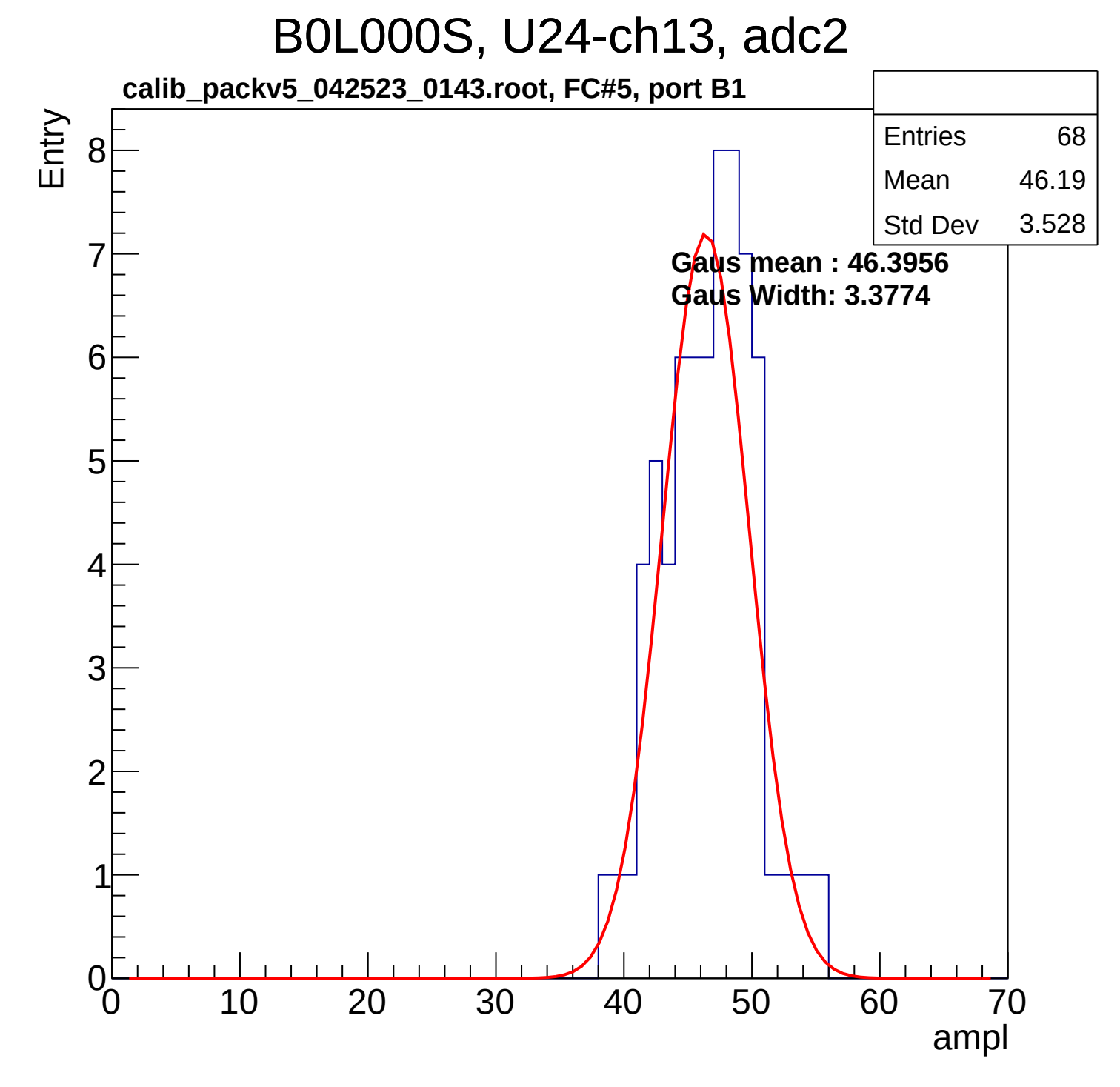
B0L000S, U24-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 46.19
Std Dev 3.528
Gaus mean : 46.3956
Gaus Width: 3.3774
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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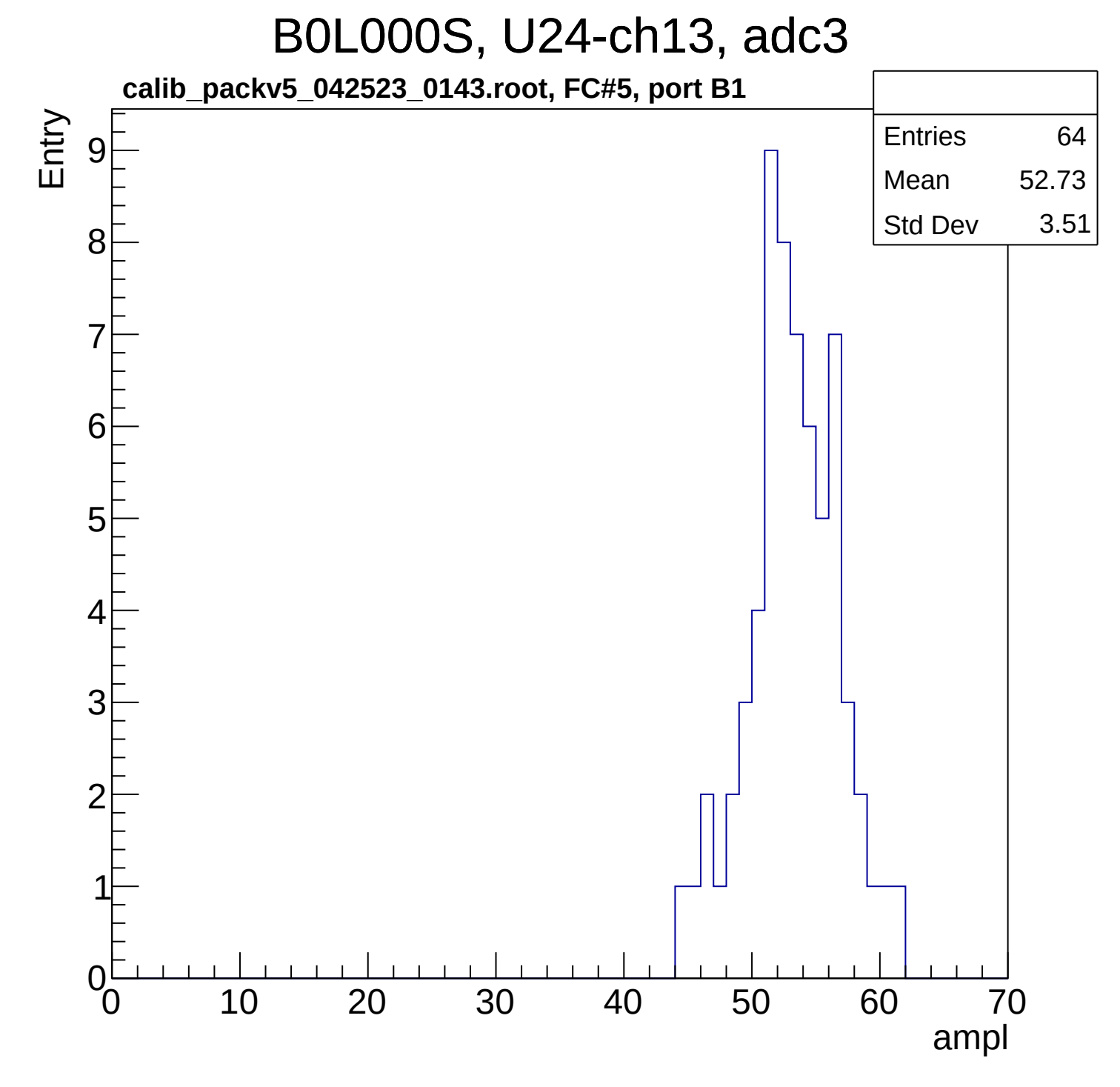
B0L000S, U24-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 52.73
Std Dev 3.51
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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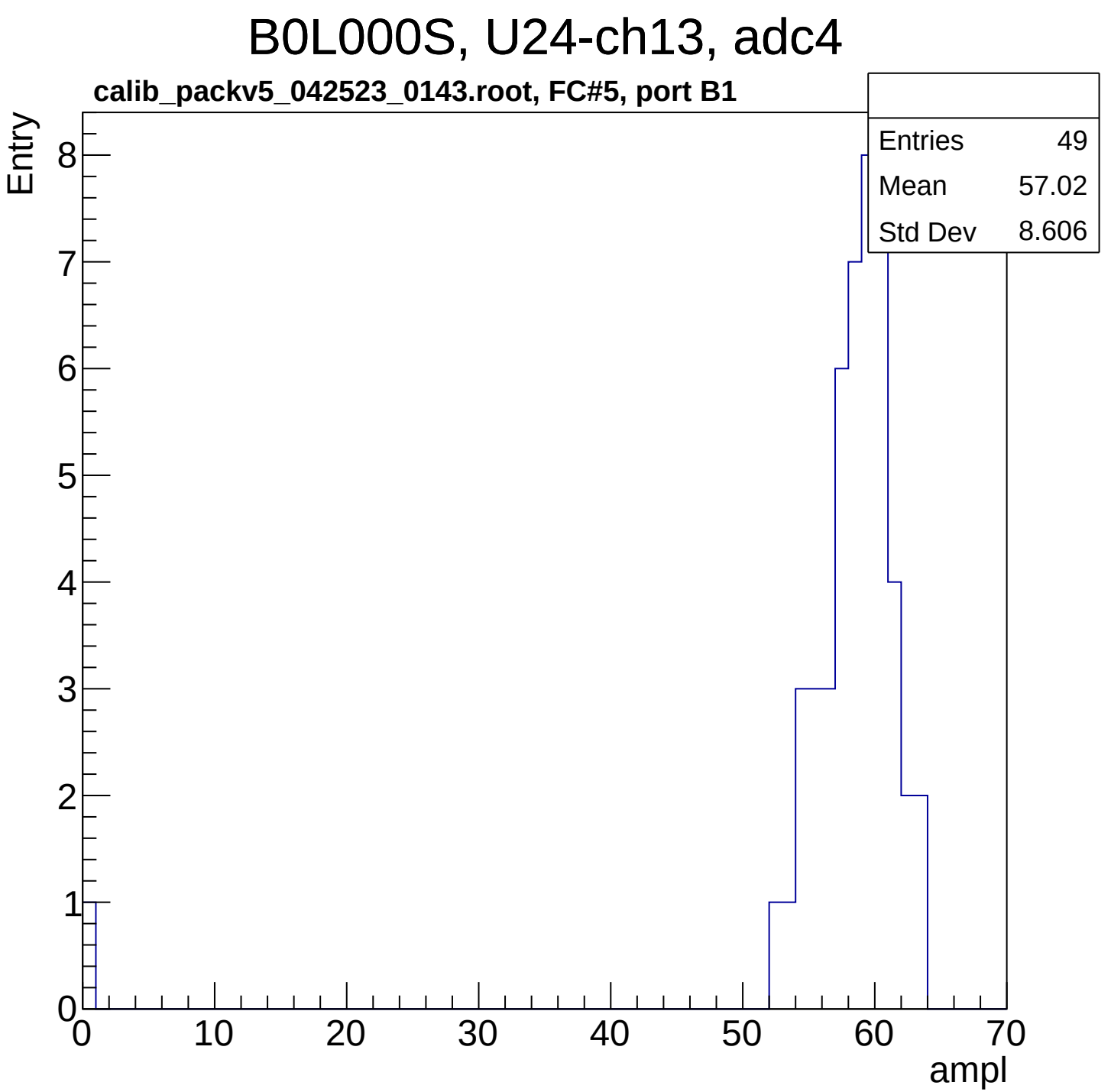
B0L000S, U24-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.02
Std Dev 8.606
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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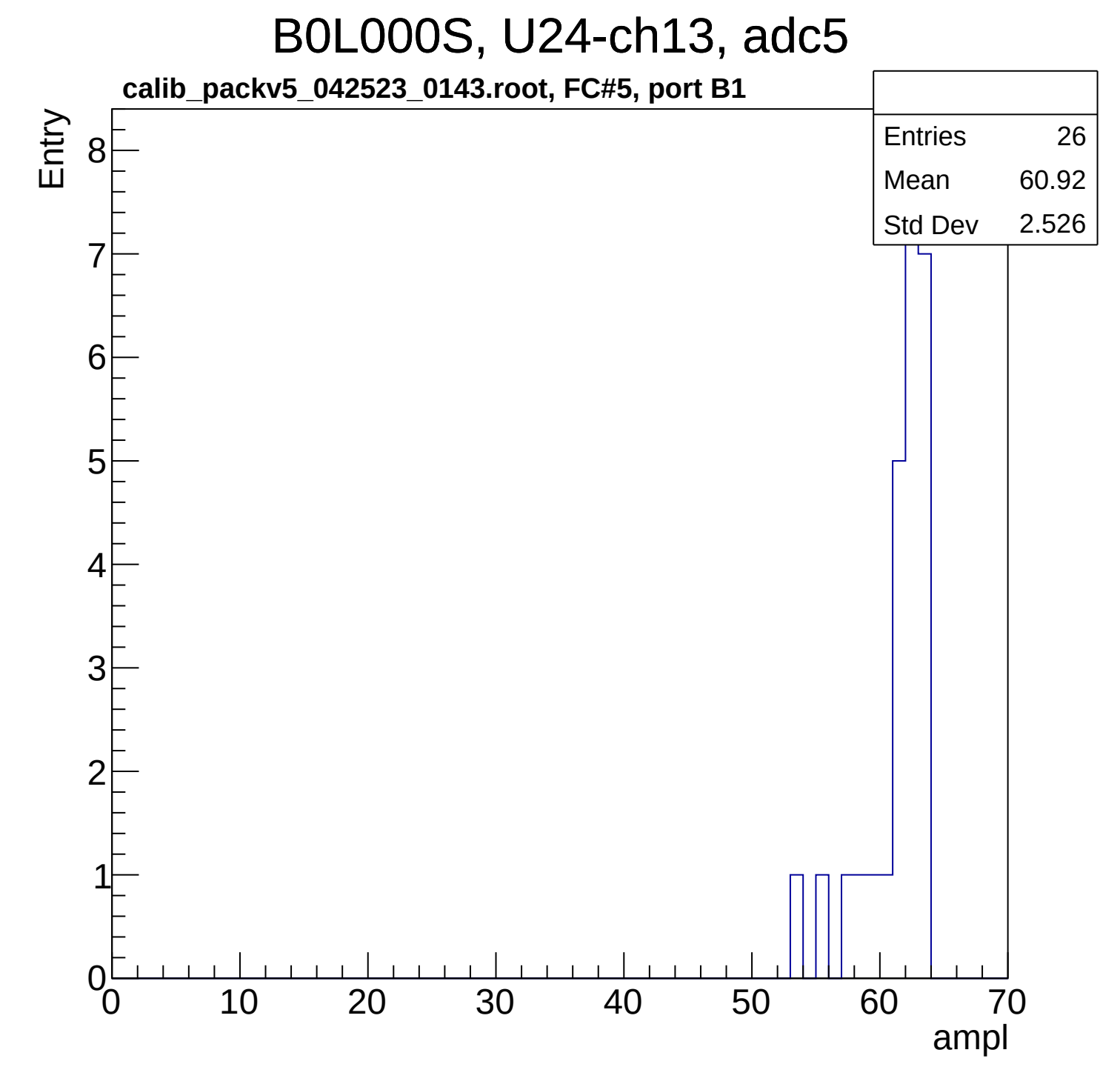
40
50
60
70


B0L000S, U24-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 60.92
Std Dev 2.526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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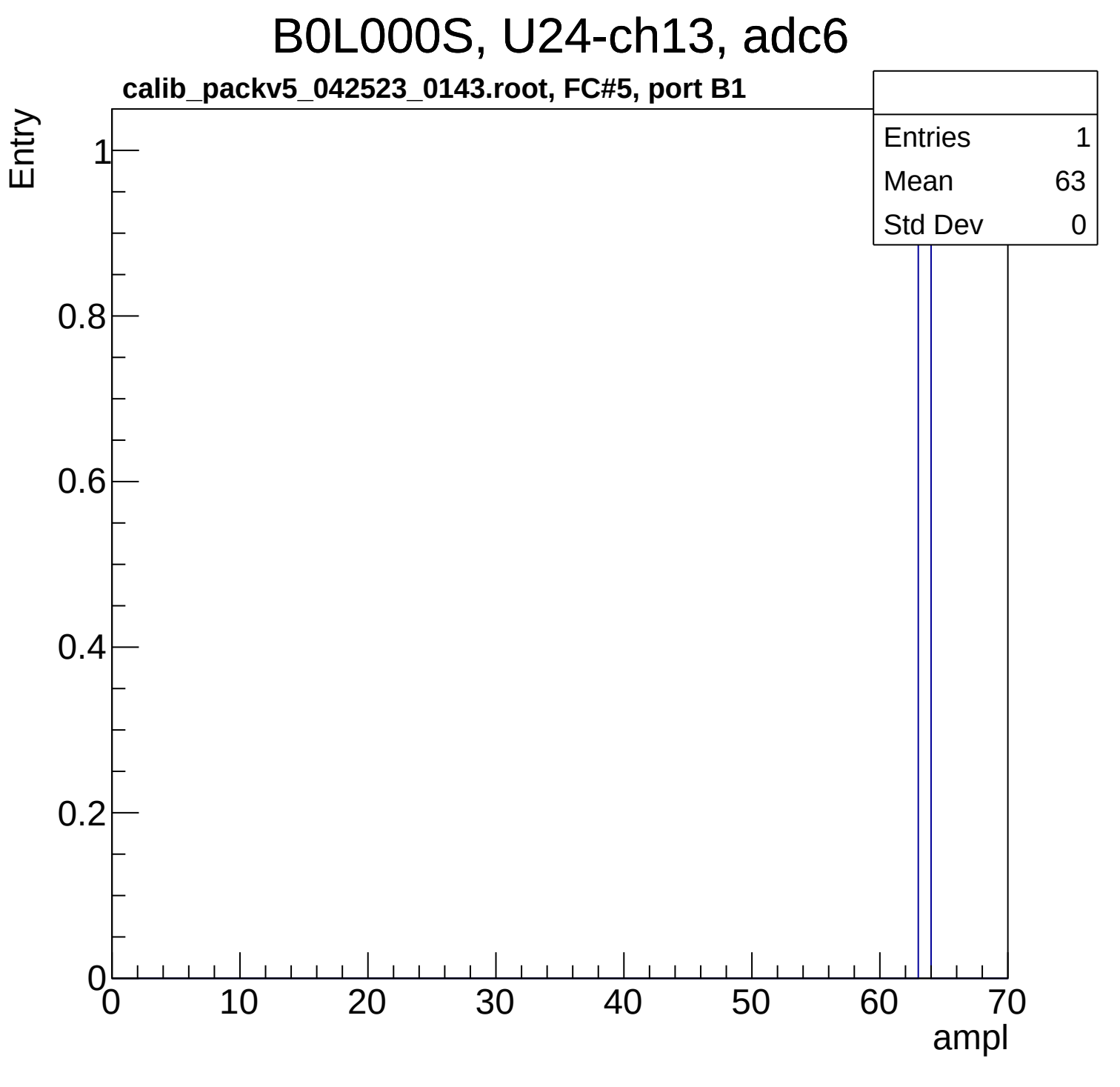
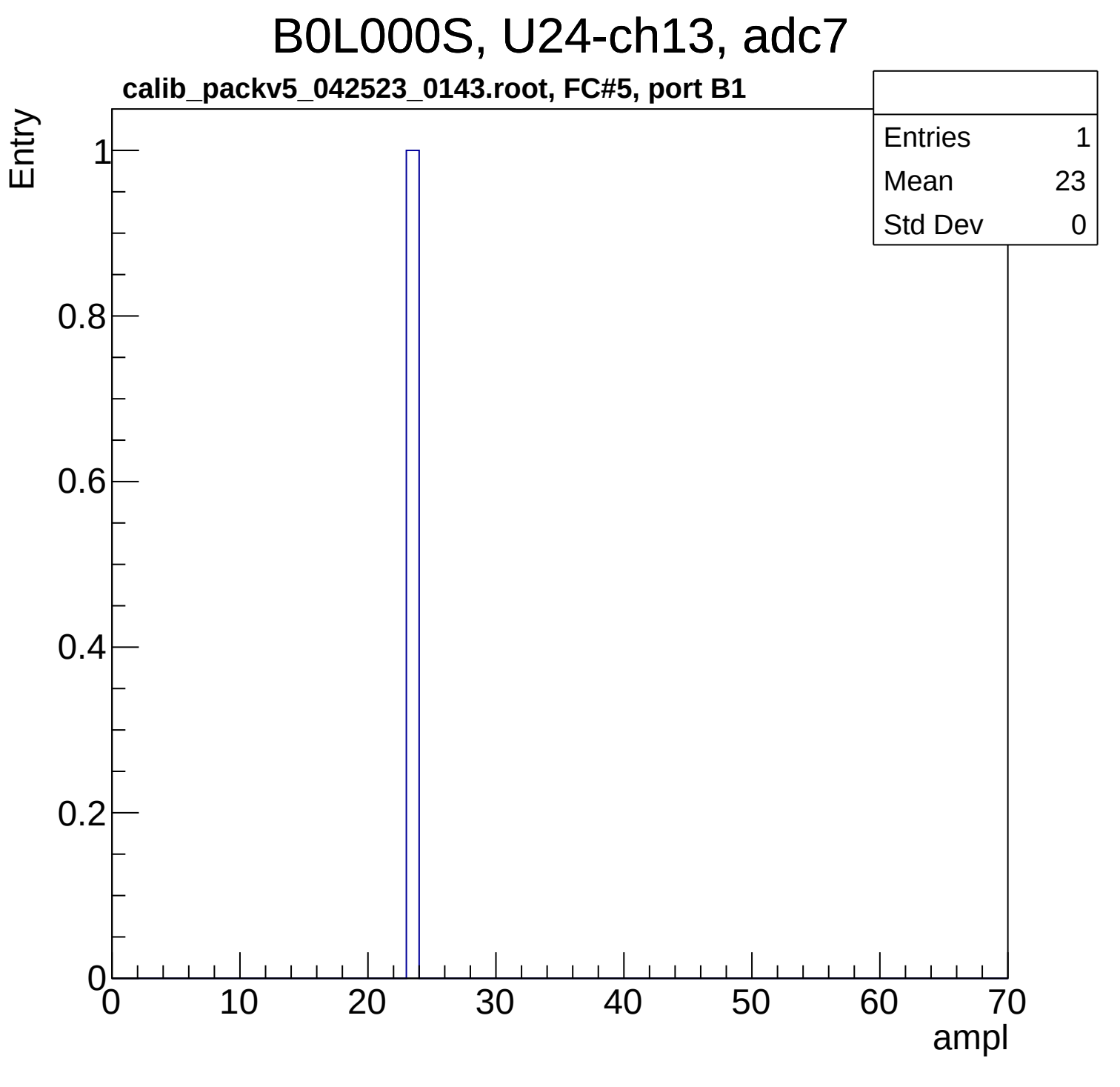
50
60
70


B0L000S, U24-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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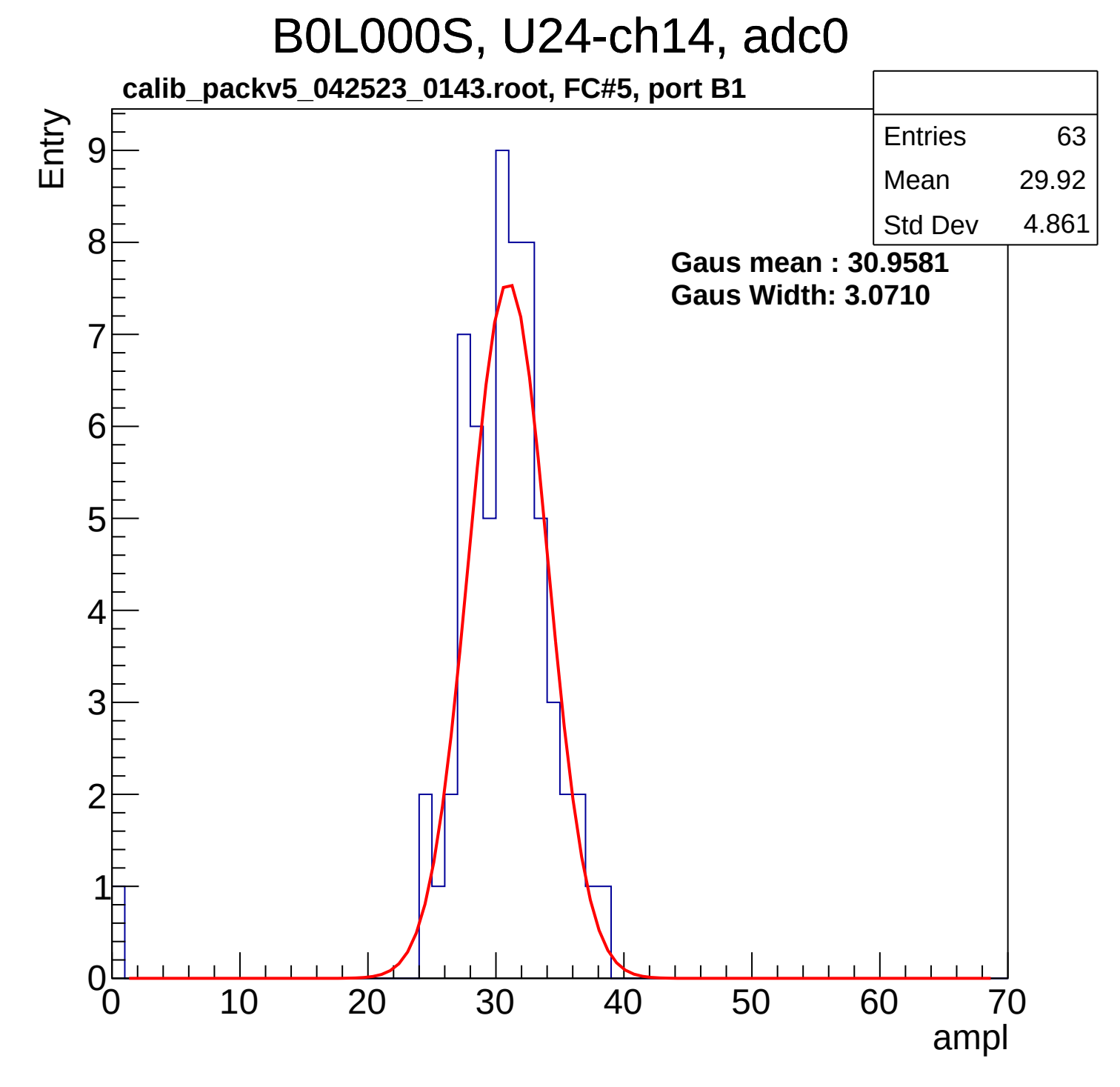
50
60
70
ampl


B0L000S, U24-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.92
Std Dev 4.861
Gaus mean : 30.9581
Gaus Width: 3.0710
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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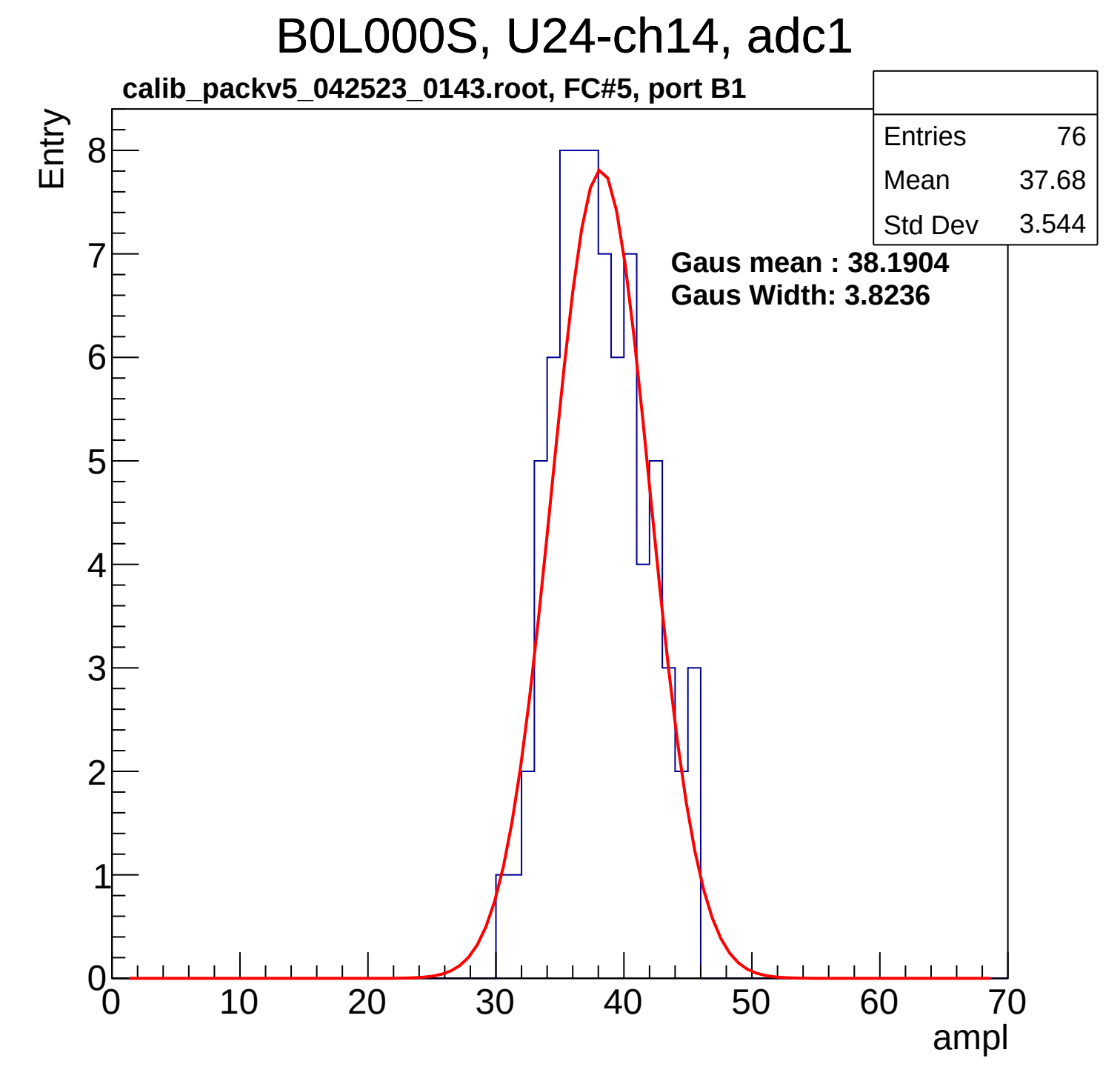
B0L000S, U24-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.68
Std Dev 3.544
Gaus mean : 38.1904
Gaus Width: 3.8236
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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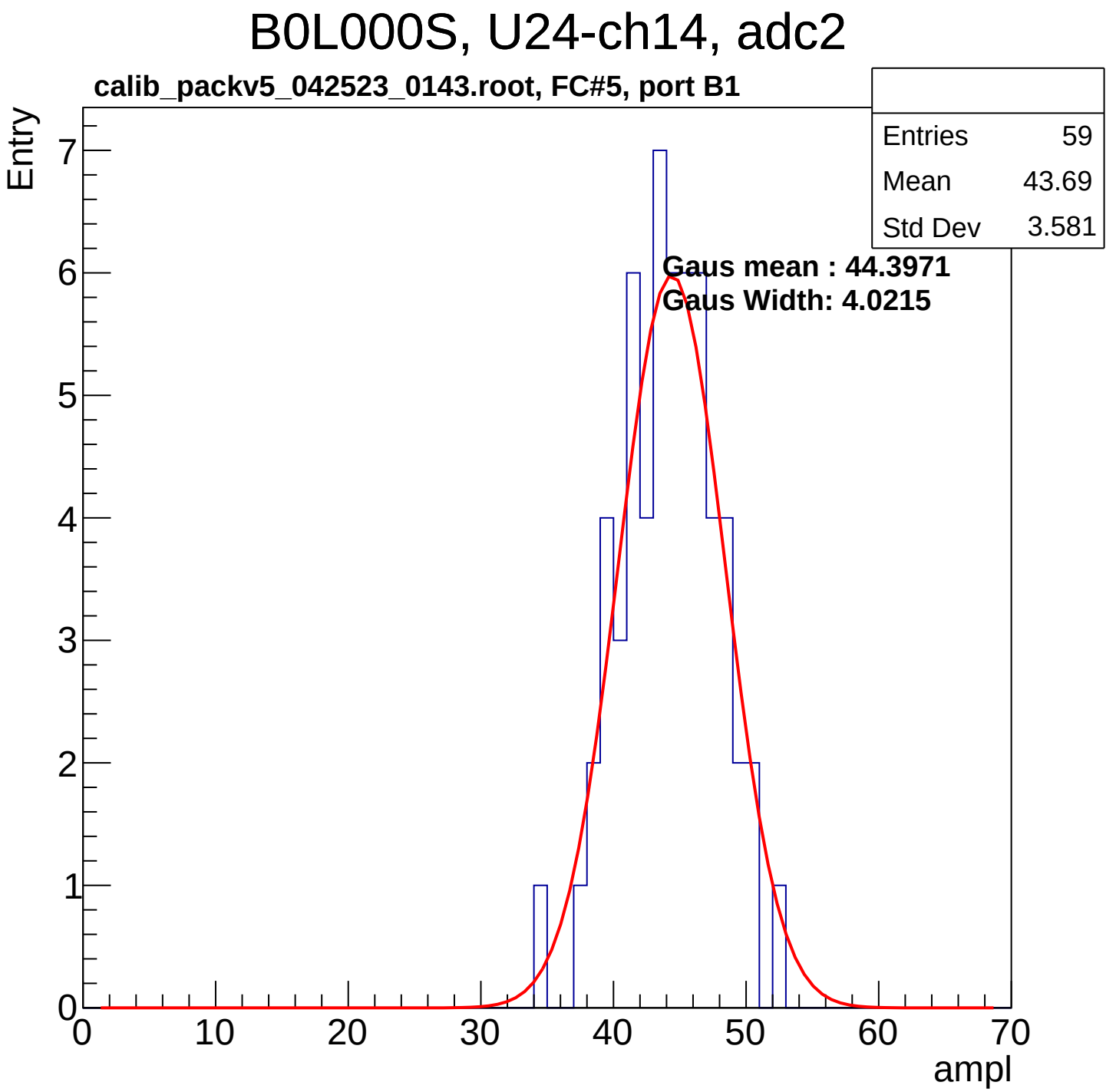
B0L000S, U24-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.69
Std Dev 3.581
Gaus mean : 44.3971
Gaus Width: 4.0215
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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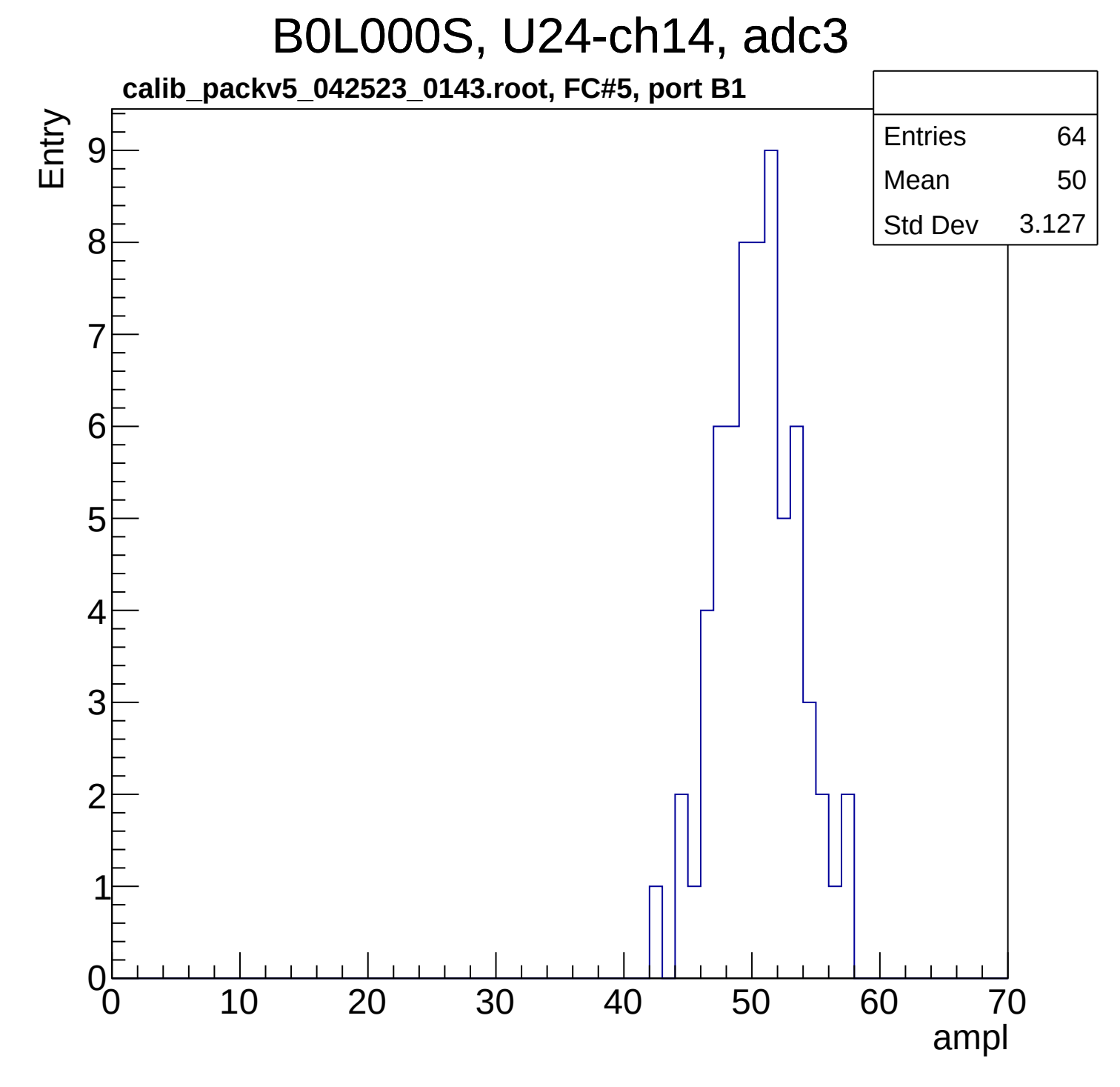
B0L000S, U24-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50
Std Dev 3.127
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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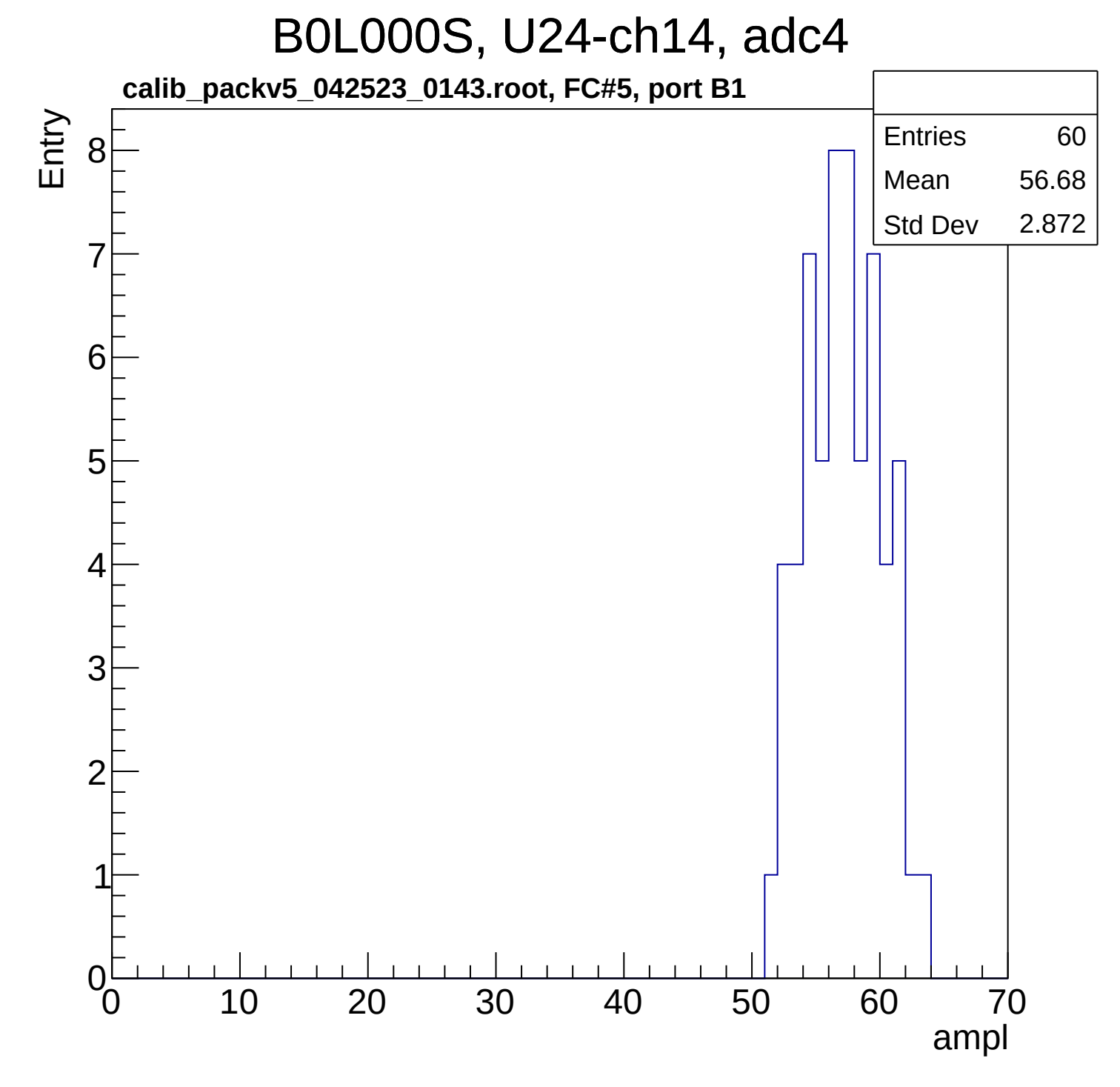
B0L000S, U24-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.68
Std Dev 2.872
Entry
ampl
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3
4
5
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7
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0
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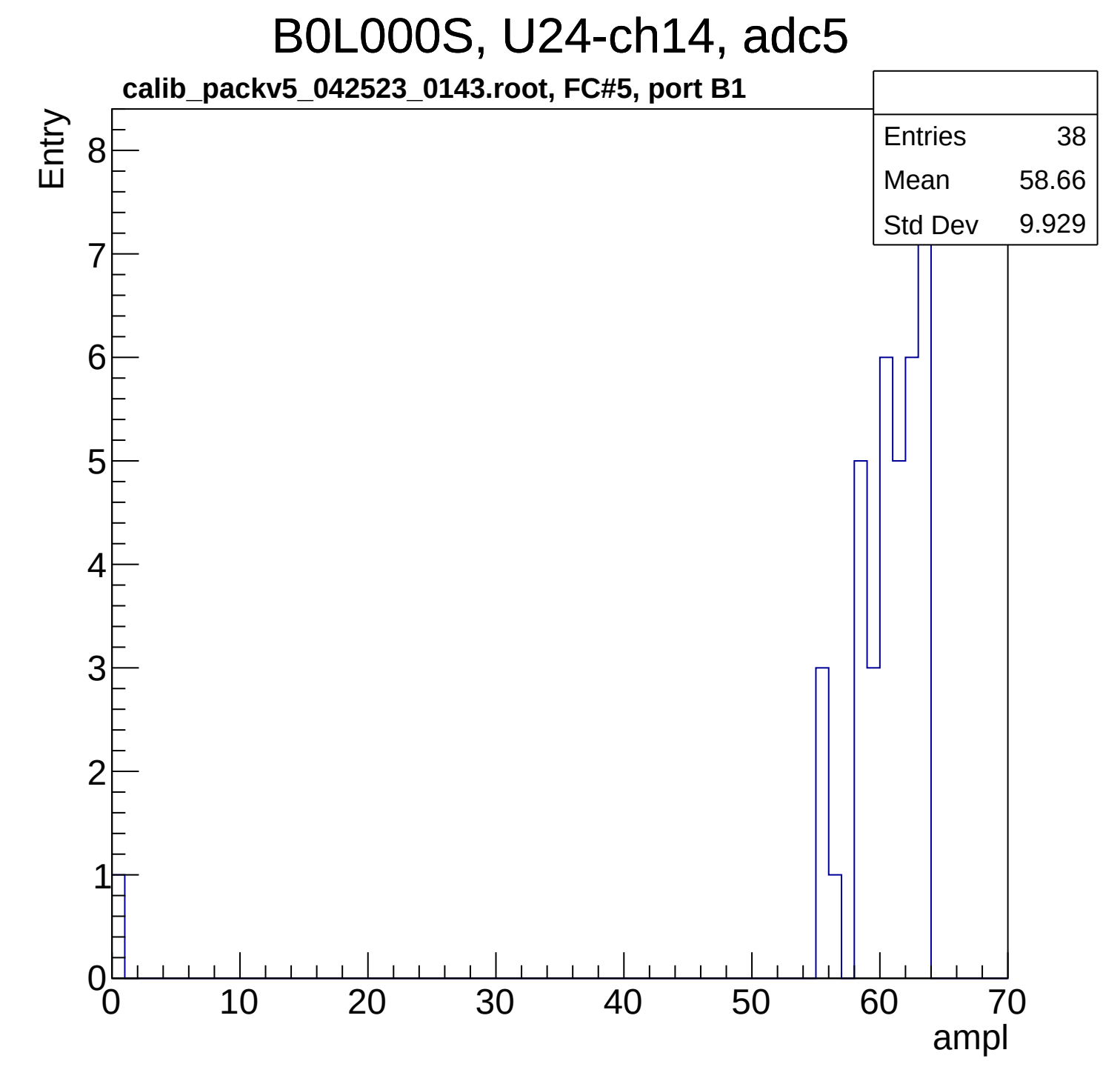
B0L000S, U24-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.66
Std Dev 9.929
Entry
ampl
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2
3
4
5
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7
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0
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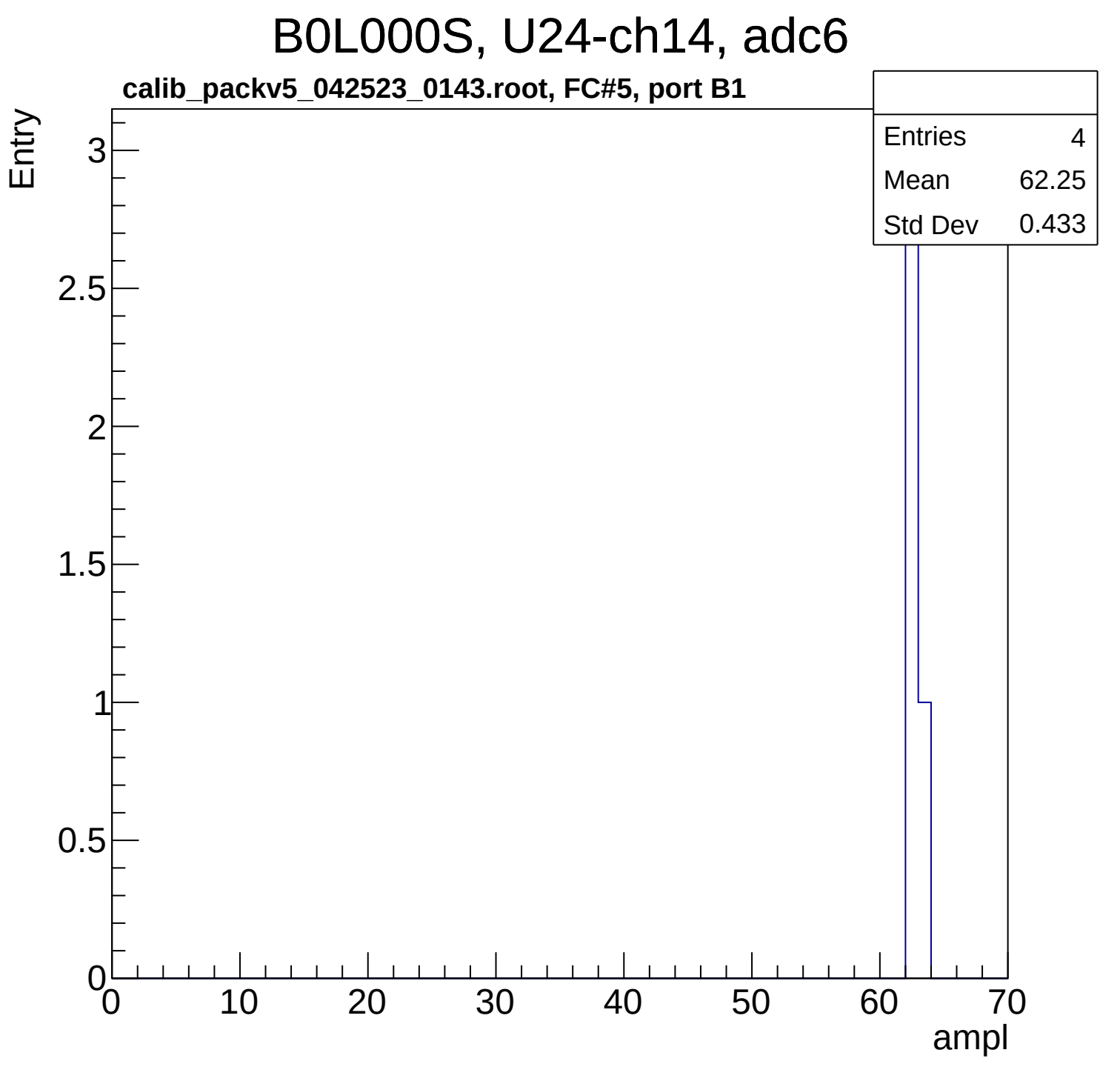
B0L000S, U24-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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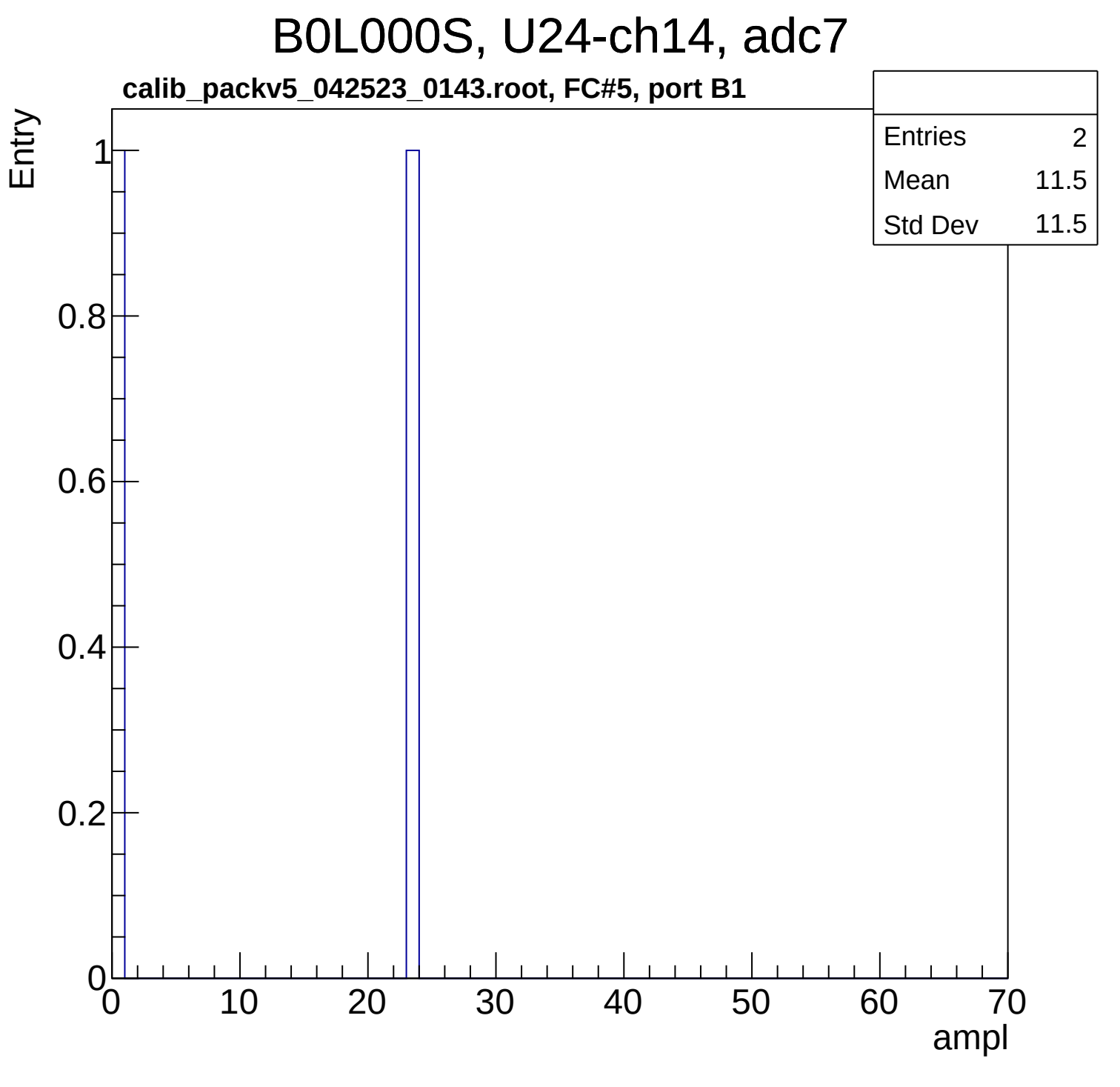
B0L000S, U24-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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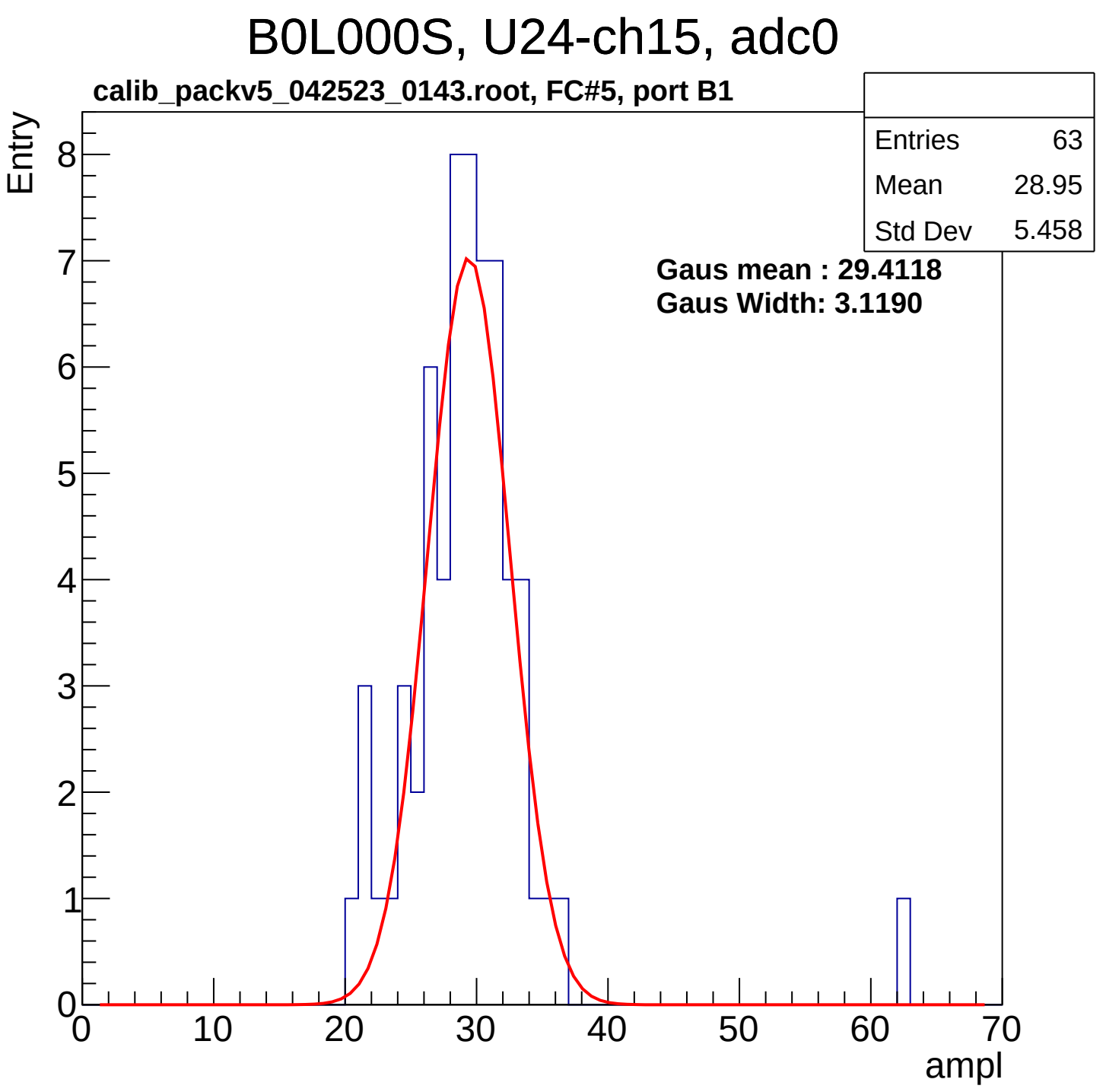
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 28.95
Std Dev 5.458
Gaus mean : 29.4118
Gaus Width: 3.1190
Entry
ampl
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3
4
5
6
7
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0
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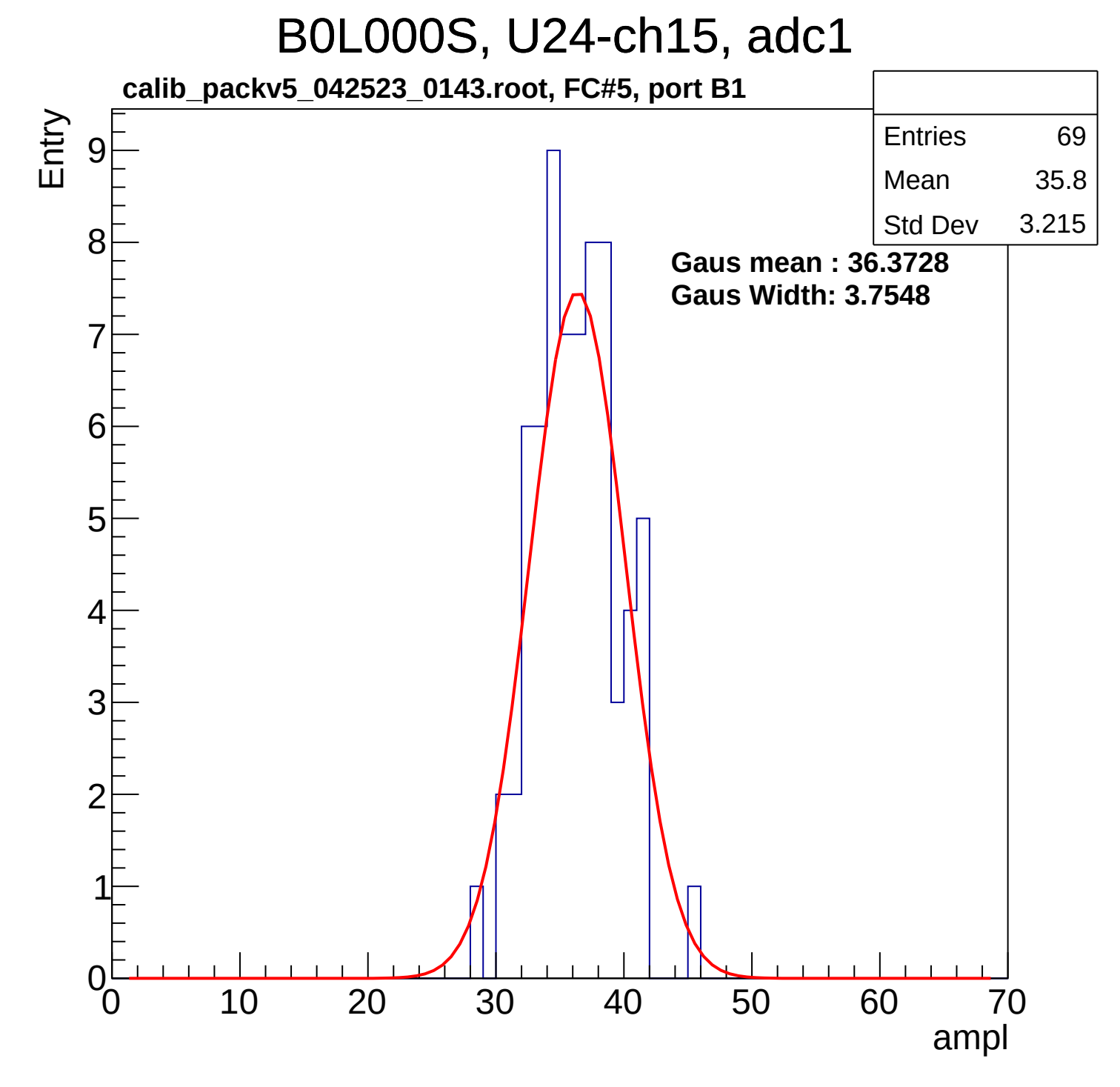
B0L000S, U24-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.8
Std Dev 3.215
Gaus mean : 36.3728
Gaus Width: 3.7548
Entry
ampl
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2
3
4
5
6
7
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9
0
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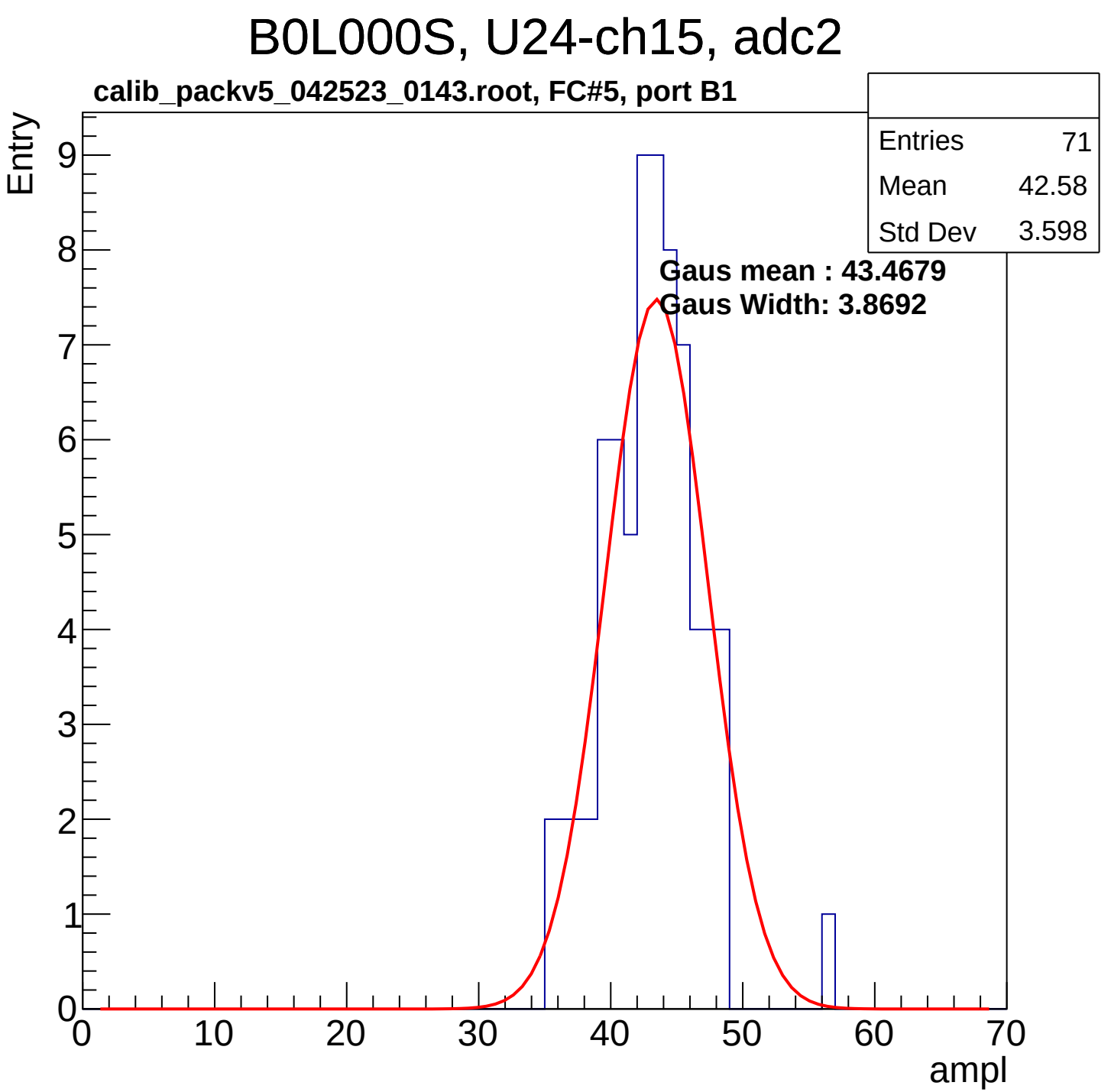
B0L000S, U24-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.58
Std Dev 3.598
Gaus mean : 43.4679
Gaus Width: 3.8692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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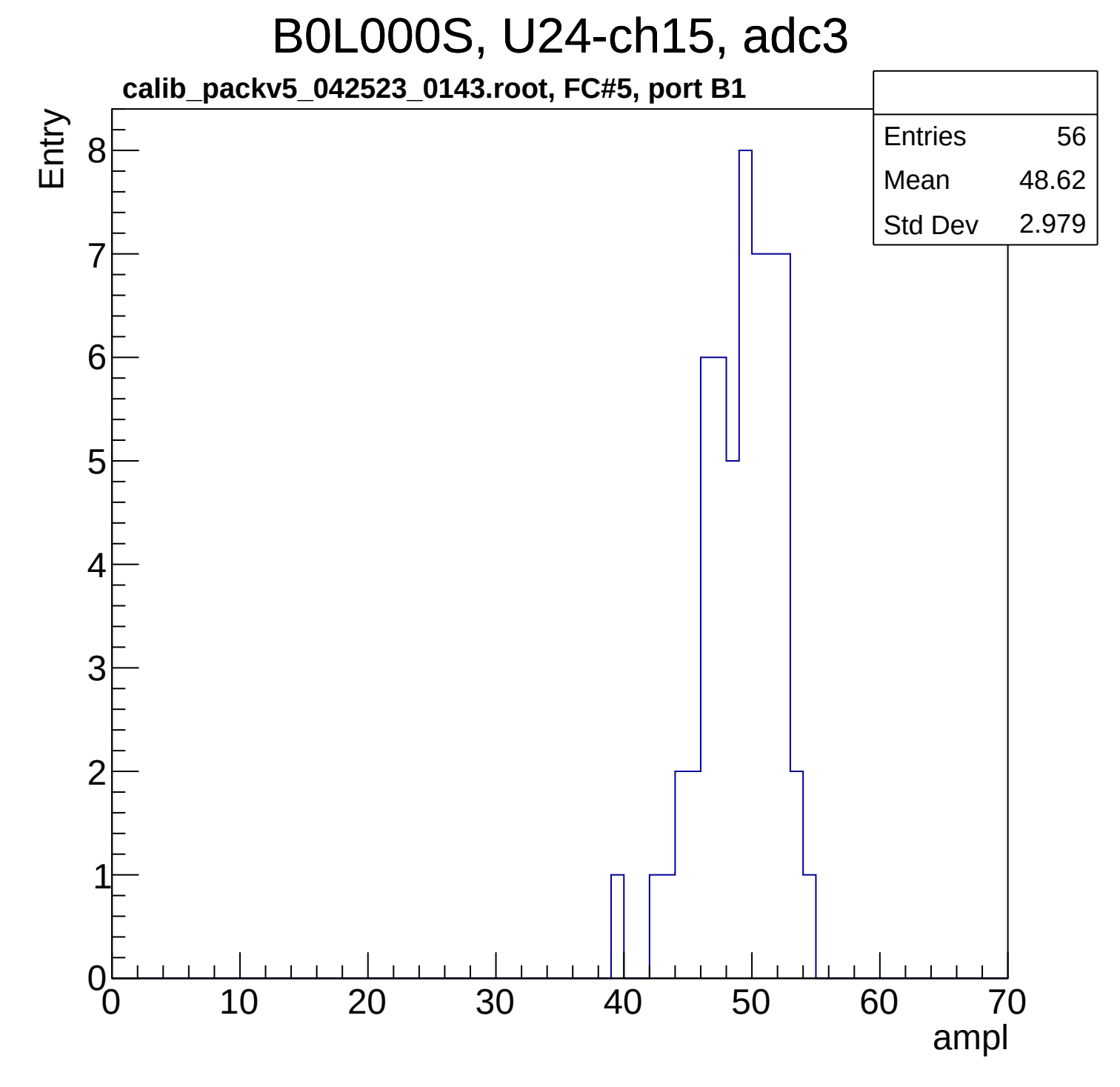
B0L000S, U24-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.62
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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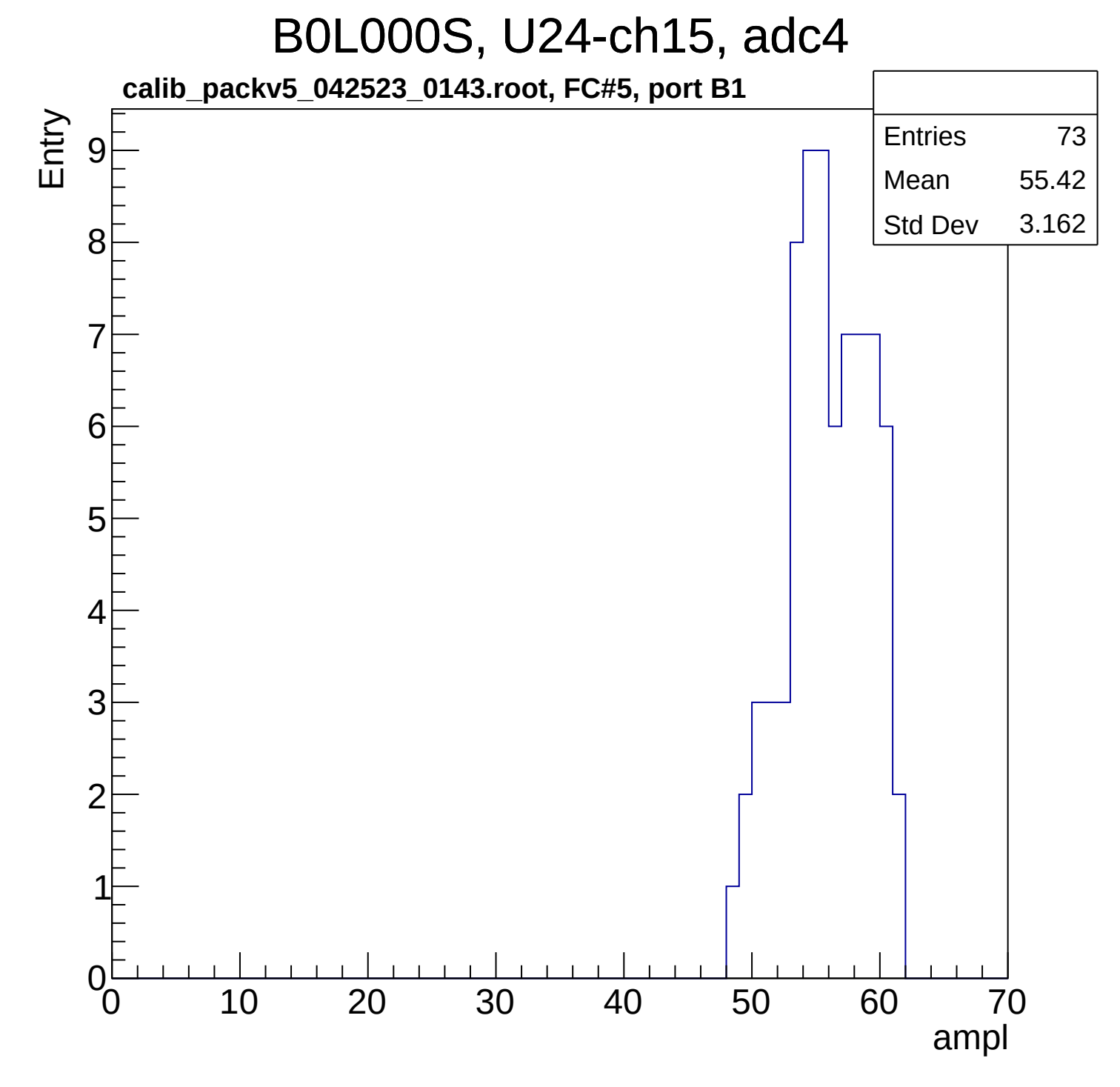
B0L000S, U24-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 55.42
Std Dev 3.162
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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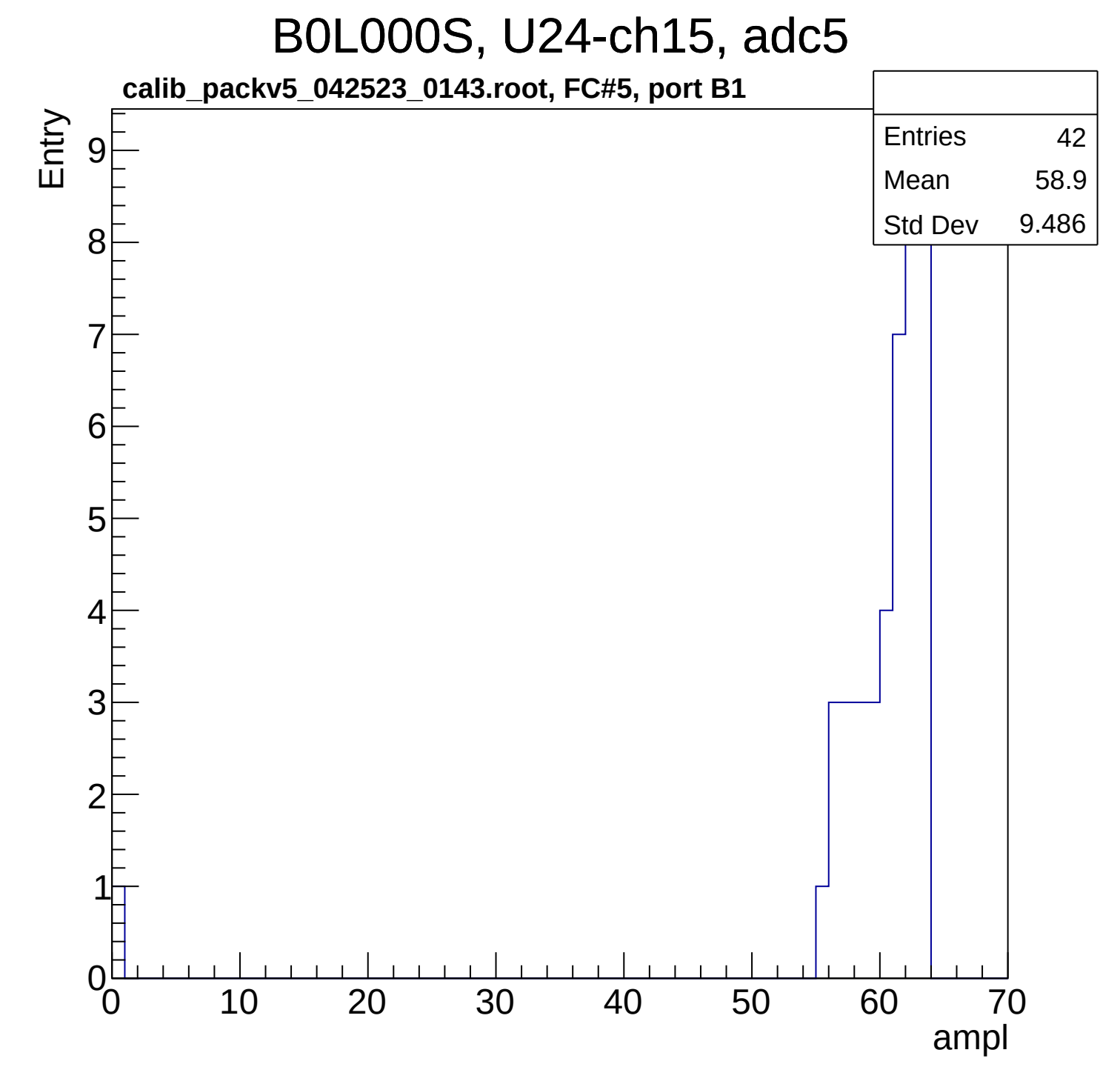
B0L000S, U24-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.9
Std Dev 9.486
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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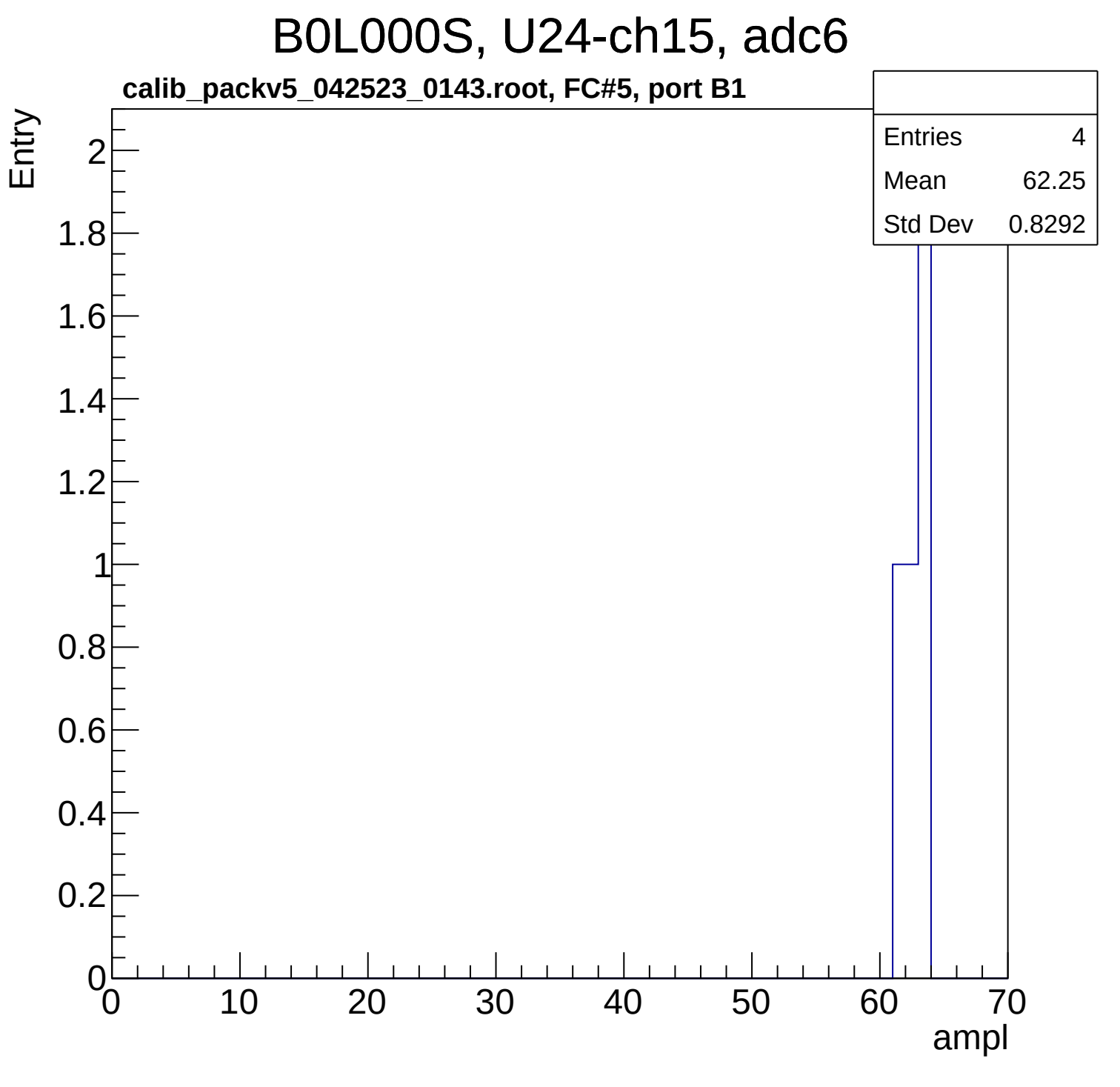
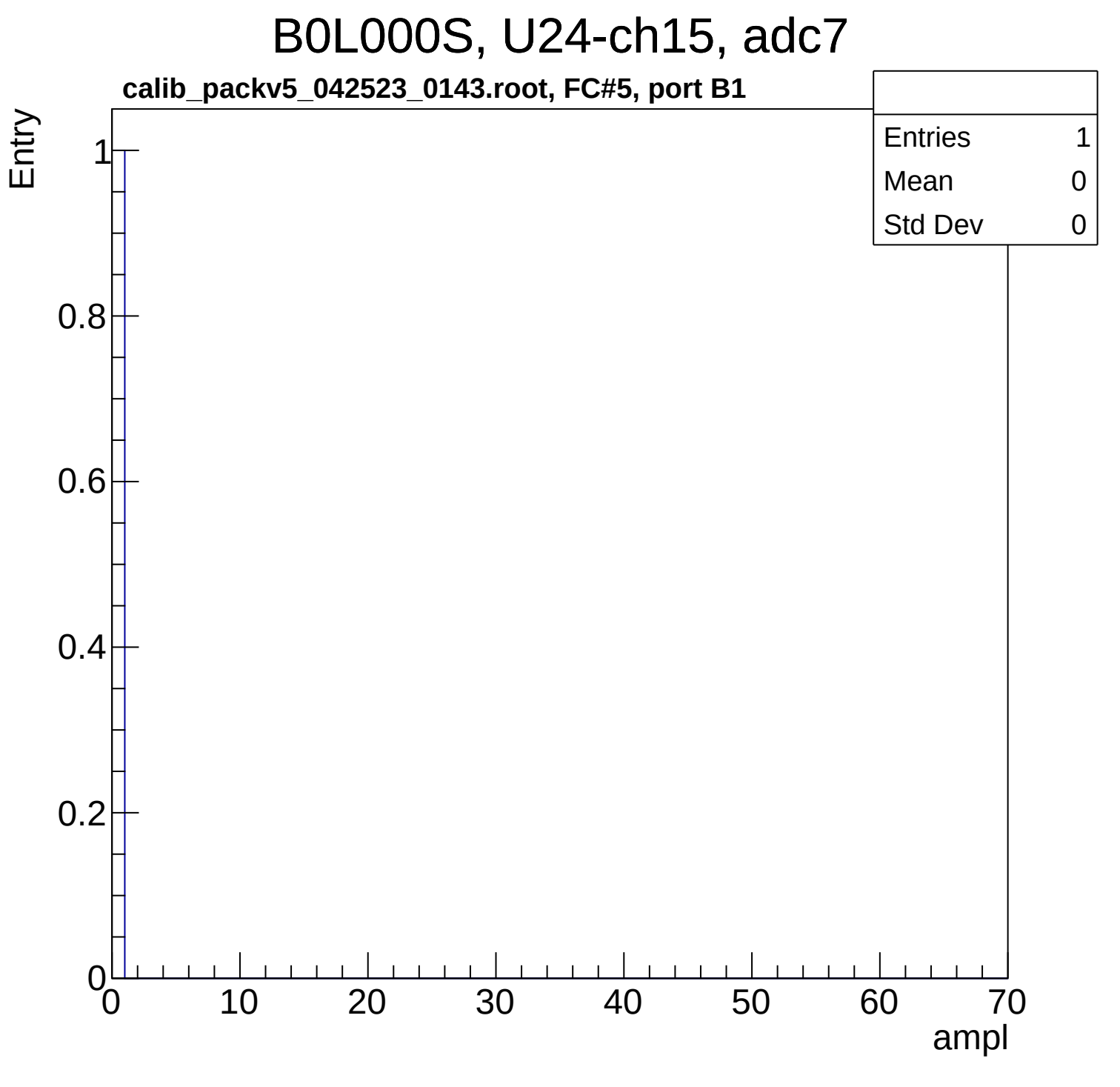
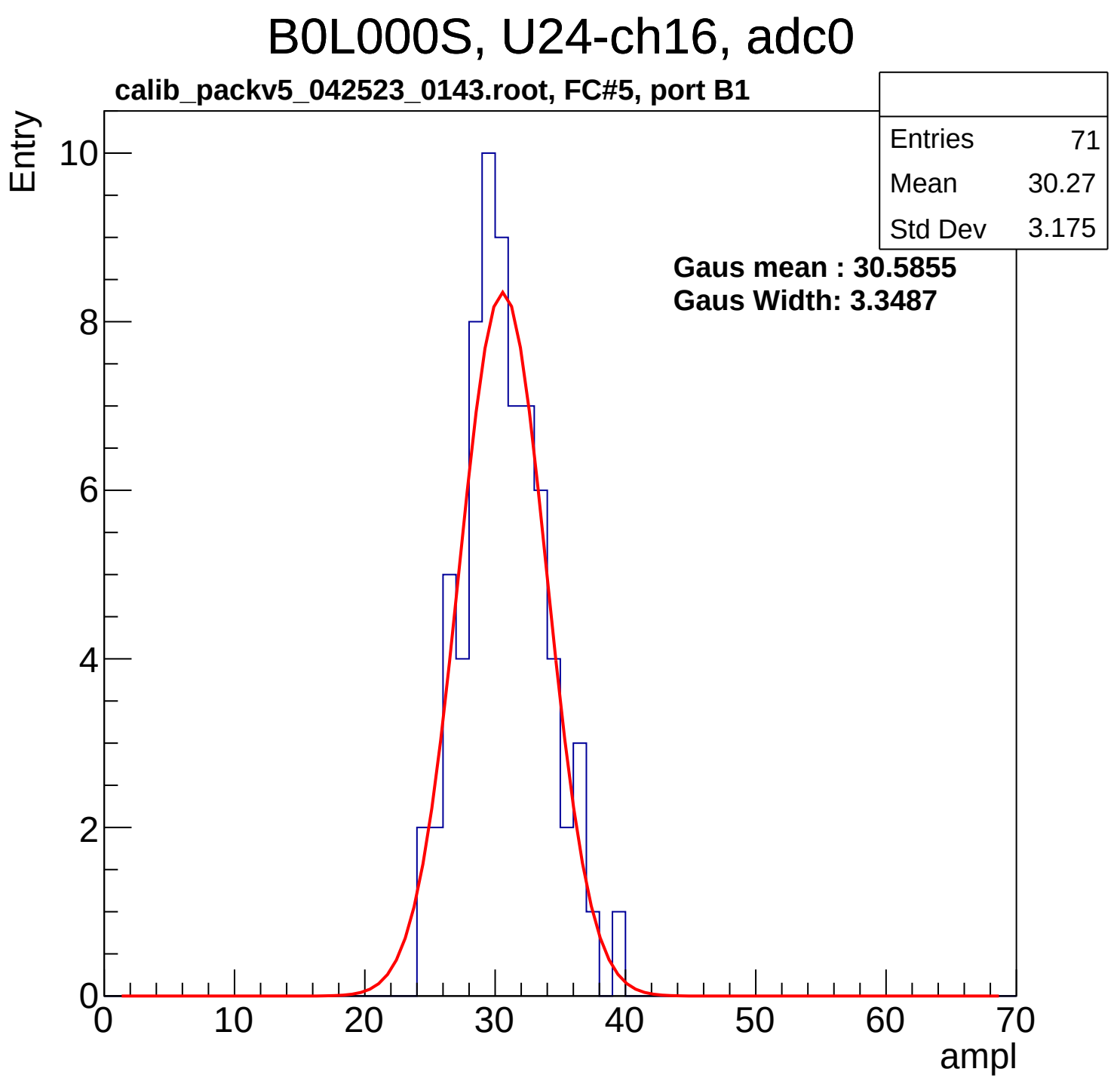
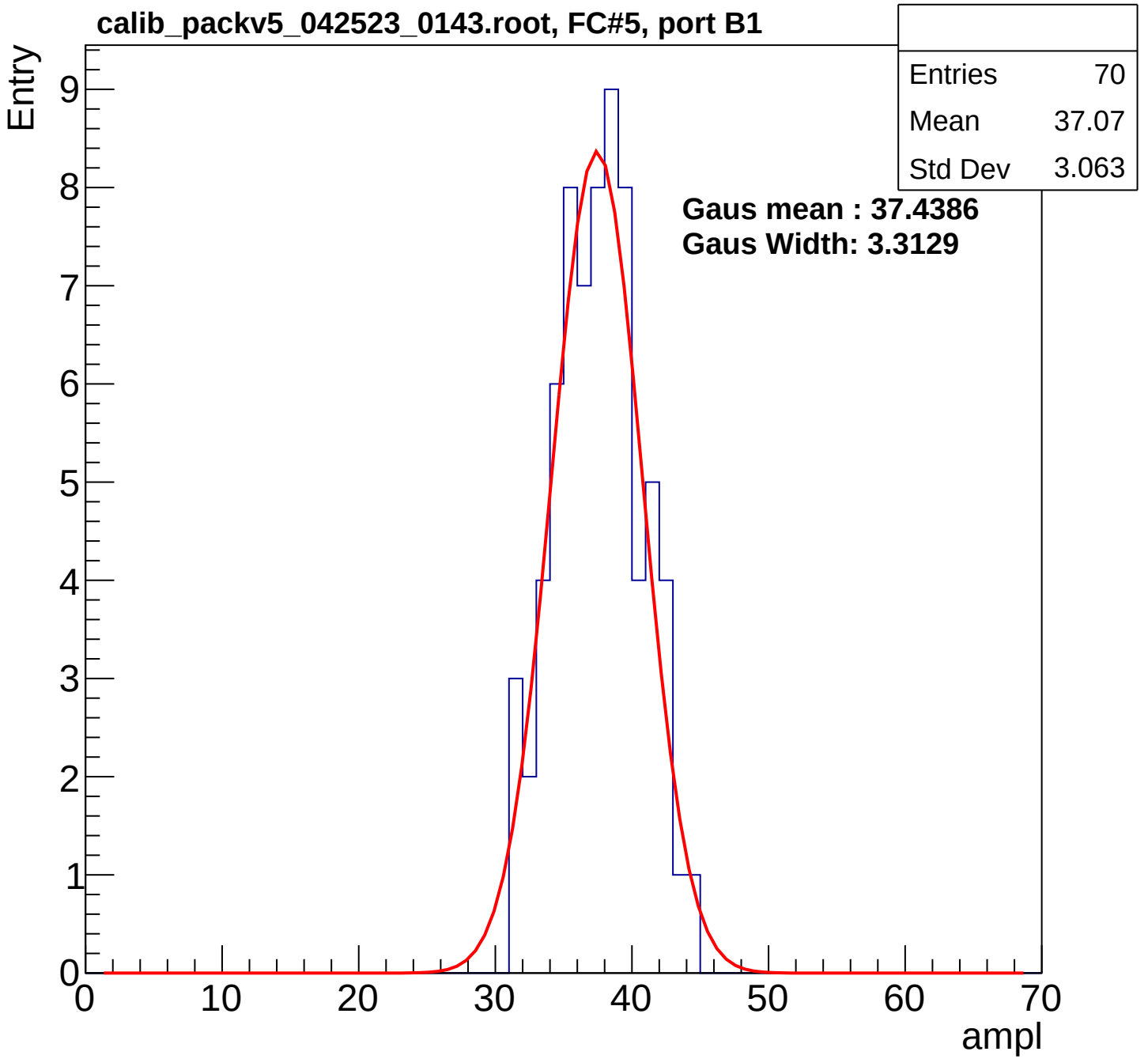
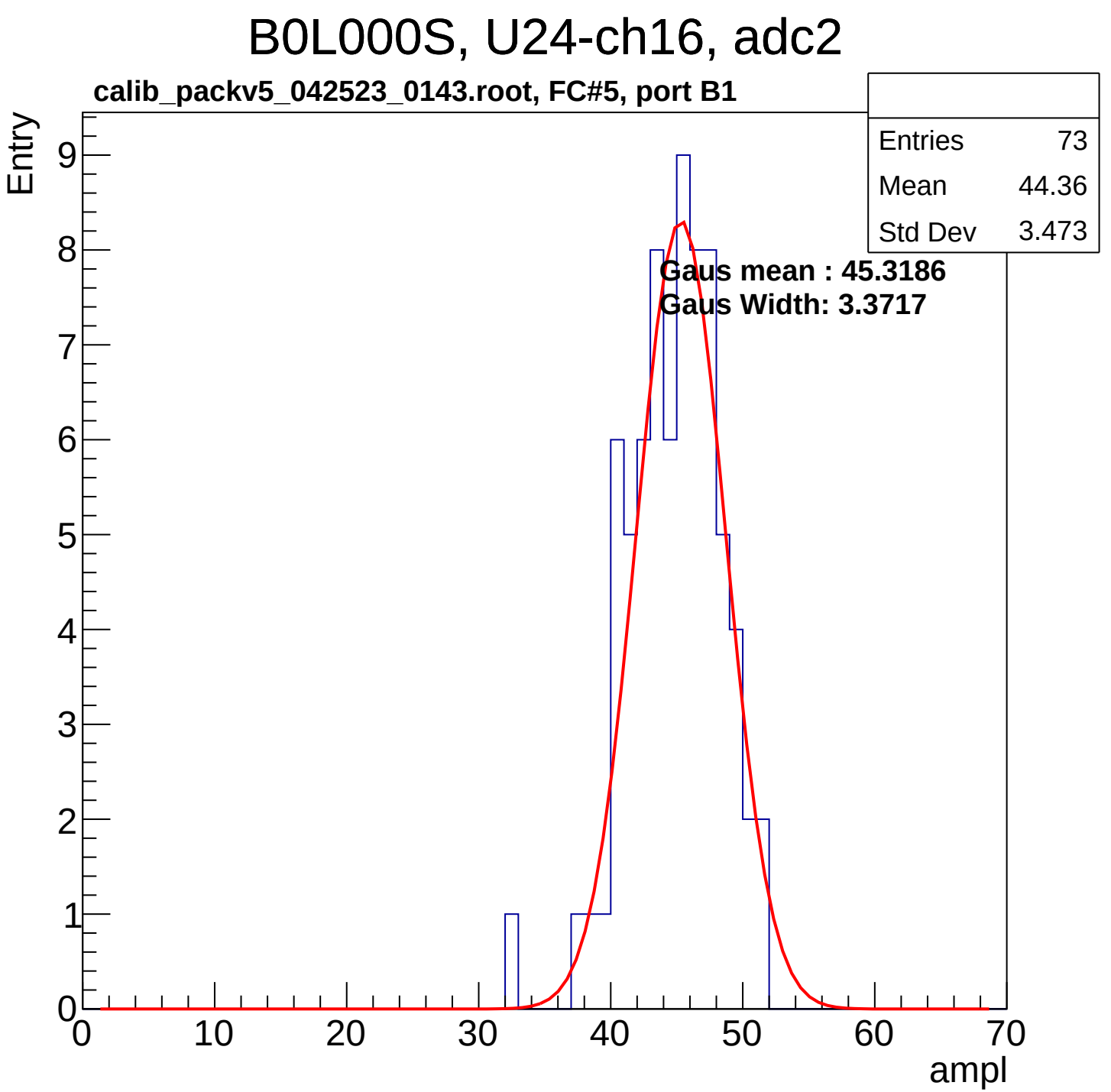

B0L000S, U24-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.27
Std Dev 3.175
Gaus mean : 30.5855
Gaus Width: 3.3487
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.07
Std Dev 3.063
Gaus mean : 37.4386
Gaus Width: 3.3129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.36
Std Dev 3.473
Gaus mean : 45.3186
Gaus Width: 3.3717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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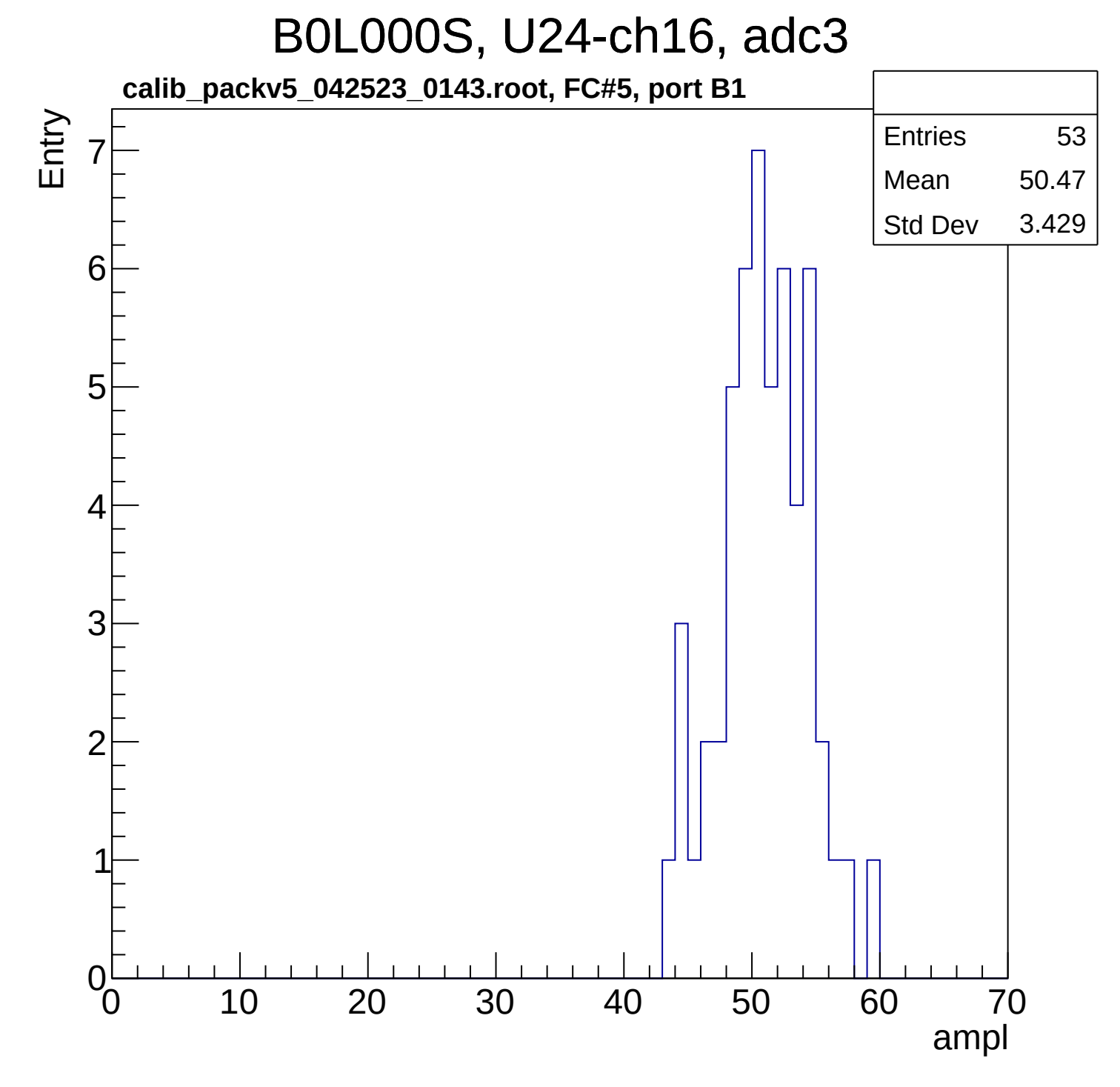
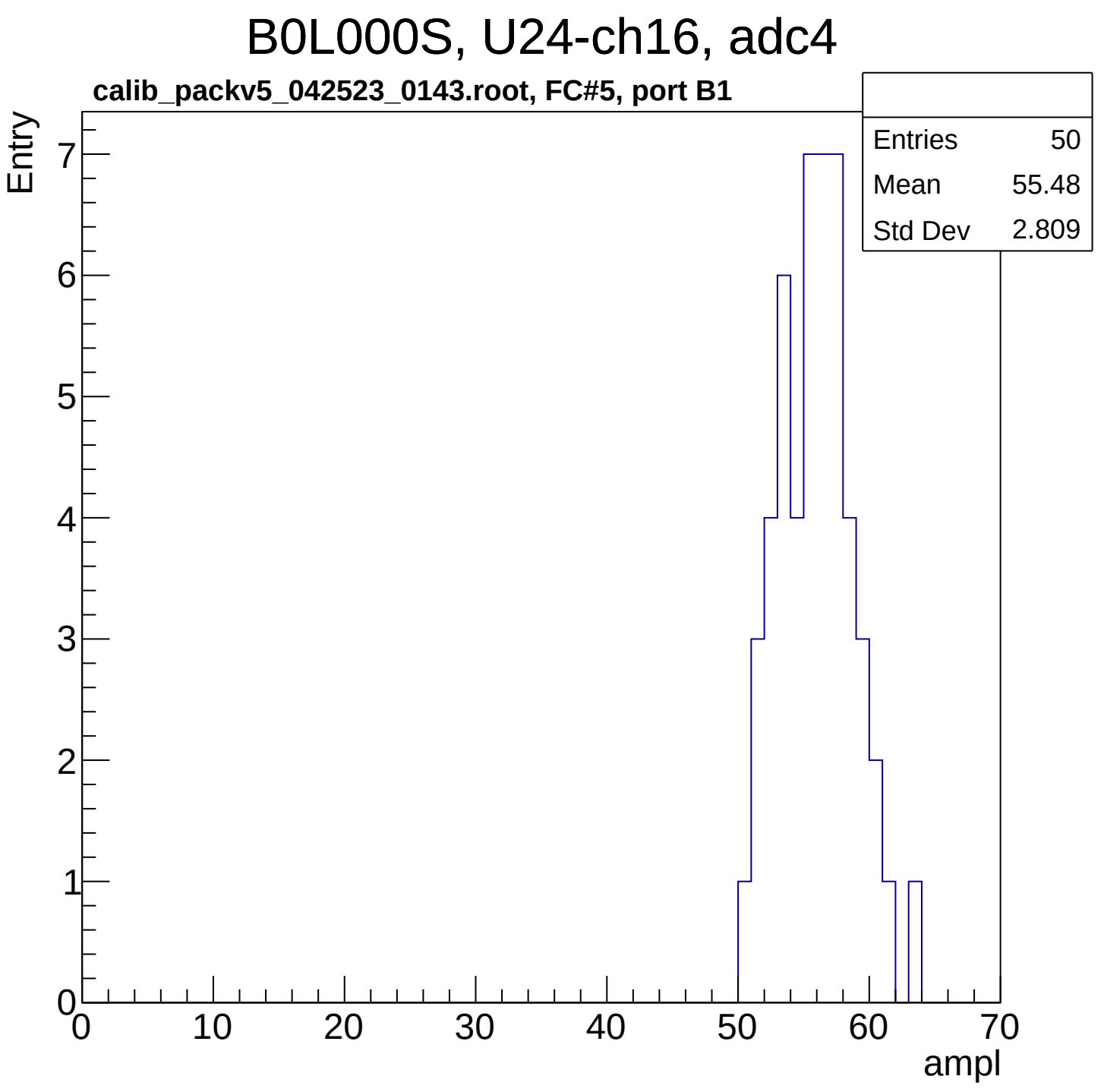
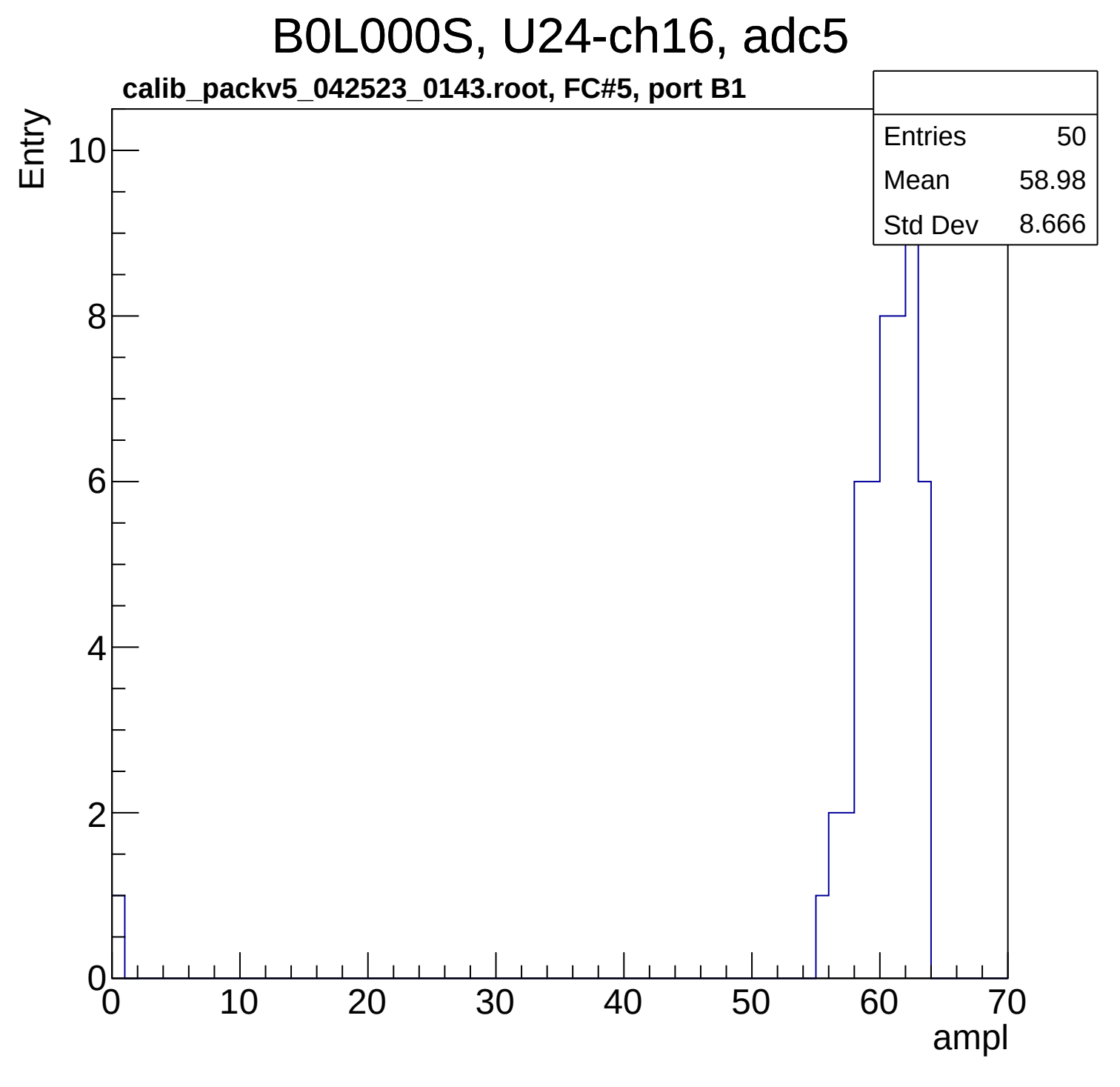
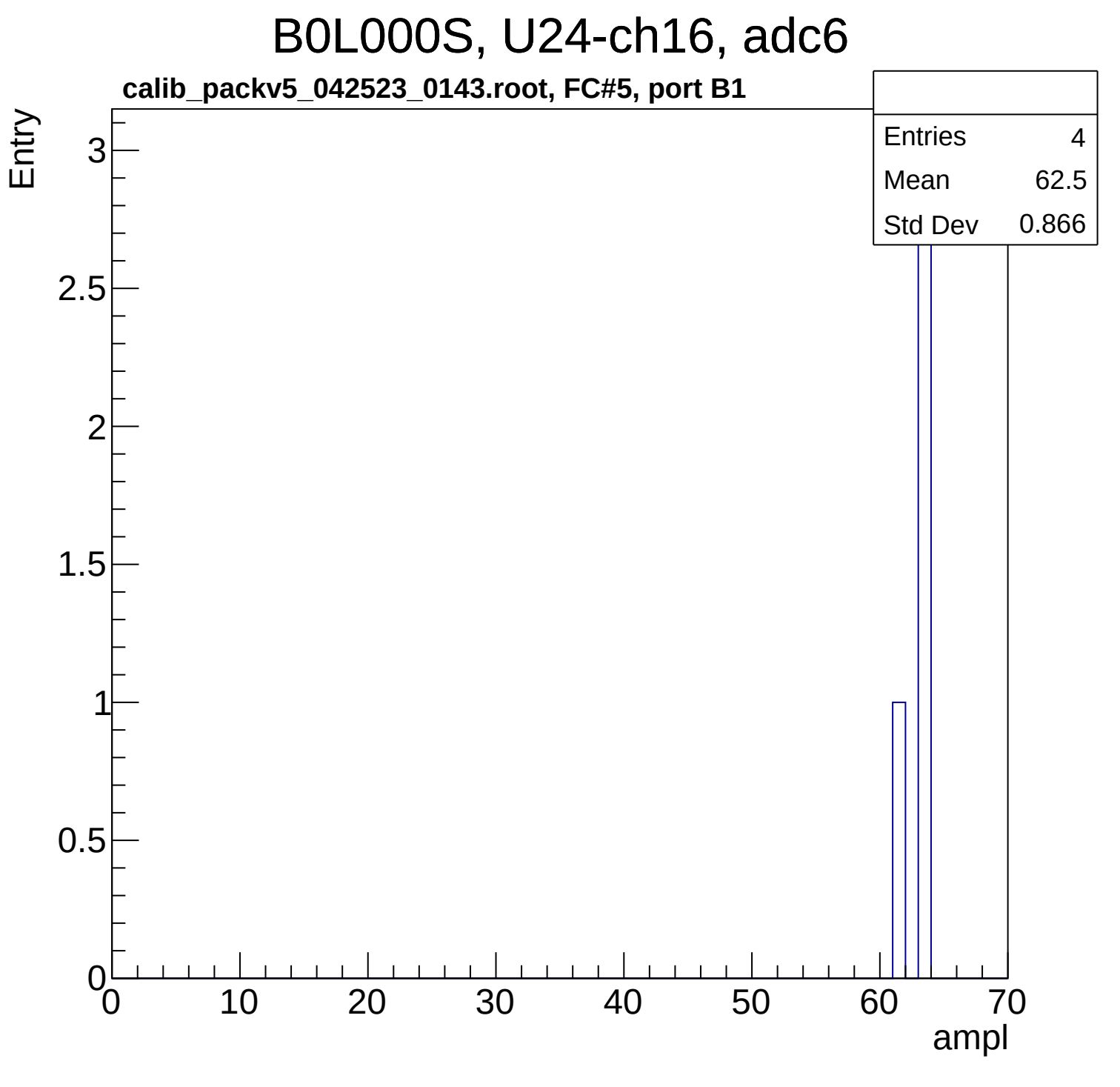

B0L000S, U24-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 50.47
Std Dev 3.429
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.48
Std Dev 2.809
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.98
Std Dev 8.666
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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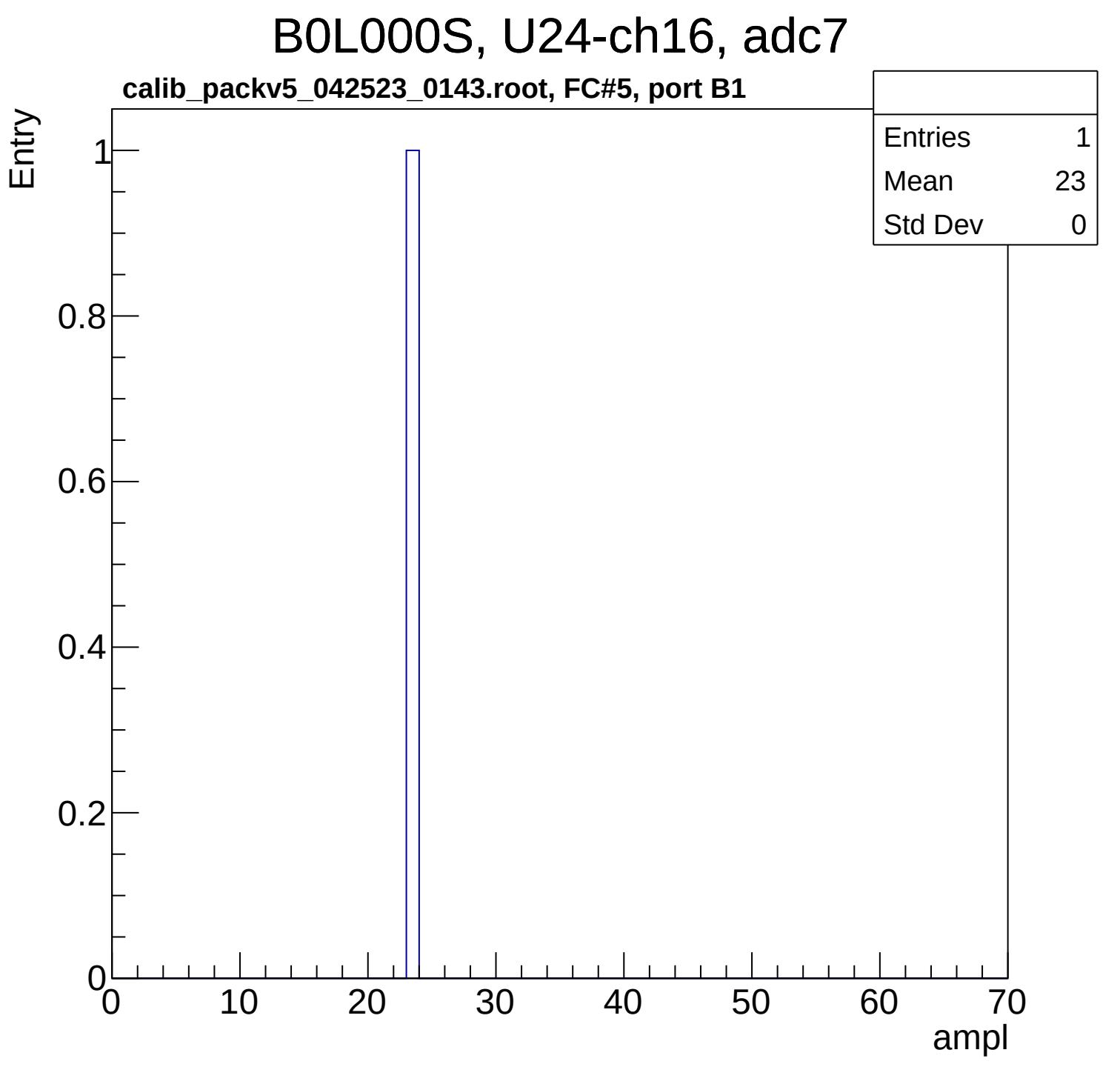
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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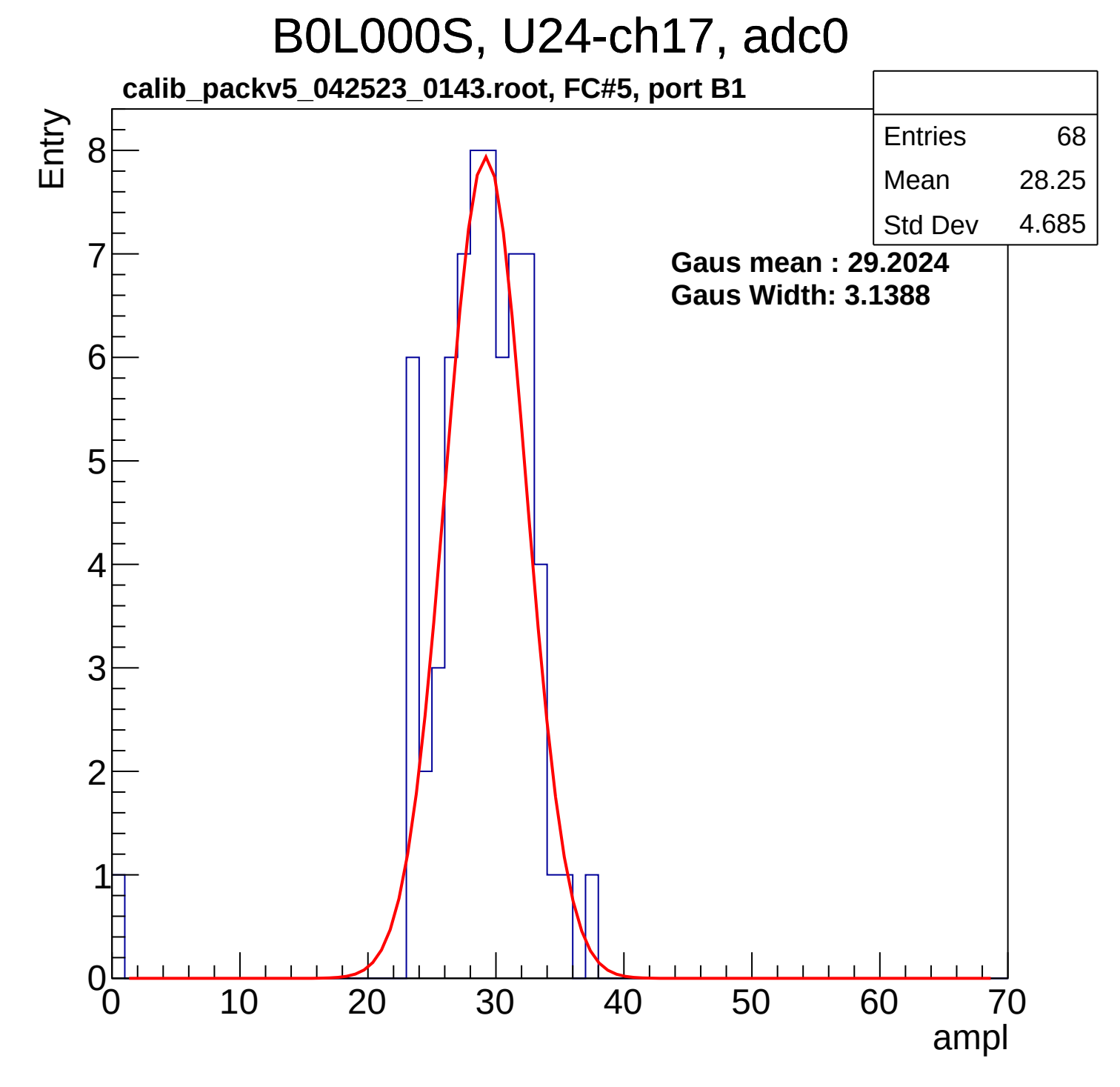
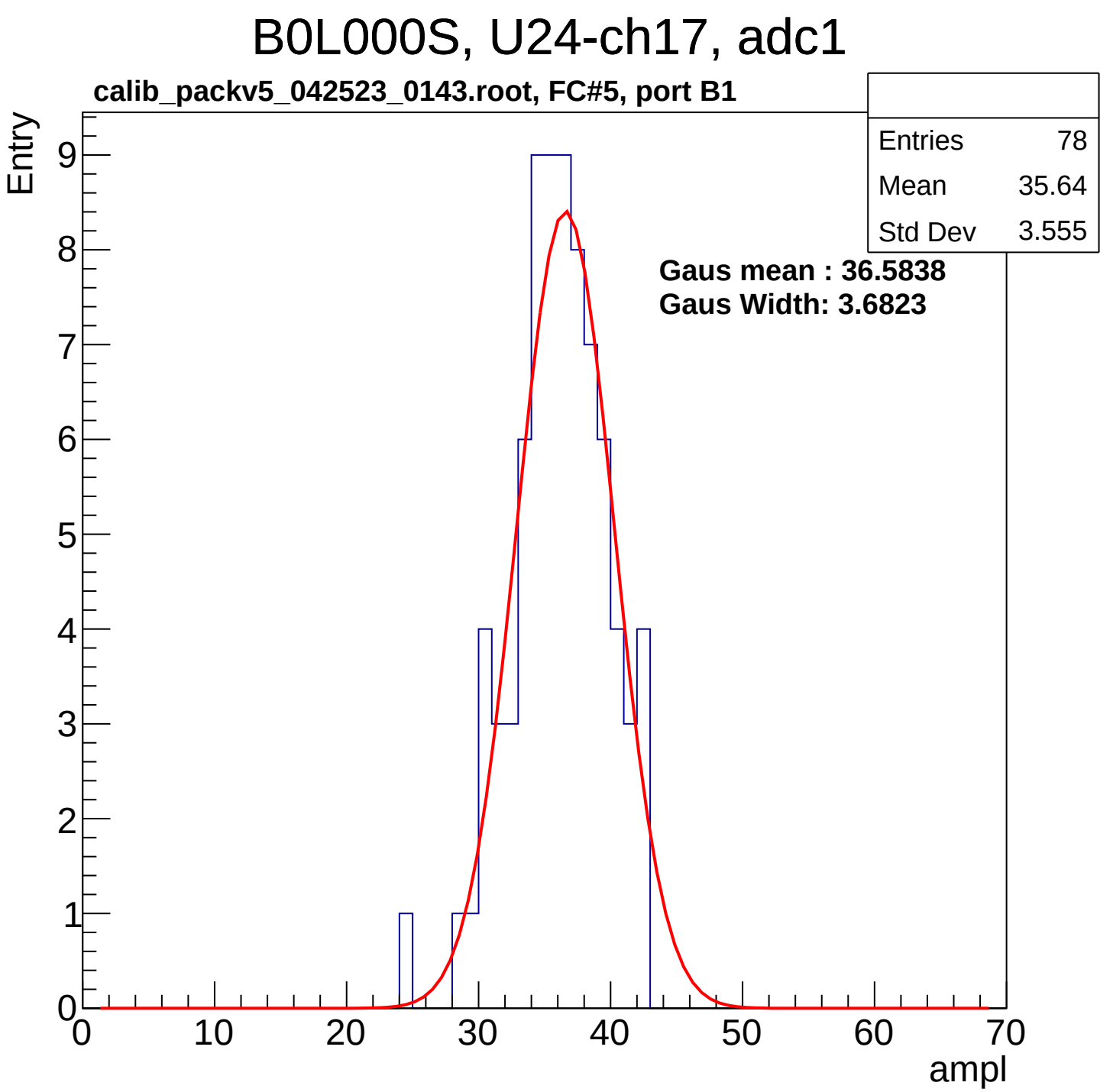
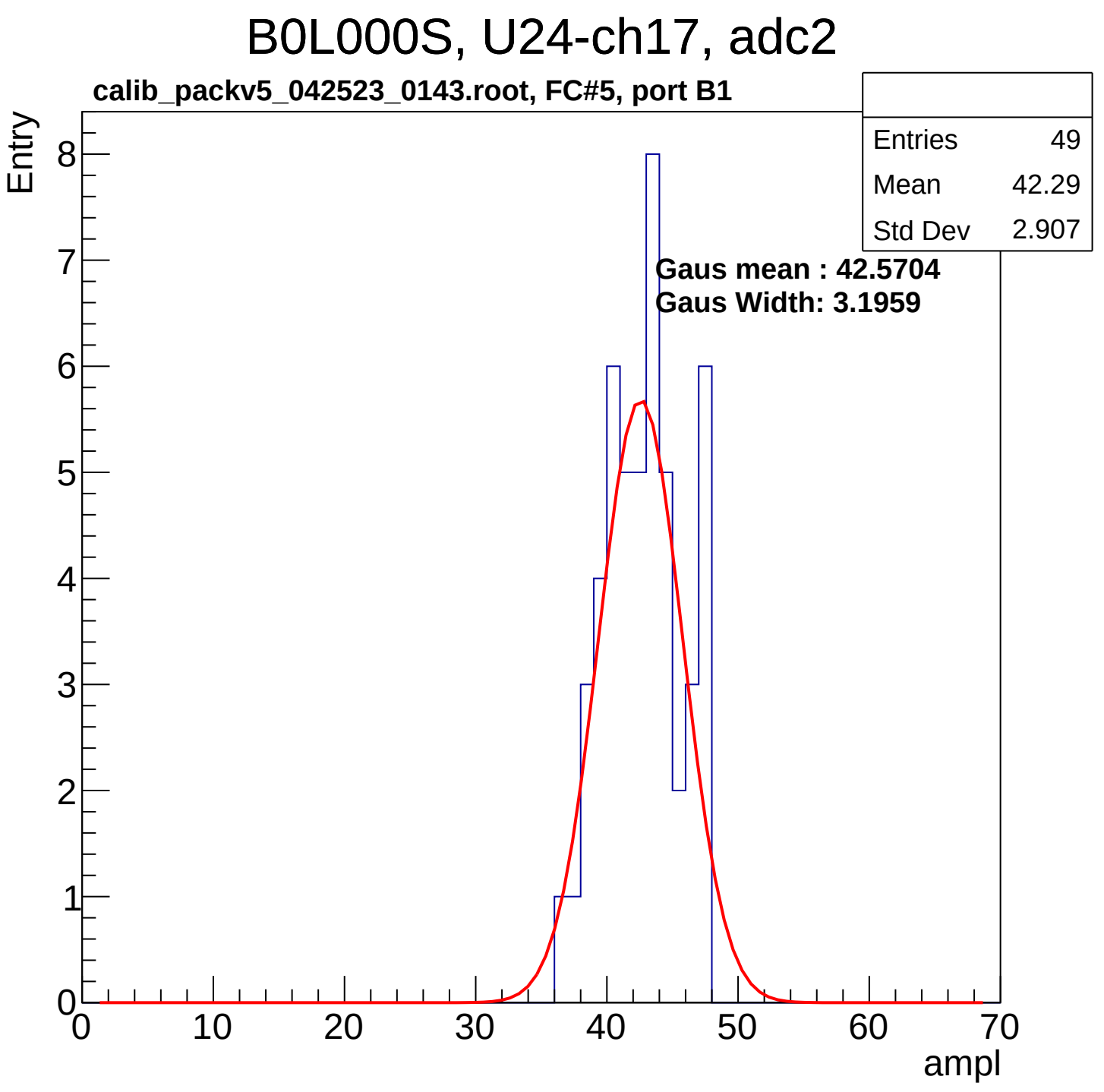
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.25
Std Dev 4.685
Gaus mean : 29.2024
Gaus Width: 3.1388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 35.64
Std Dev 3.555
Gaus mean : 36.5838
Gaus Width: 3.6823
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 42.29
Std Dev 2.907
Gaus mean : 42.5704
Gaus Width: 3.1959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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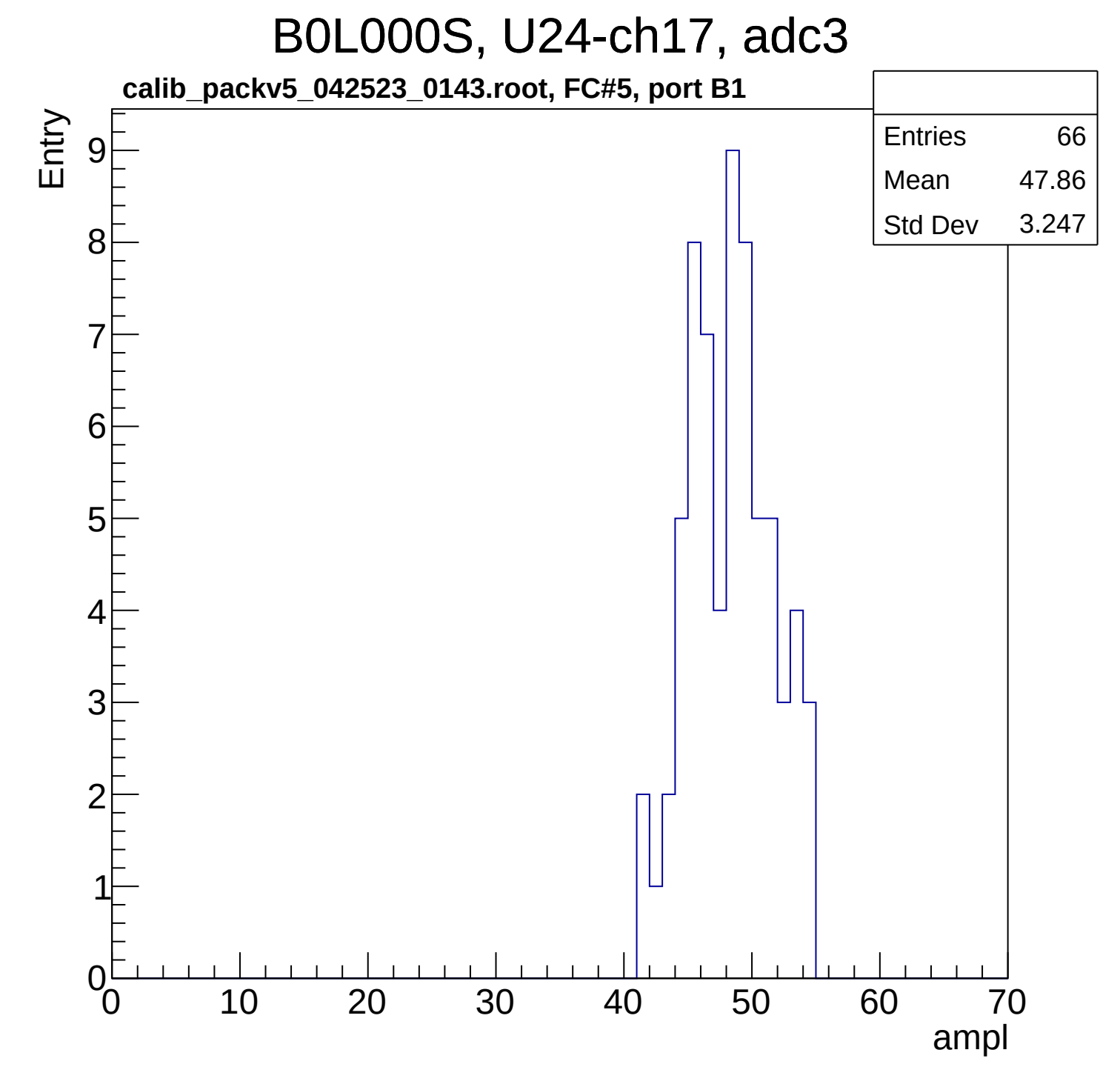
B0L000S, U24-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 47.86
Std Dev 3.247
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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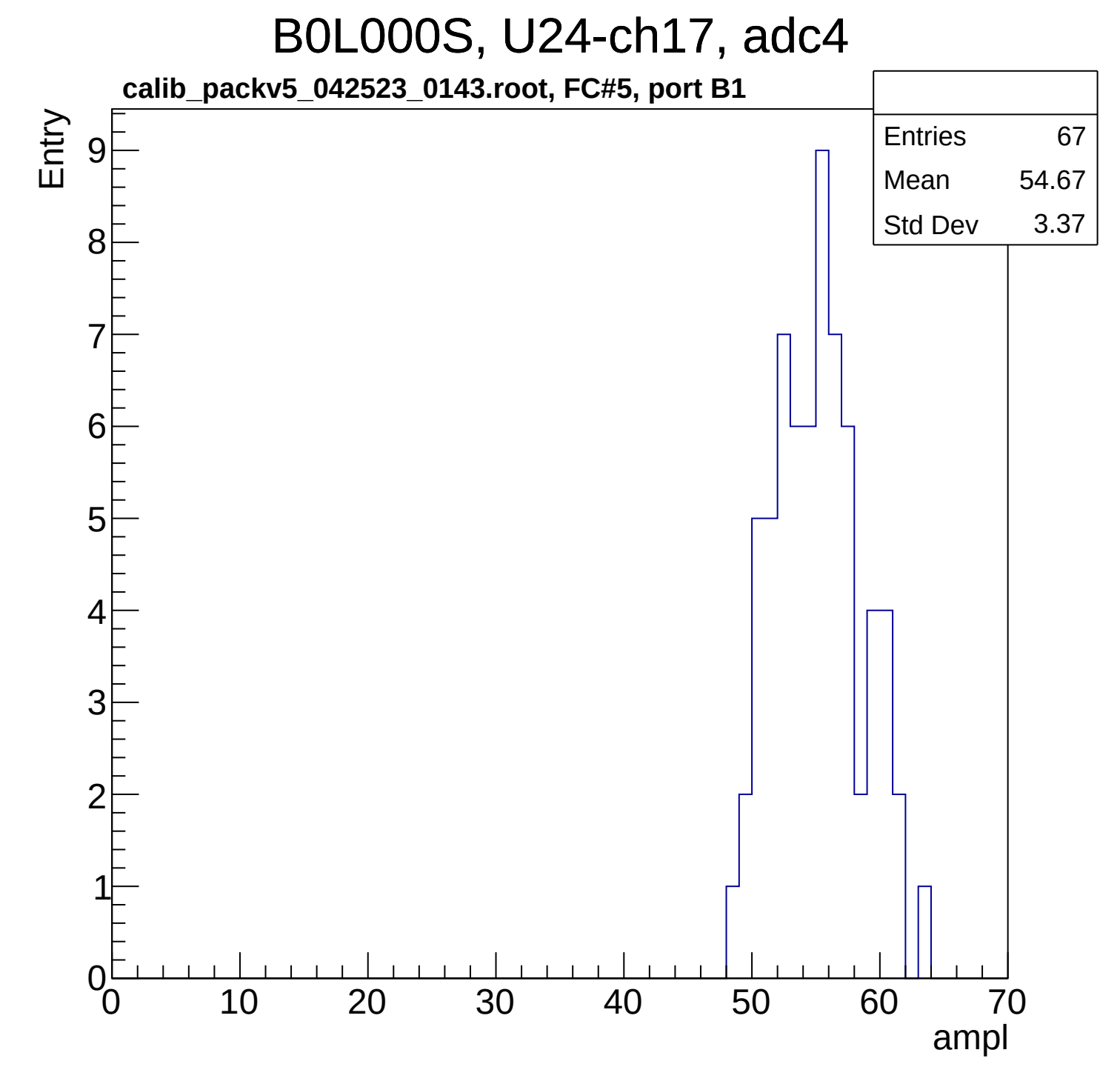
B0L000S, U24-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 54.67
Std Dev 3.37
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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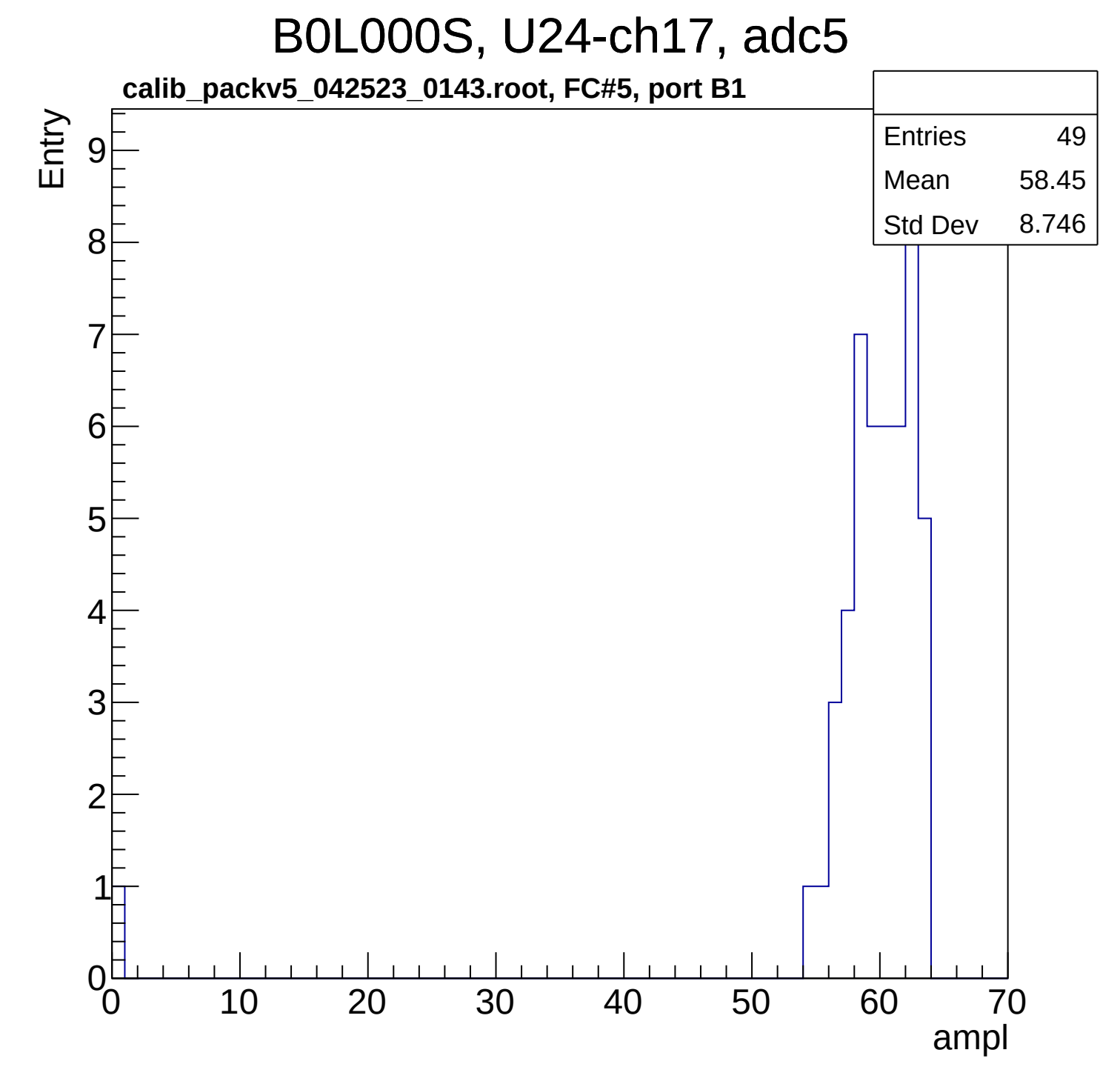
B0L000S, U24-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.45
Std Dev 8.746
Entry
ampl
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3
4
5
6
7
8
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0
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60
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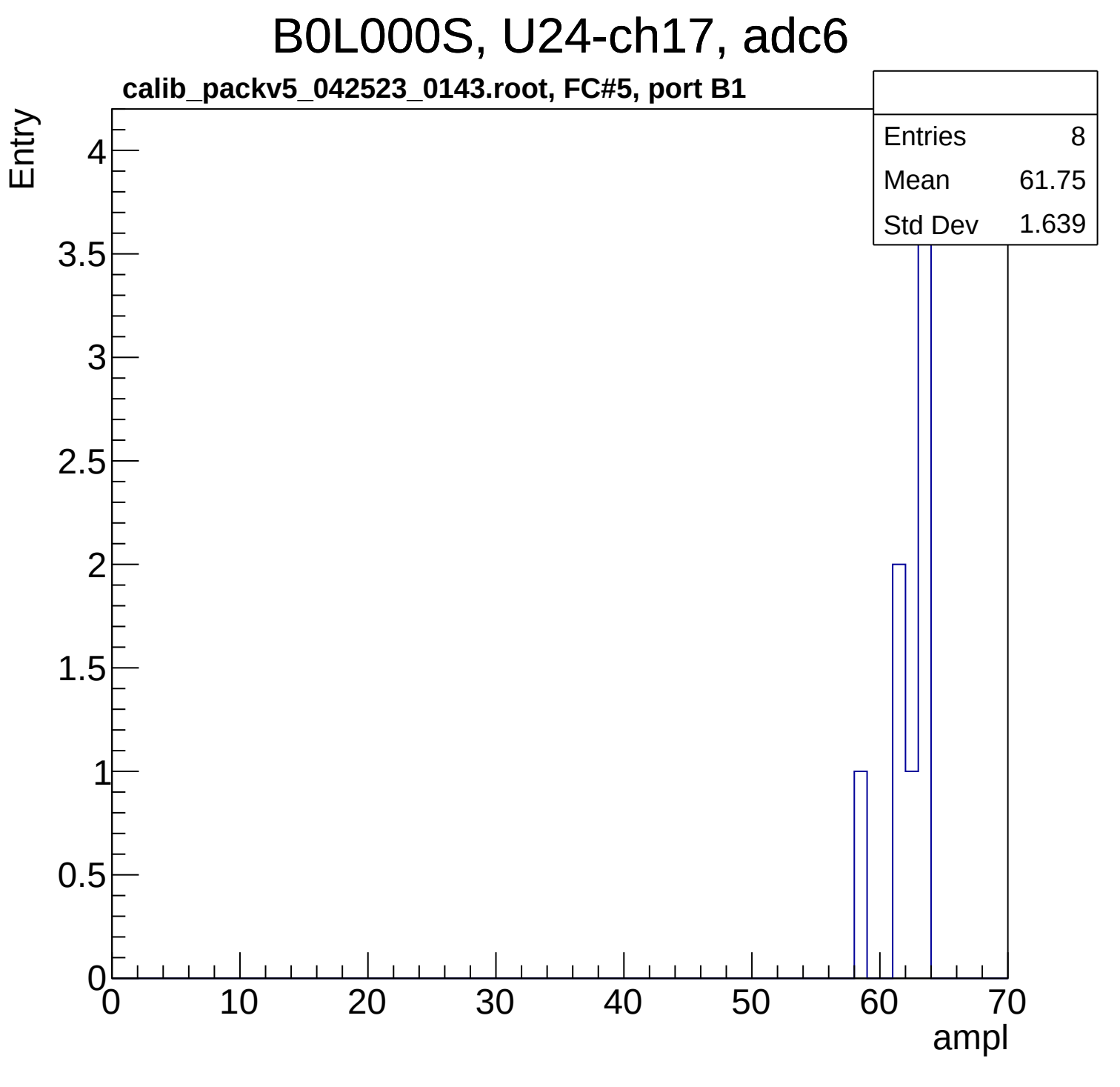
B0L000S, U24-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.75
Std Dev 1.639
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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50
60
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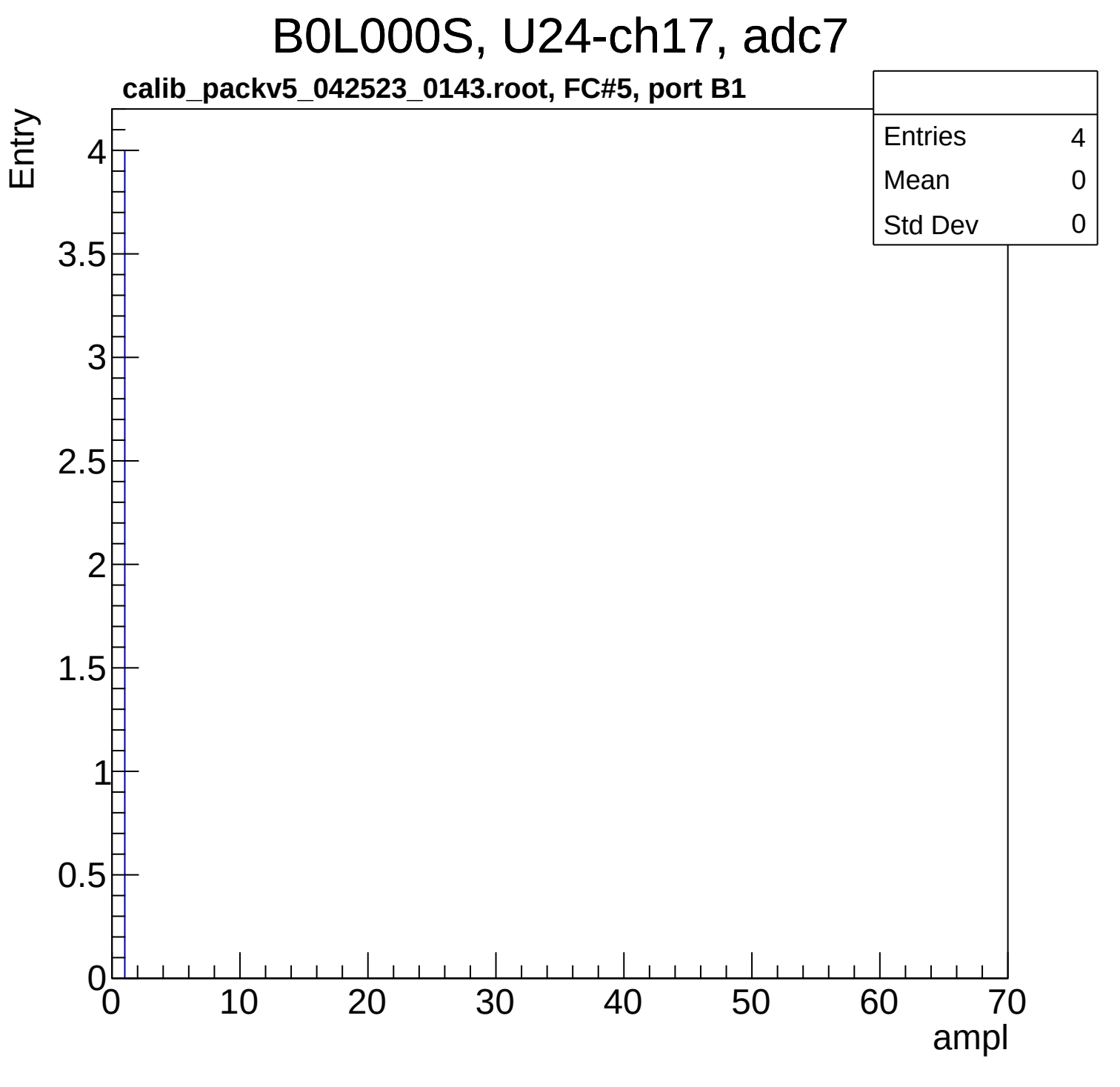
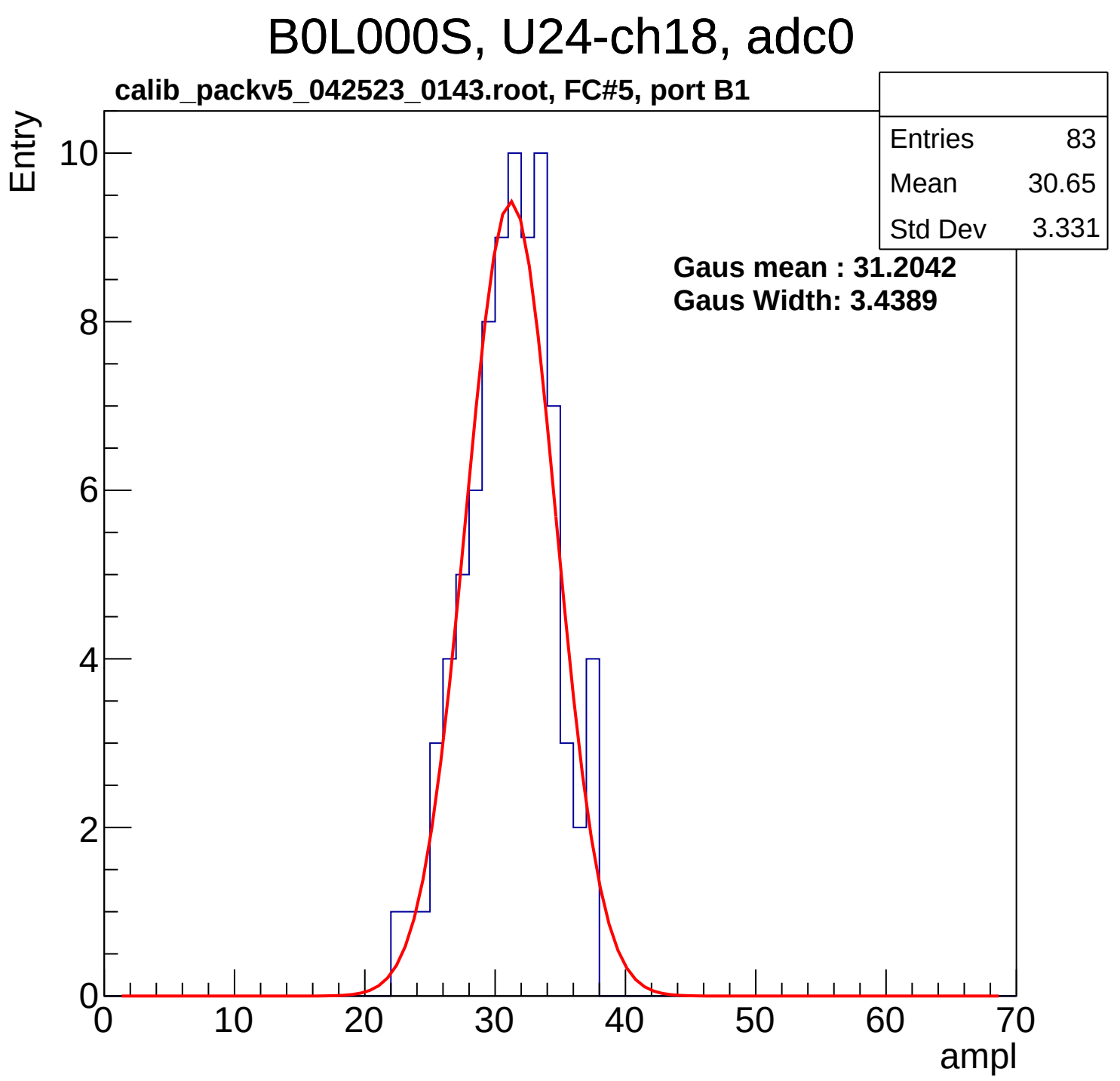
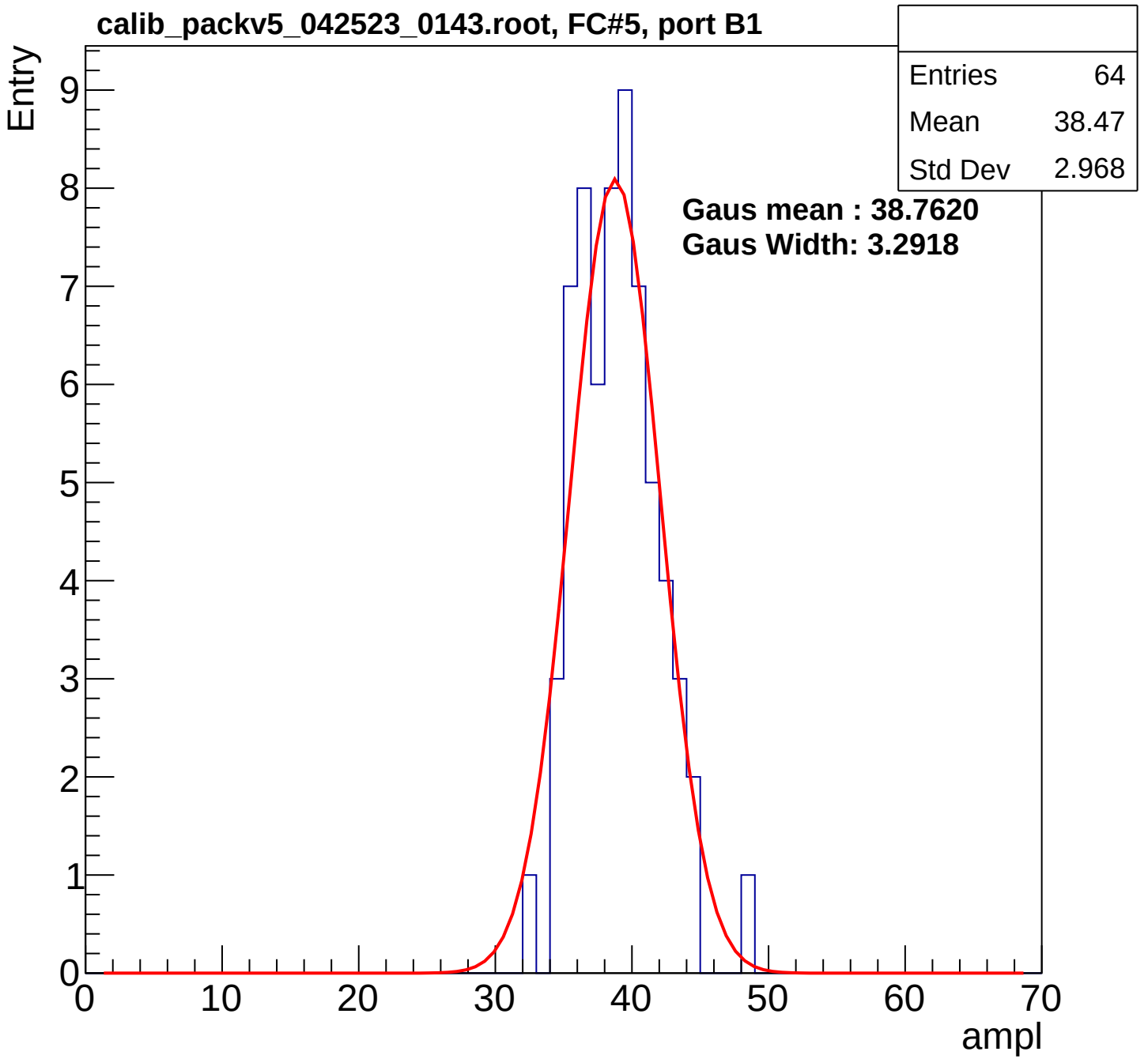

B0L000S, U24-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.65
Std Dev 3.331
Gaus mean : 31.2042
Gaus Width: 3.4389
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 38.47
Std Dev 2.968
Gaus mean : 38.7620
Gaus Width: 3.2918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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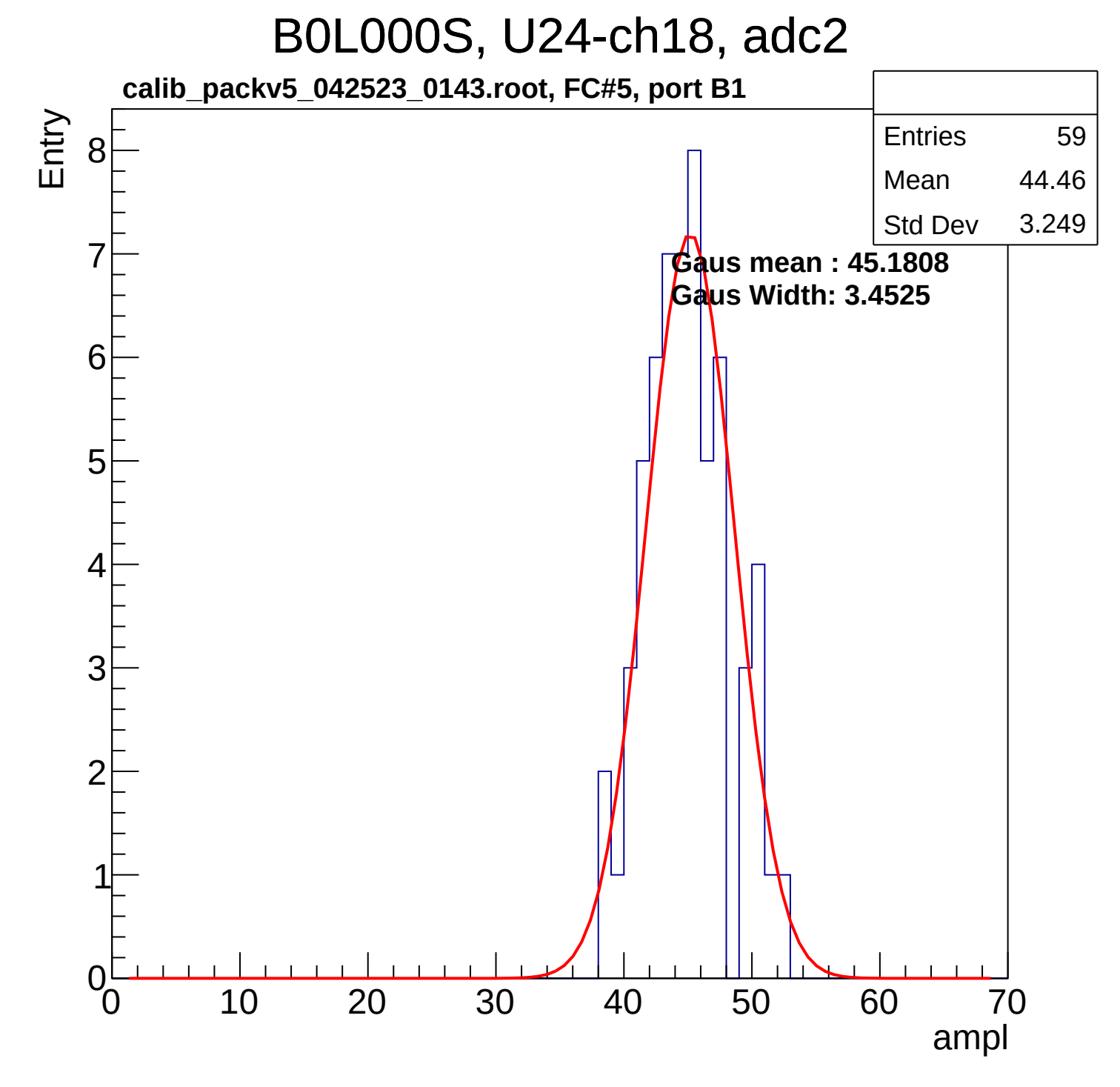
B0L000S, U24-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 44.46
Std Dev 3.249
Gaus mean : 45.1808
Gaus Width: 3.4525
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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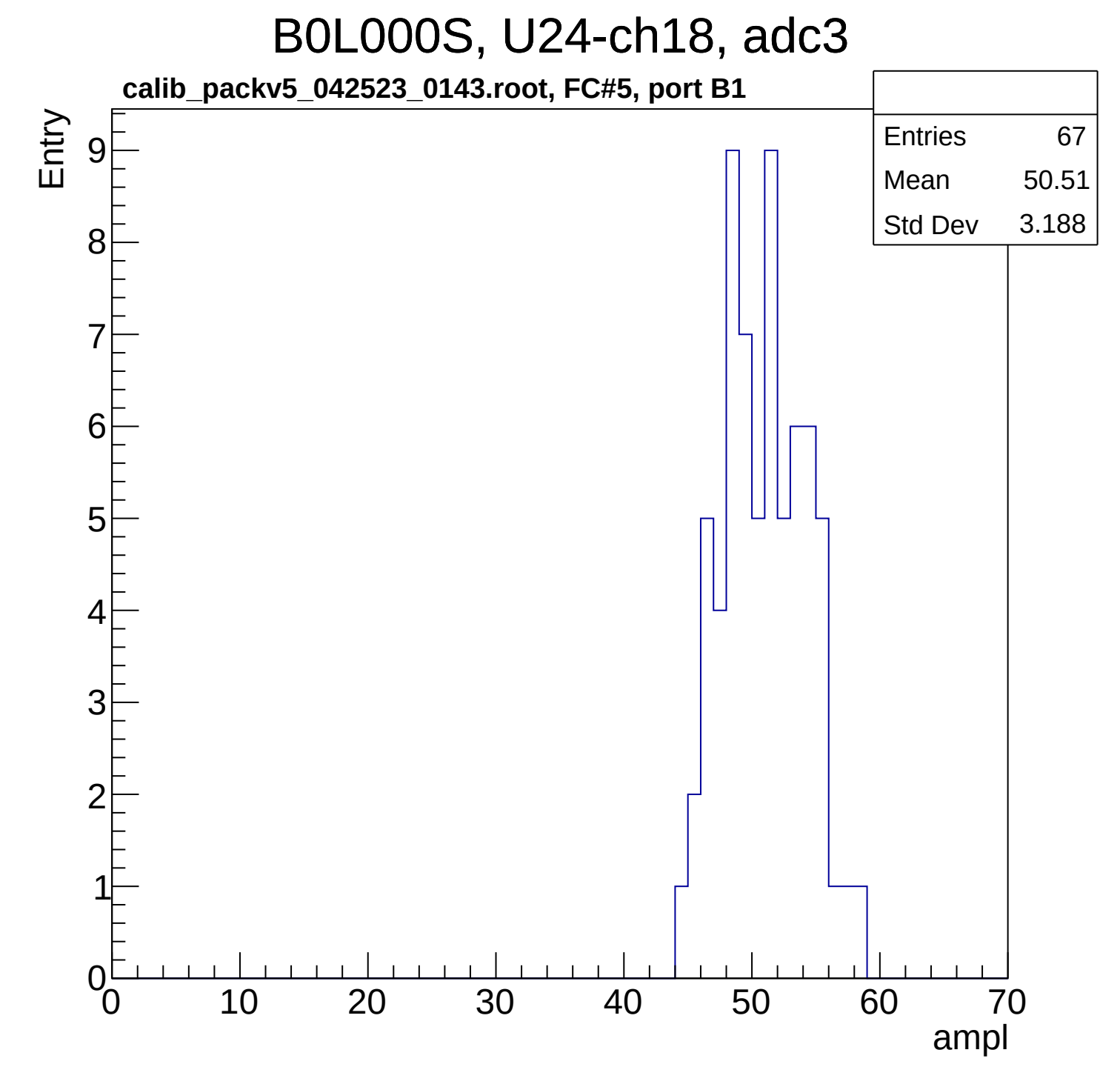
B0L000S, U24-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.51
Std Dev 3.188
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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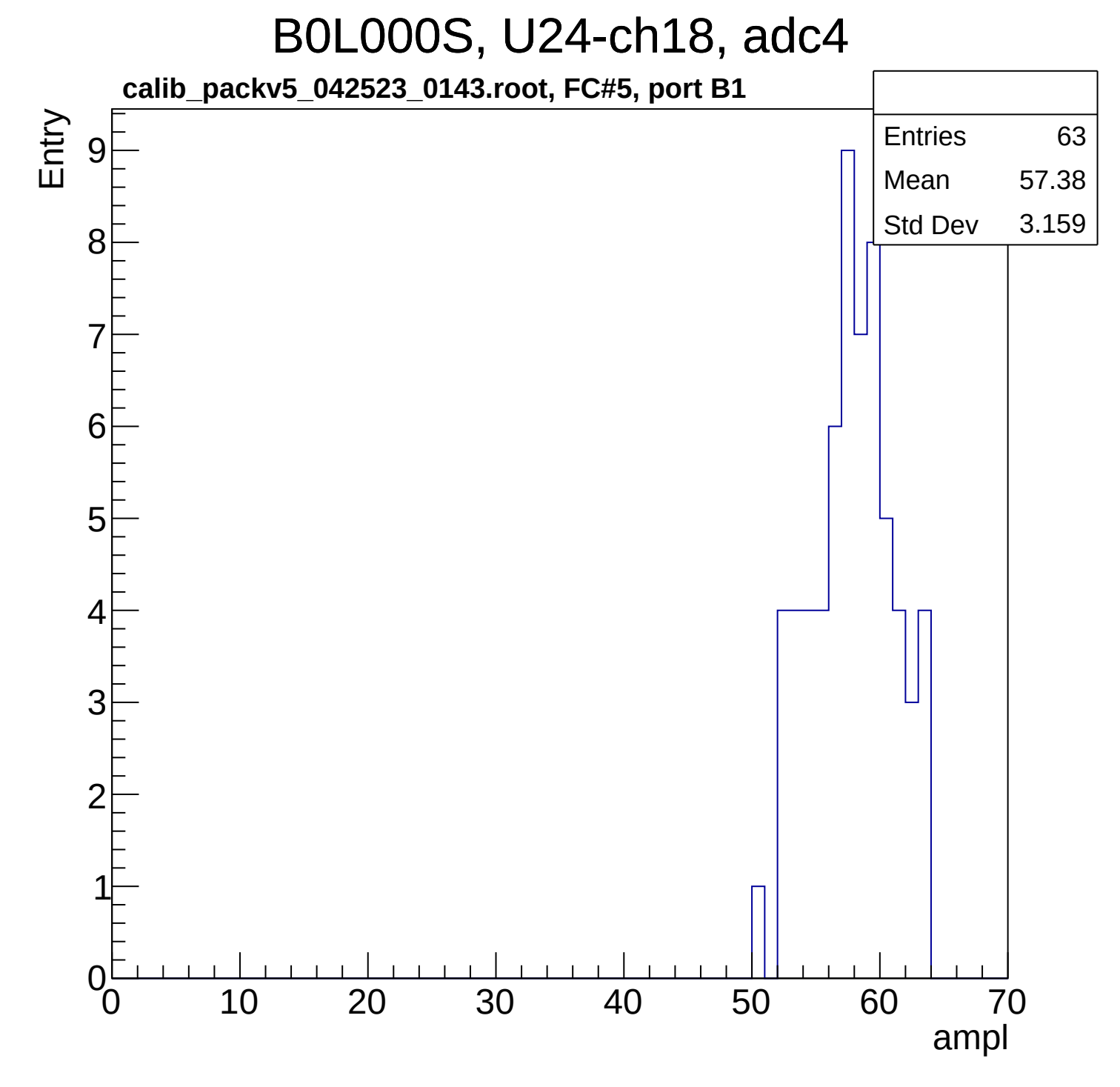
B0L000S, U24-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 57.38
Std Dev 3.159
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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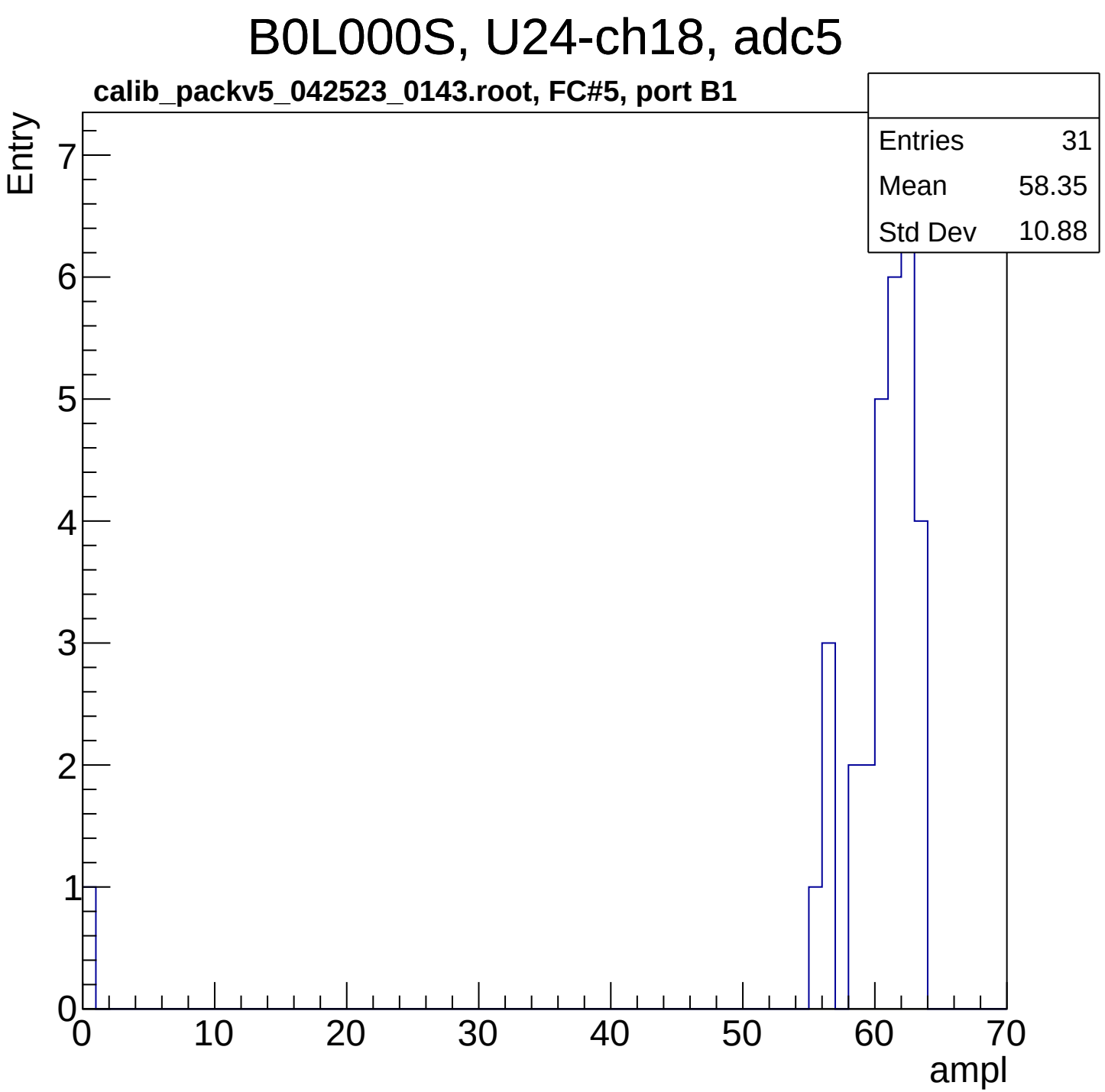
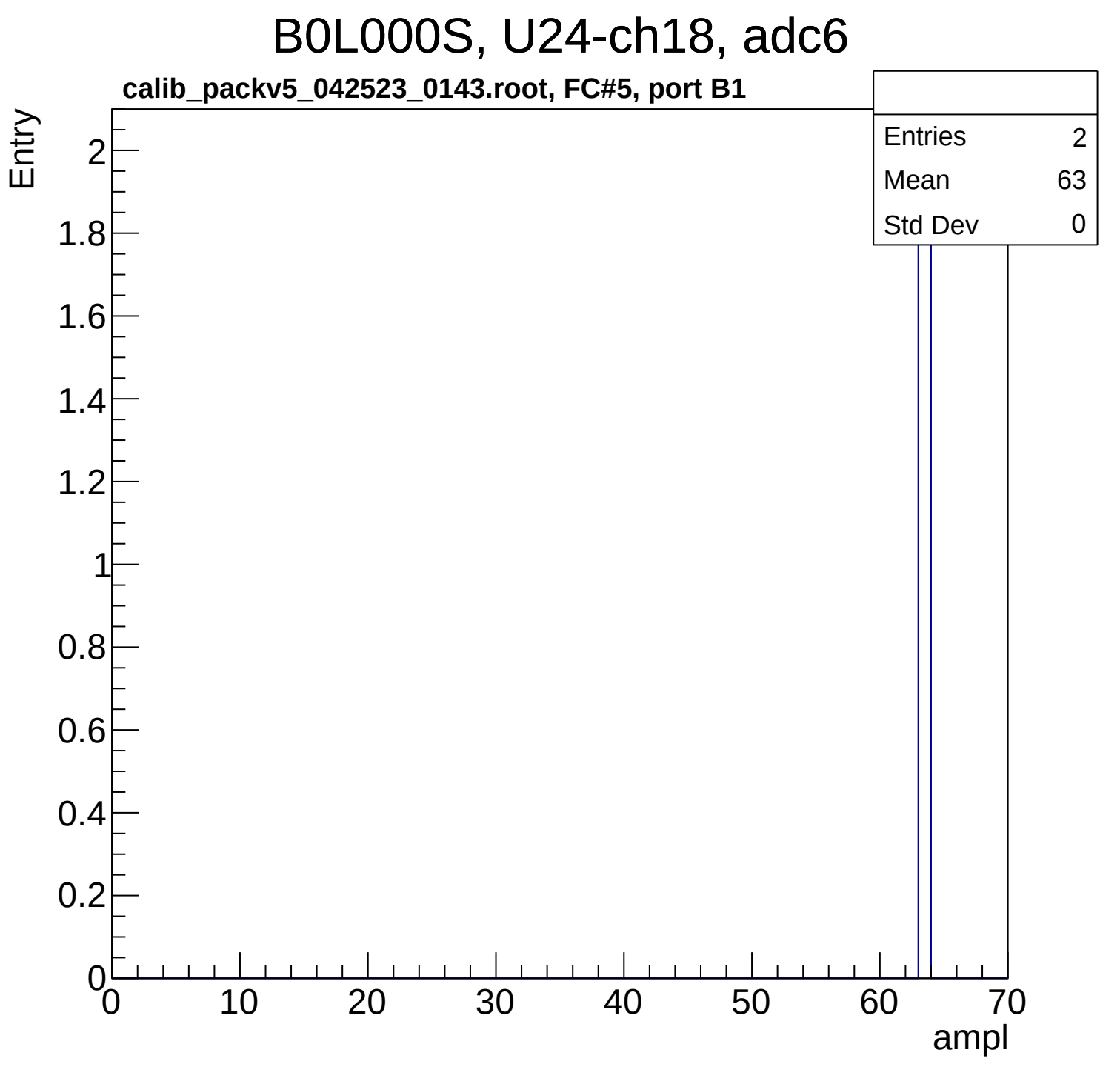
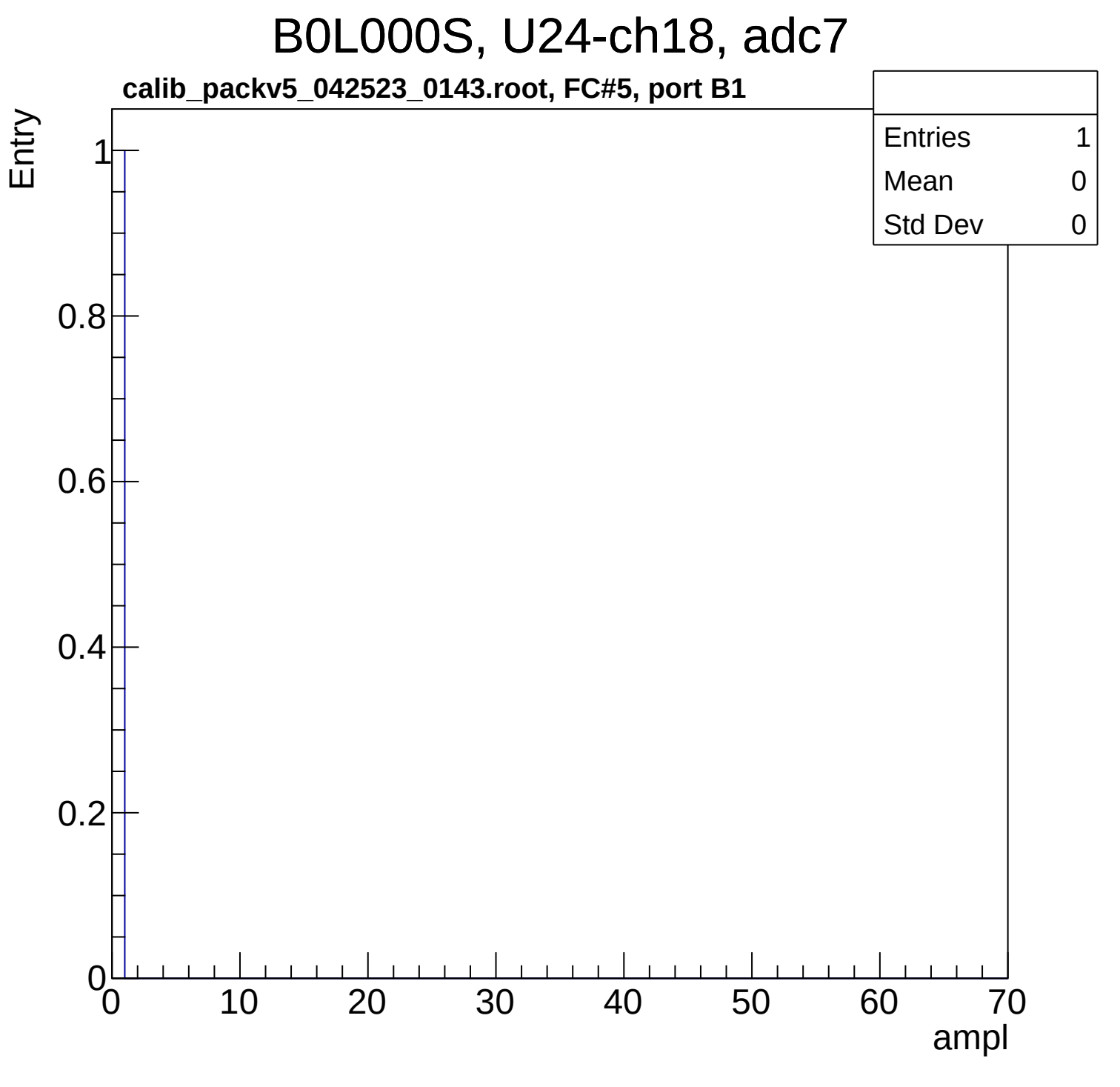

B0L000S, U24-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.35
Std Dev 10.88
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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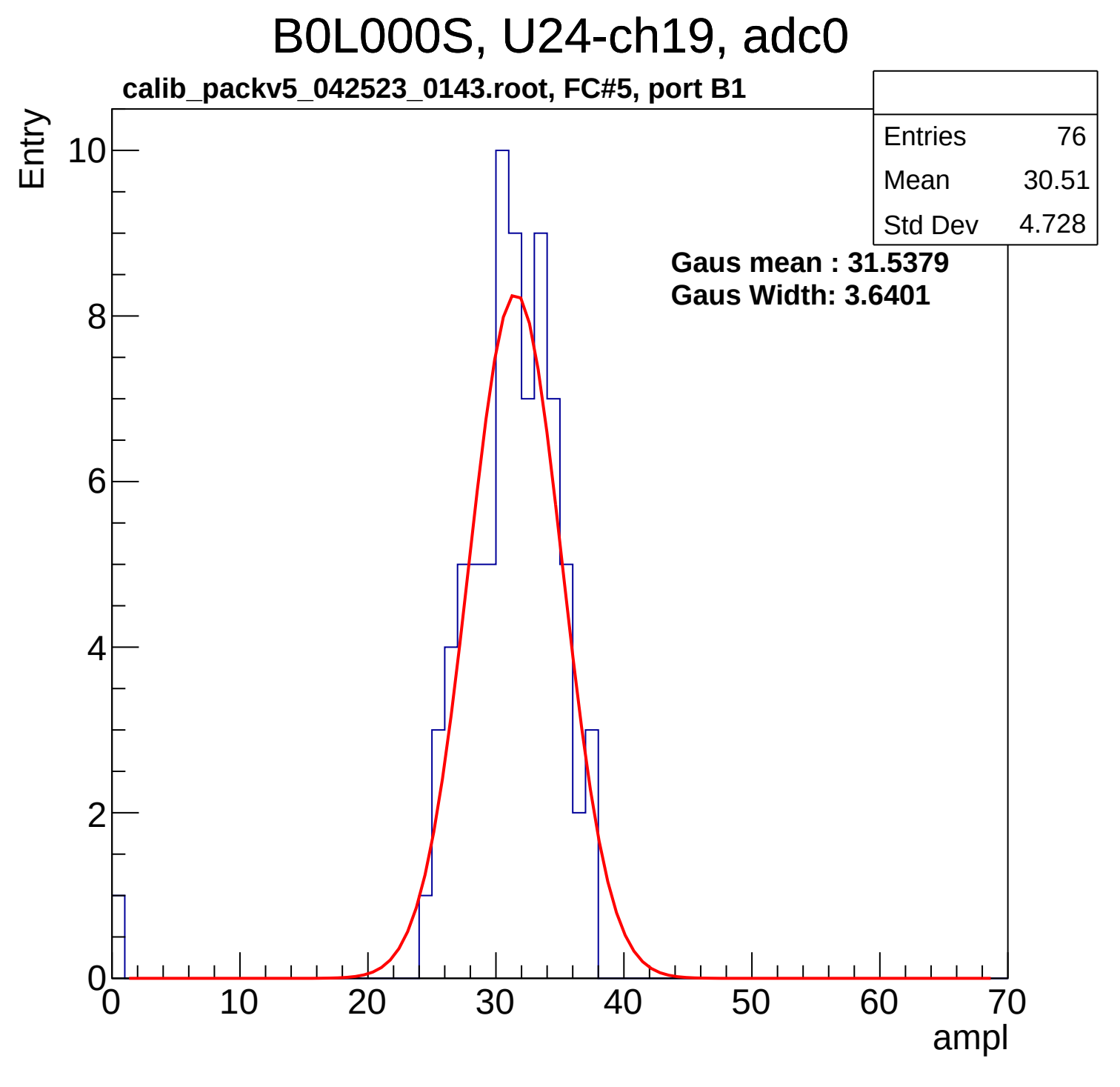
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.51
Std Dev 4.728
Gaus mean : 31.5379
Gaus Width: 3.6401
Entry
ampl
0
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4
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0
10
20
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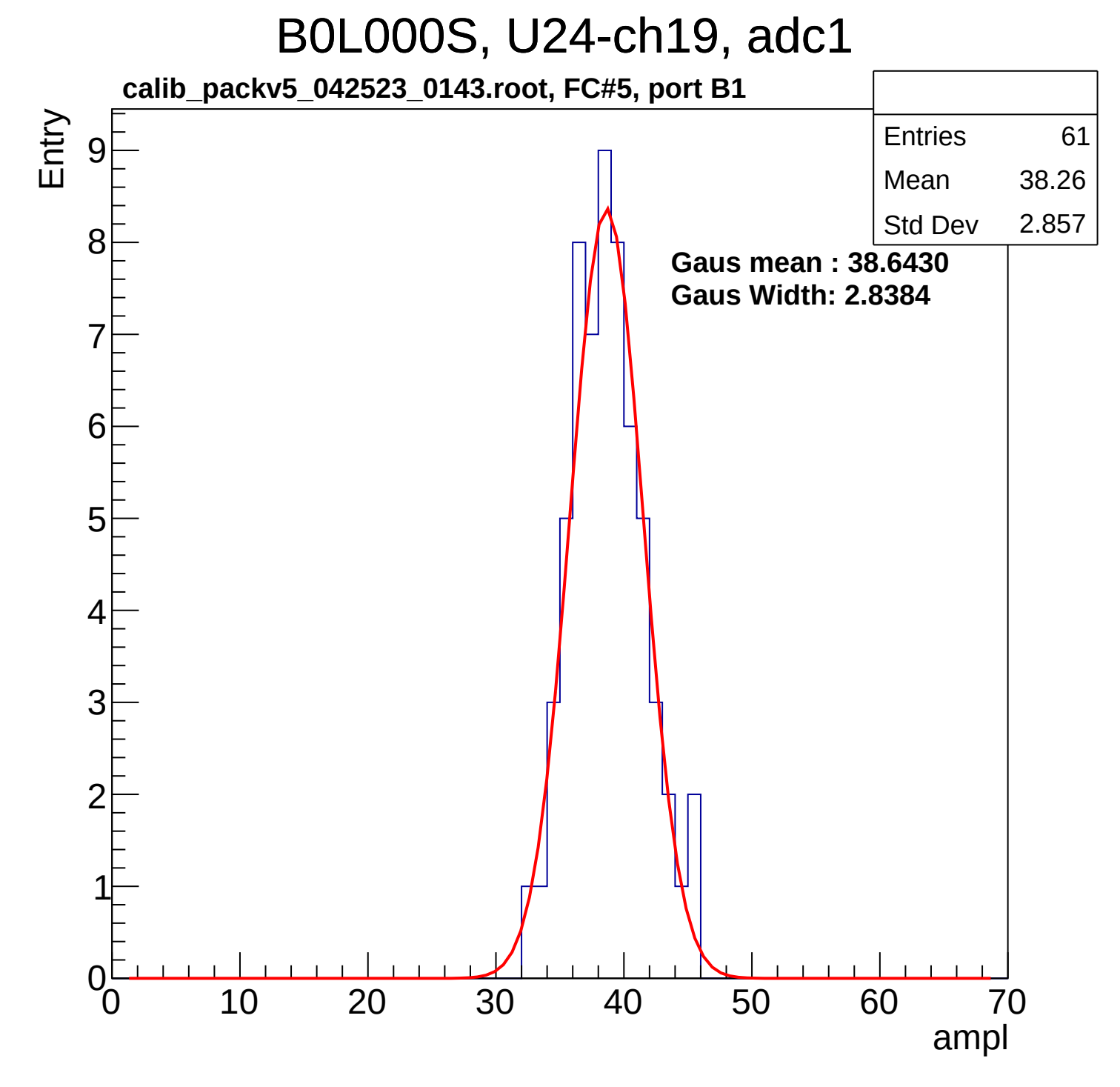
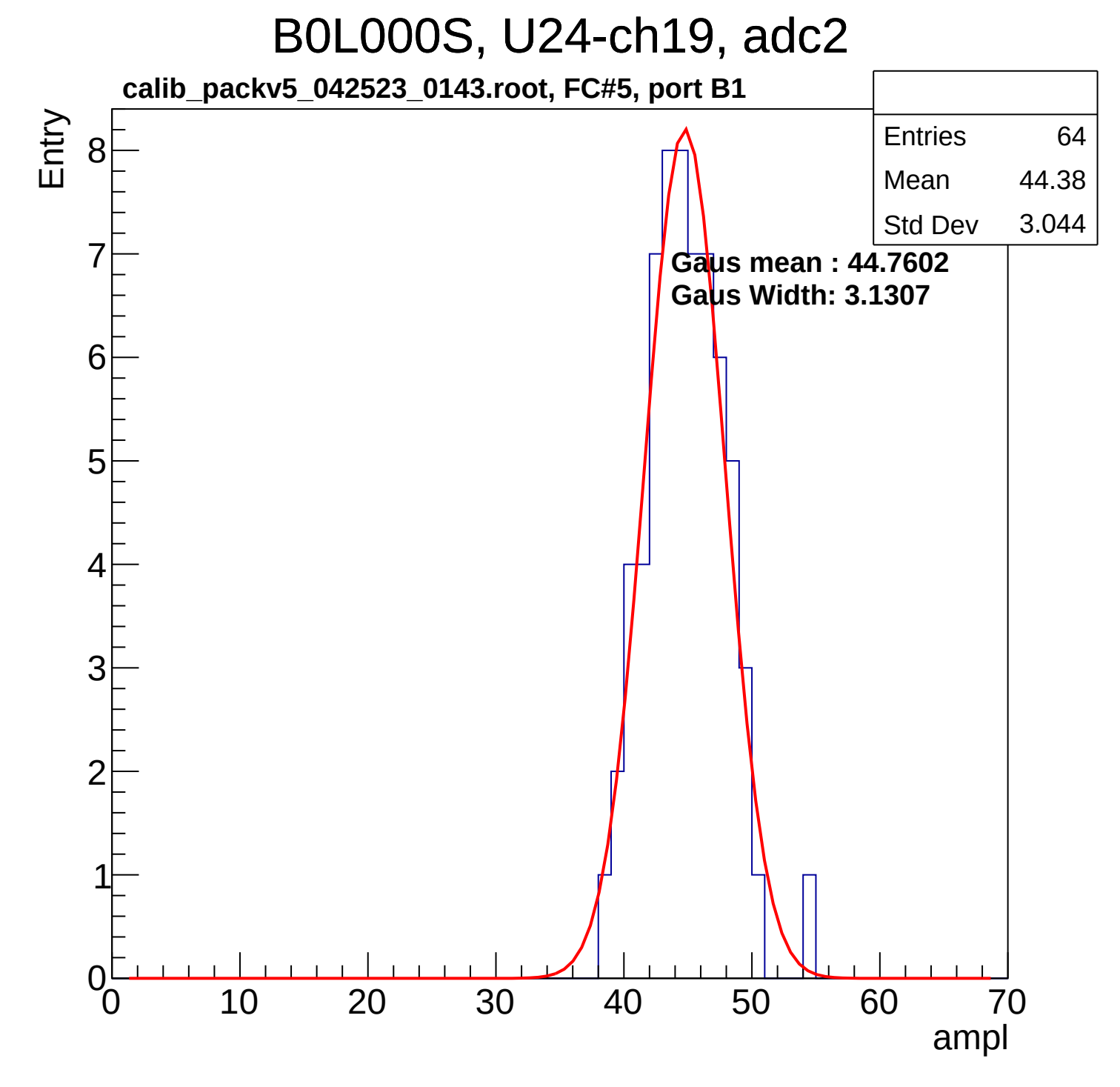
40
50
60
70


B0L000S, U24-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 38.26
Std Dev 2.857
Gaus mean : 38.6430
Gaus Width: 2.8384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.38
Std Dev 3.044
Gaus mean : 44.7602
Gaus Width: 3.1307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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30
40
50
60
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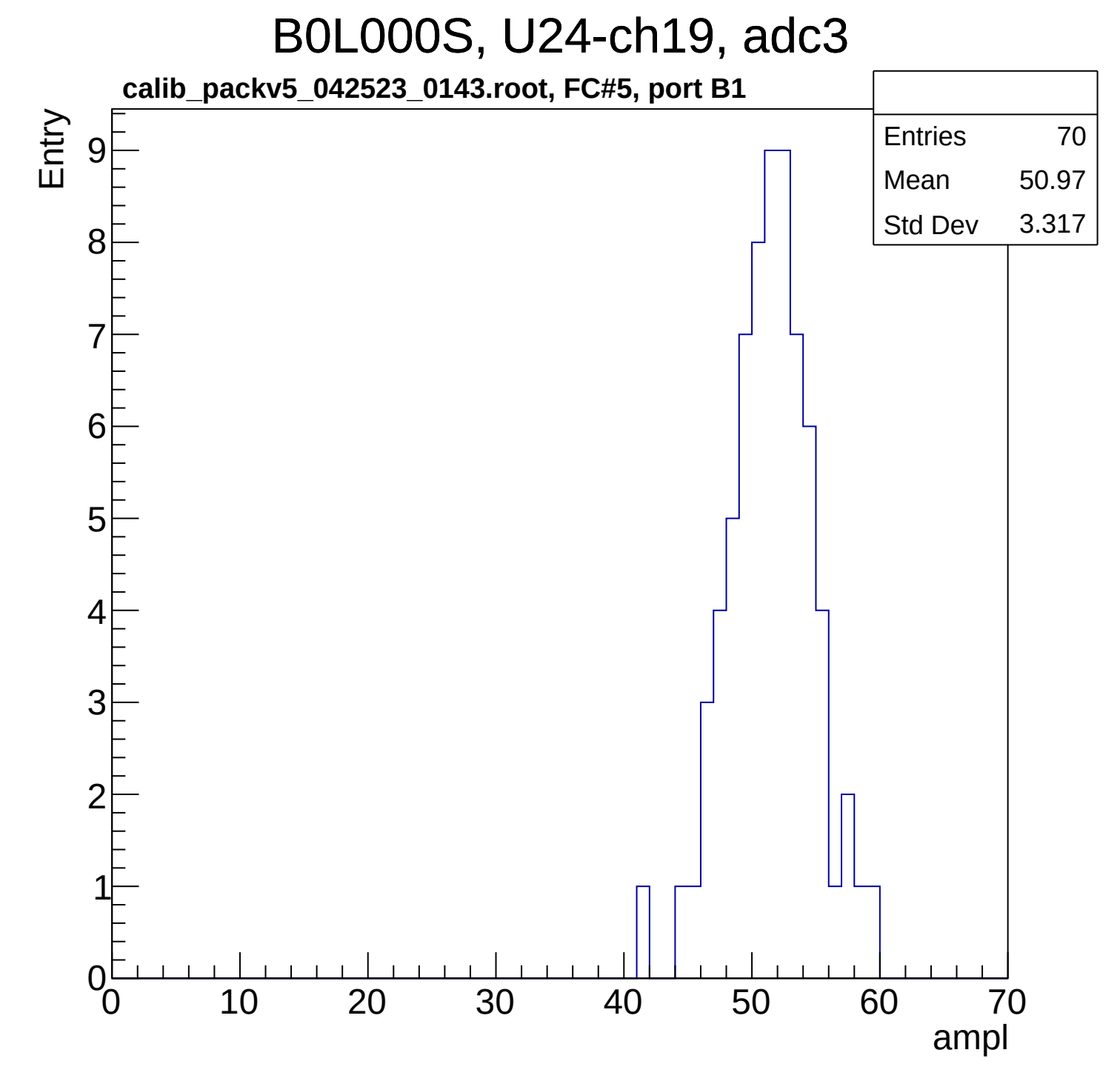
B0L000S, U24-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 50.97
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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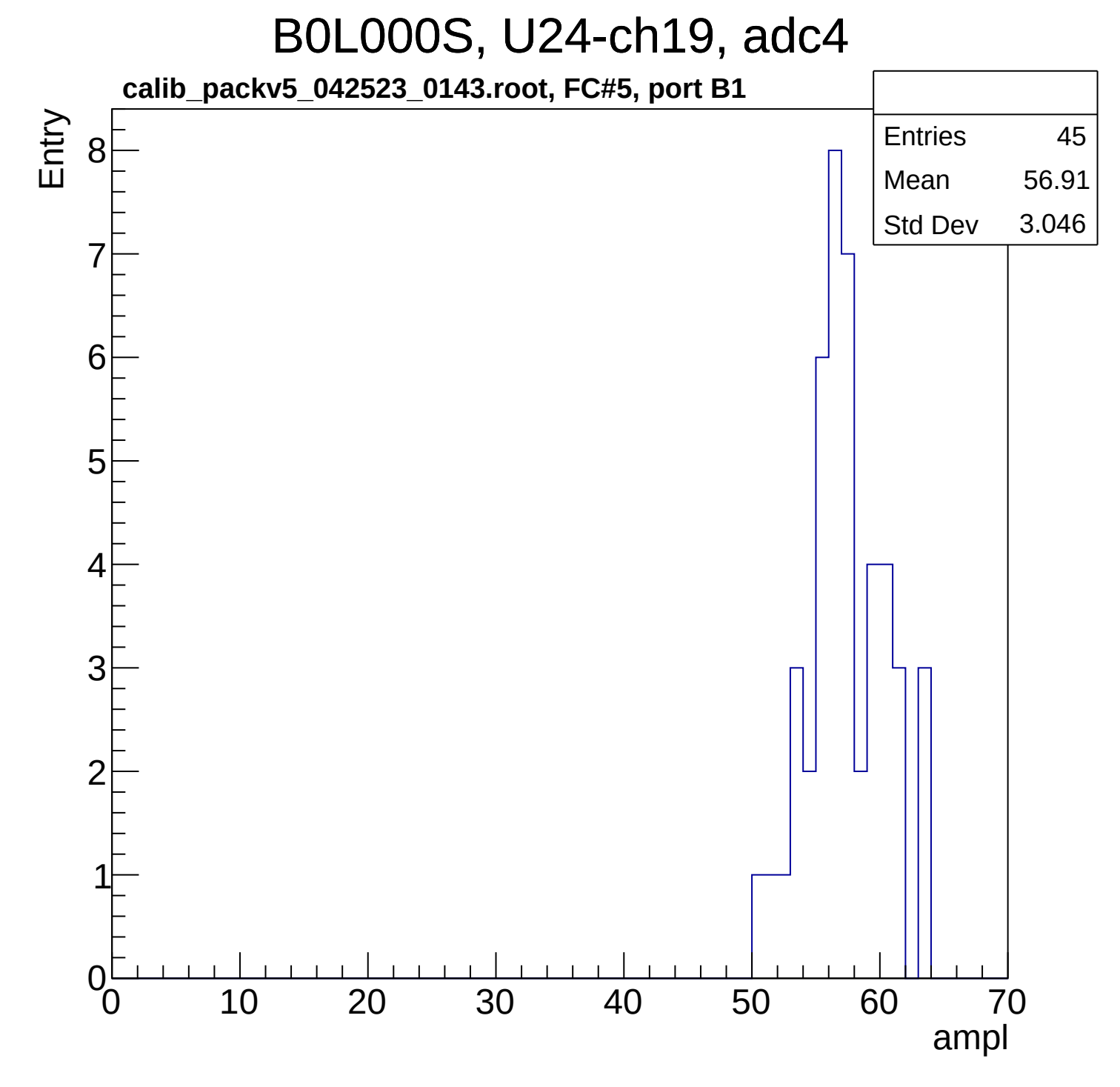
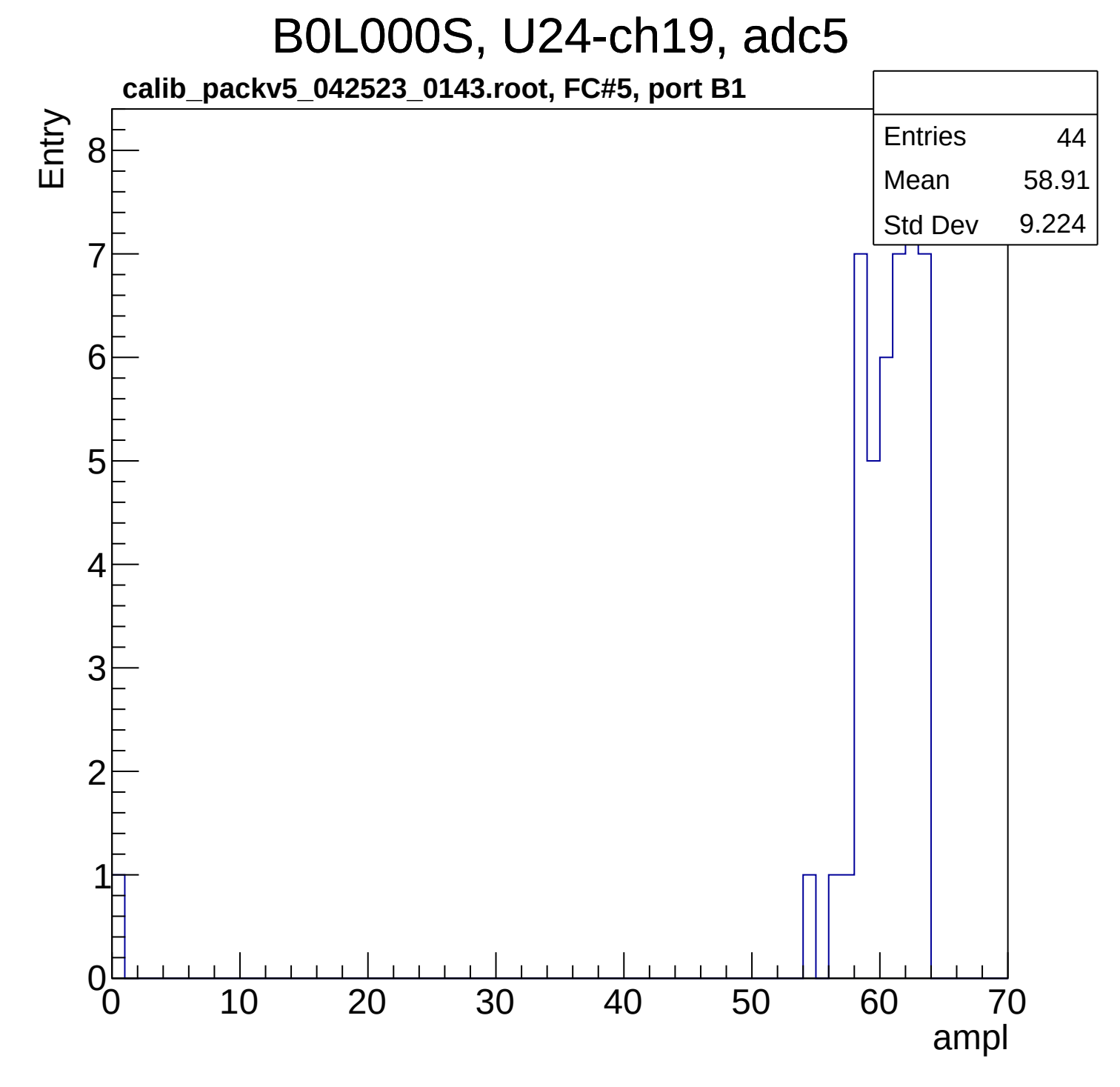

B0L000S, U24-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.91
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.91
Std Dev 9.224
Entry
ampl
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3
4
5
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0
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60
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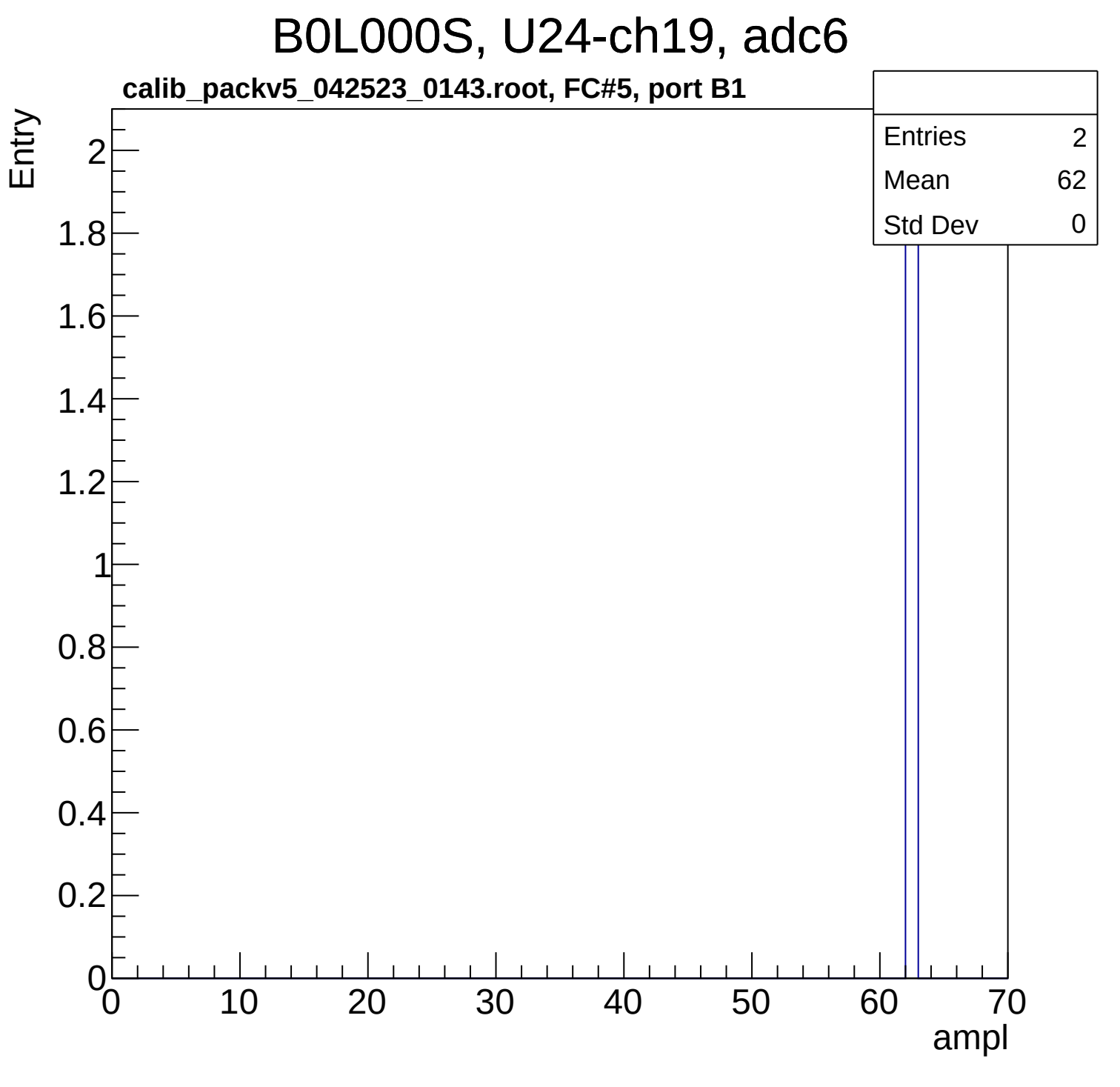
B0L000S, U24-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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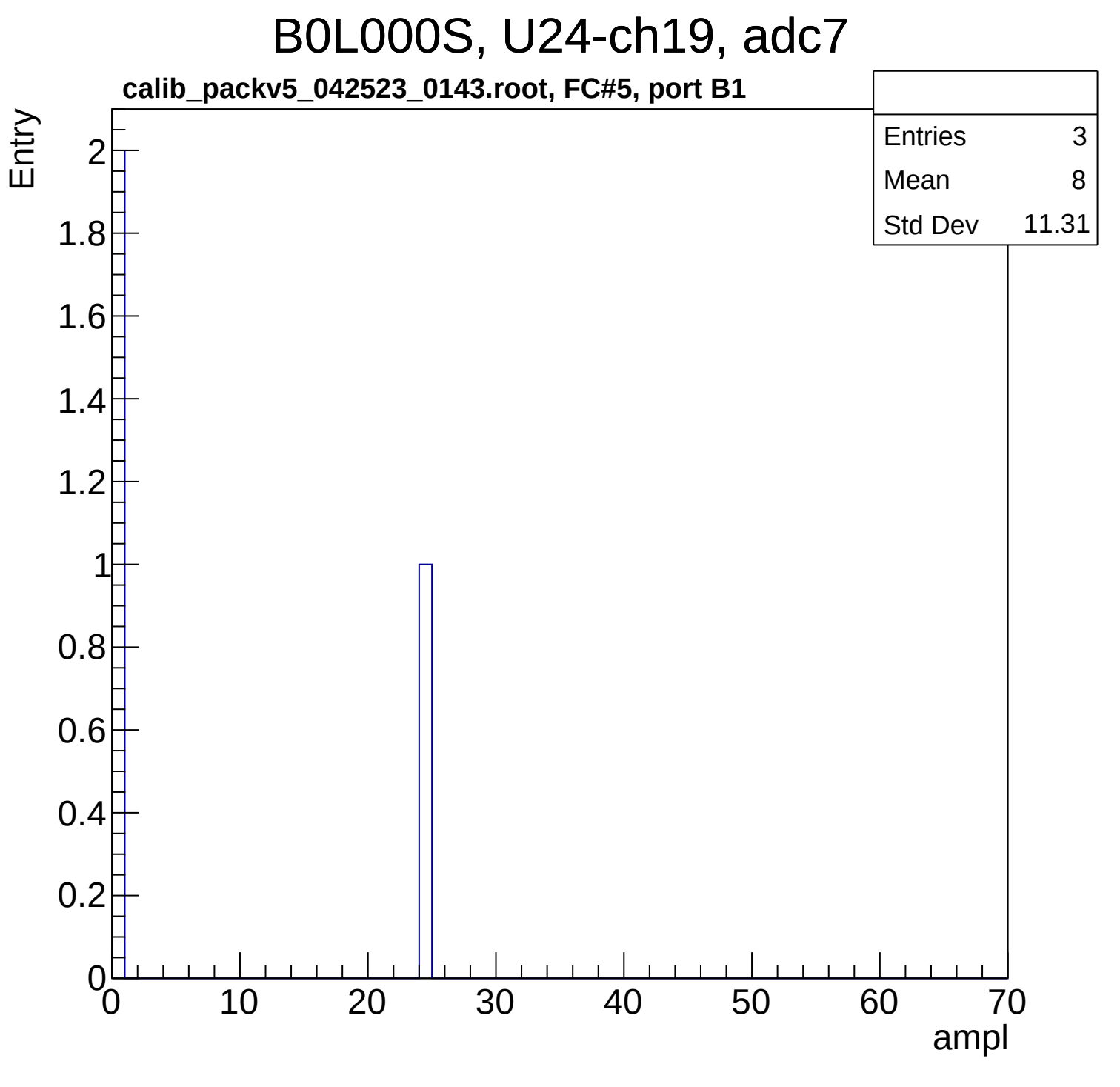
B0L000S, U24-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
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60
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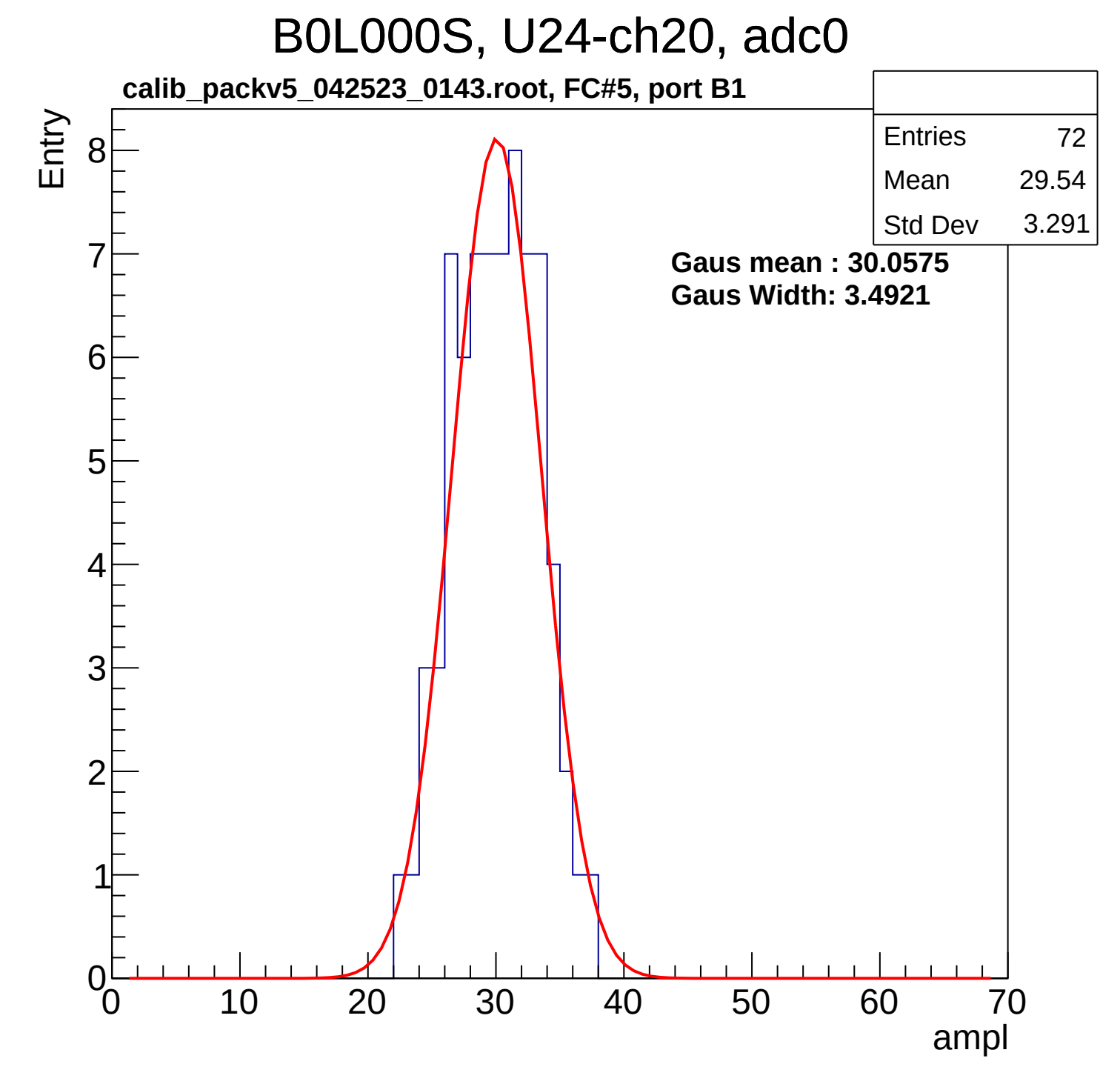
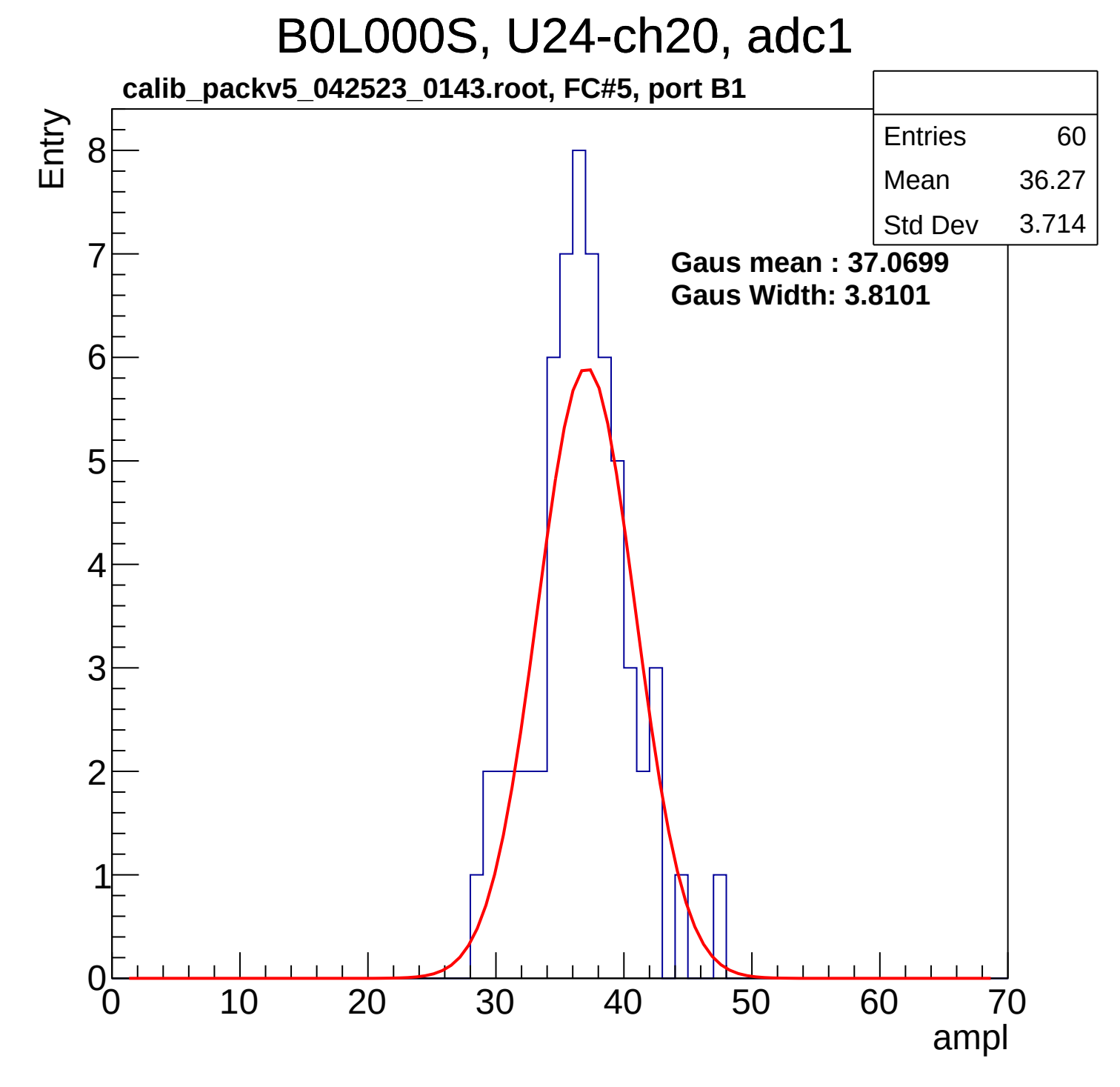

B0L000S, U24-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.54
Std Dev 3.291
Gaus mean : 30.0575
Gaus Width: 3.4921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.27
Std Dev 3.714
Gaus mean : 37.0699
Gaus Width: 3.8101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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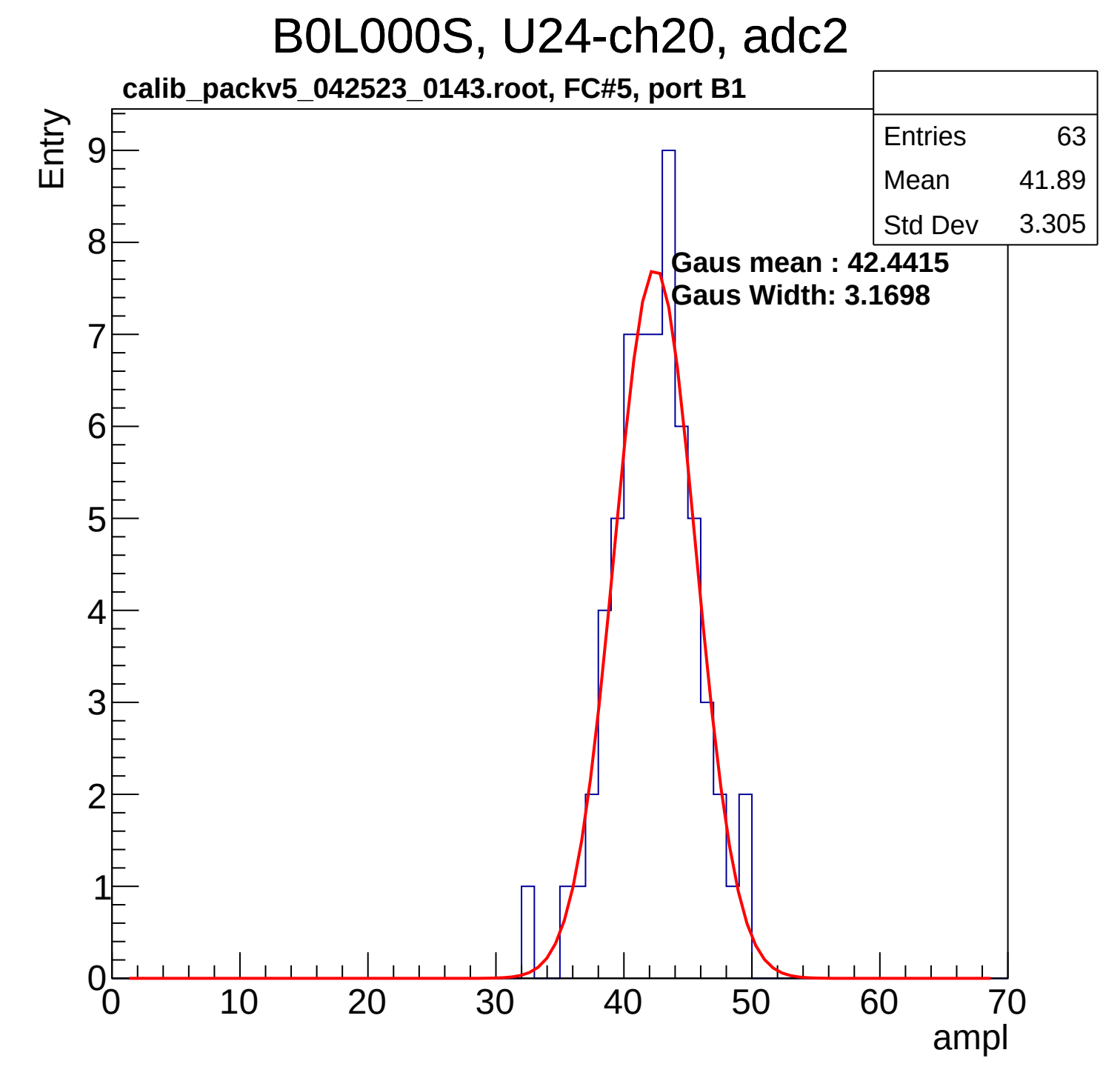
B0L000S, U24-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.89
Std Dev 3.305
Gaus mean : 42.4415
Gaus Width: 3.1698
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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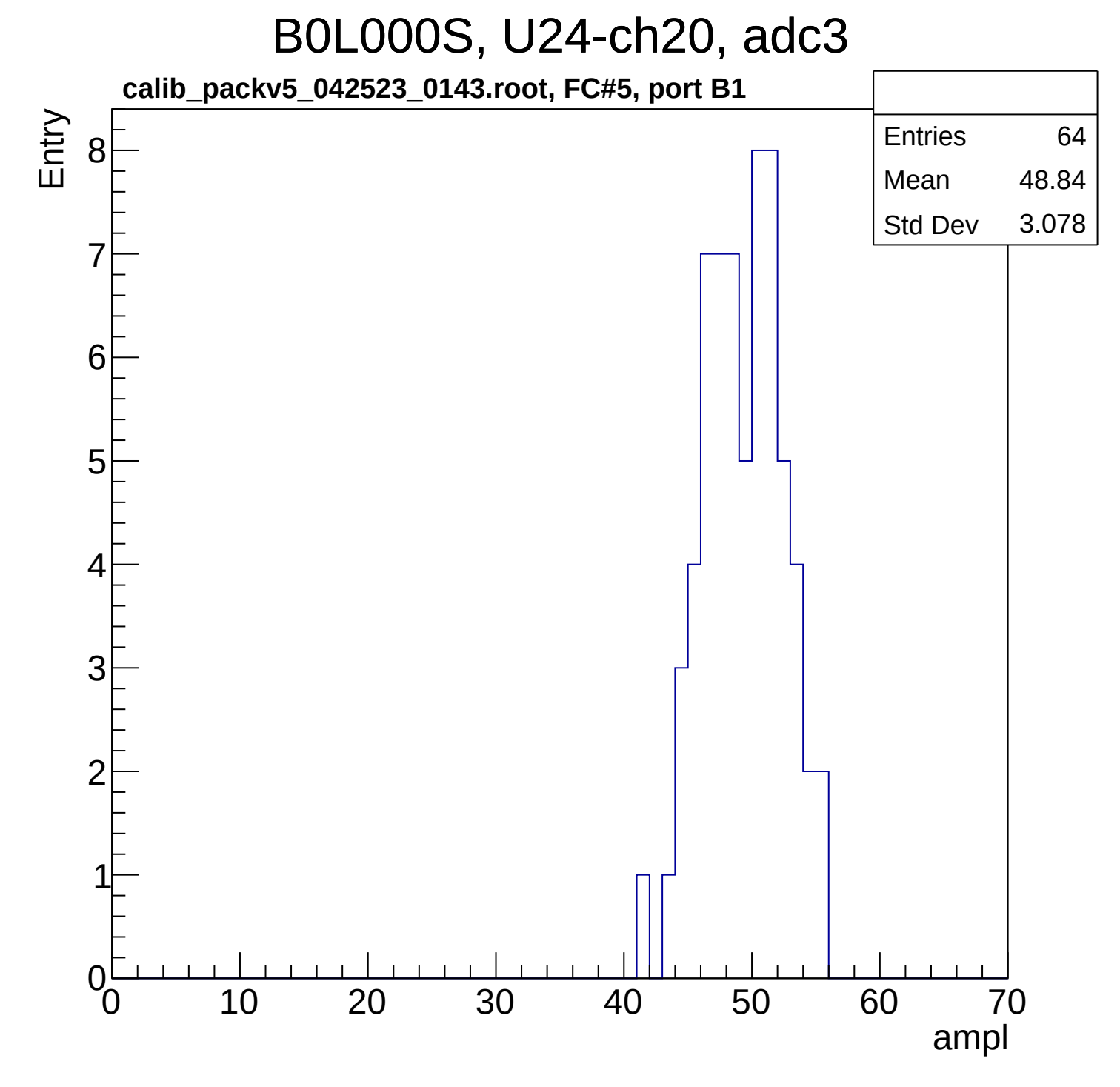
B0L000S, U24-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 48.84
Std Dev 3.078
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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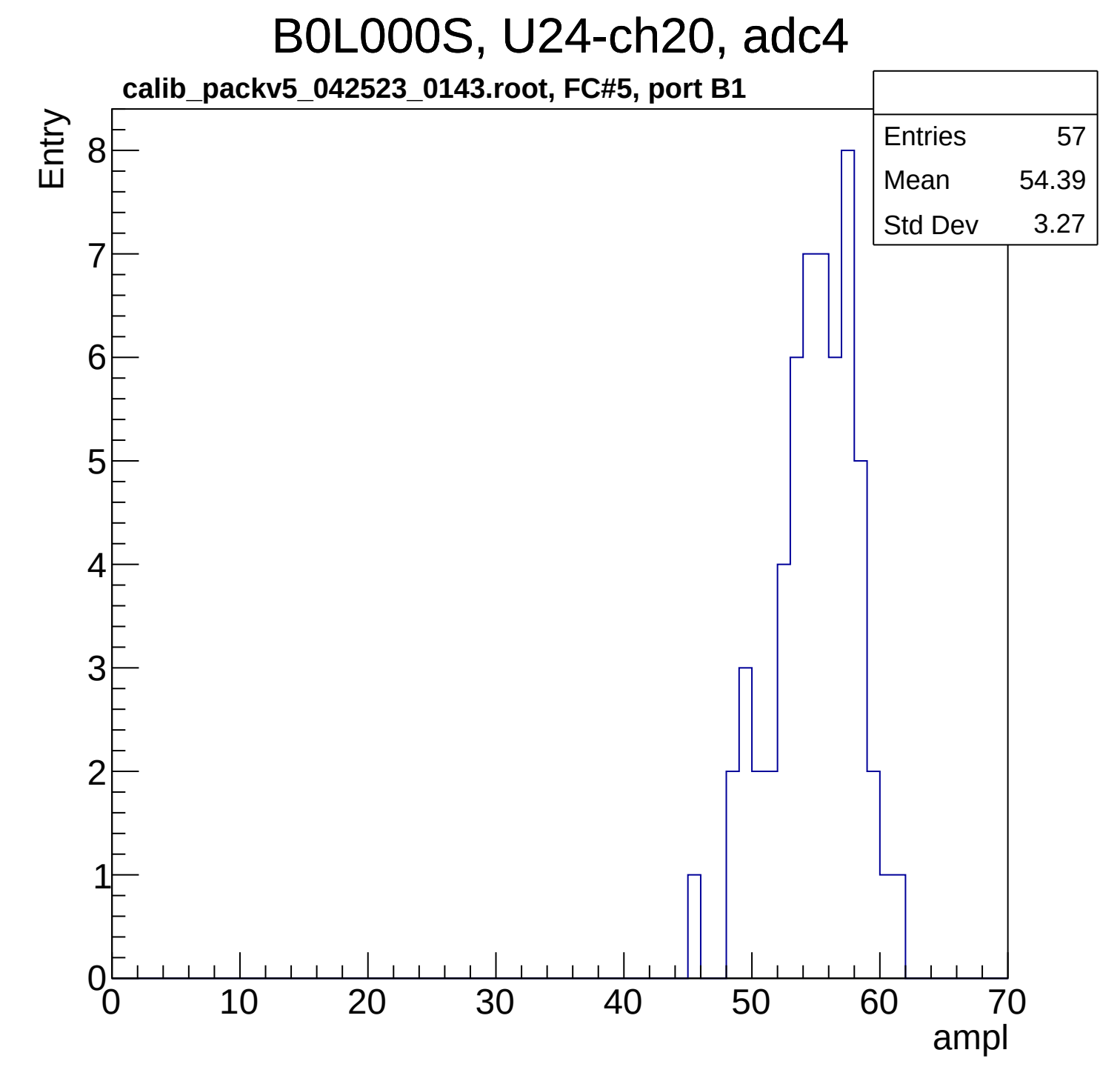
B0L000S, U24-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.39
Std Dev 3.27
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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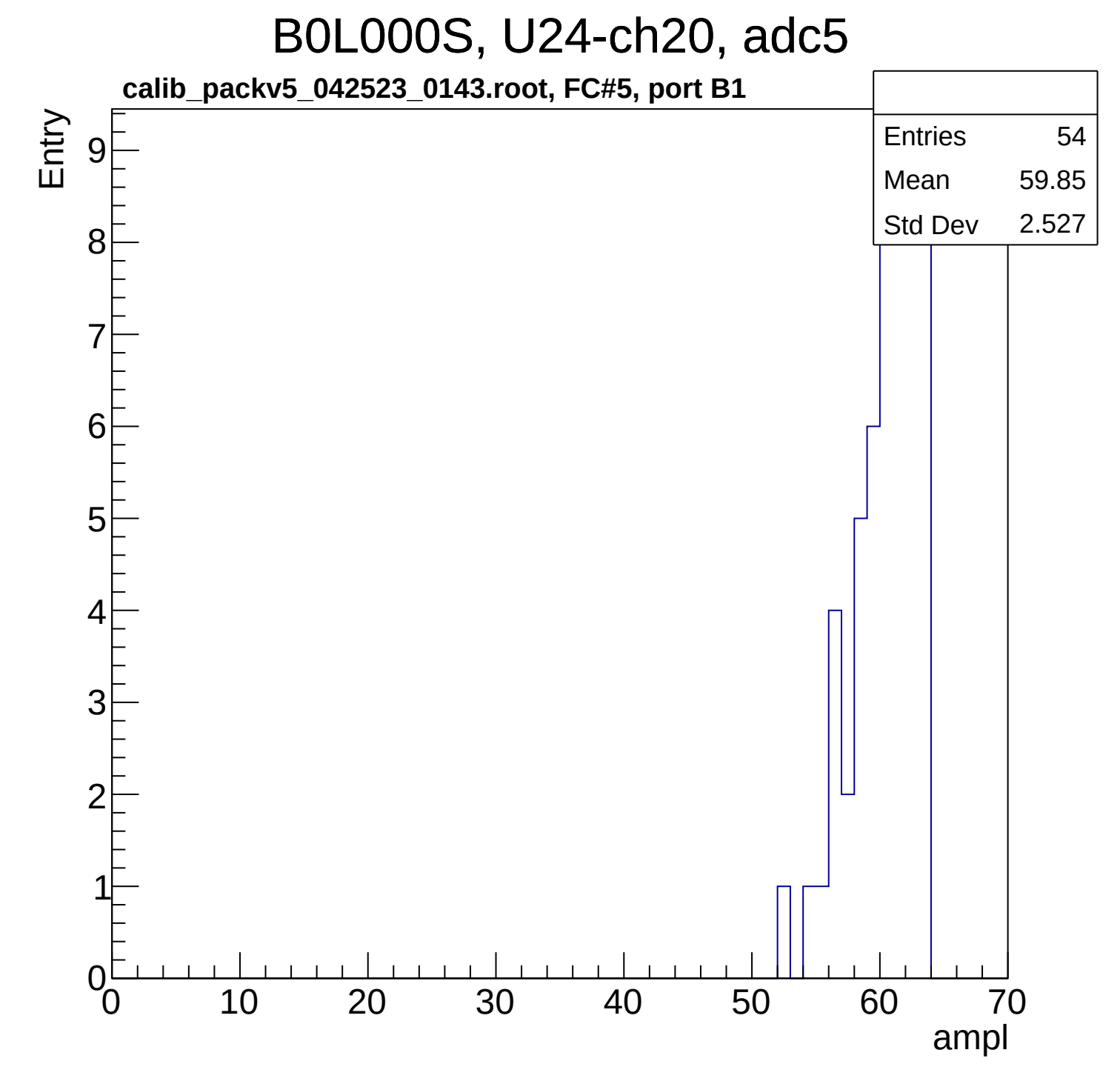
B0L000S, U24-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.85
Std Dev 2.527
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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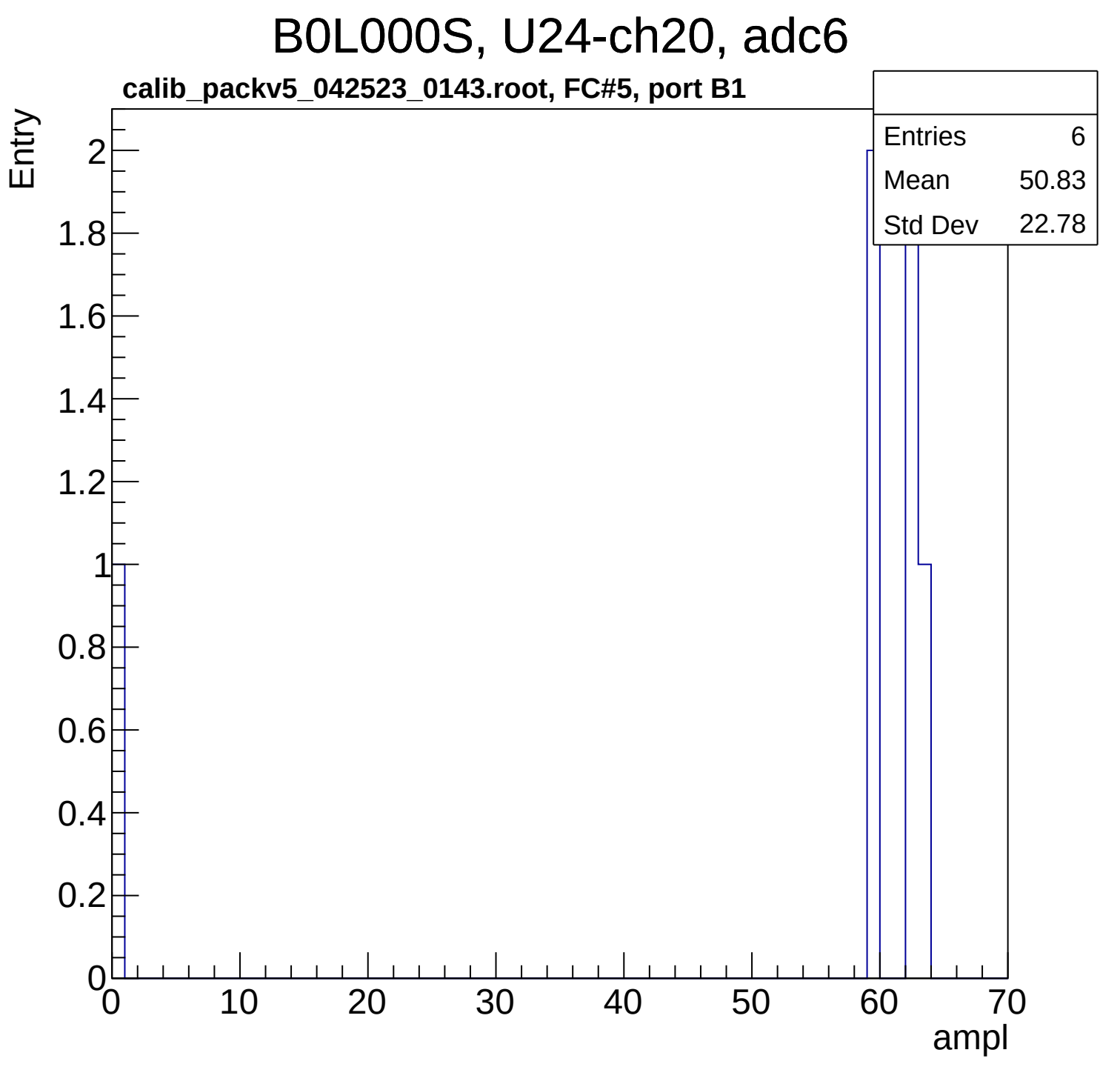
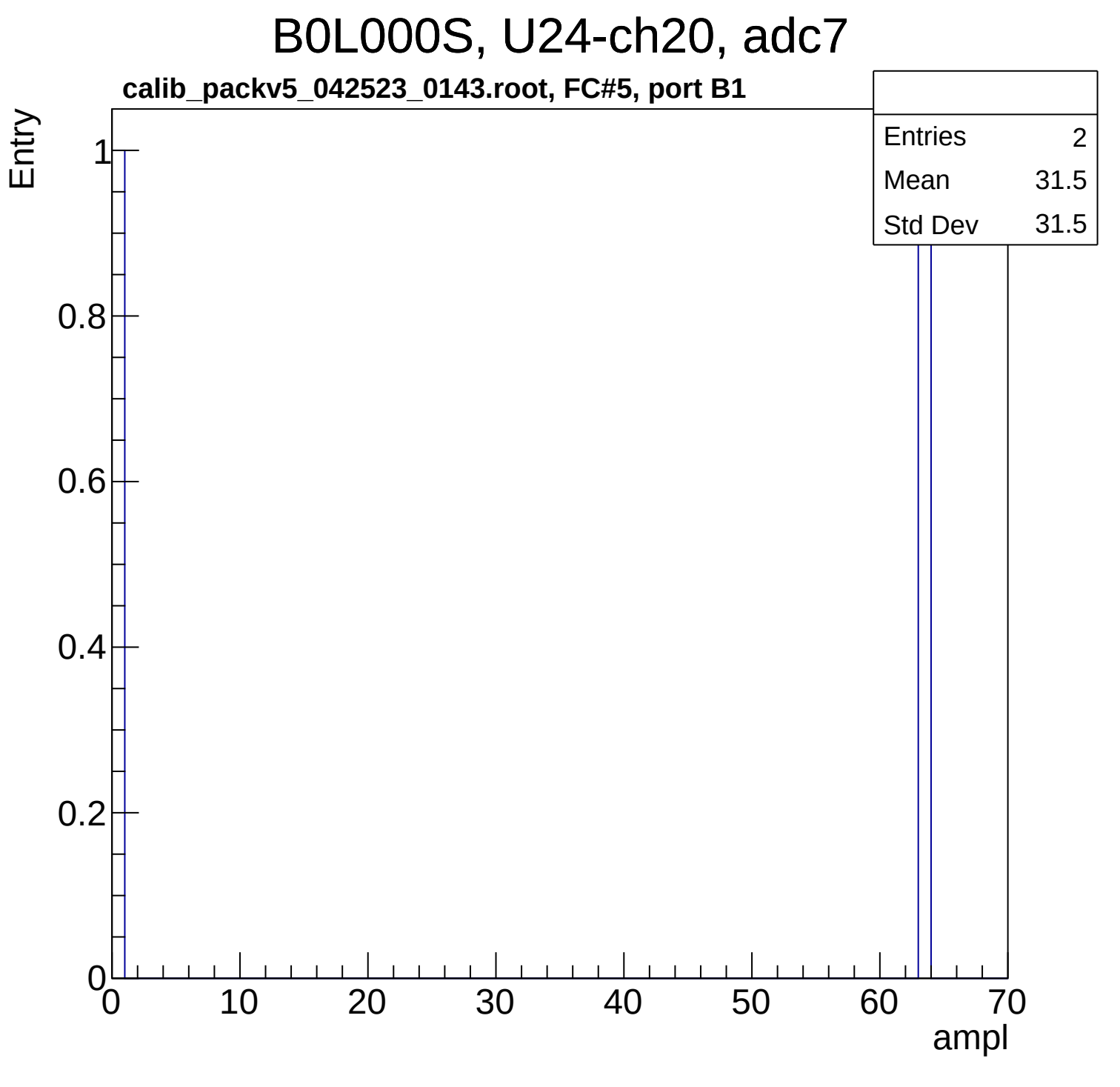
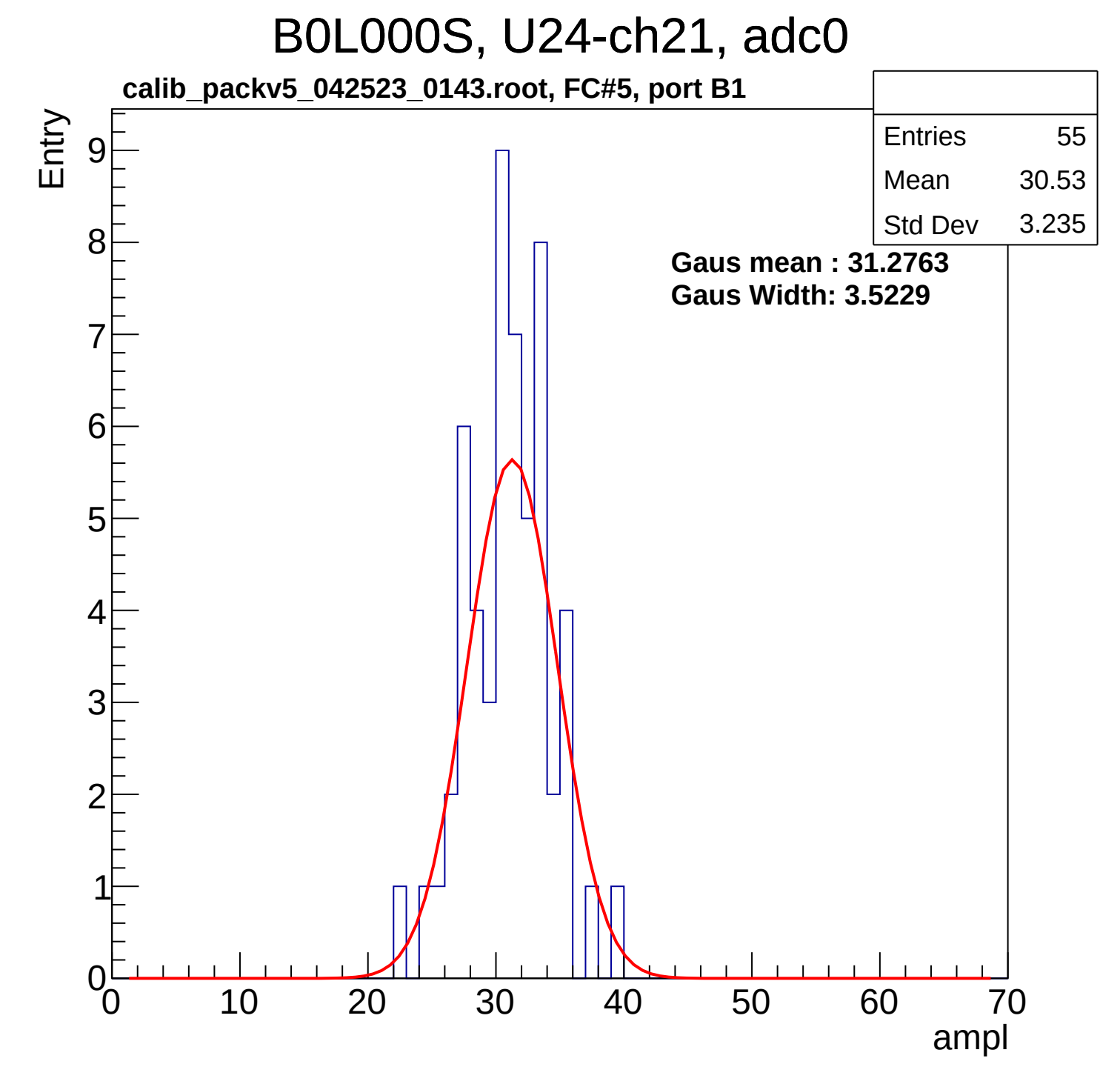

B0L000S, U24-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 50.83
Std Dev 22.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.53
Std Dev 3.235
Gaus mean : 31.2763
Gaus Width: 3.5229
Entry
ampl
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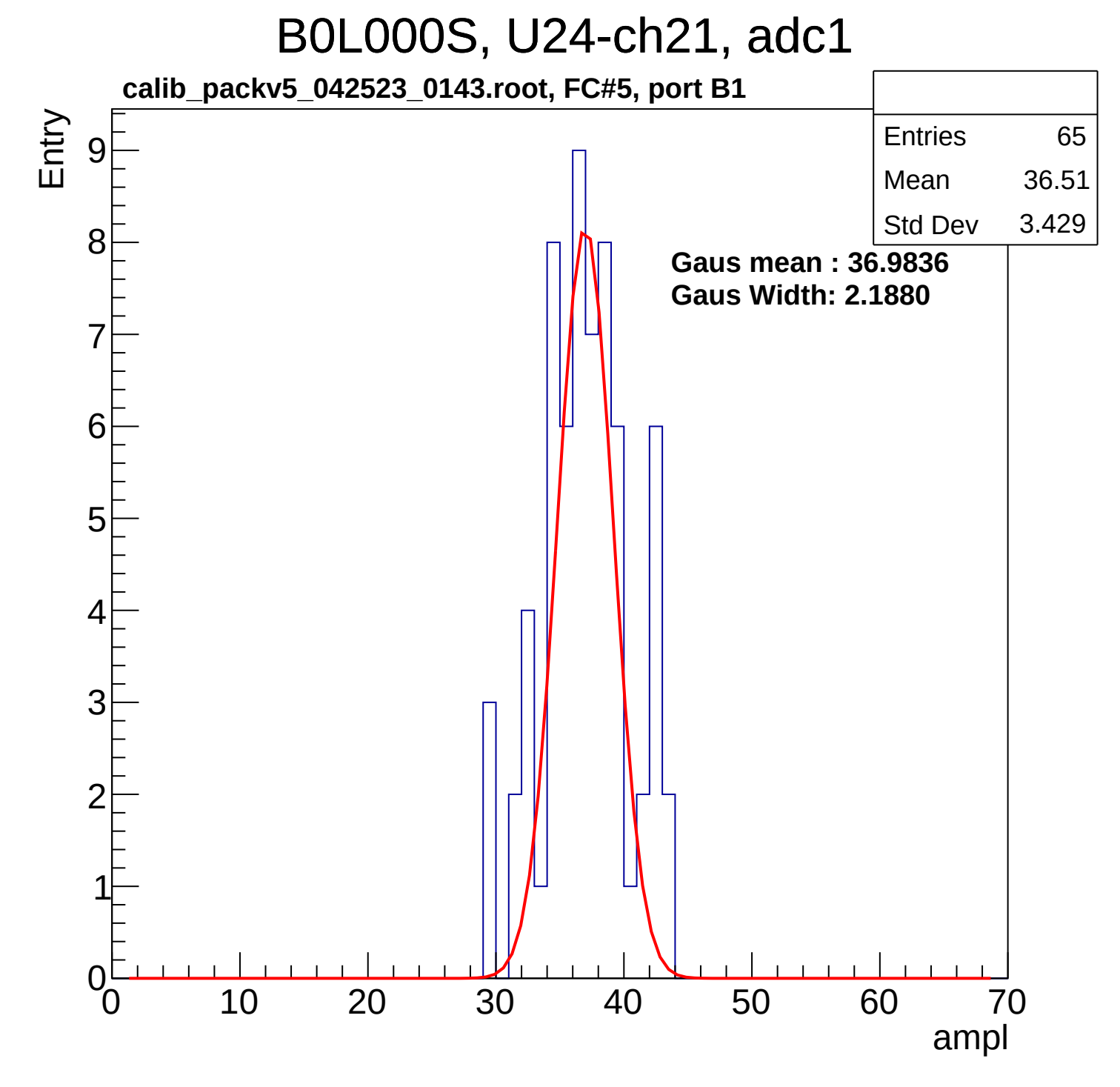
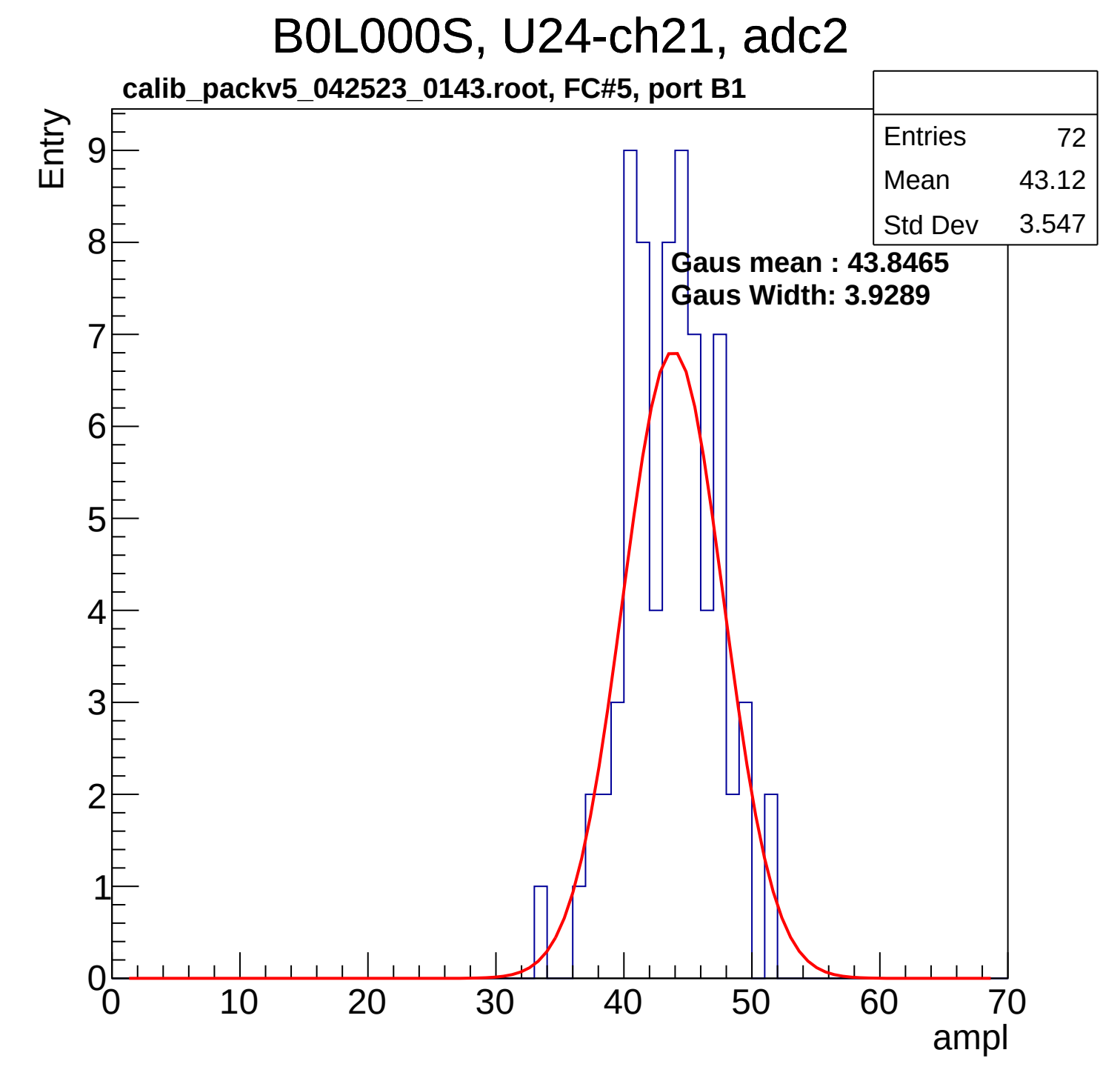
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.51
Std Dev 3.429
Gaus mean : 36.9836
Gaus Width: 2.1880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.12
Std Dev 3.547
Gaus mean : 43.8465
Gaus Width: 3.9289
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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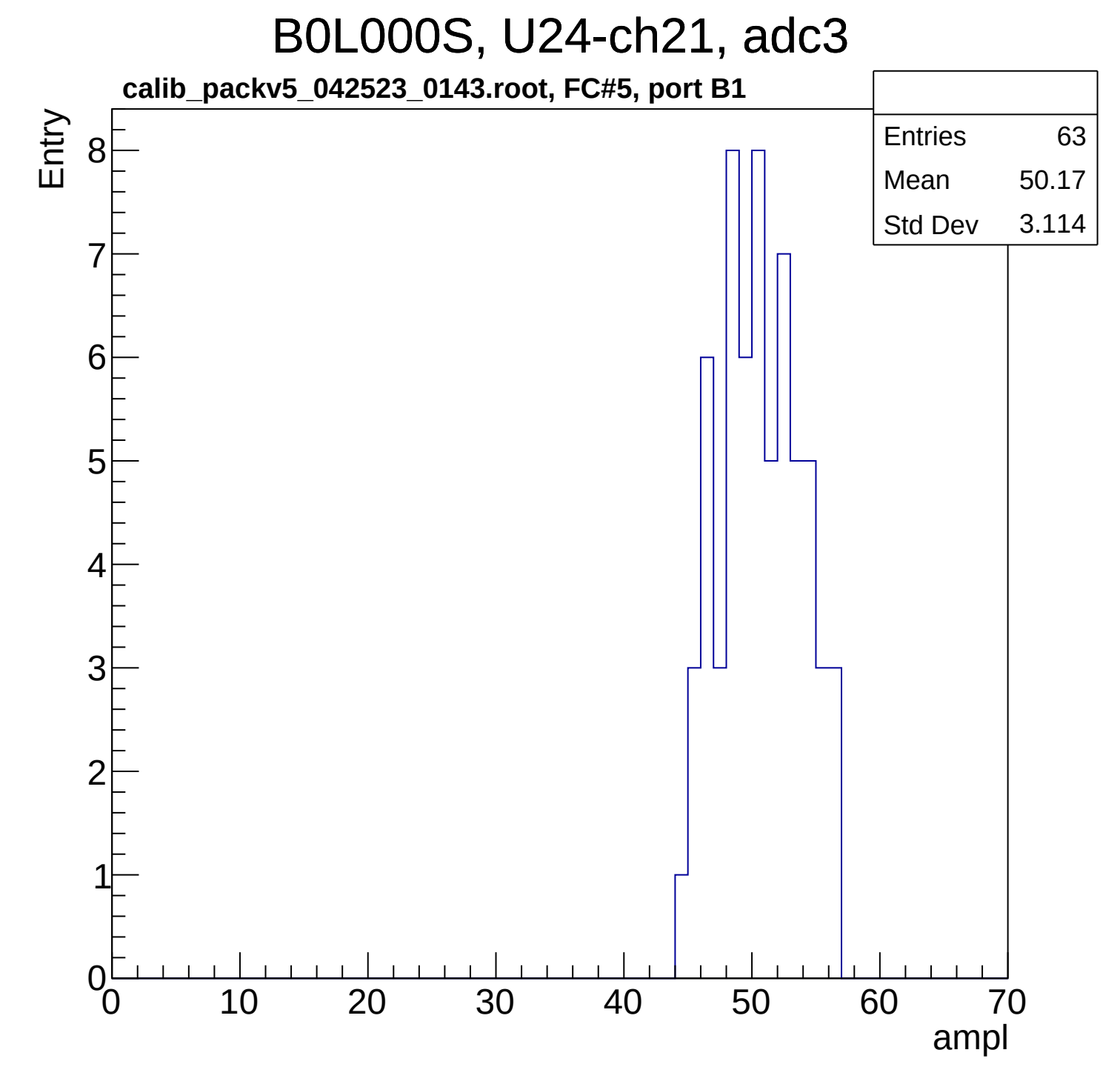
B0L000S, U24-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 50.17
Std Dev 3.114
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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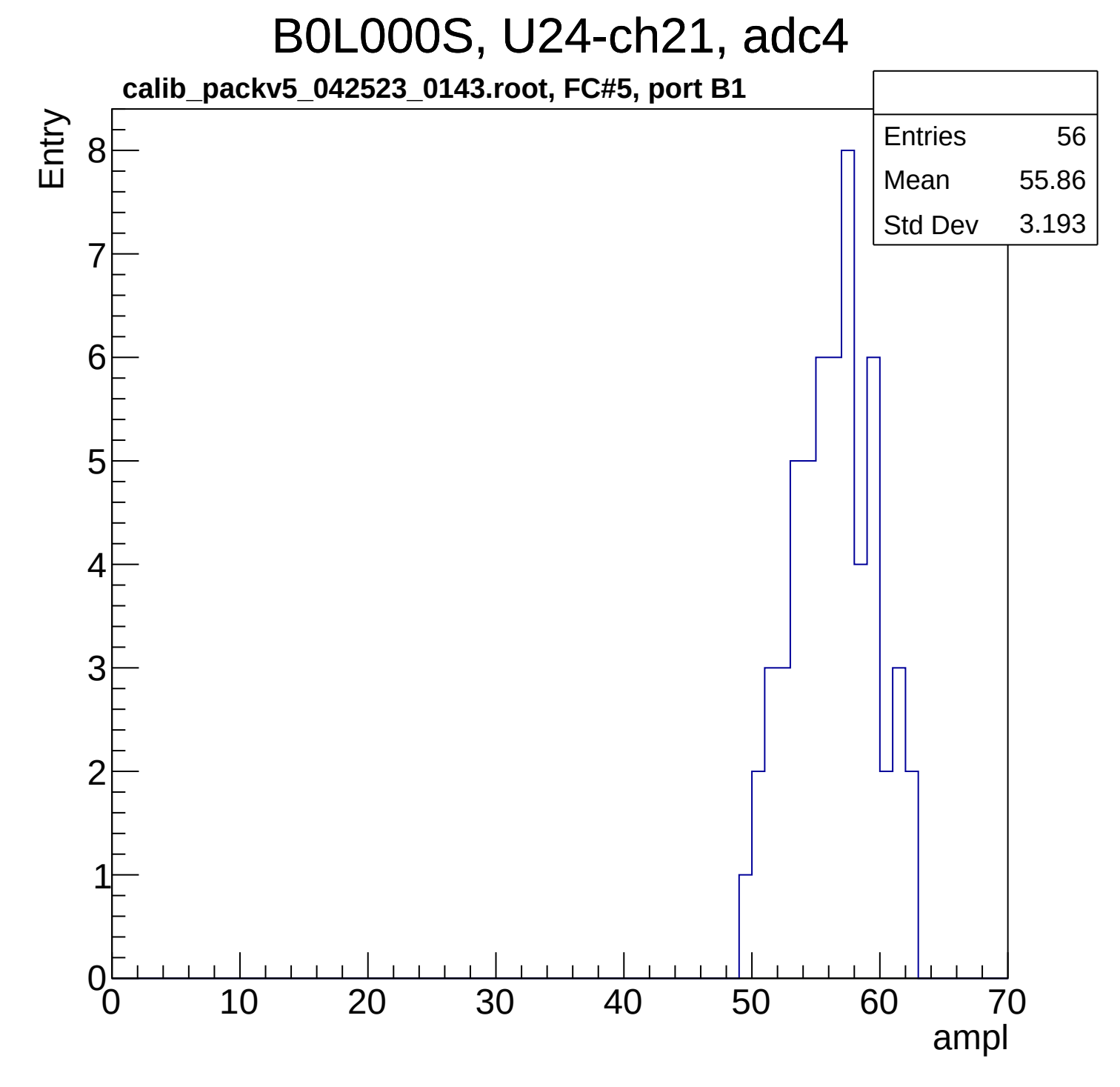
B0L000S, U24-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.86
Std Dev 3.193
Entry
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2
3
4
5
6
7
8
0
10
20
30
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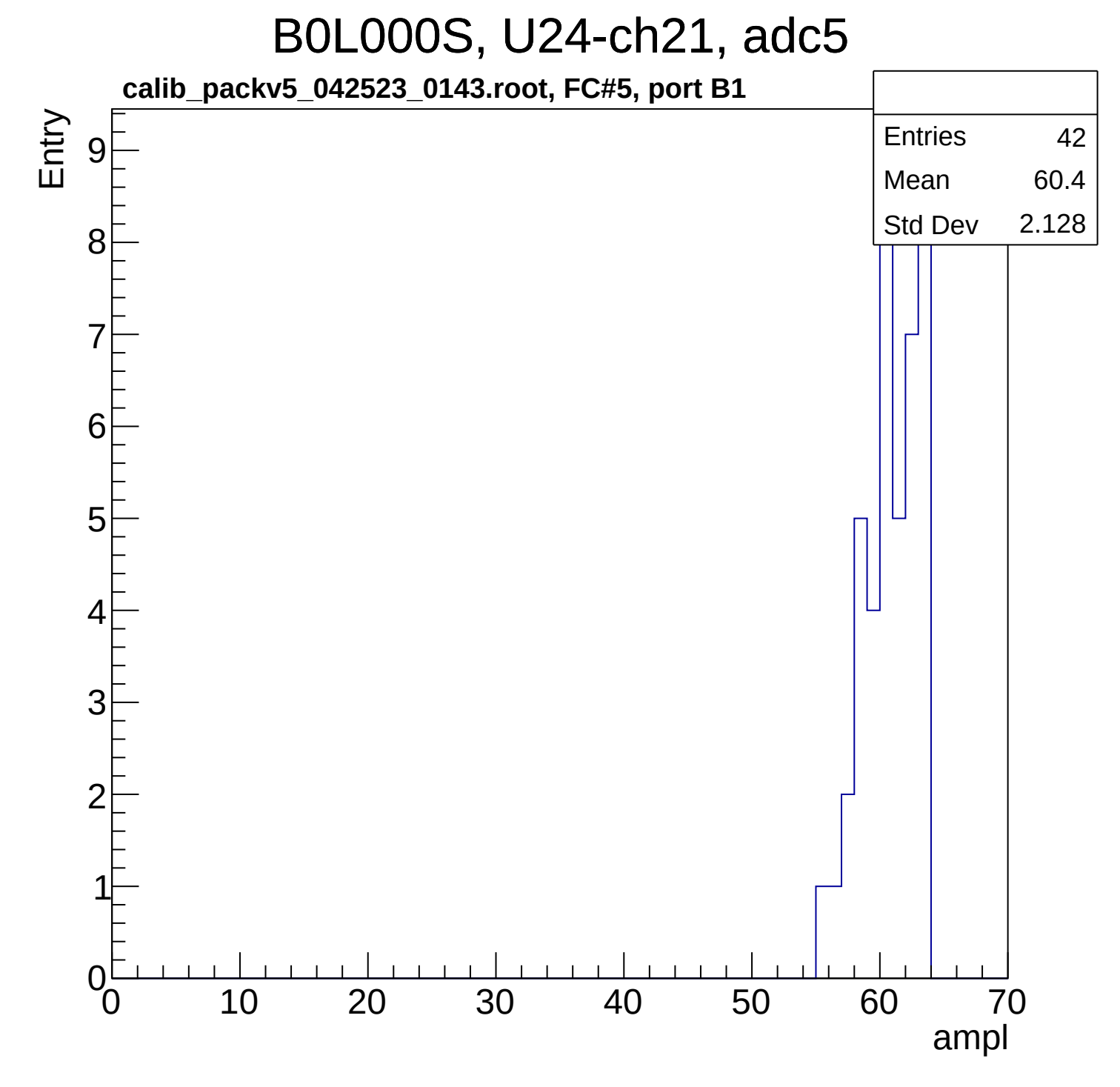
50
60
70
ampl


B0L000S, U24-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 60.4
Std Dev 2.128
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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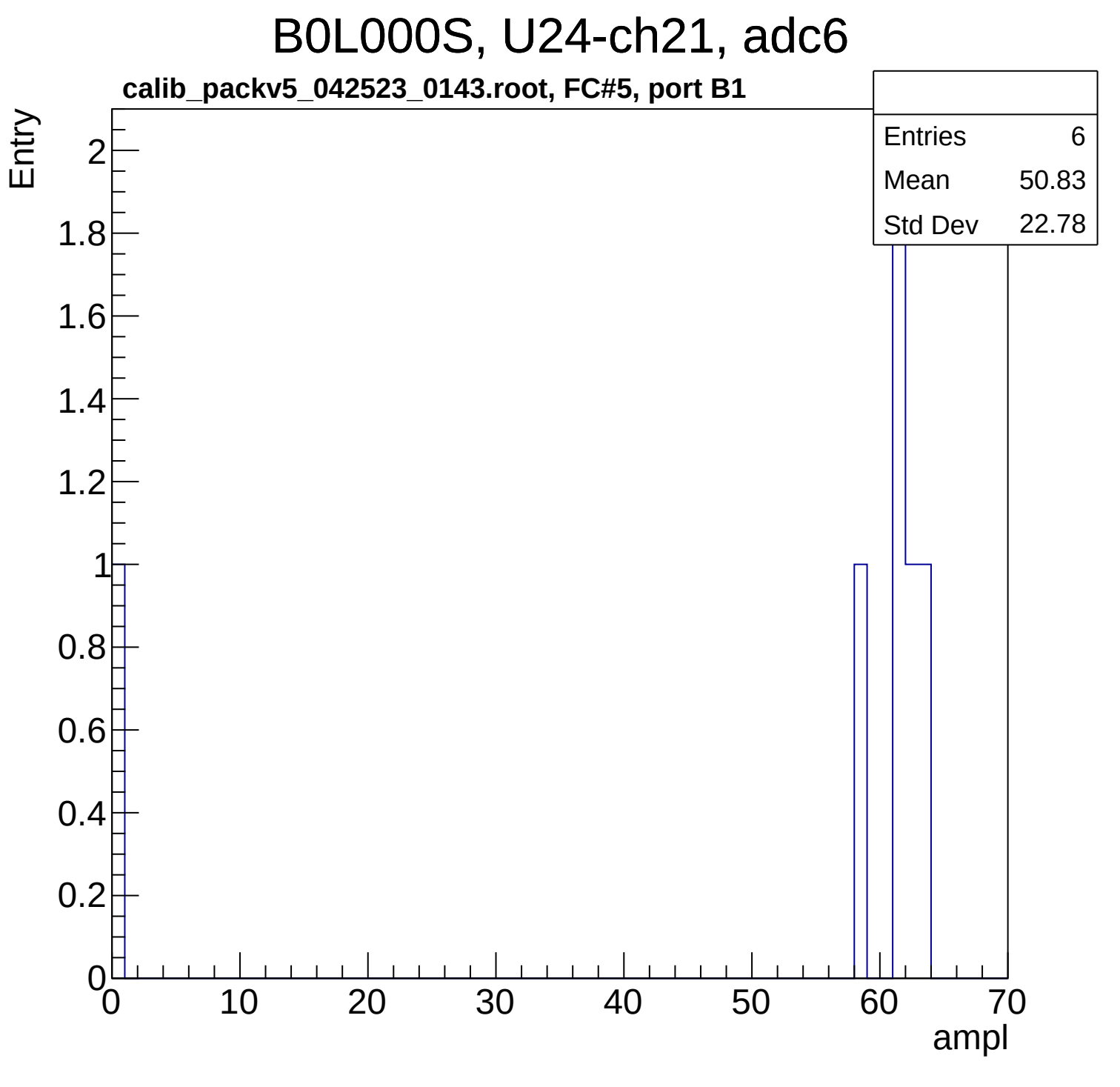
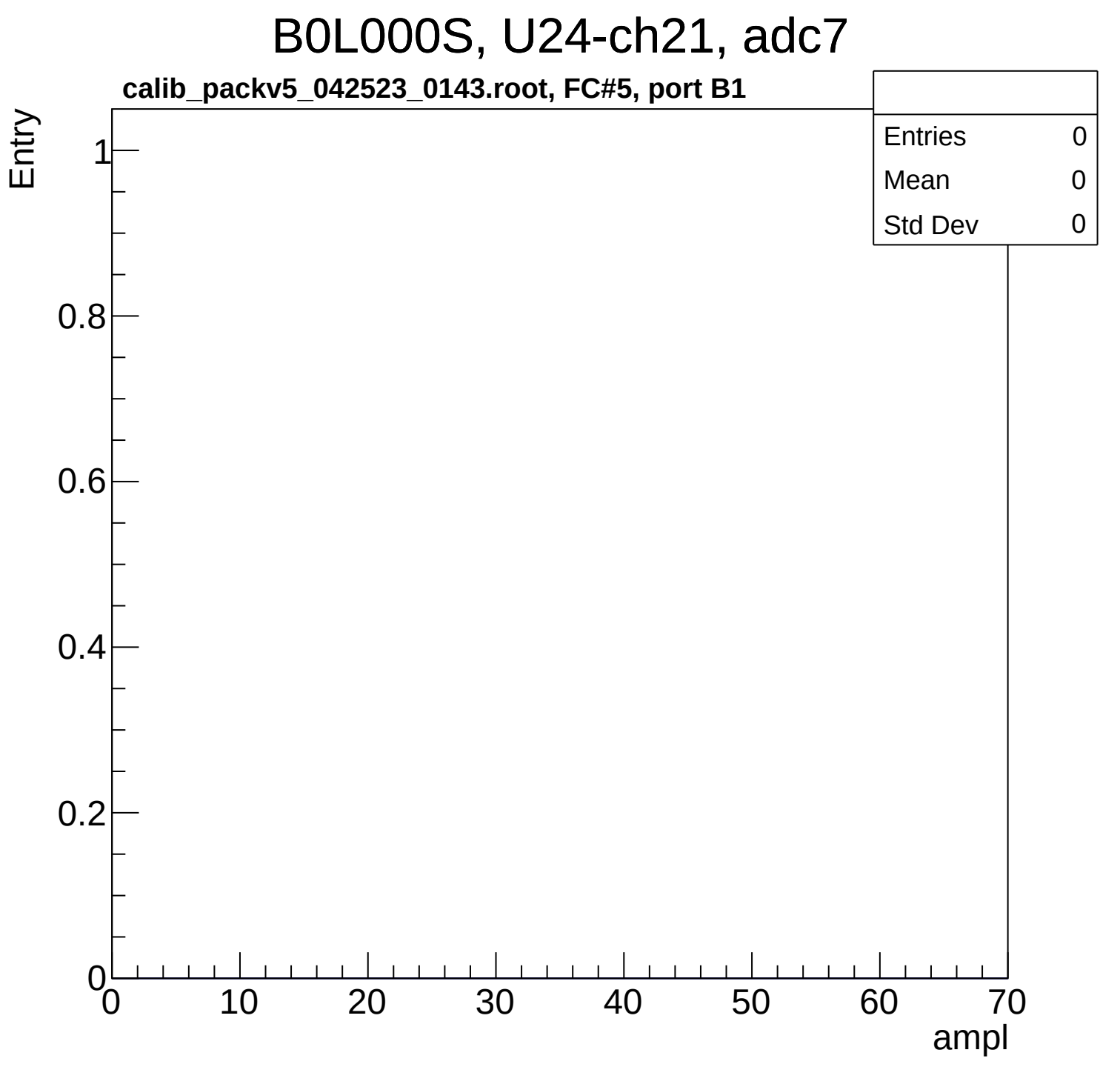
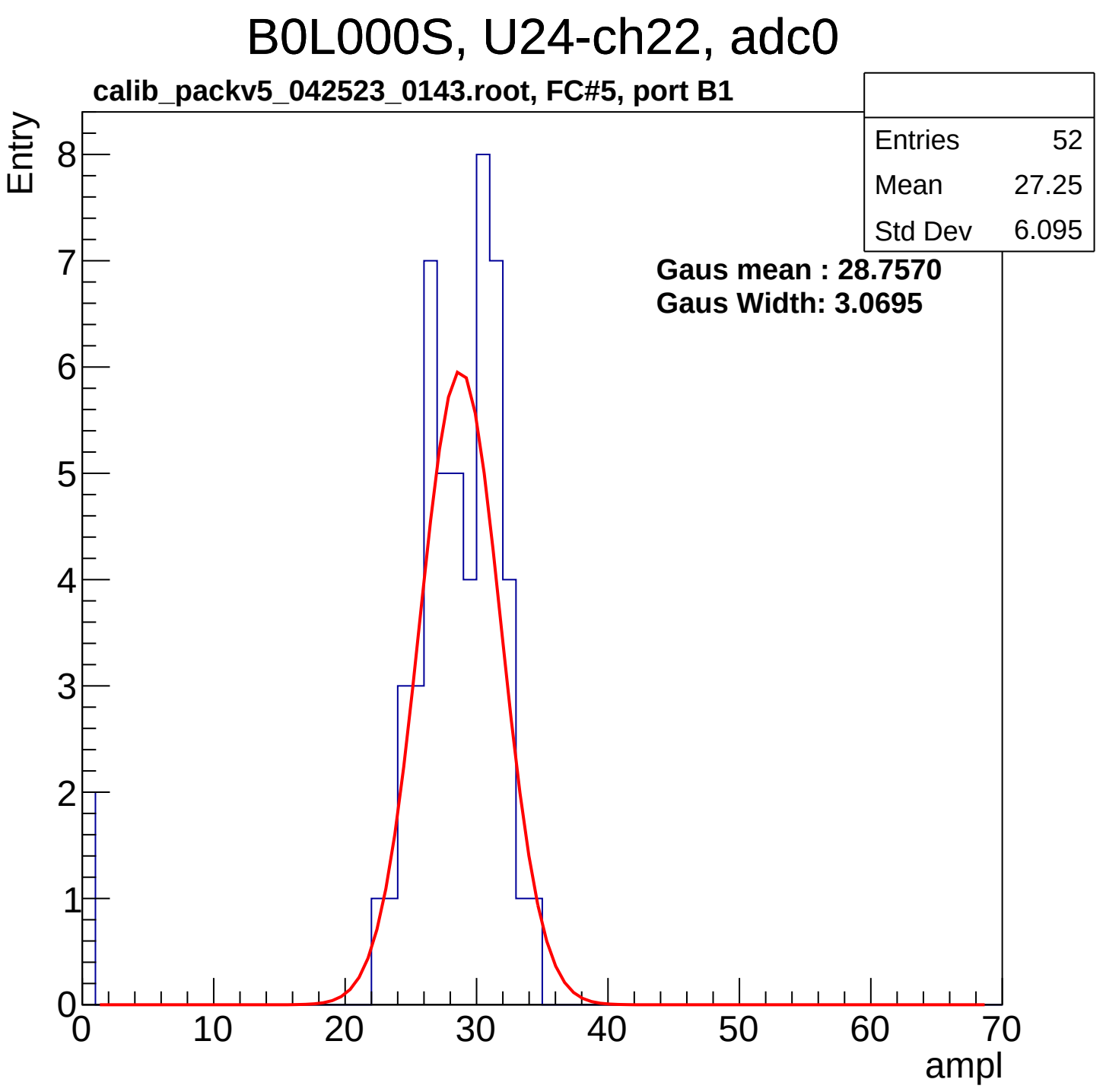

B0L000S, U24-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 50.83
Std Dev 22.78
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 27.25
Std Dev 6.095
Gaus mean : 28.7570
Gaus Width: 3.0695
Entry
ampl
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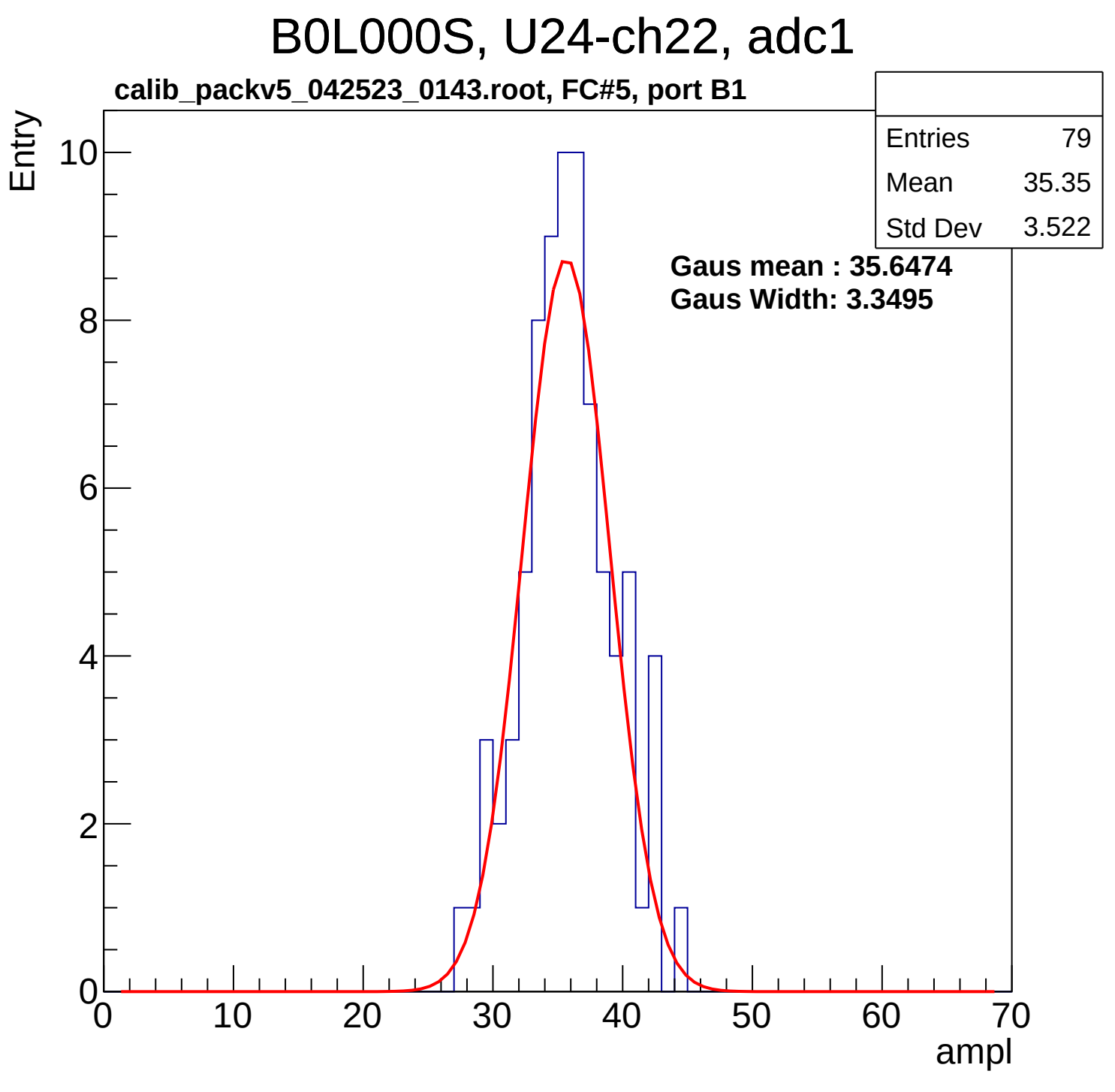
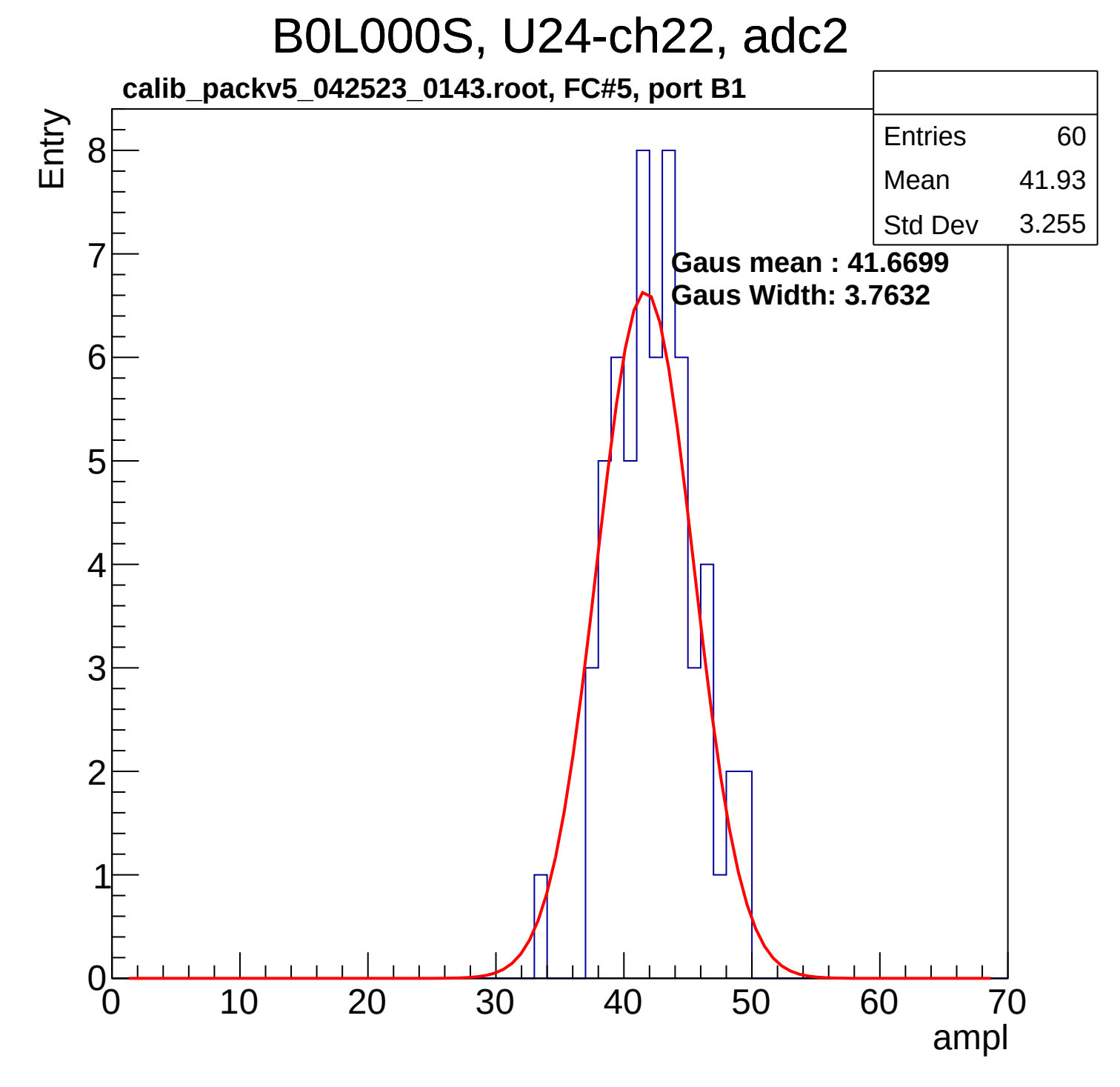
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 35.35
Std Dev 3.522
Gaus mean : 35.6474
Gaus Width: 3.3495
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 41.93
Std Dev 3.255
Gaus mean : 41.6699
Gaus Width: 3.7632
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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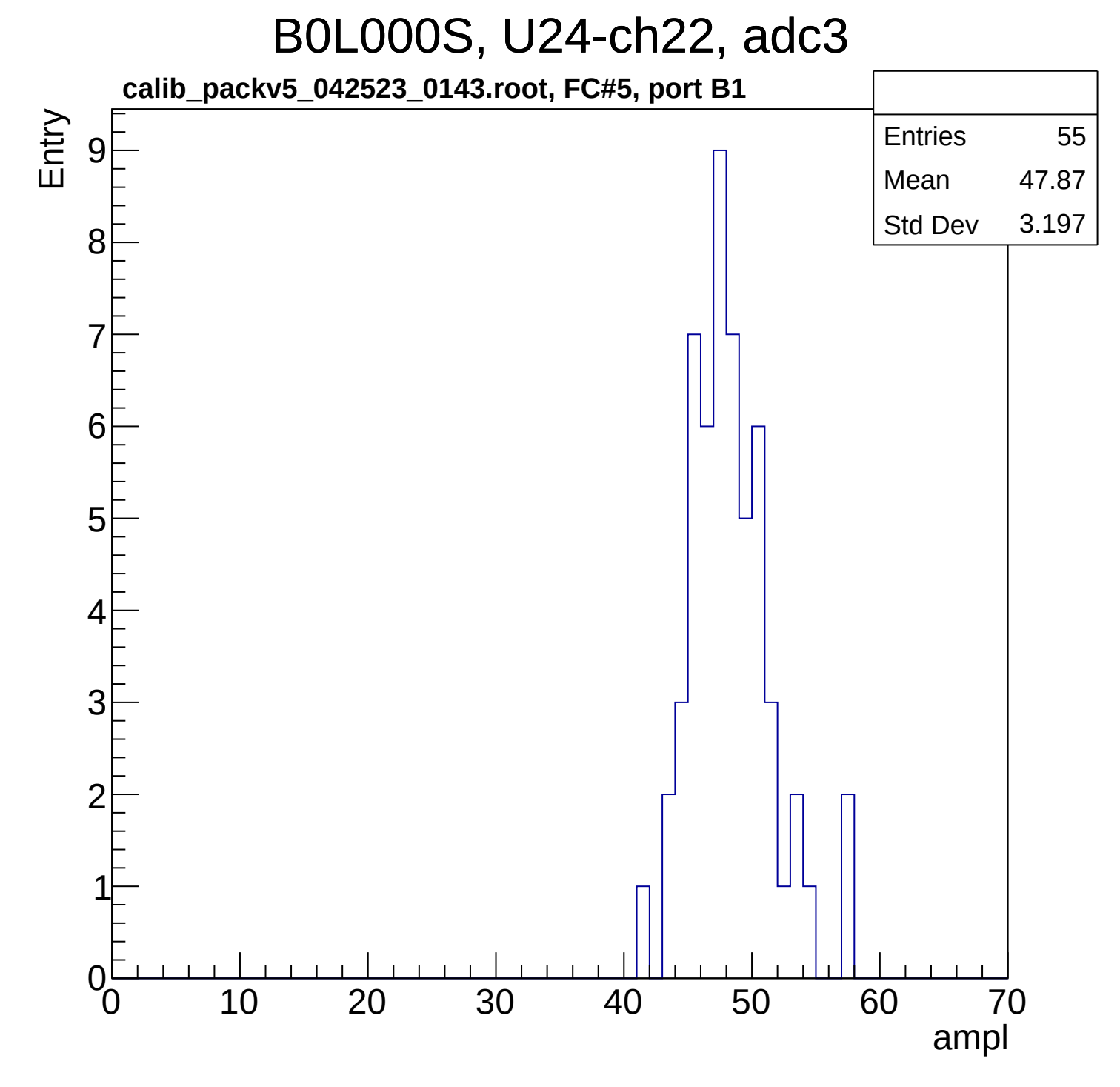
B0L000S, U24-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.87
Std Dev 3.197
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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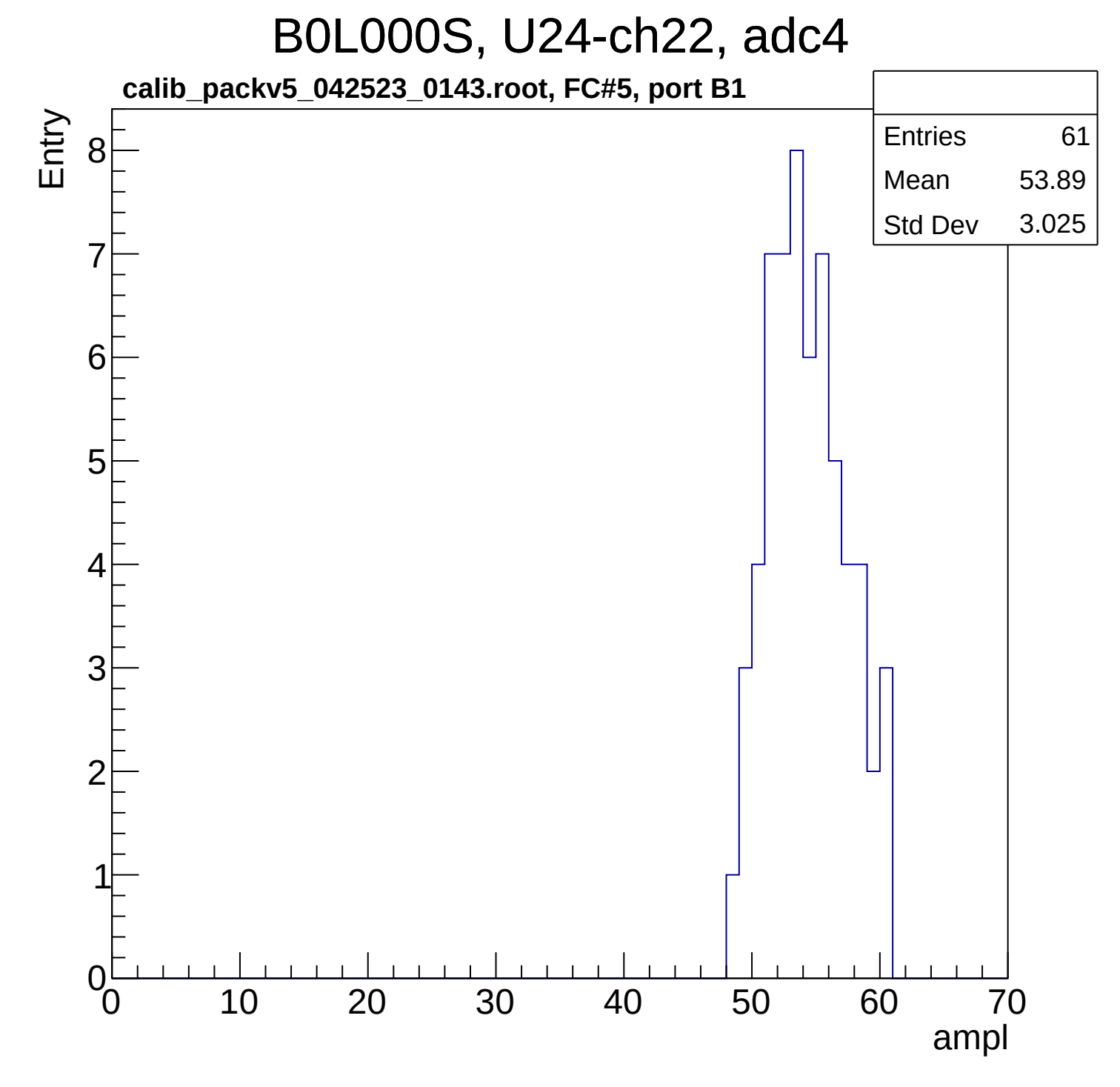
B0L000S, U24-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.89
Std Dev 3.025
Entry
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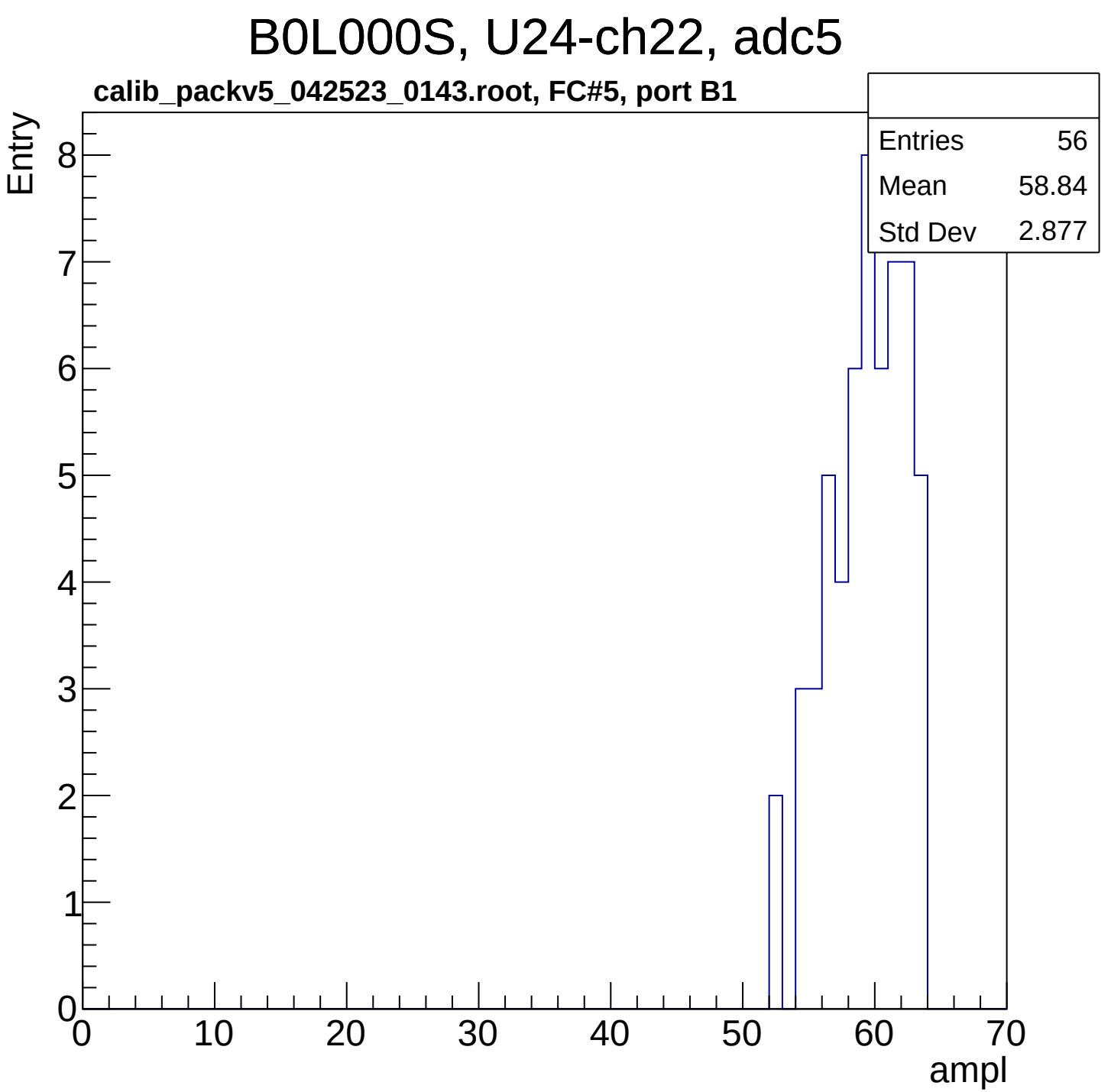
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.84
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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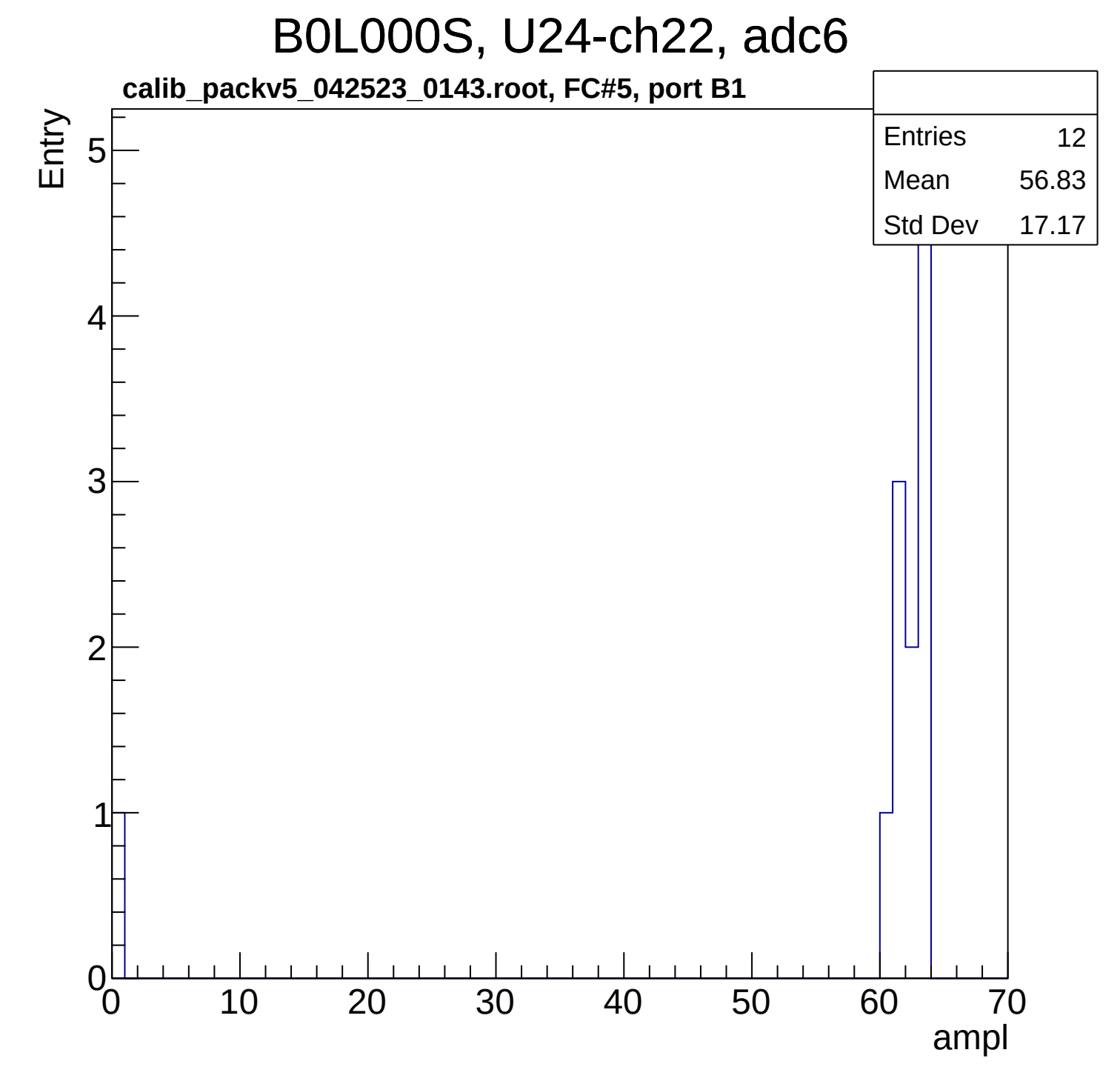
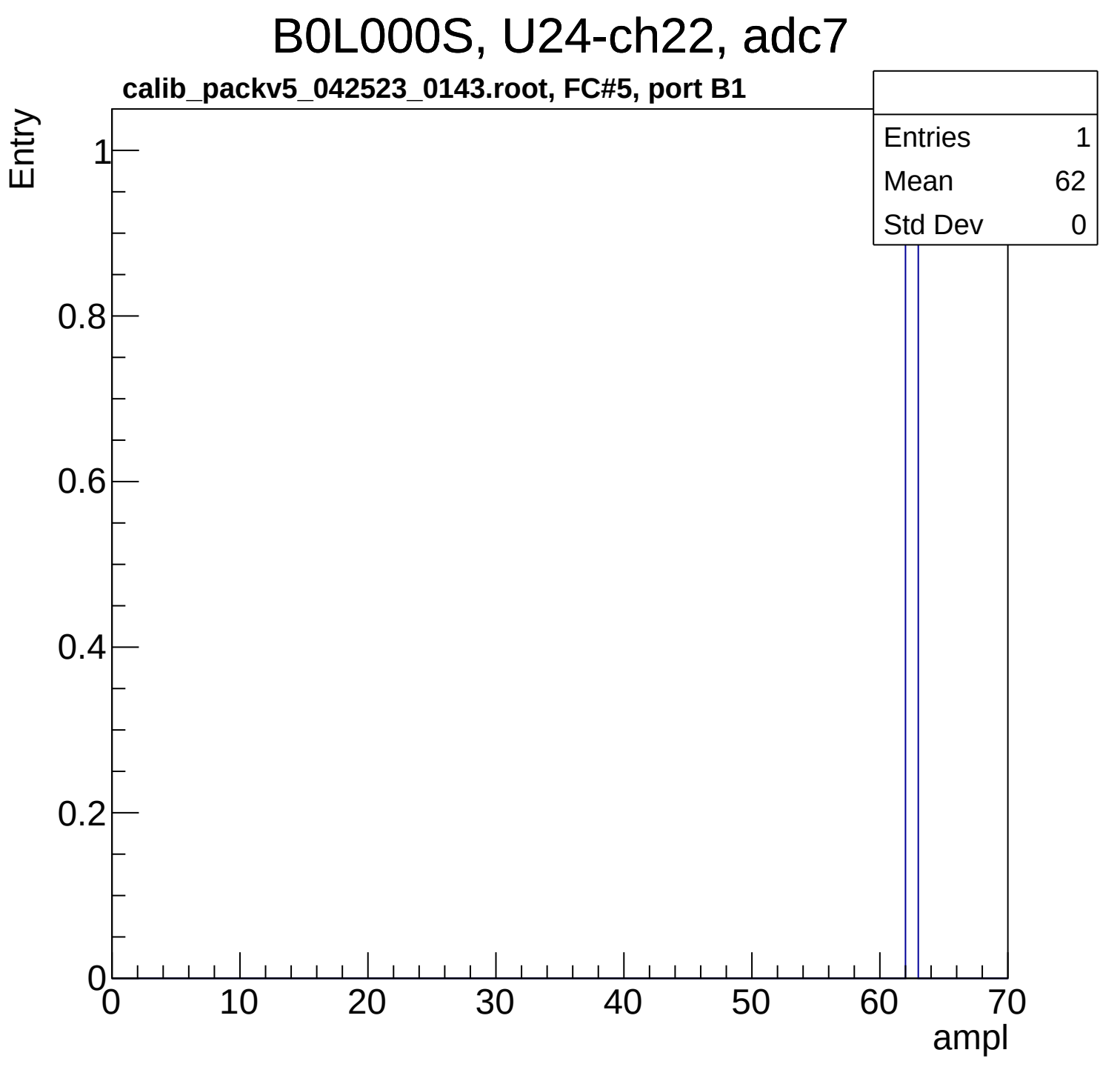
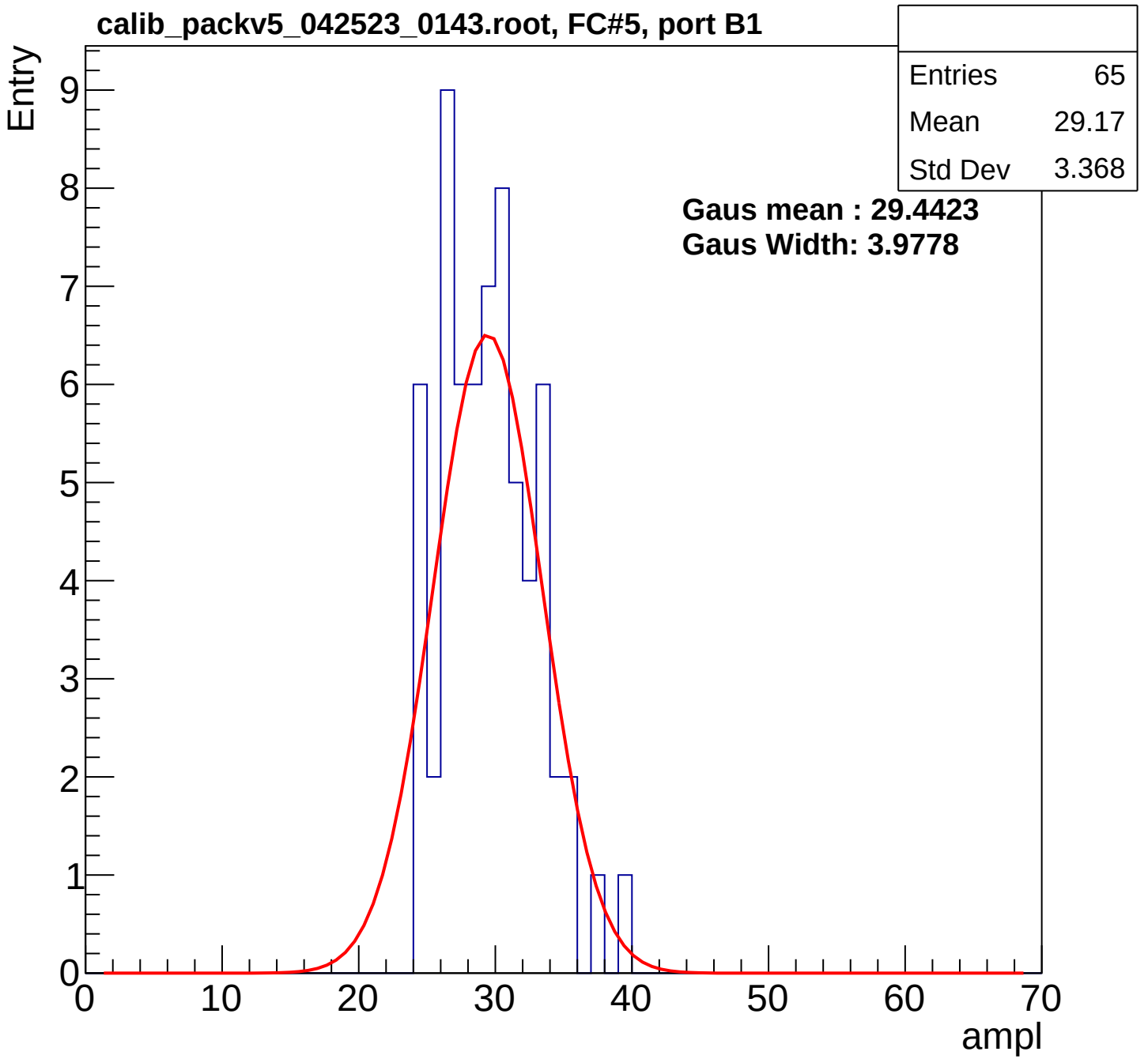

B0L000S, U24-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.17
Std Dev 3.368
Gaus mean : 29.4423
Gaus Width: 3.9778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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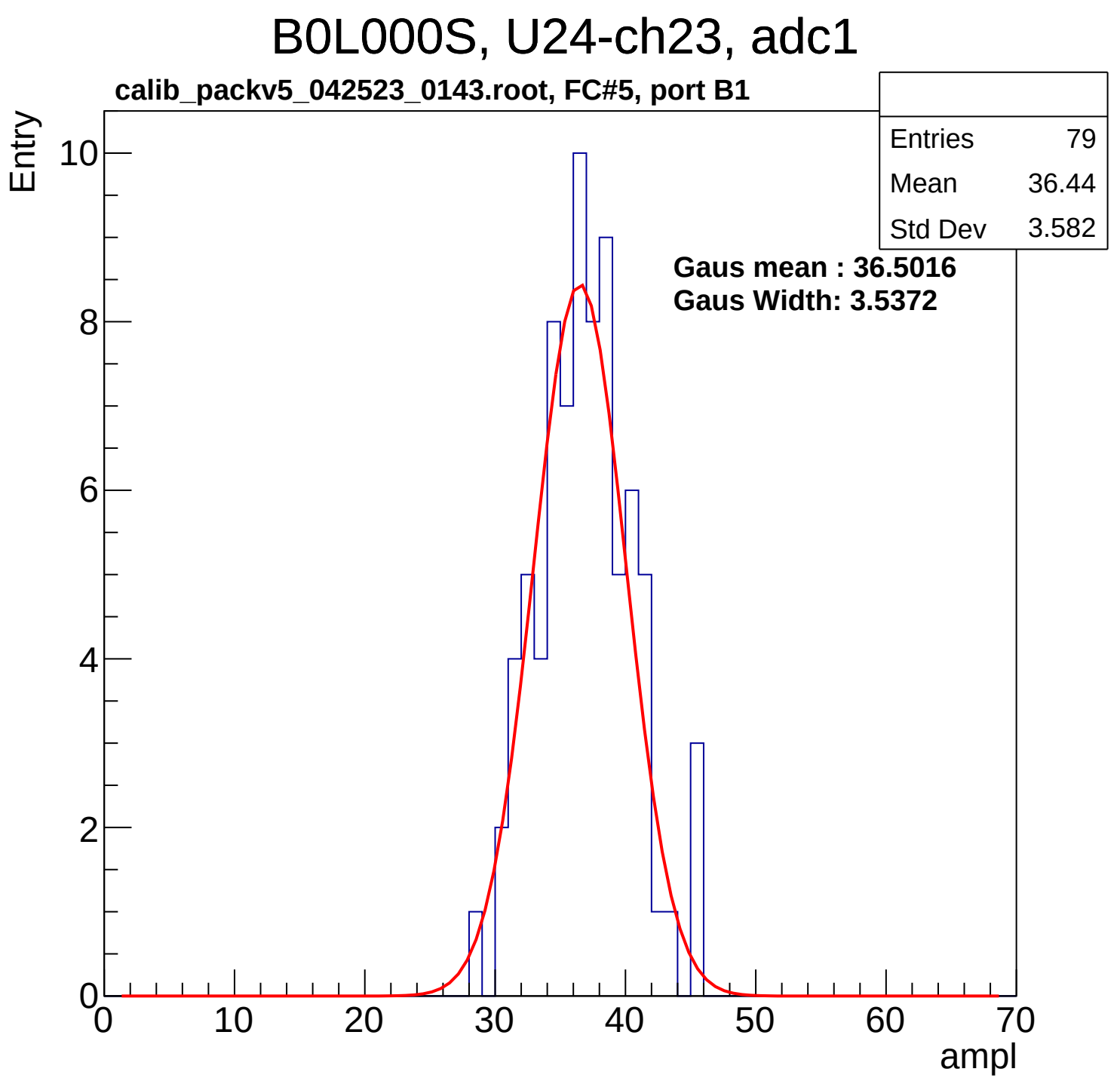
B0L000S, U24-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 36.44
Std Dev 3.582
Gaus mean : 36.5016
Gaus Width: 3.5372
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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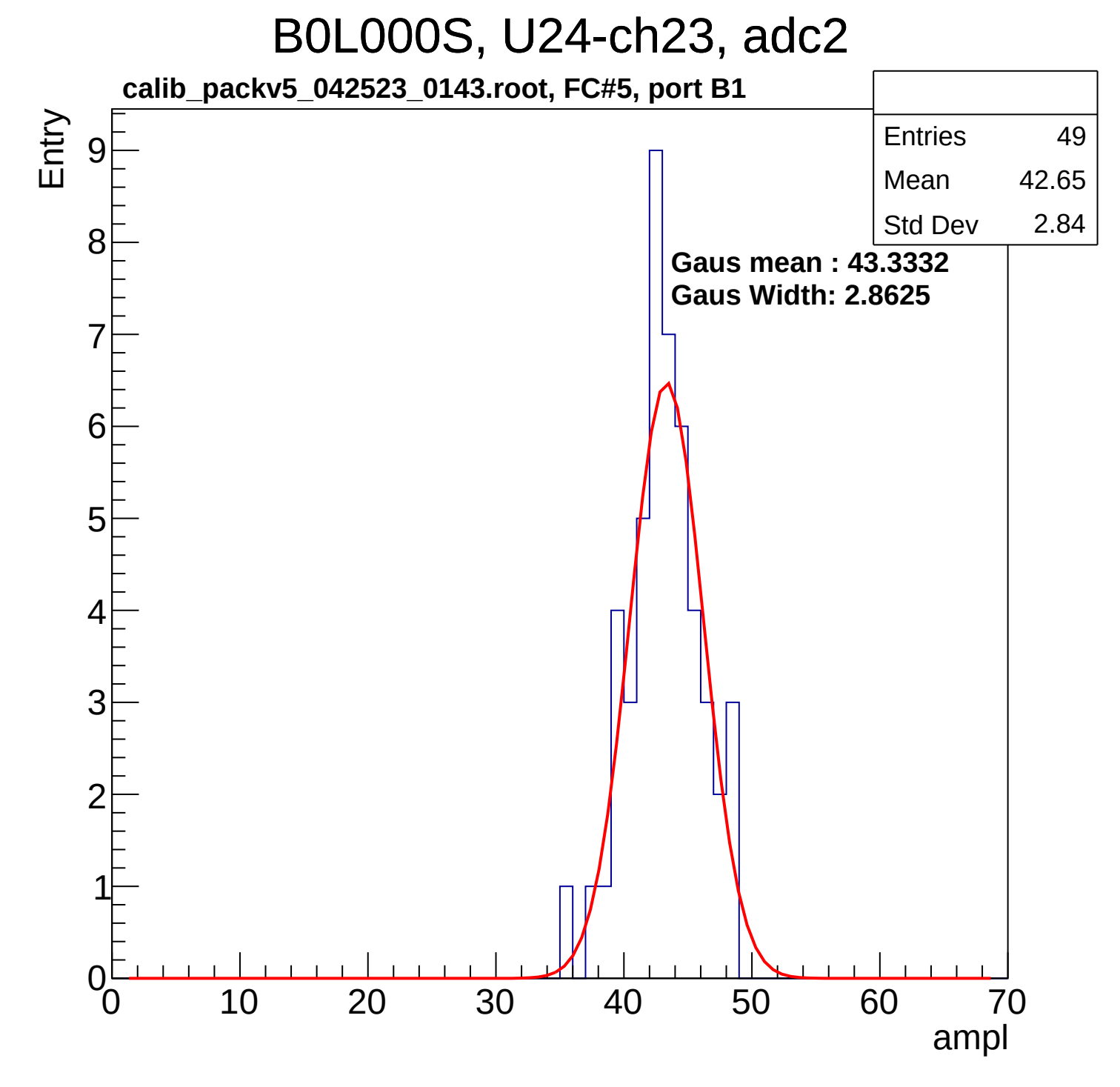
B0L000S, U24-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 42.65
Std Dev 2.84
Gaus mean : 43.3332
Gaus Width: 2.8625
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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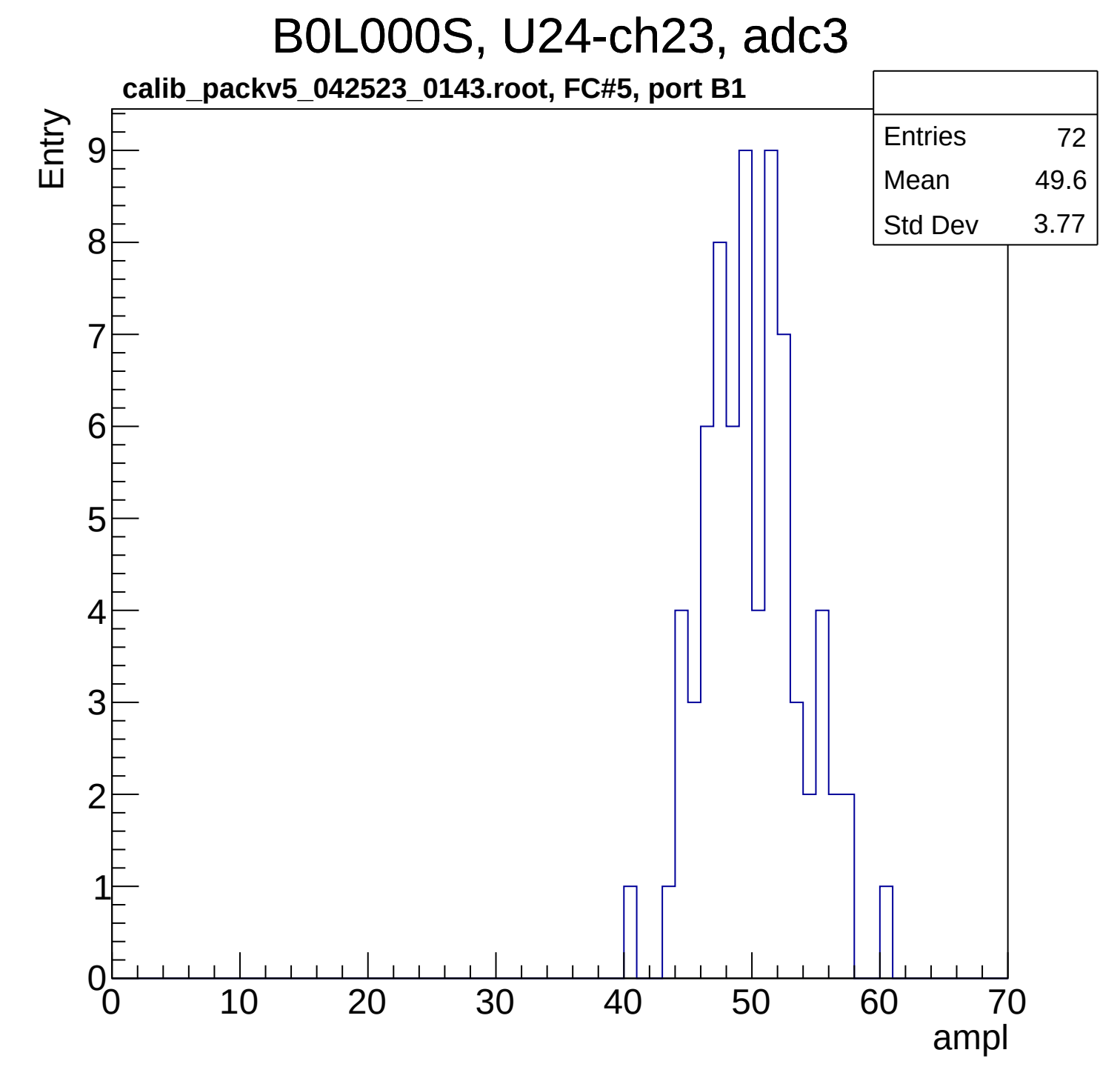
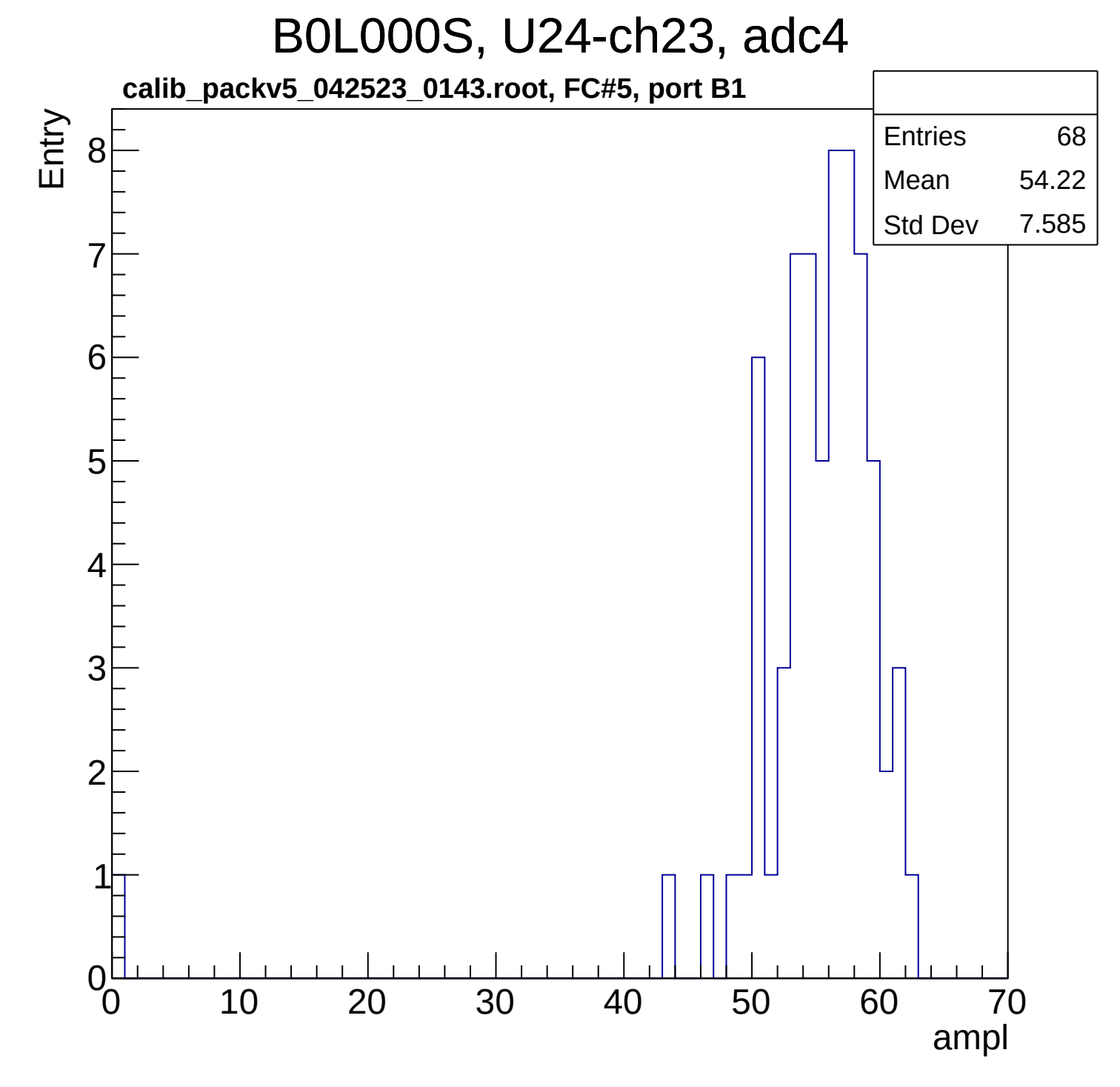

B0L000S, U24-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.6
Std Dev 3.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.22
Std Dev 7.585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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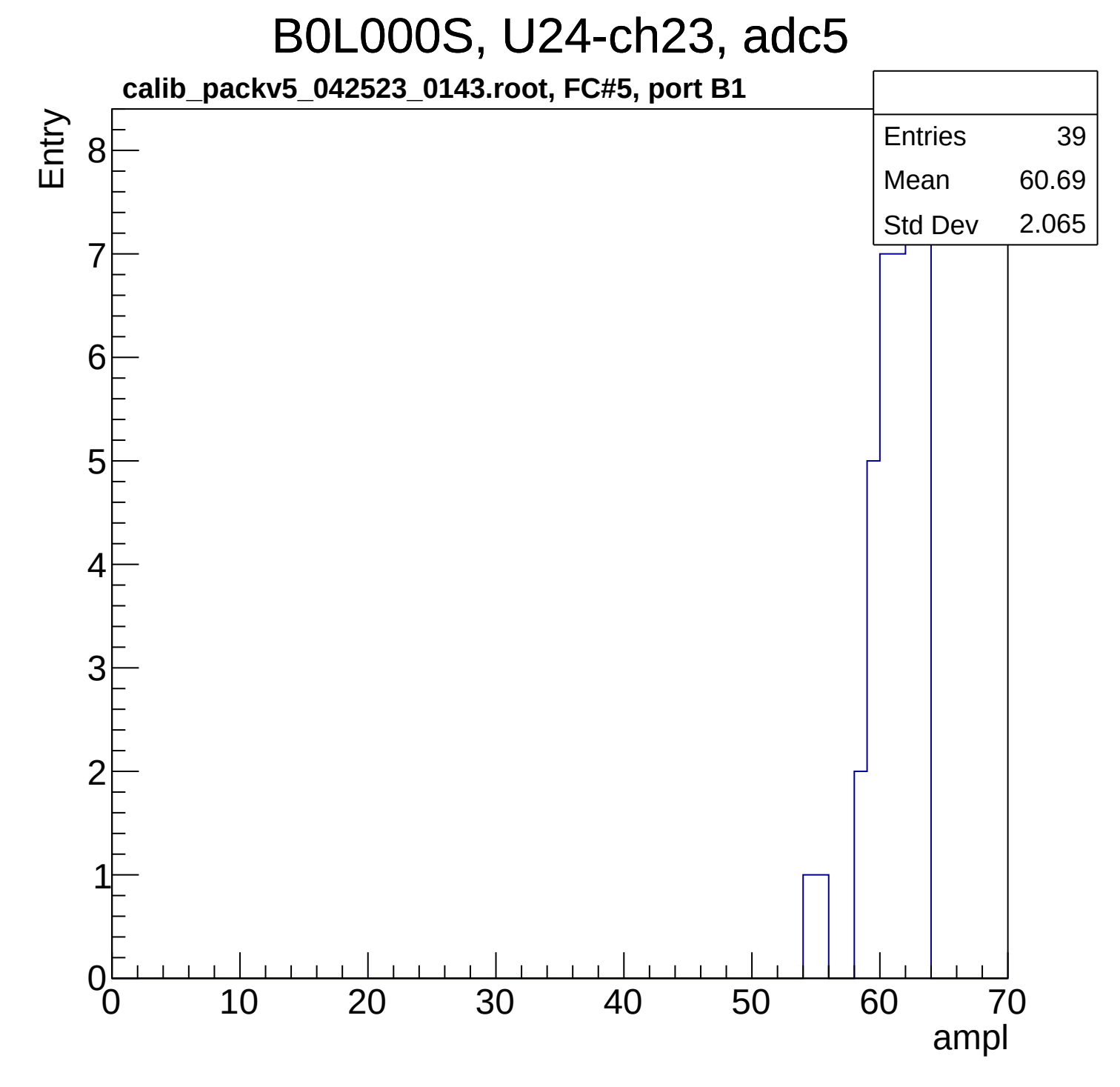
B0L000S, U24-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 60.69
Std Dev 2.065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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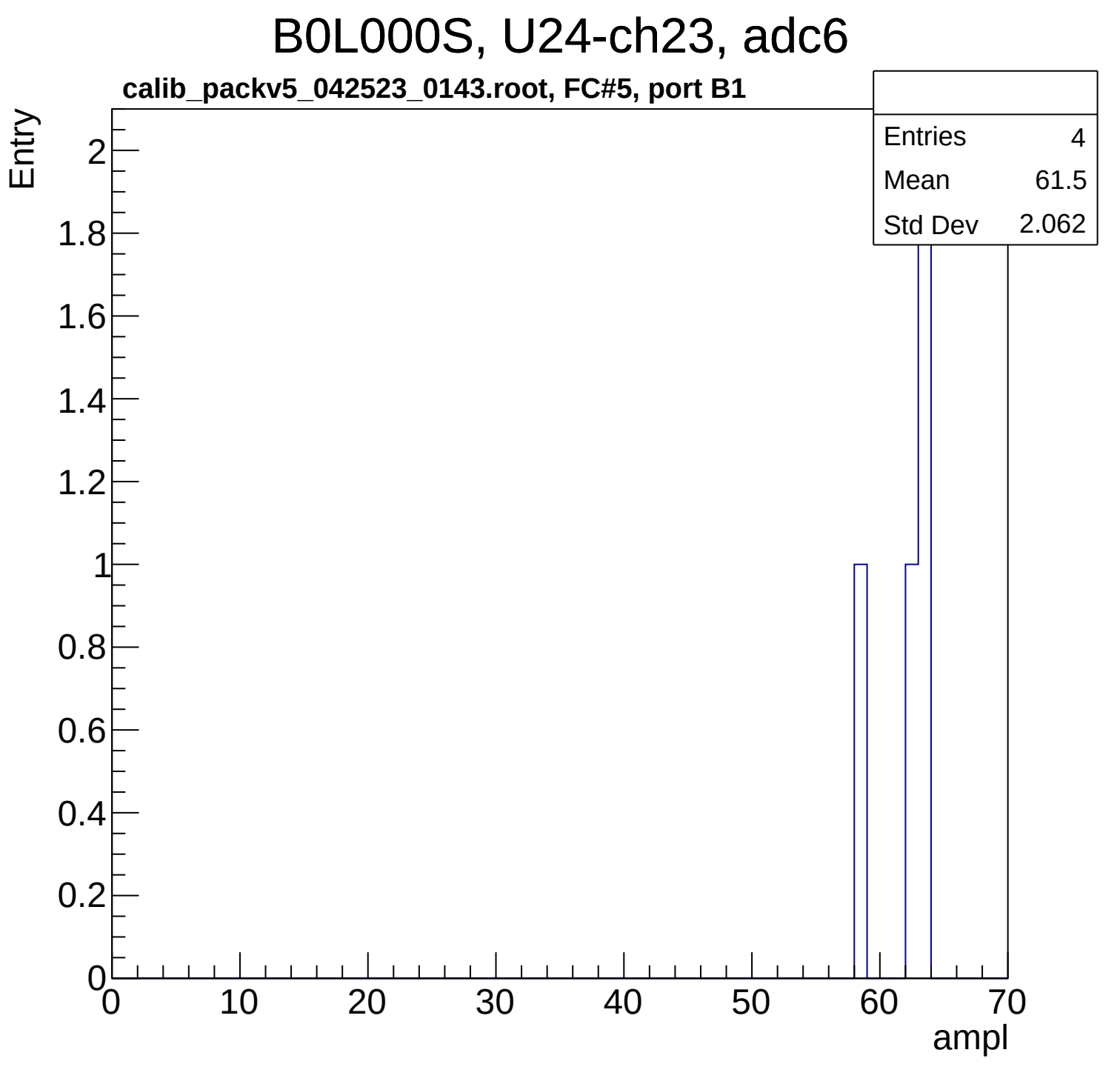
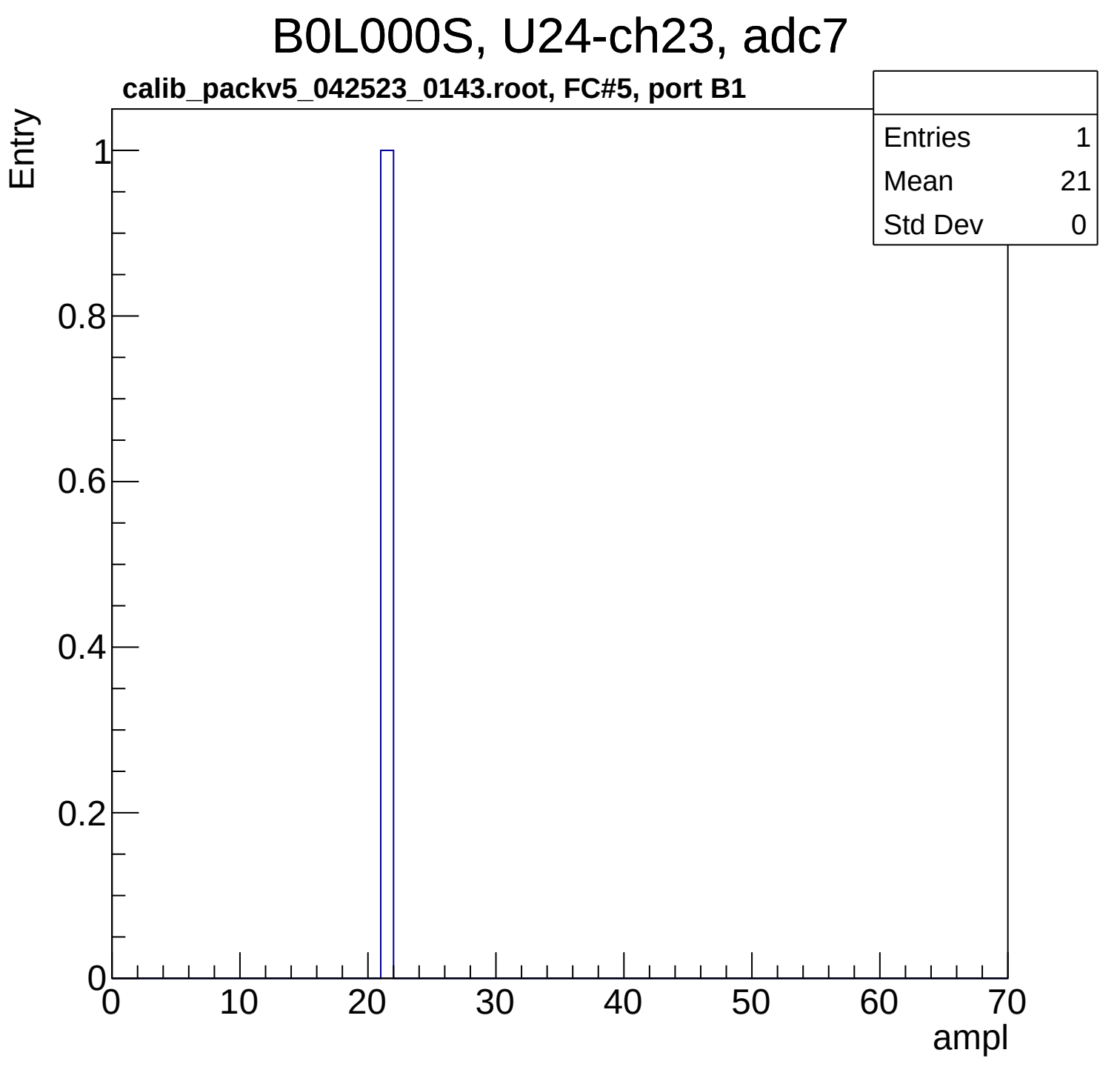
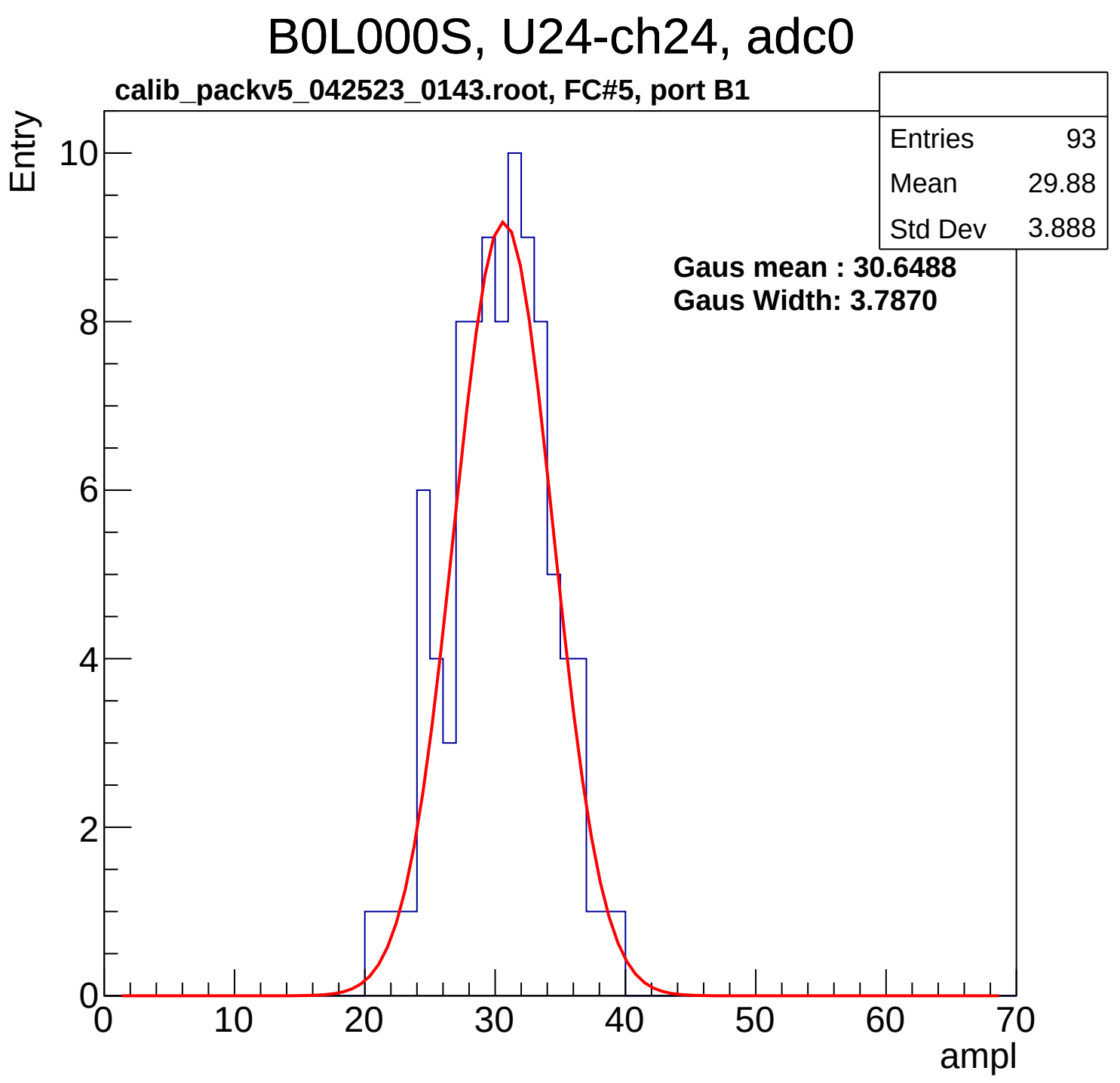
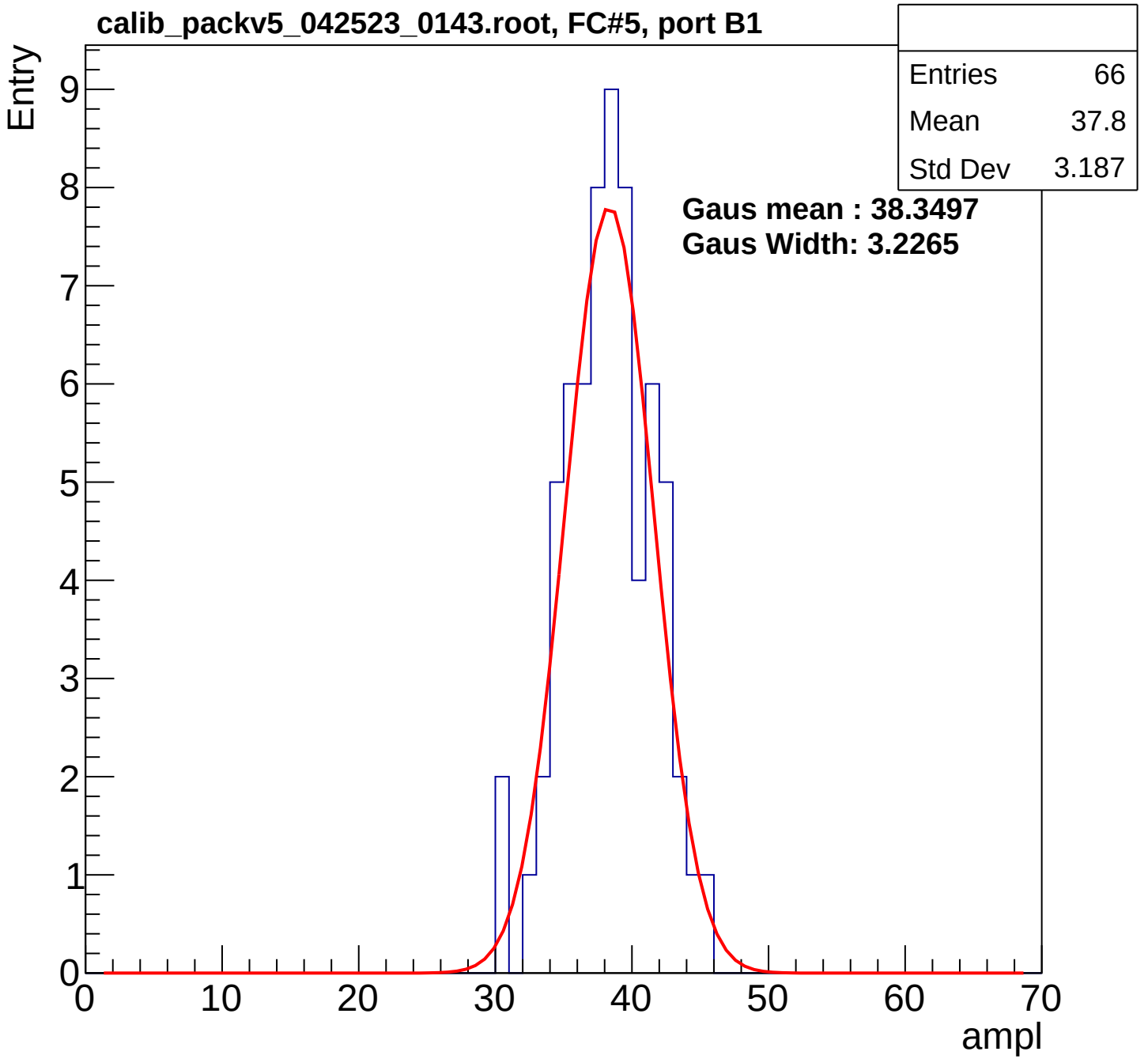
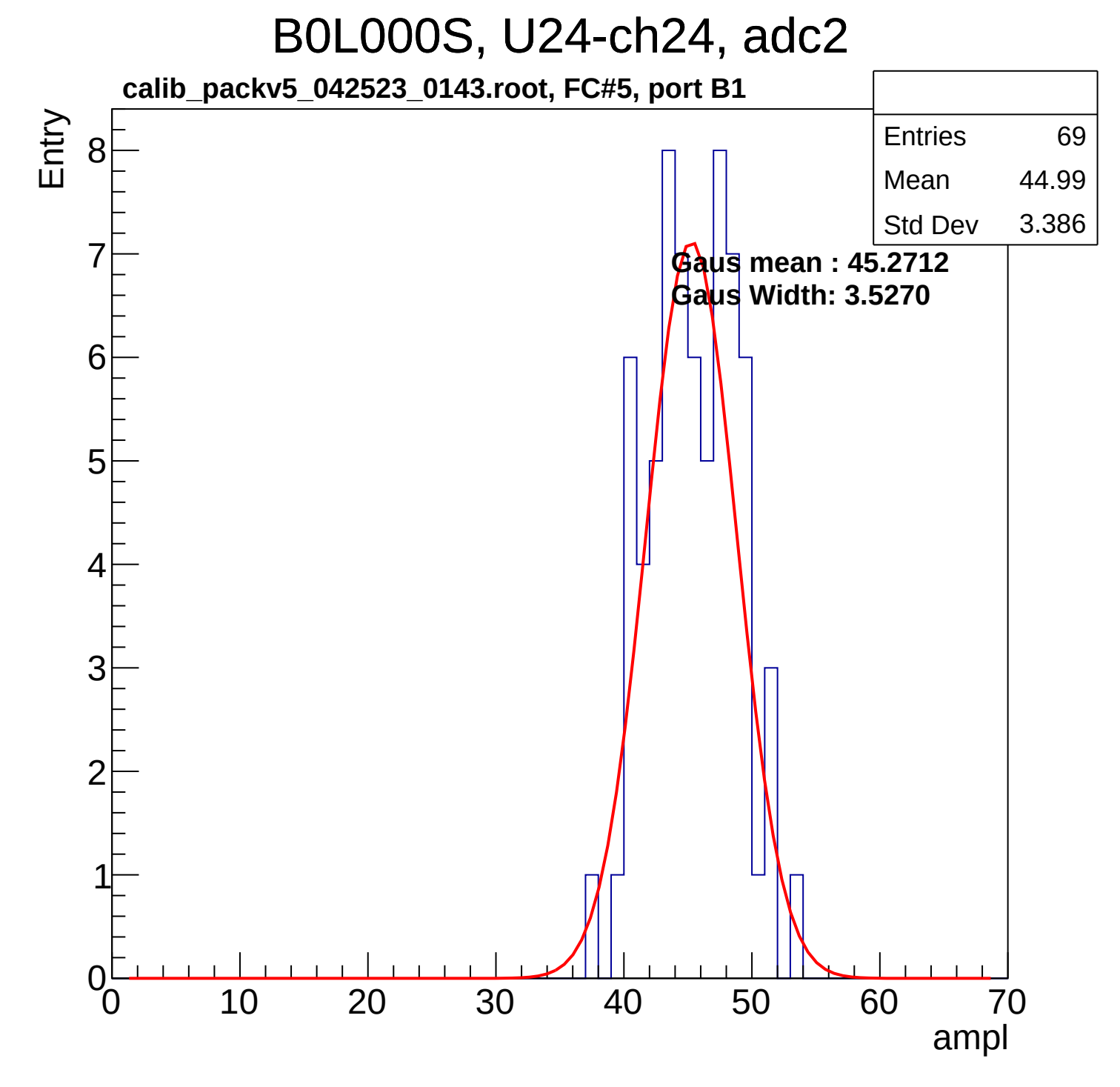

B0L000S, U24-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.5
Std Dev 2.062
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 93
Mean 29.88
Std Dev 3.888
Gaus mean : 30.6488
Gaus Width: 3.7870
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.8
Std Dev 3.187
Gaus mean : 38.3497
Gaus Width: 3.2265
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.99
Std Dev 3.386
Gaus mean : 45.2712
Gaus Width: 3.5270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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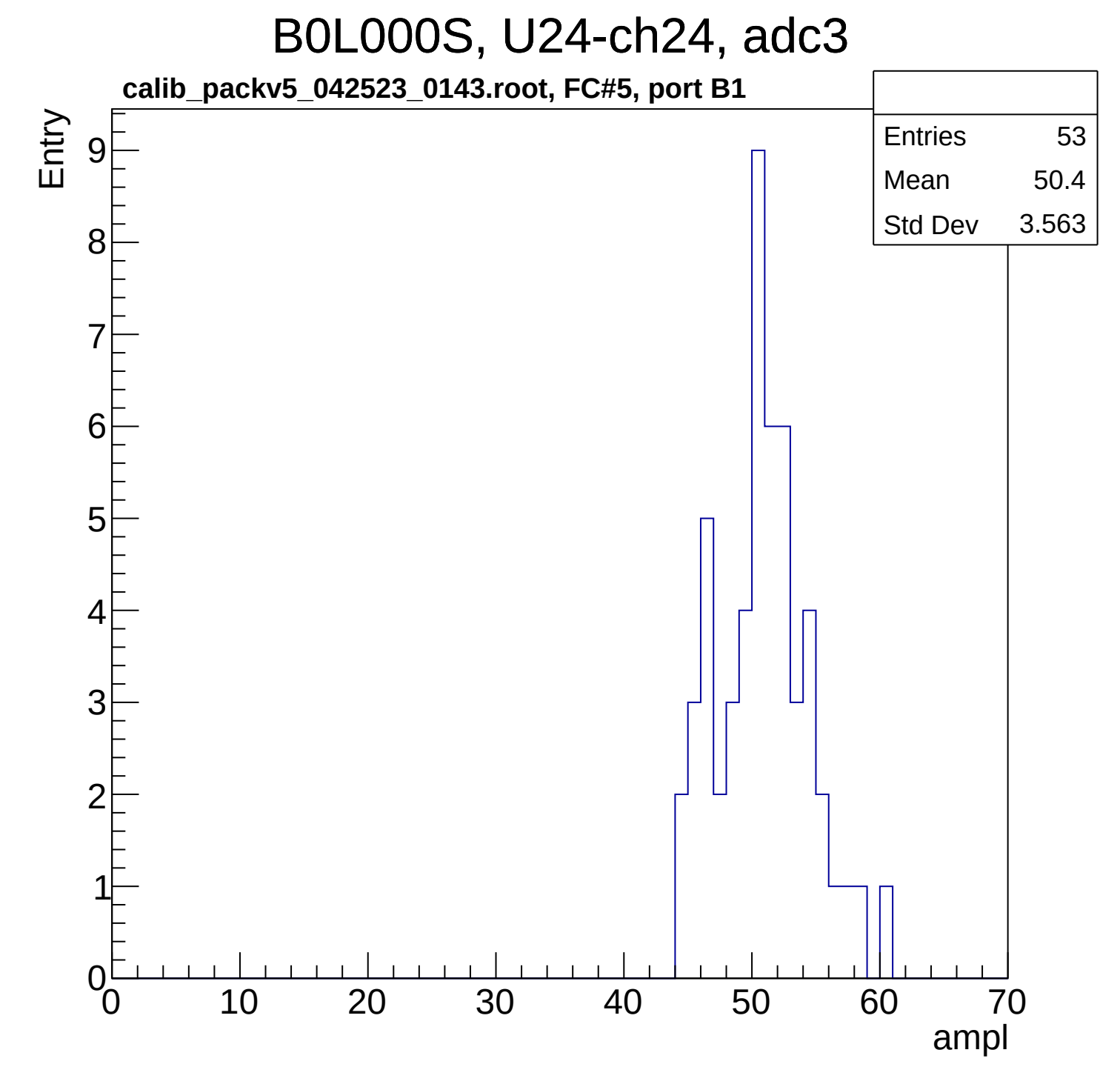
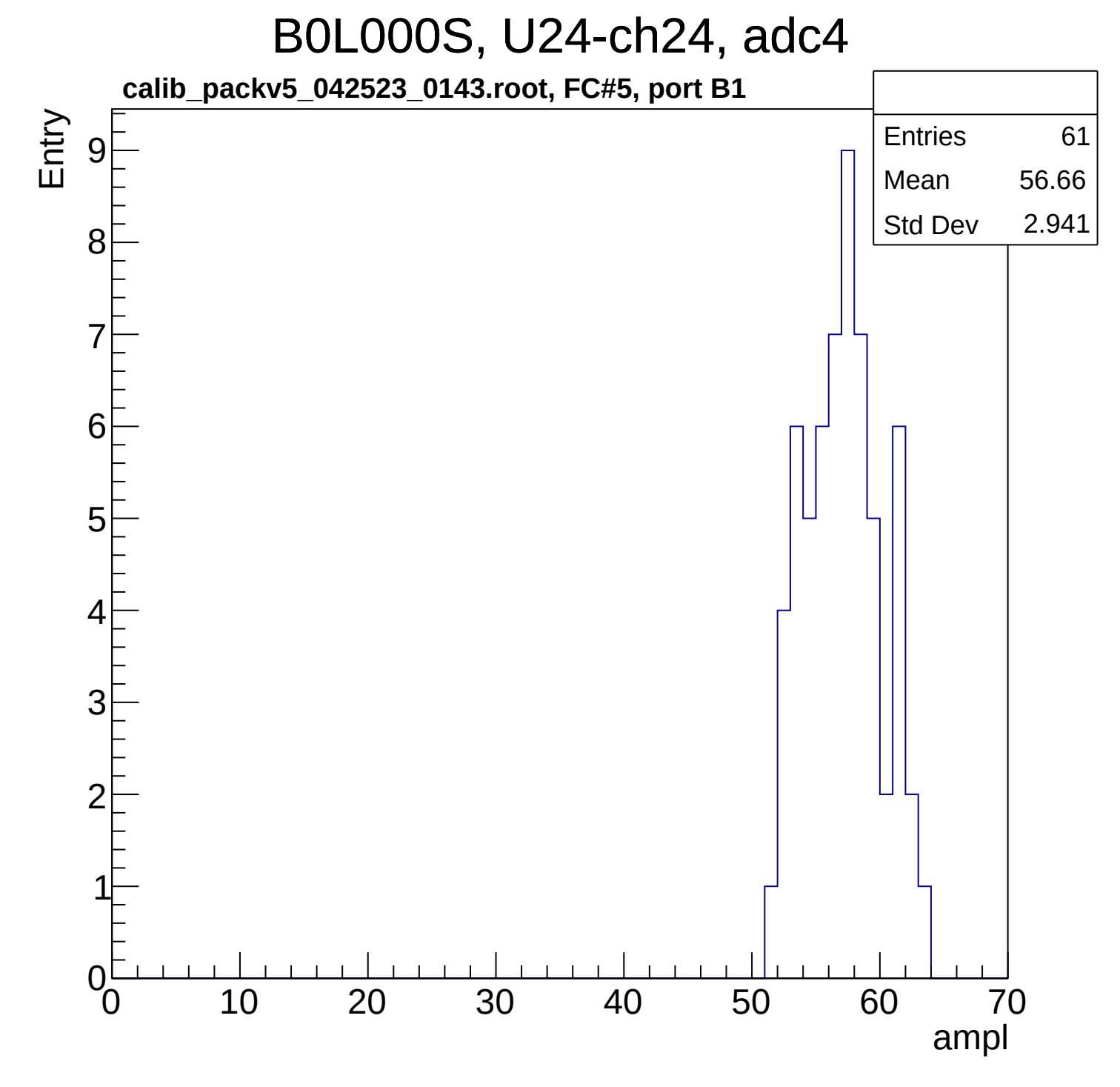
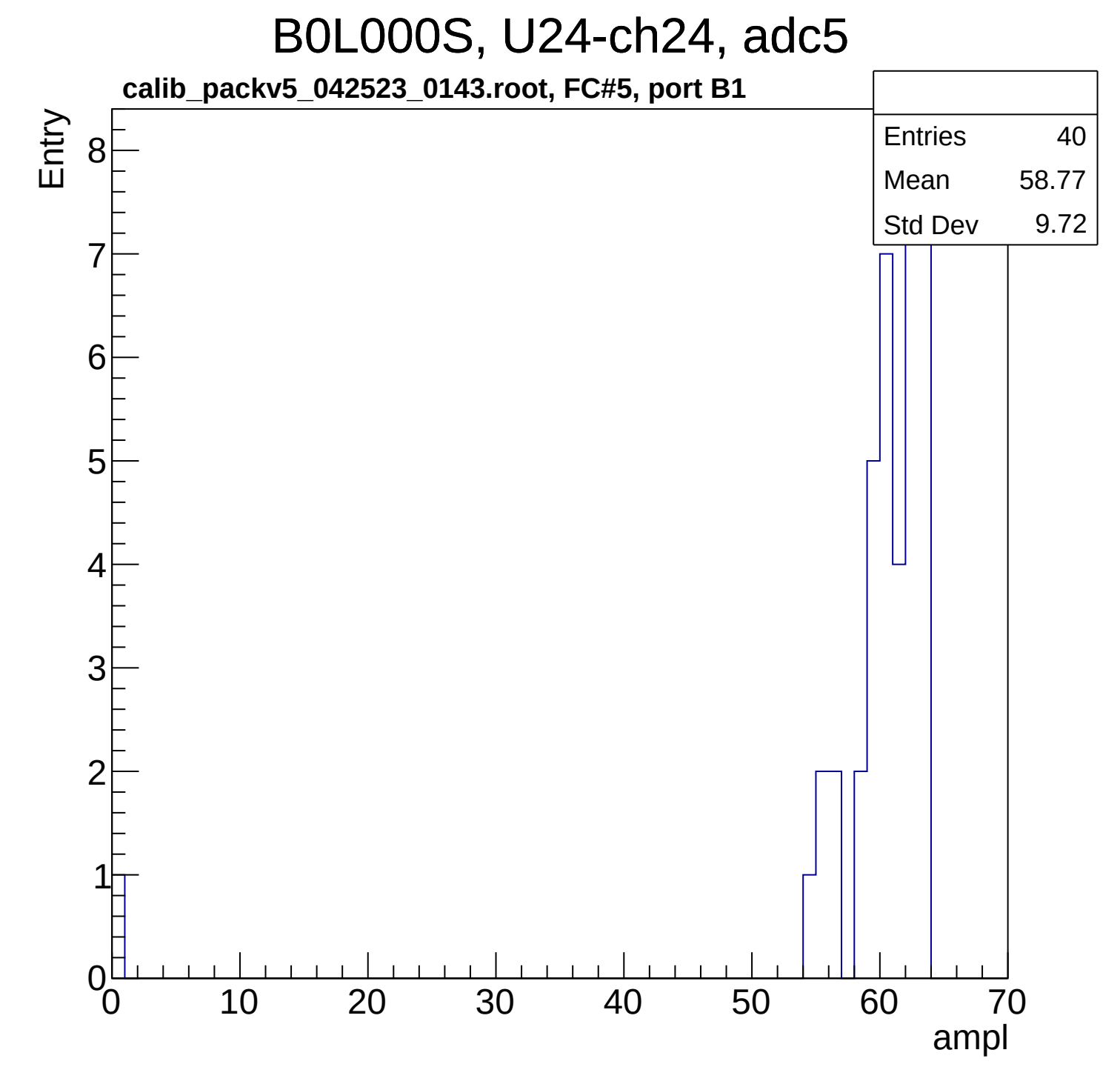
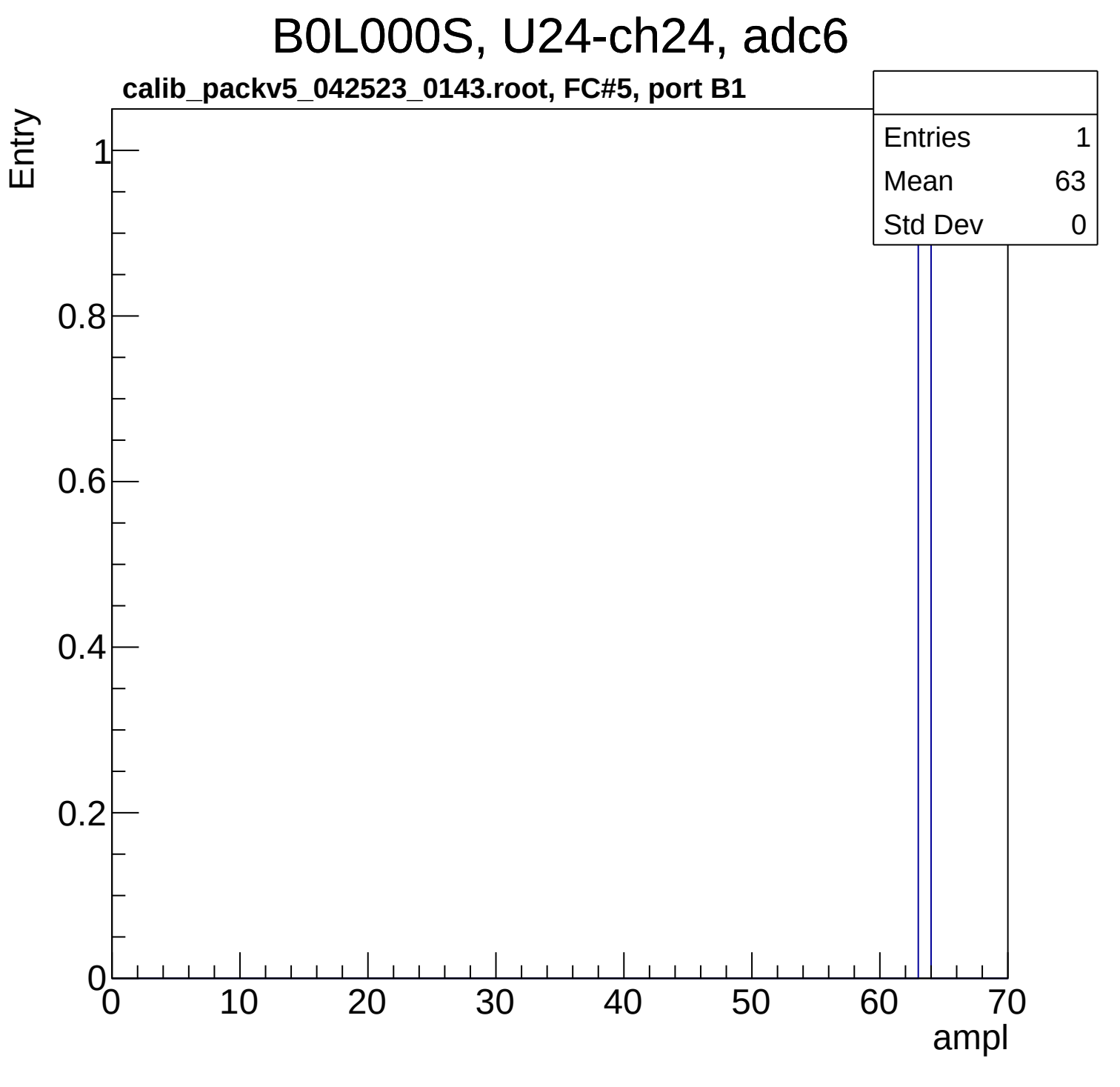
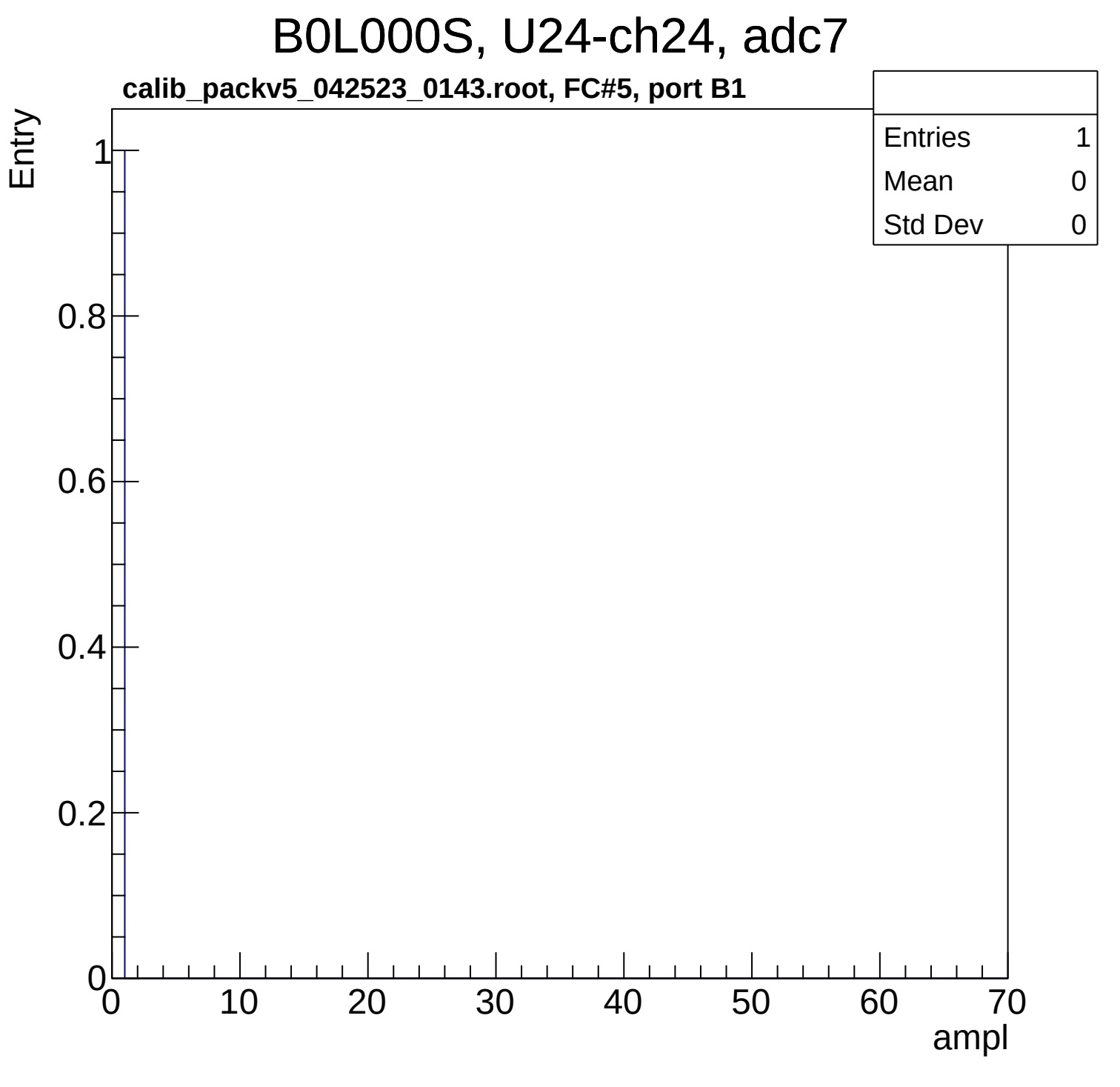
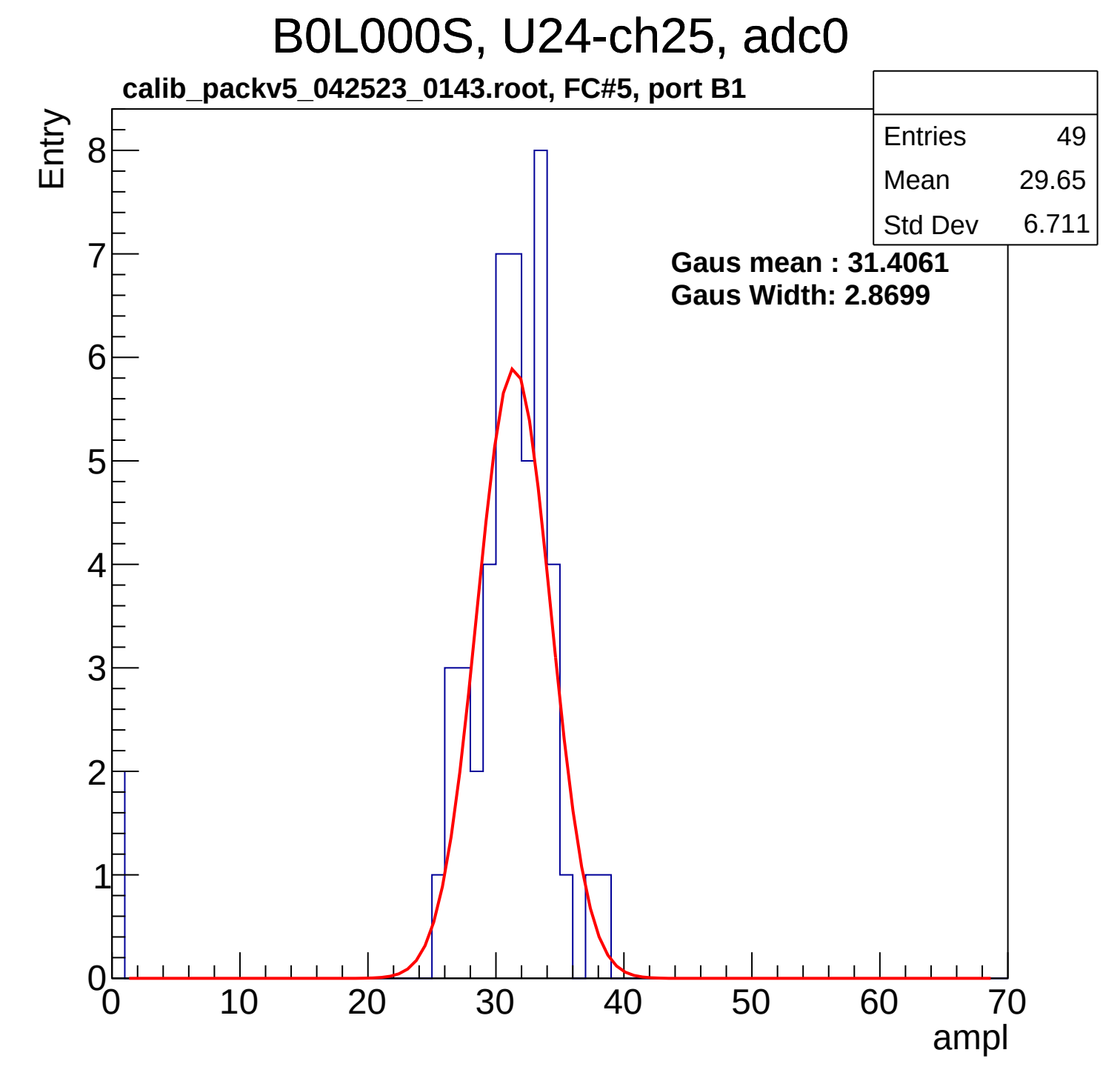

B0L000S, U24-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 50.4
Std Dev 3.563
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

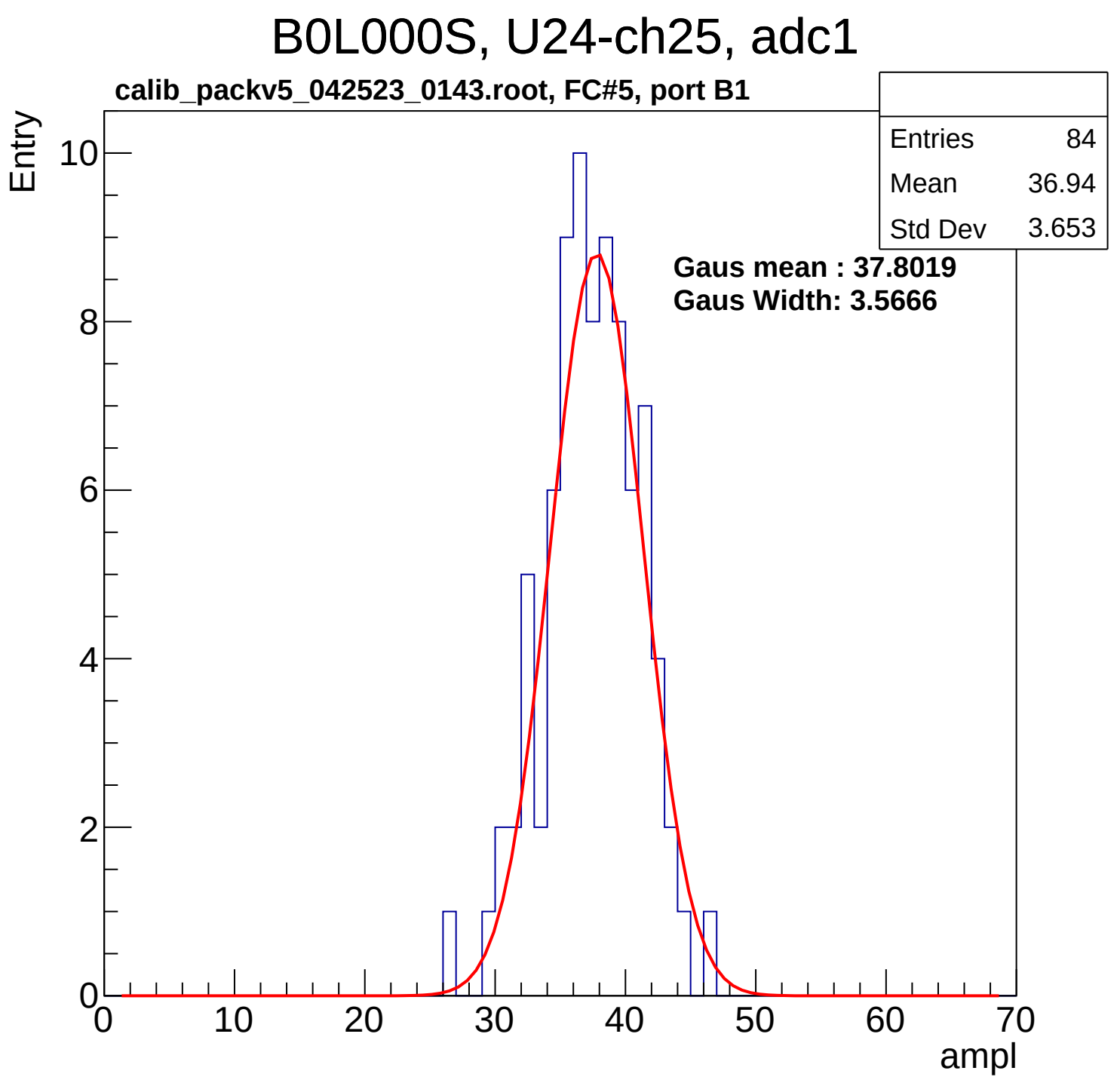

B0L000S, U24-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.66
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

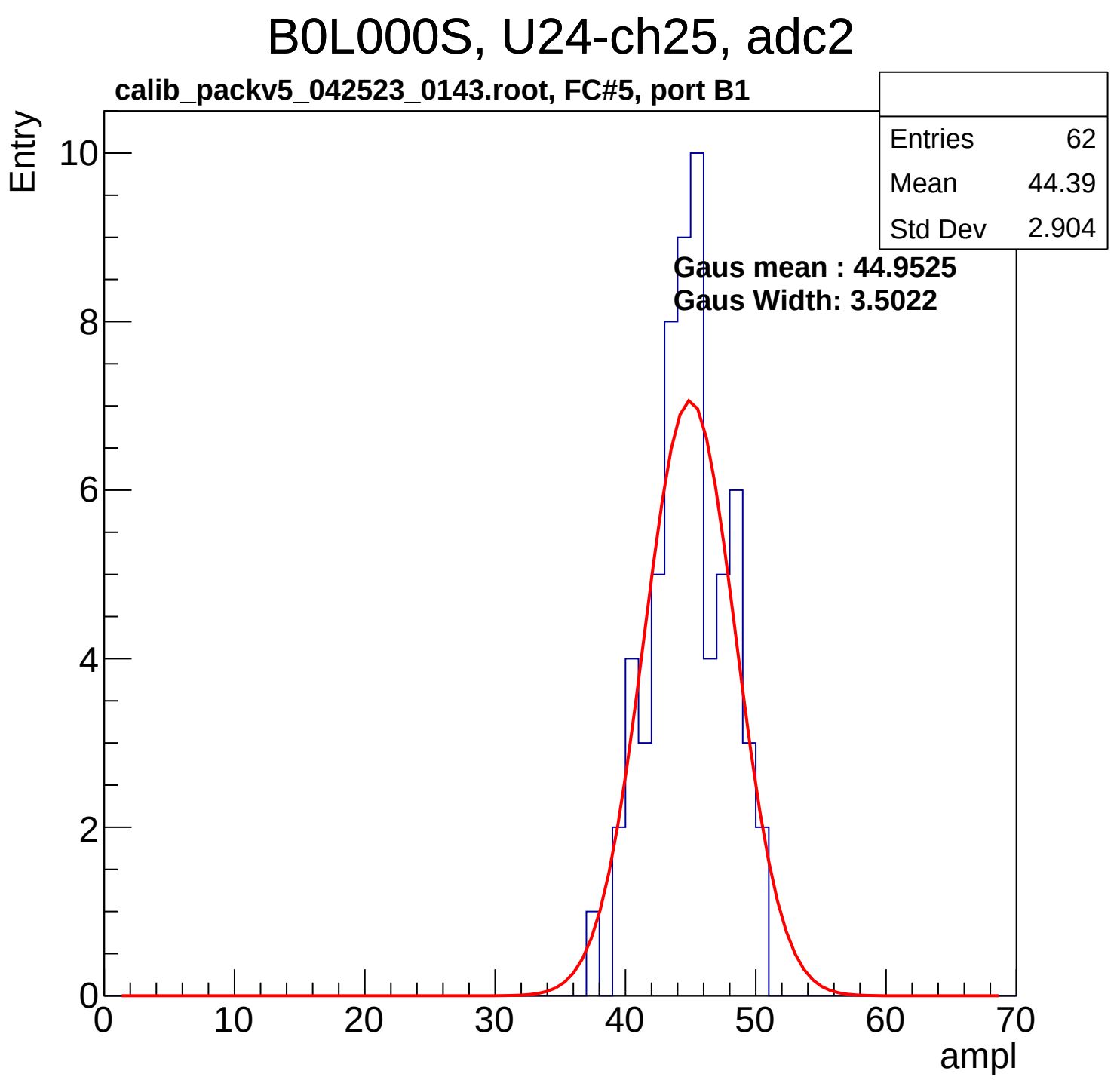

B0L000S, U24-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.77
Std Dev 9.72
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

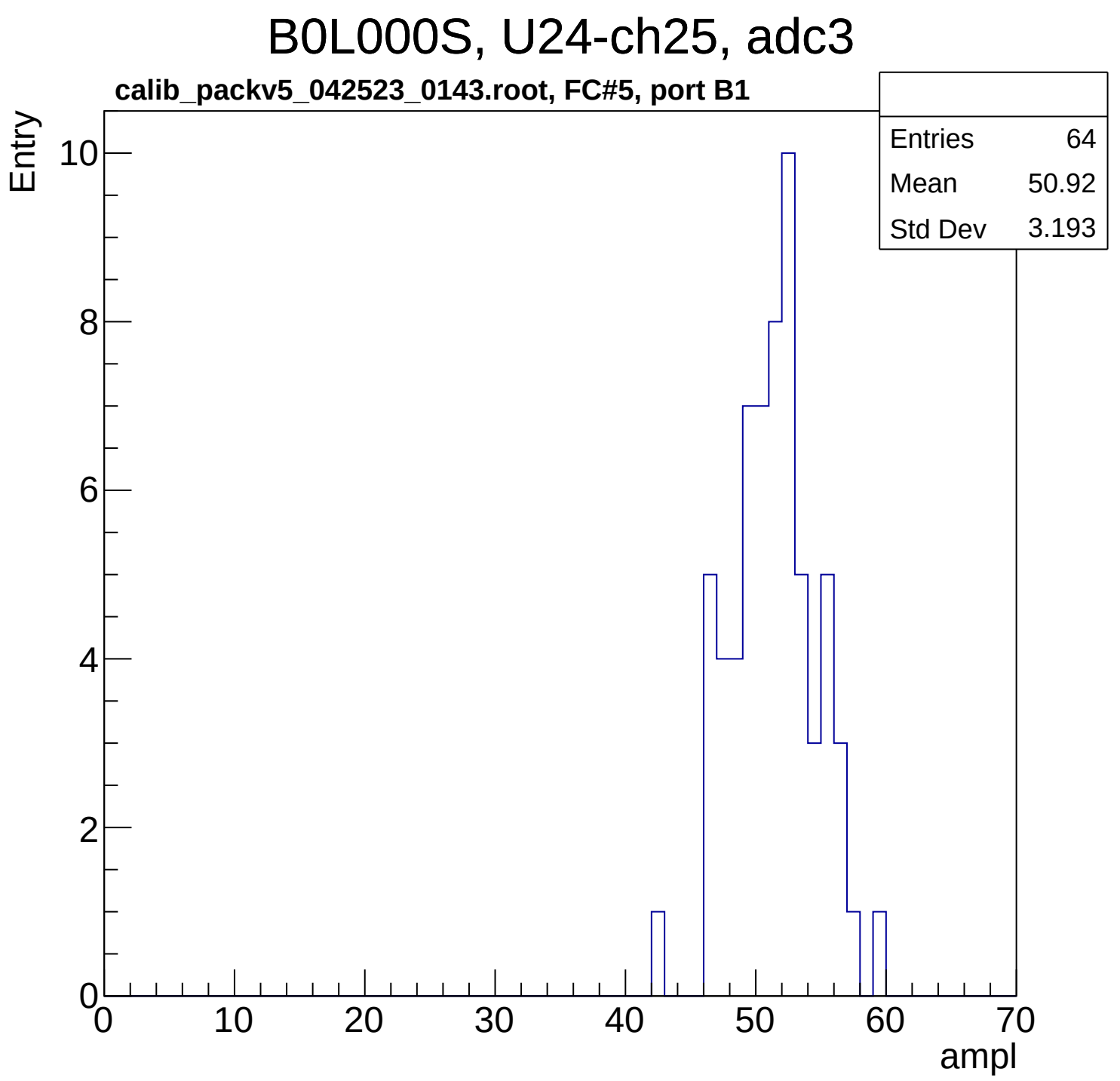

B0L000S, U24-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

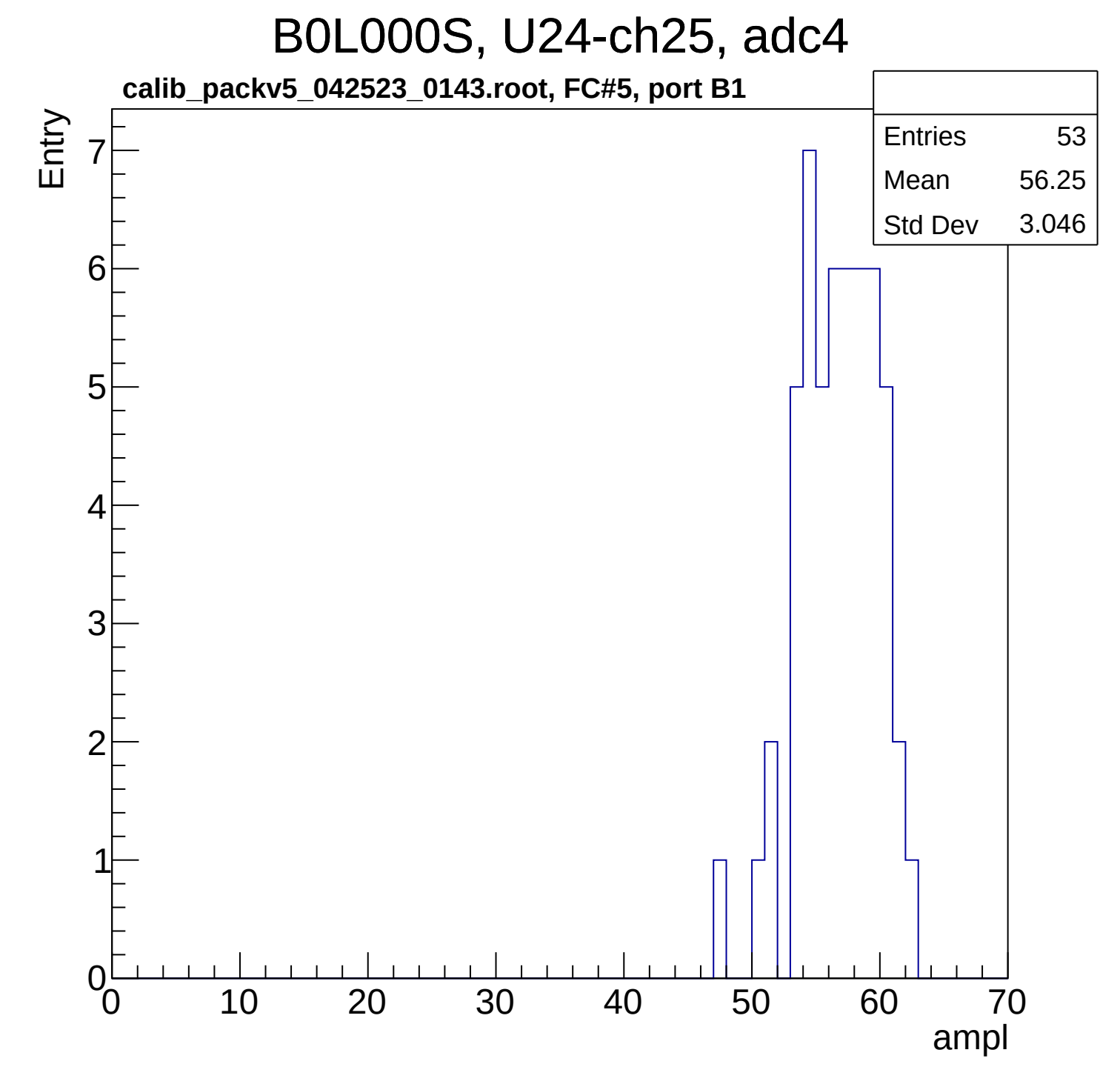

B0L000S, U24-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

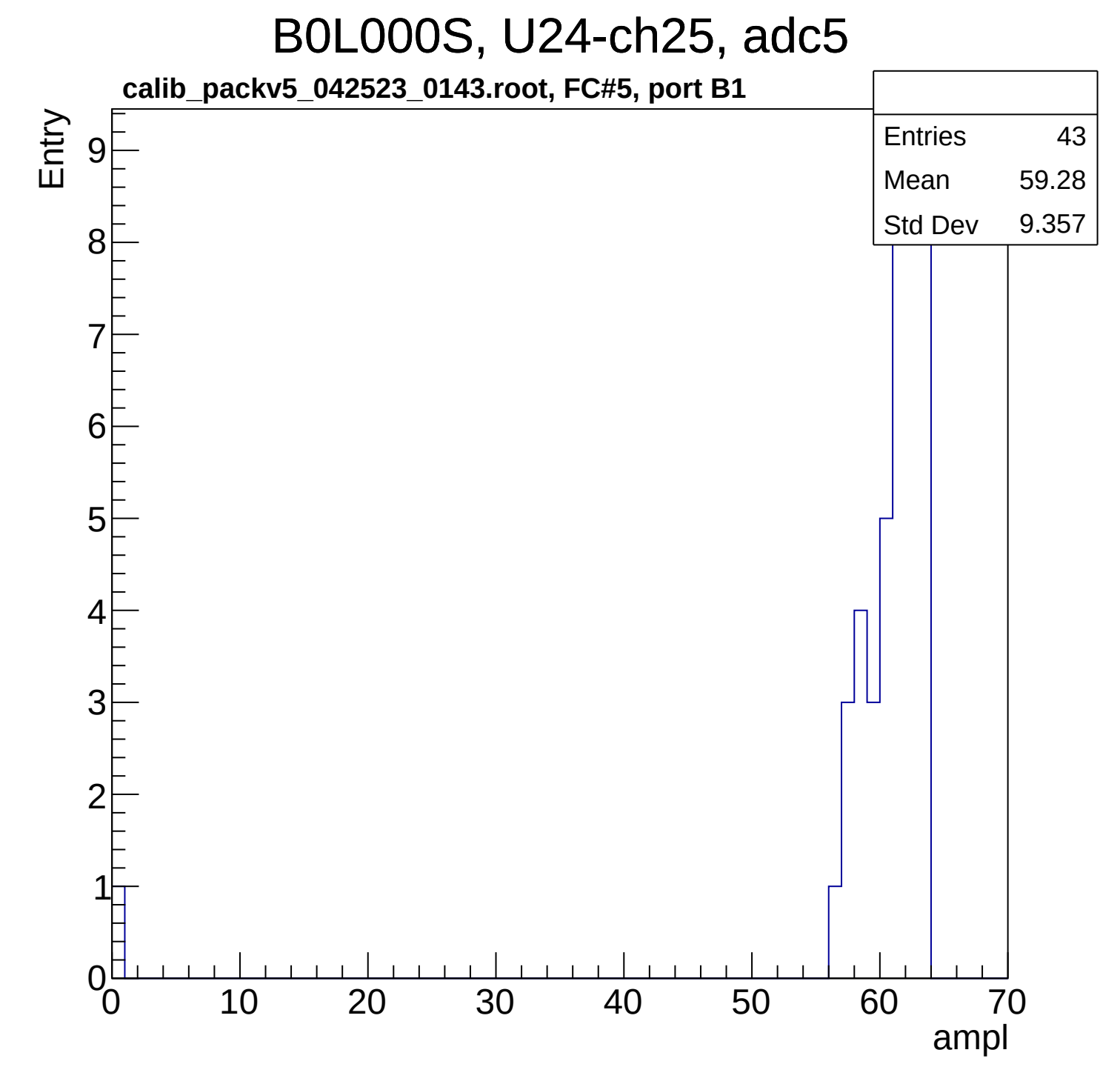

B0L000S, U24-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 29.65
Std Dev 6.711
Gaus mean : 31.4061
Gaus Width: 2.8699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 36.94
Std Dev 3.653
Gaus mean : 37.8019
Gaus Width: 3.5666
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 44.39
Std Dev 2.904
Gaus mean : 44.9525
Gaus Width: 3.5022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50.92
Std Dev 3.193
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.25
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.28
Std Dev 9.357
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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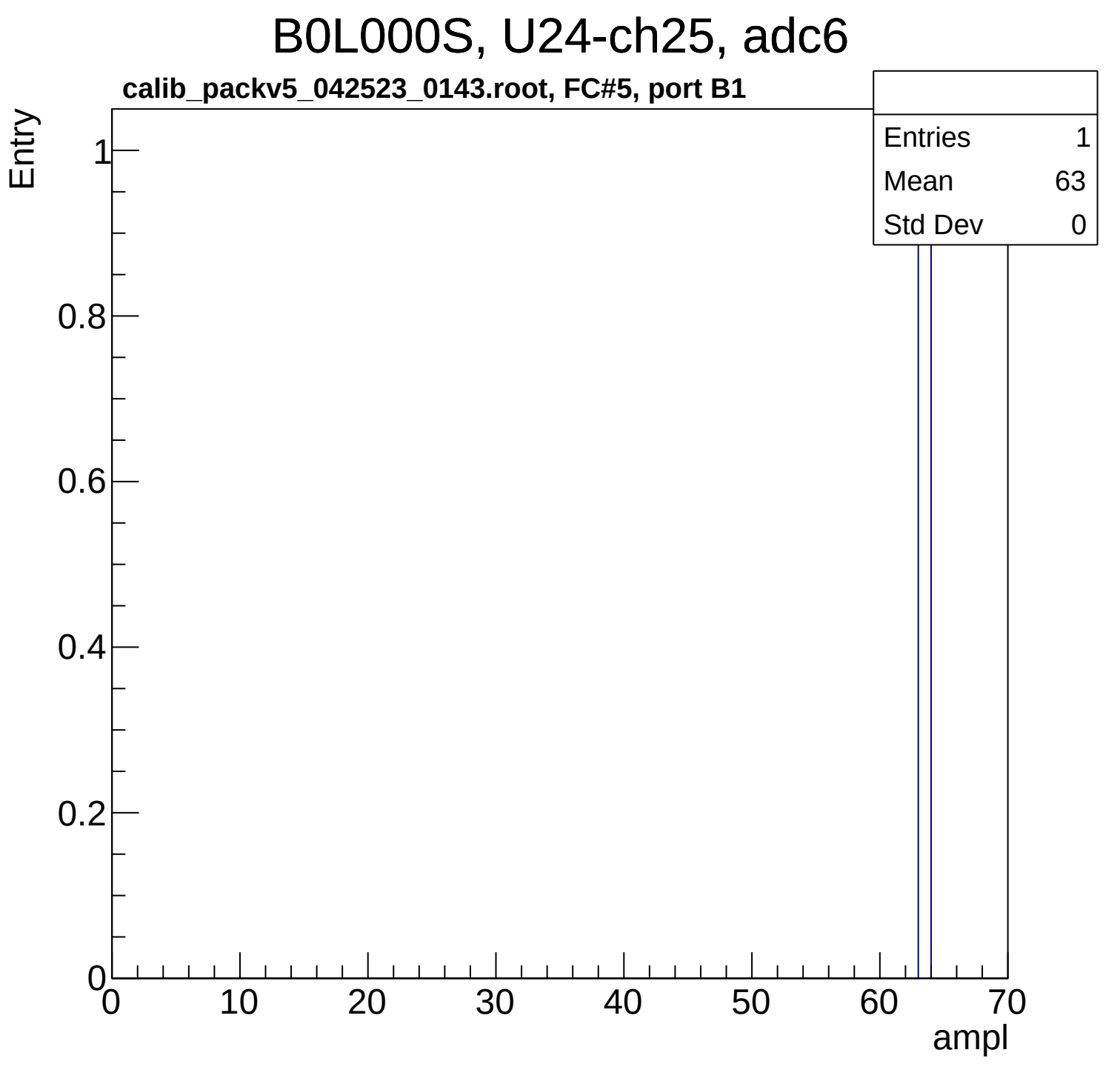
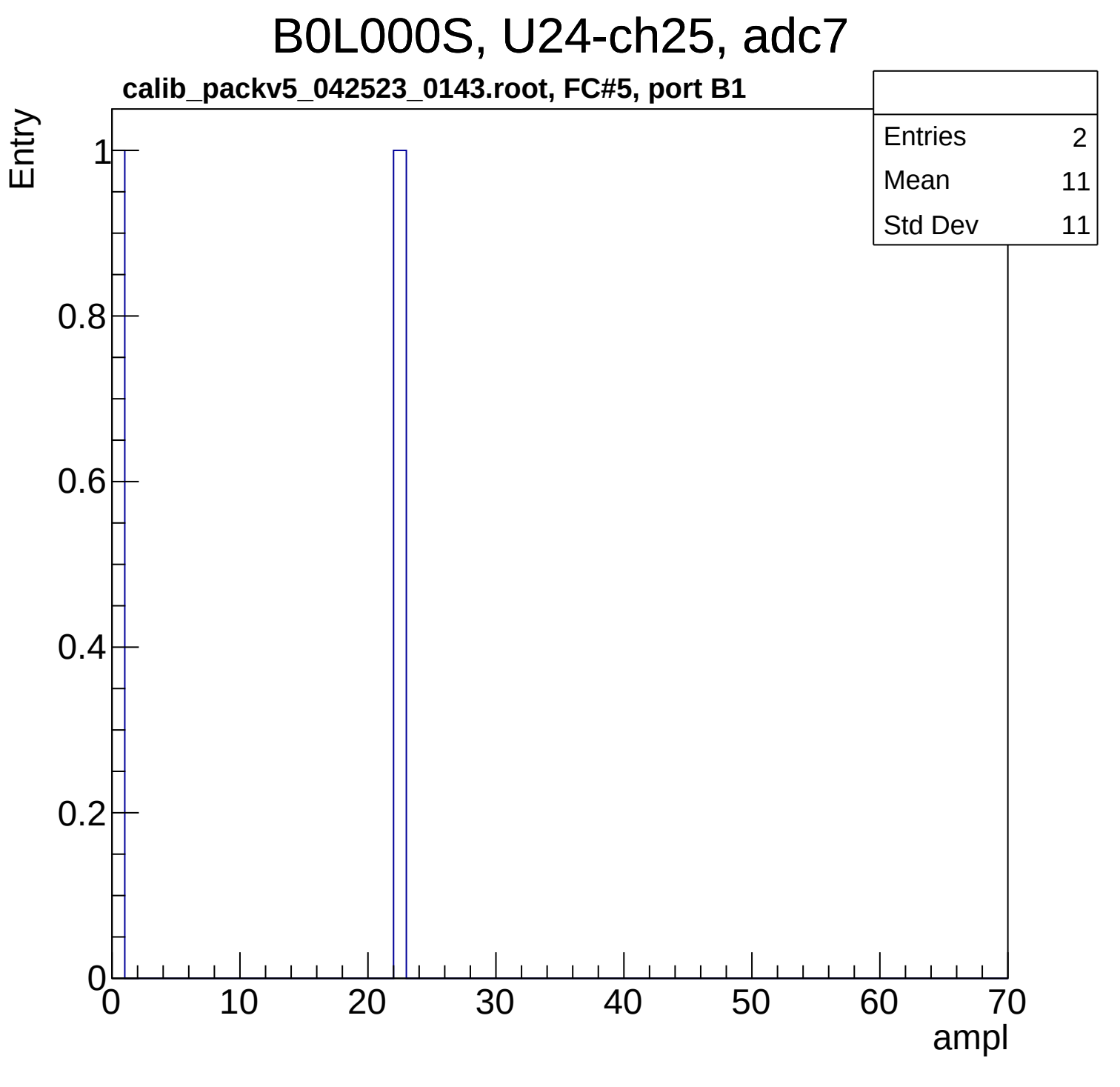

B0L000S, U24-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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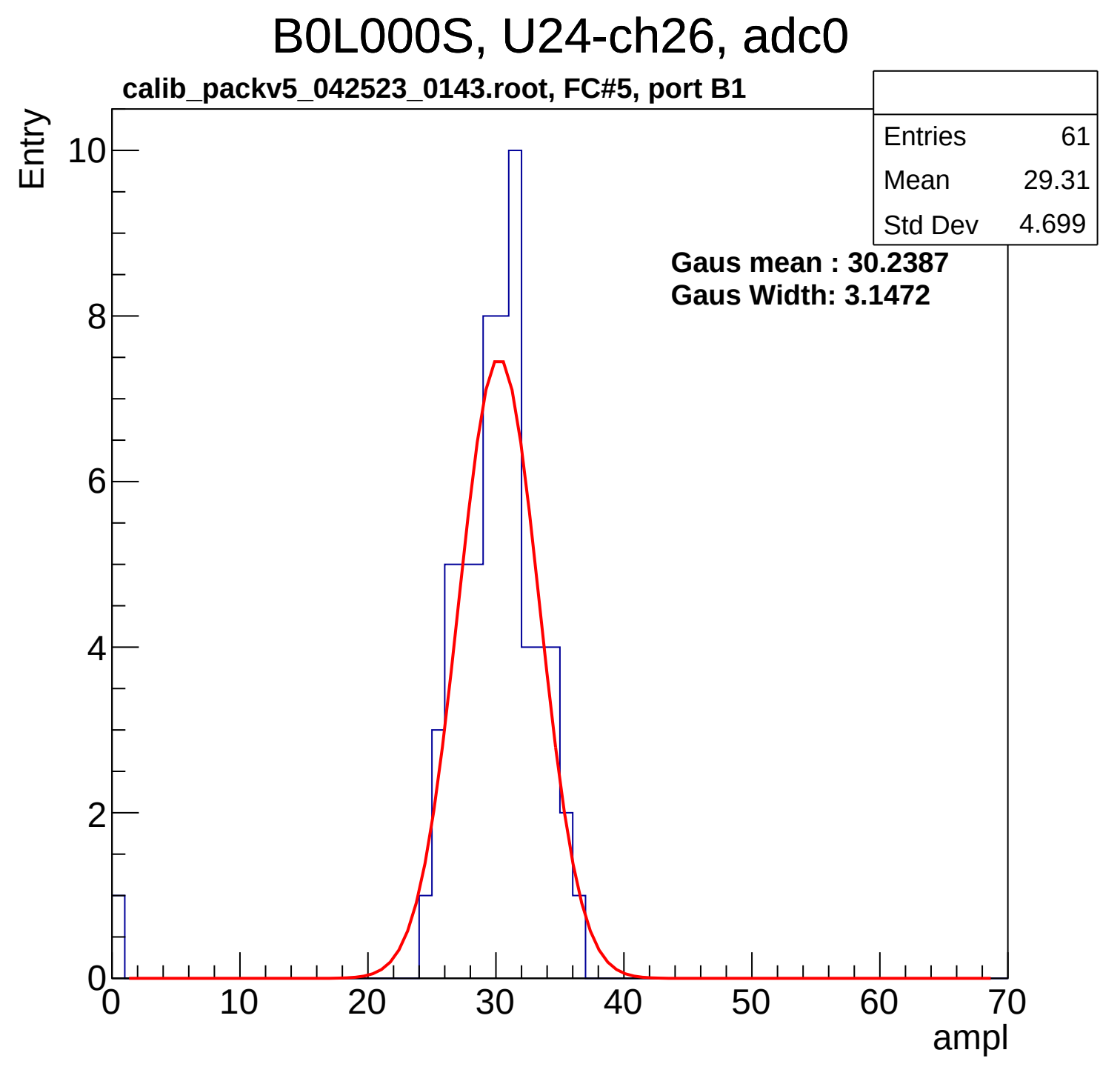
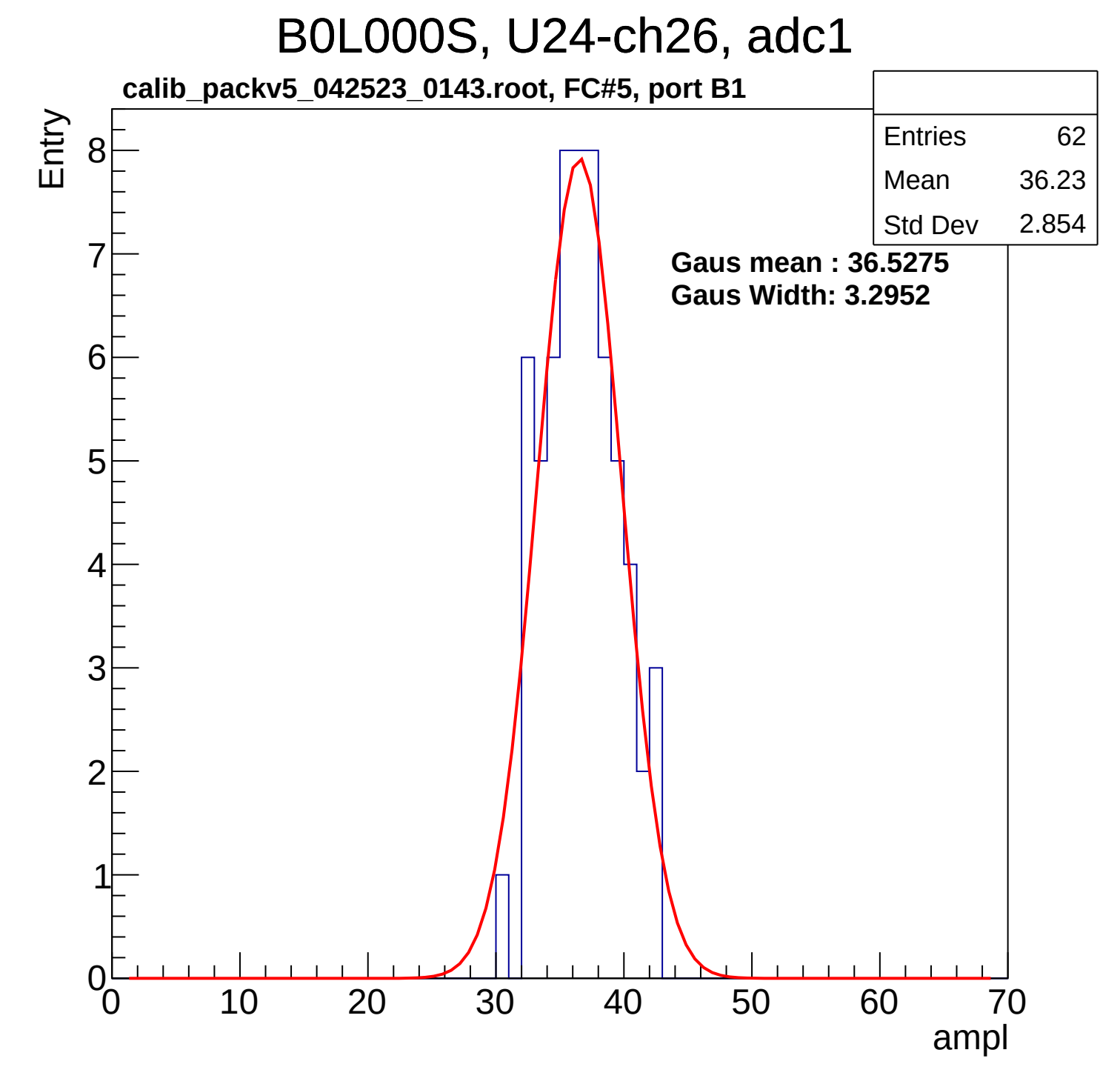
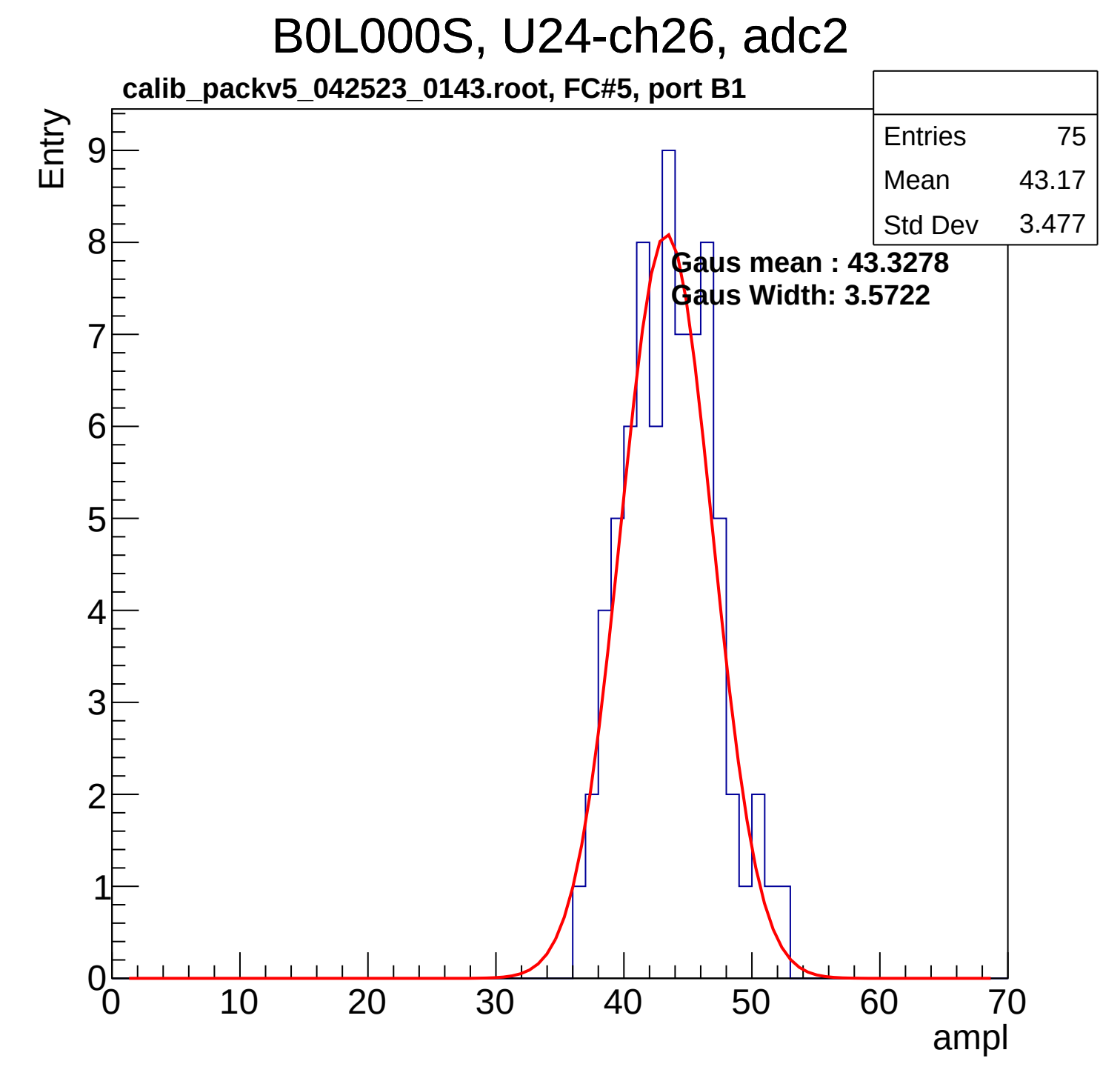
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.31
Std Dev 4.699
Gaus mean : 30.2387
Gaus Width: 3.1472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.23
Std Dev 2.854
Gaus mean : 36.5275
Gaus Width: 3.2952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 43.17
Std Dev 3.477
Gaus mean : 43.3278
Gaus Width: 3.5722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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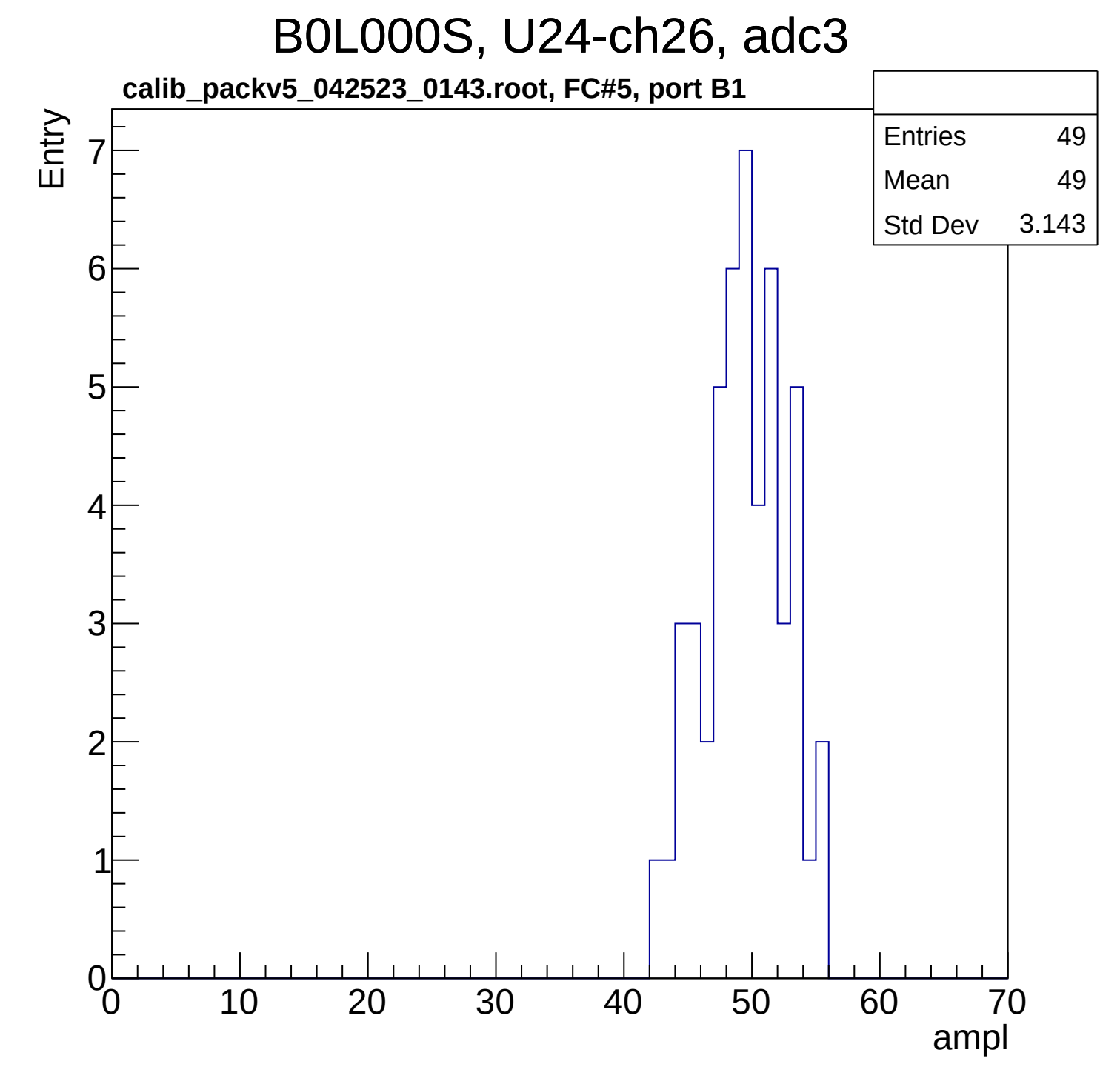
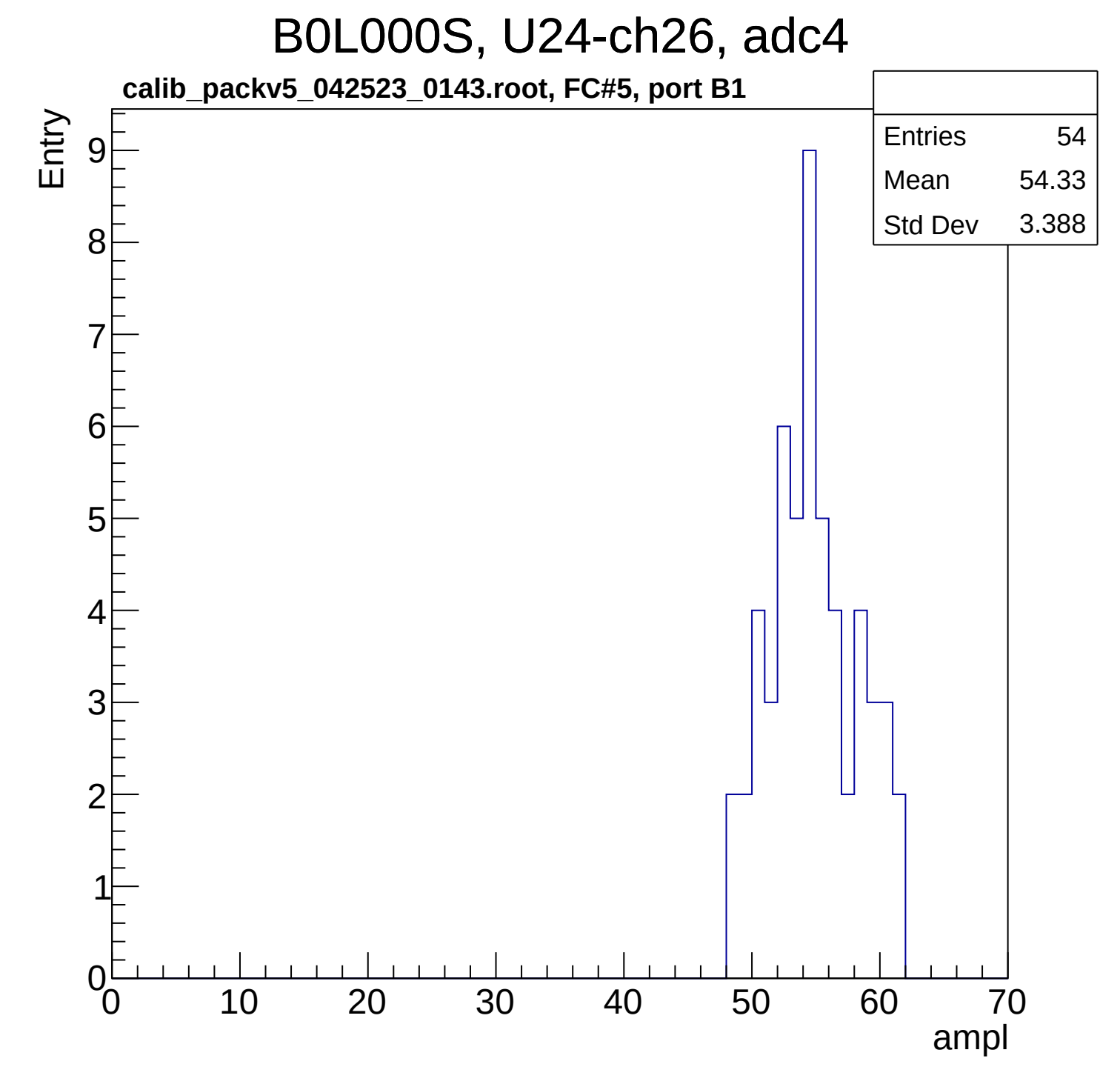
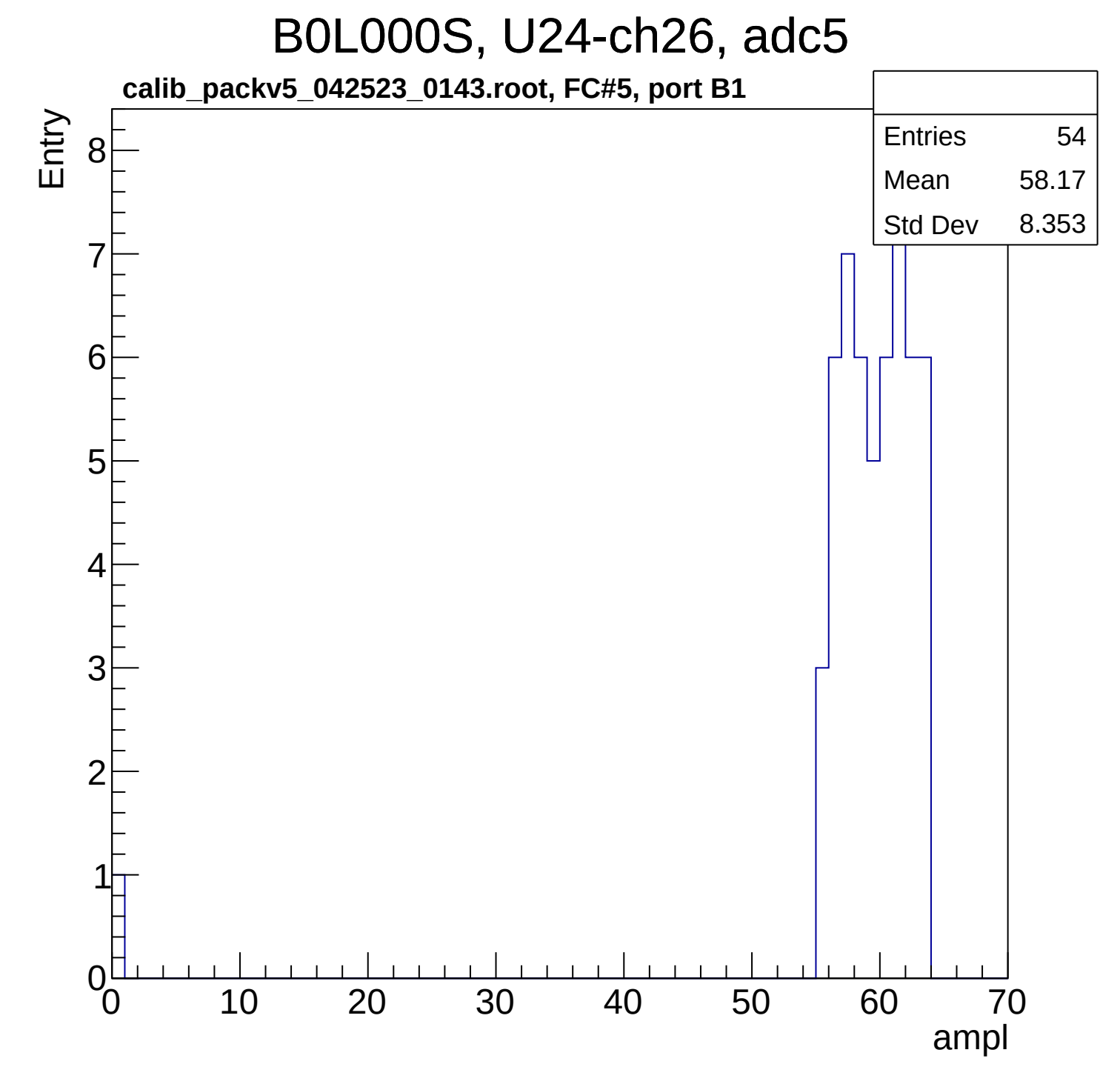

B0L000S, U24-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 49
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.33
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.17
Std Dev 8.353
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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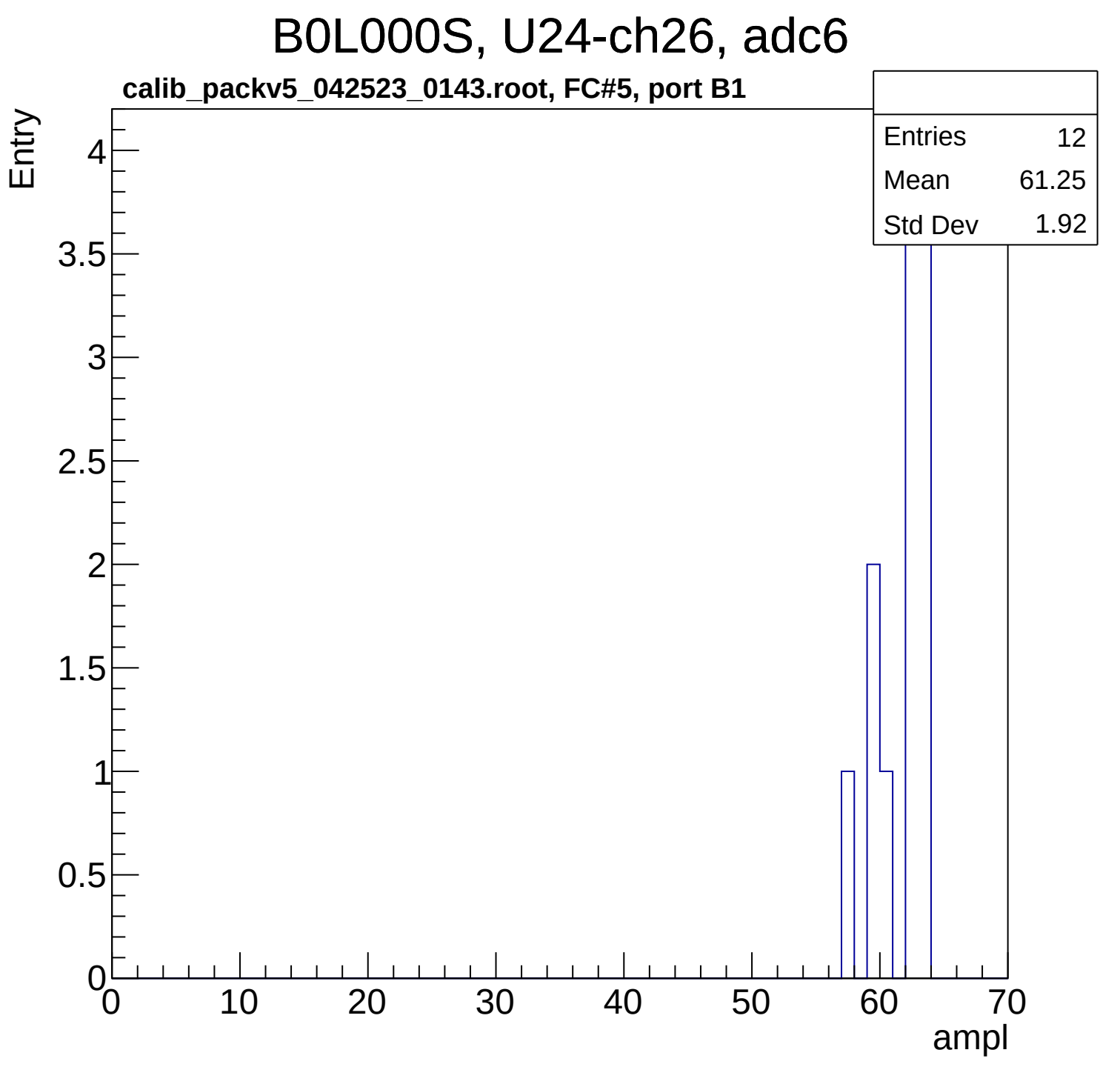
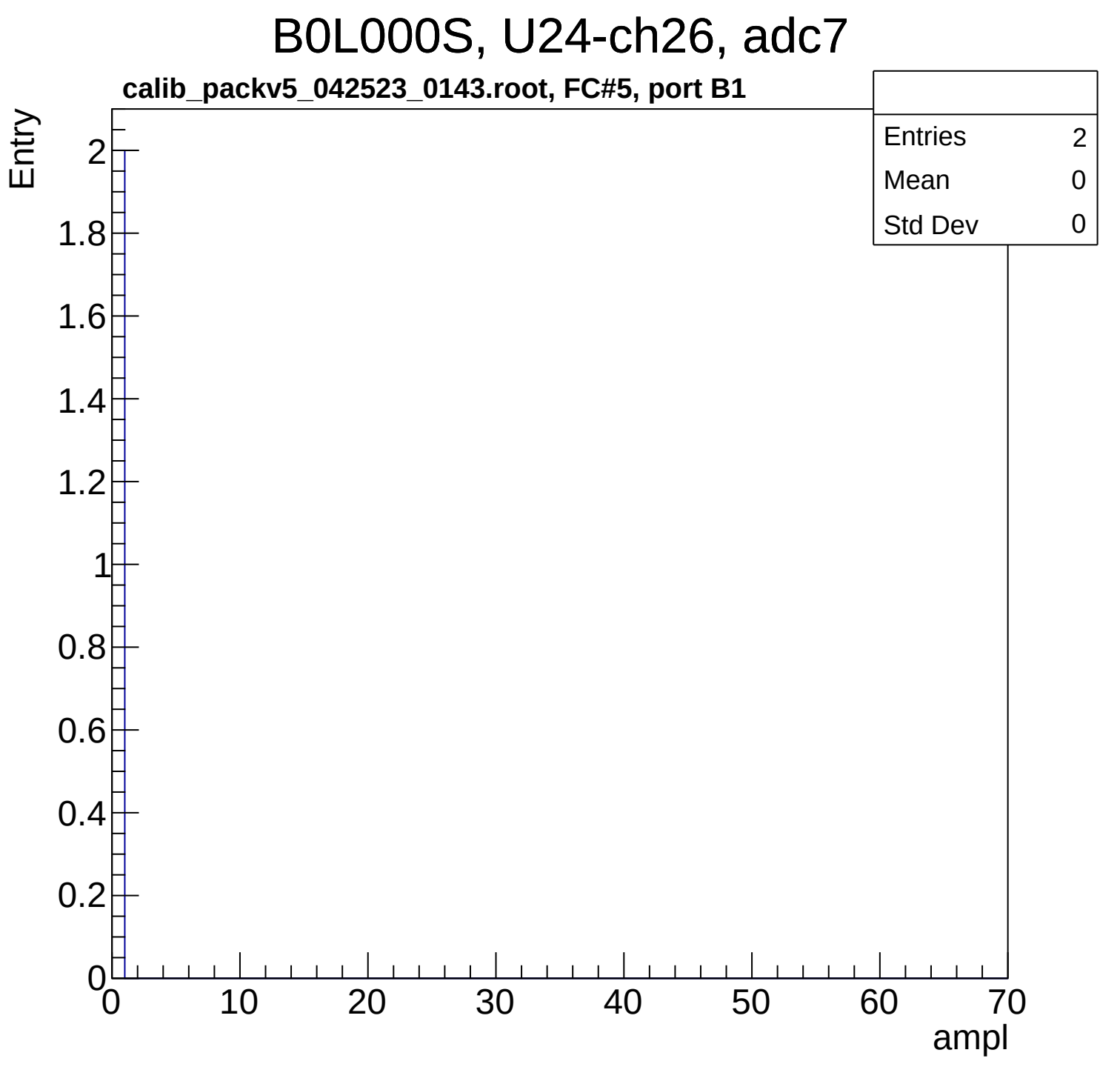

B0L000S, U24-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.25
Std Dev 1.92
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
20
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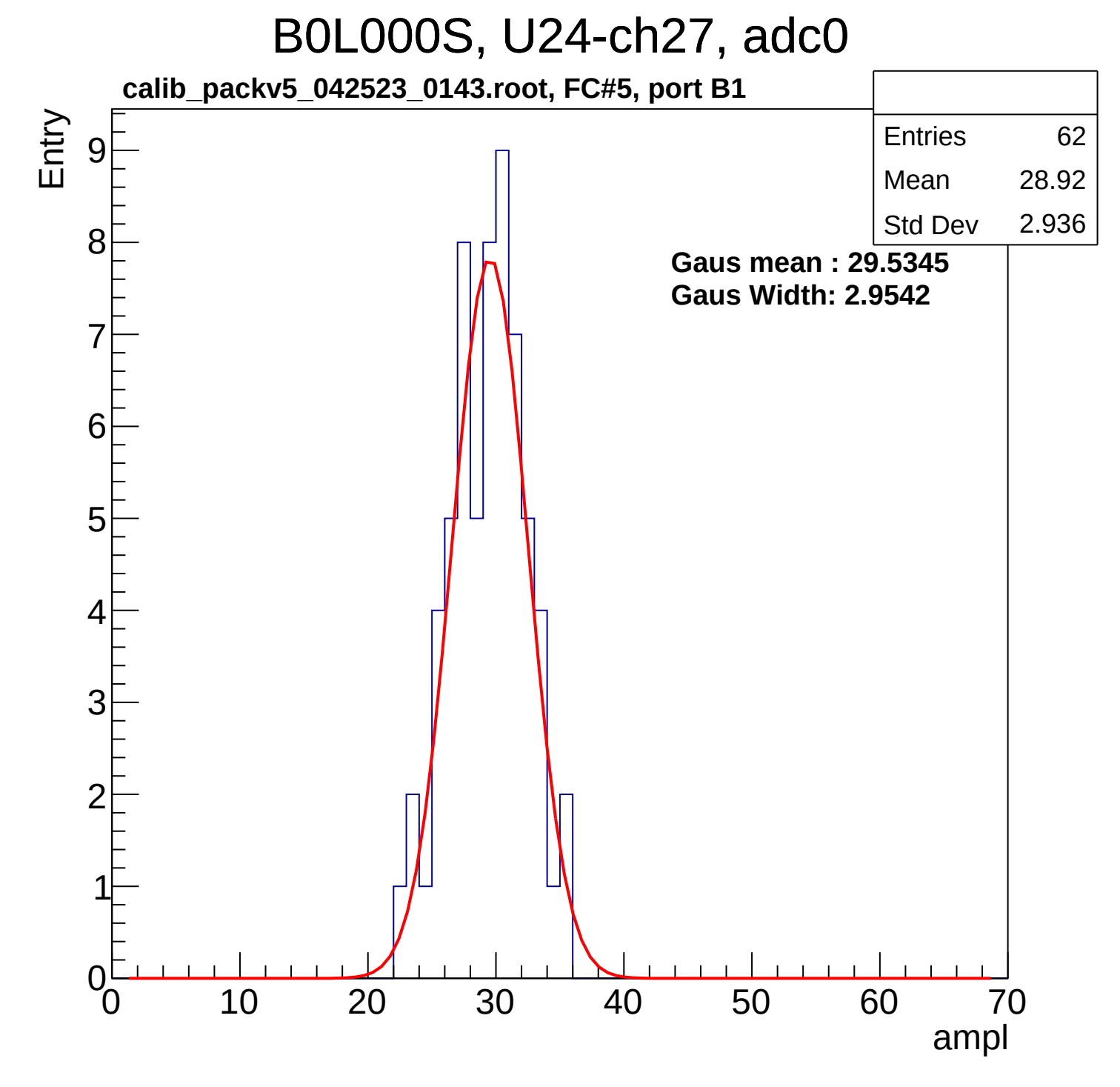
40
50
60
70
ampl


B0L000S, U24-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 28.92
Std Dev 2.936
Gaus mean : 29.5345
Gaus Width: 2.9542
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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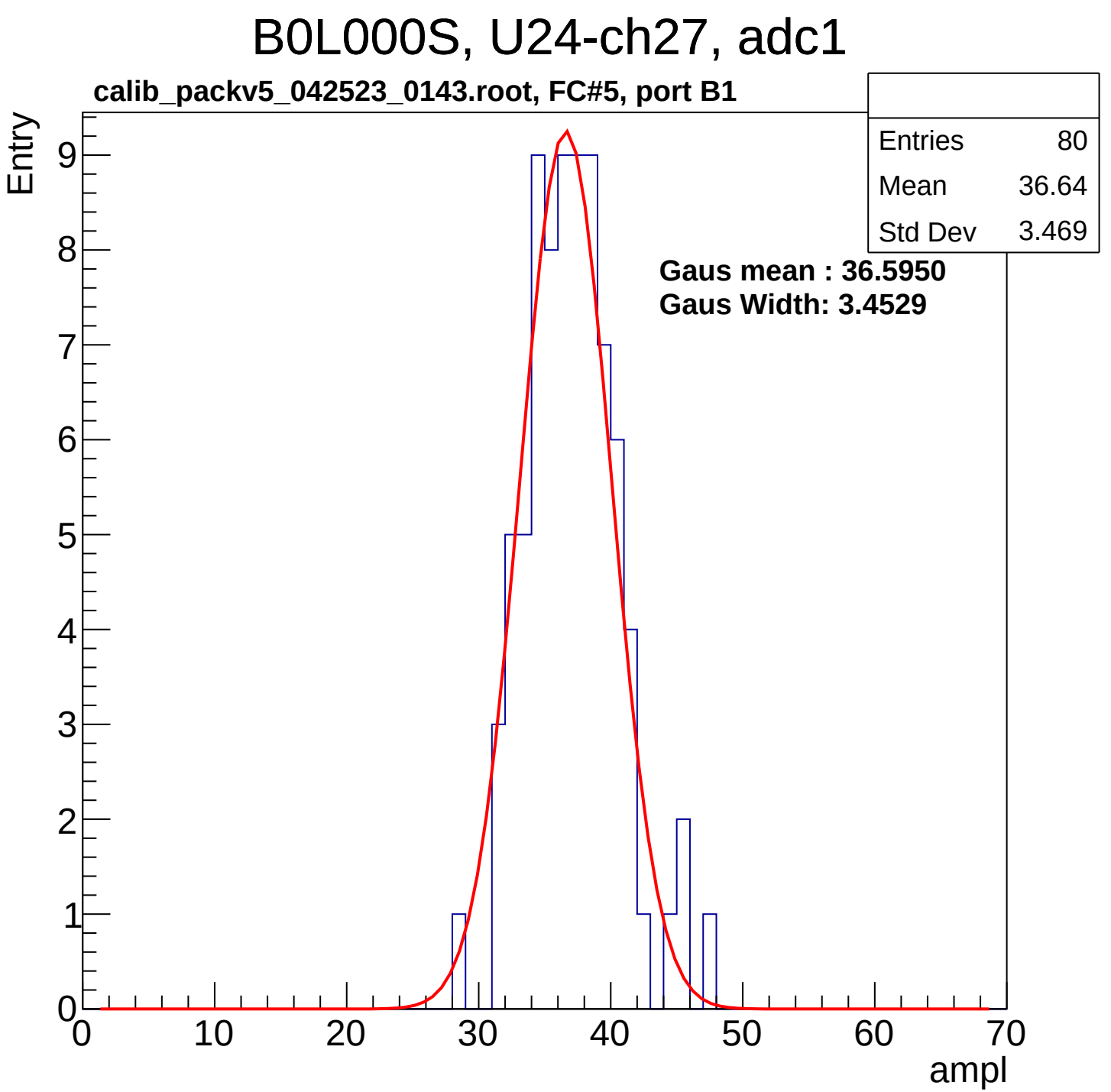
B0L000S, U24-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.64
Std Dev 3.469
Gaus mean : 36.5950
Gaus Width: 3.4529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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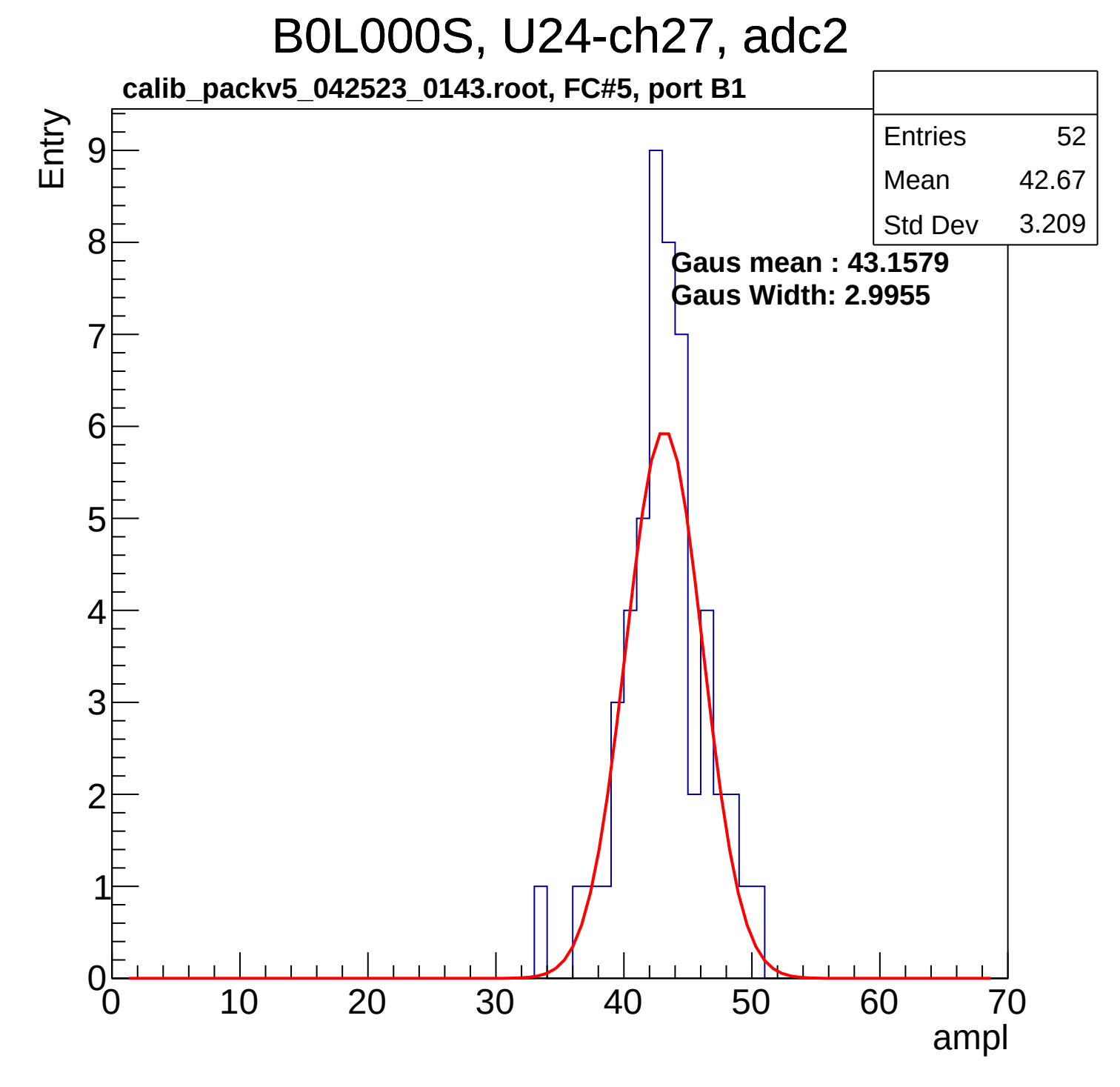
B0L000S, U24-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.67
Std Dev 3.209
Gaus mean : 43.1579
Gaus Width: 2.9955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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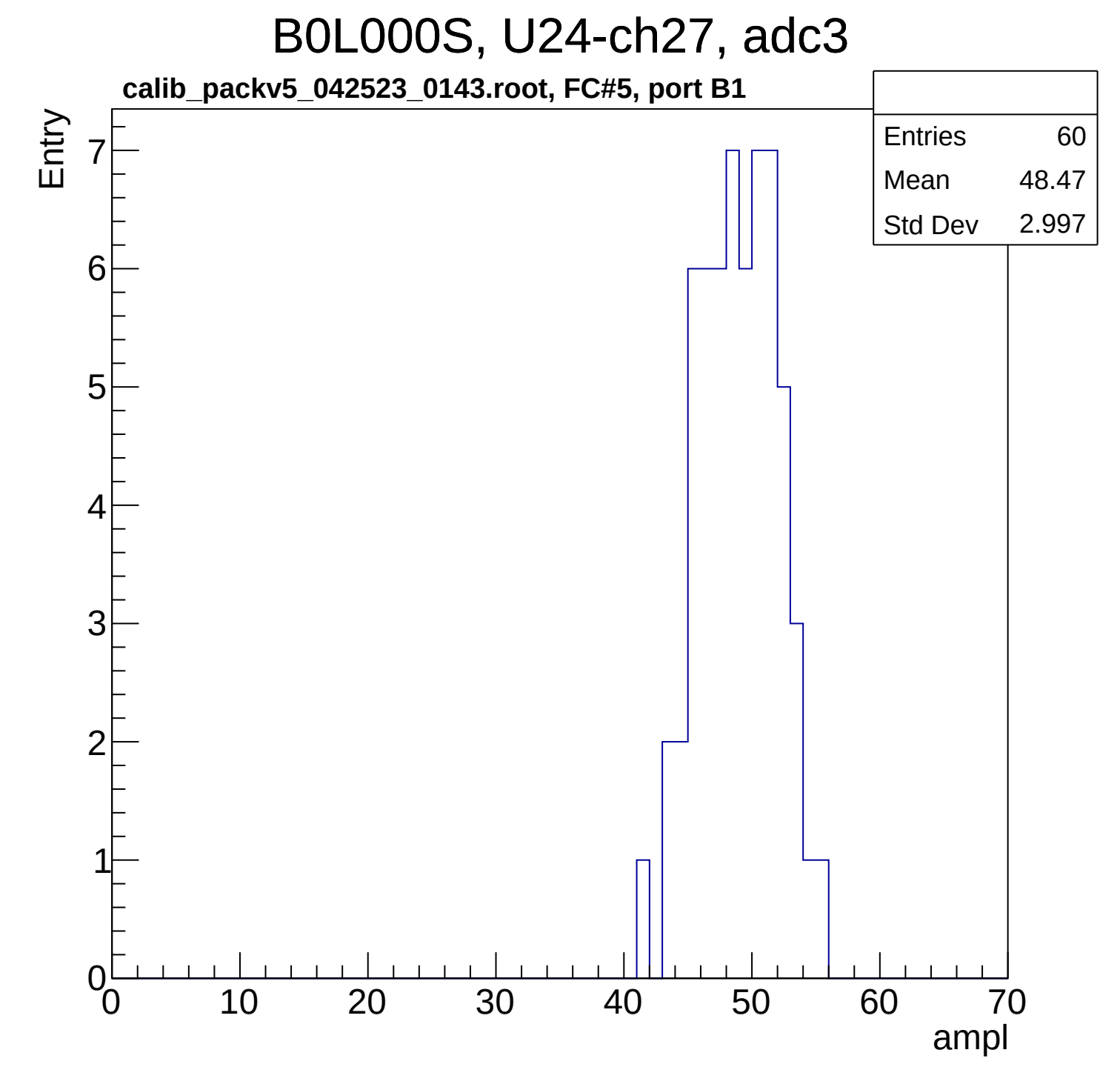
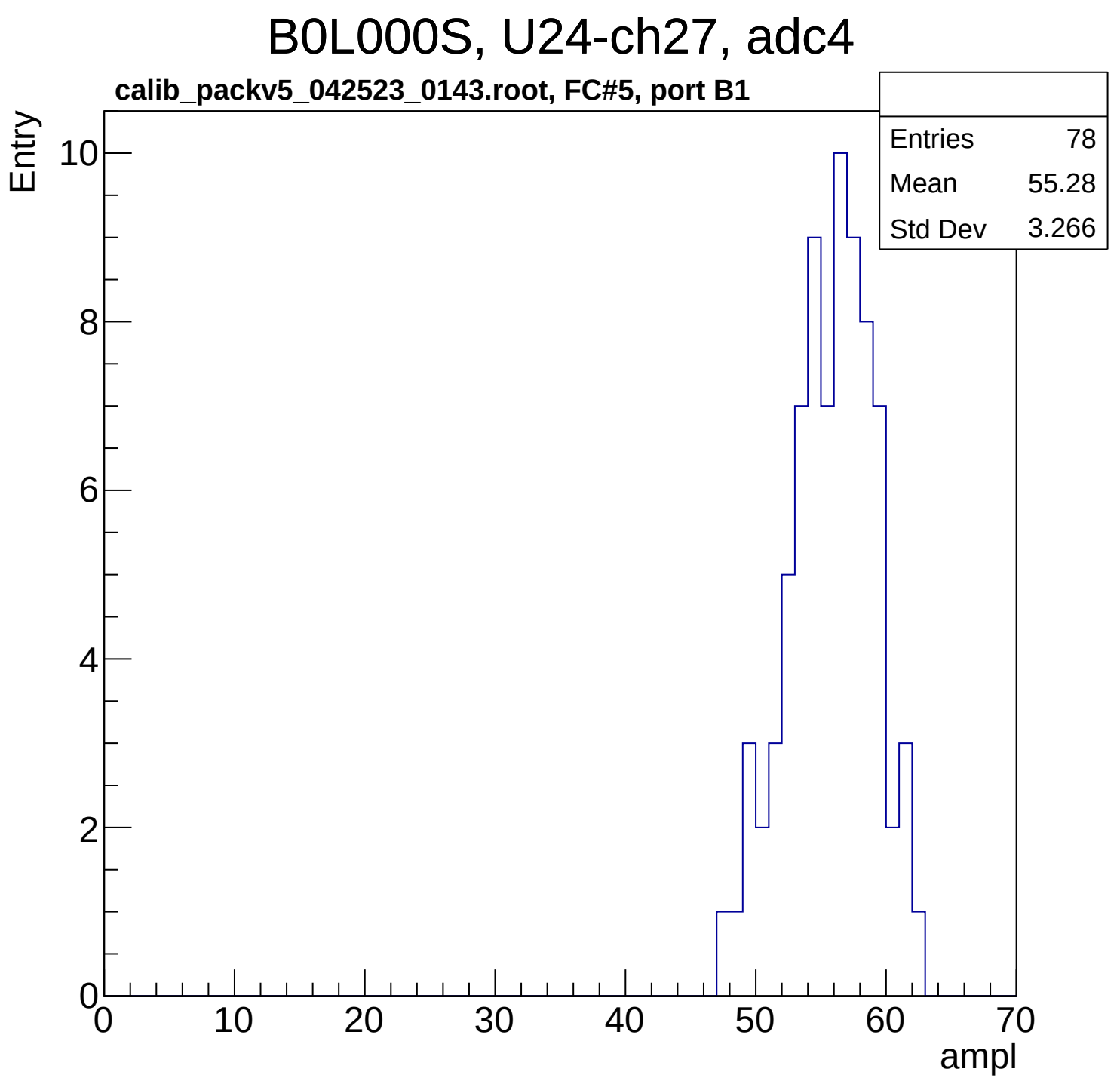
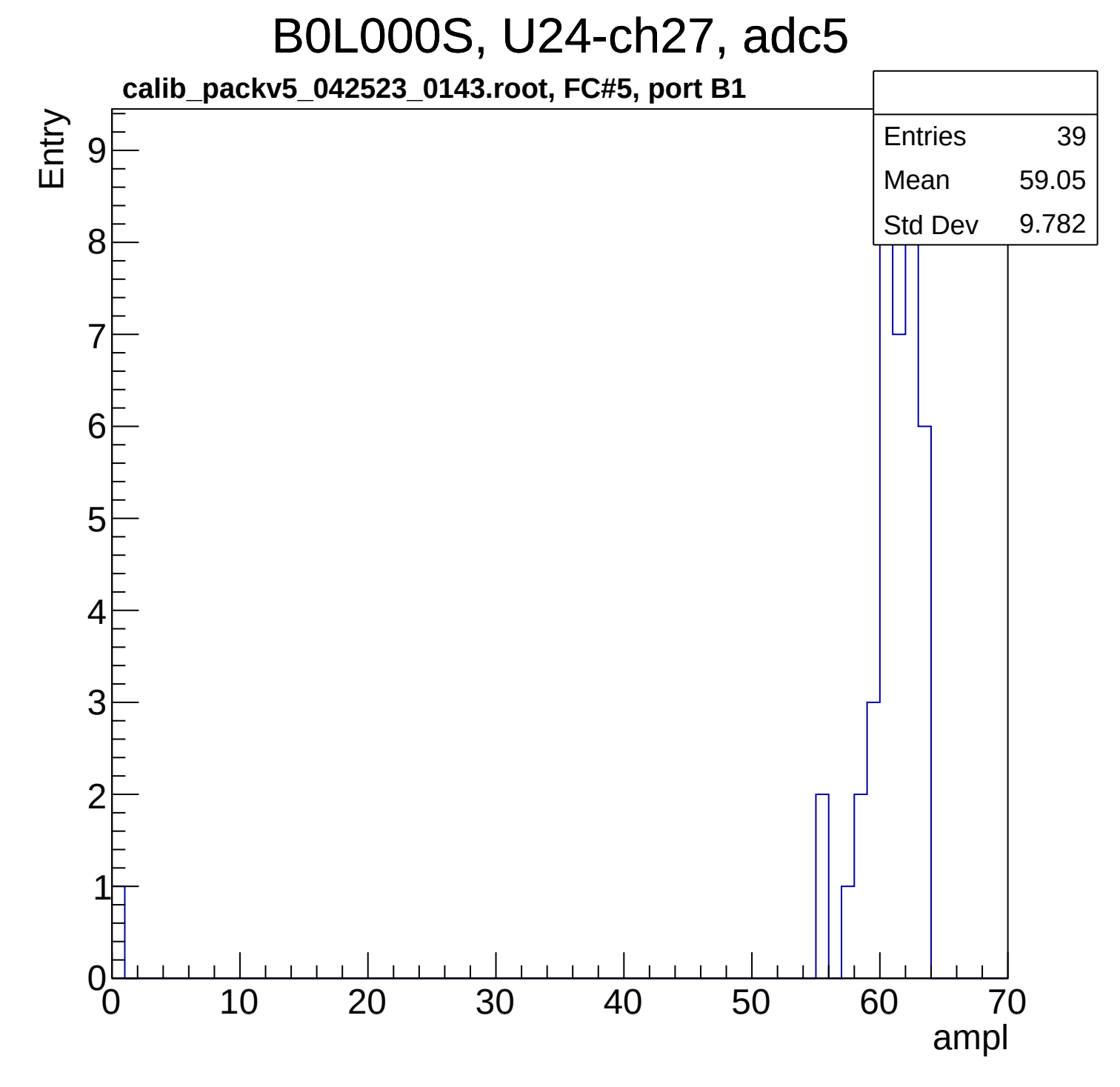

B0L000S, U24-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.47
Std Dev 2.997
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 55.28
Std Dev 3.266
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59.05
Std Dev 9.782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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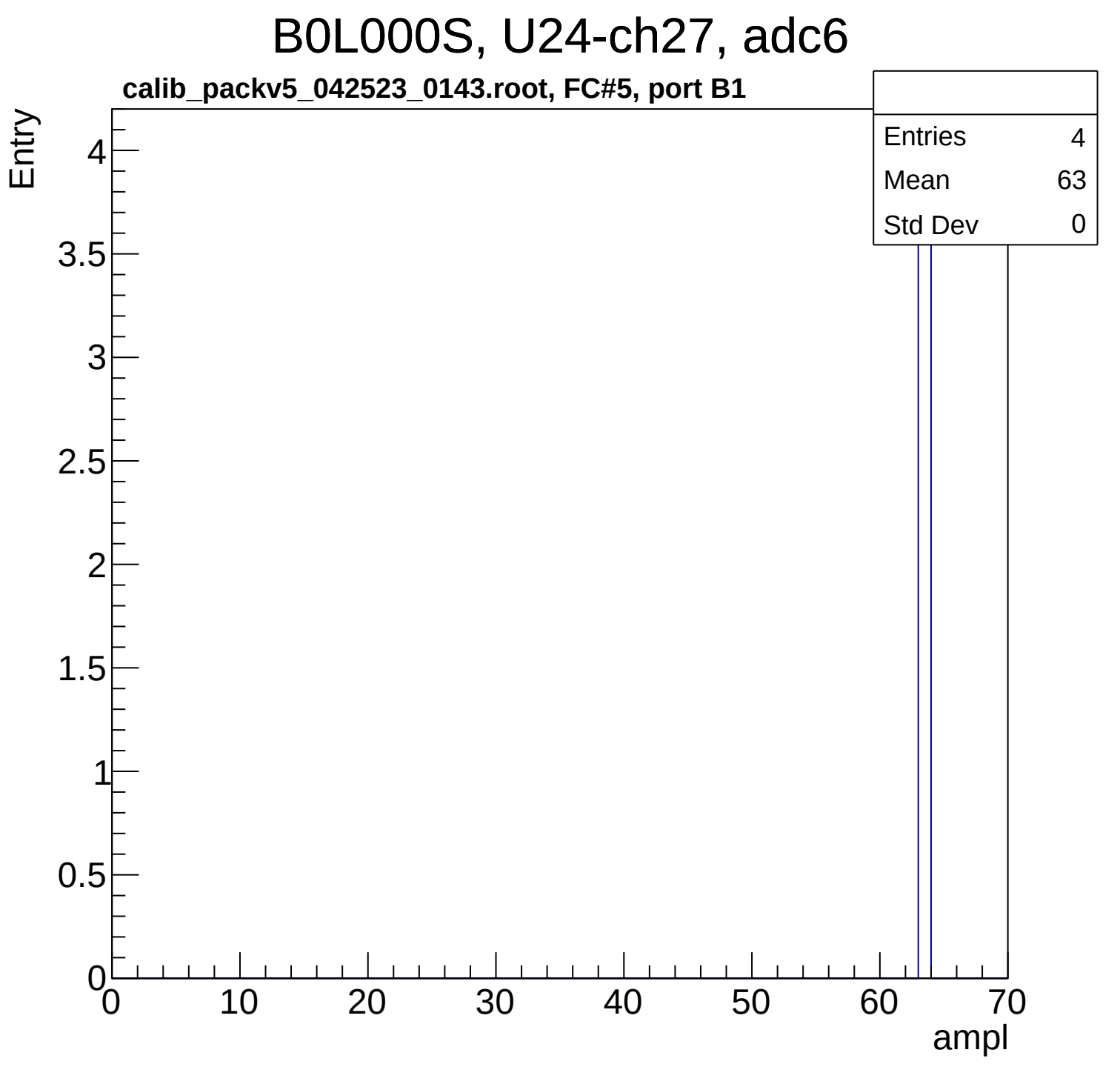
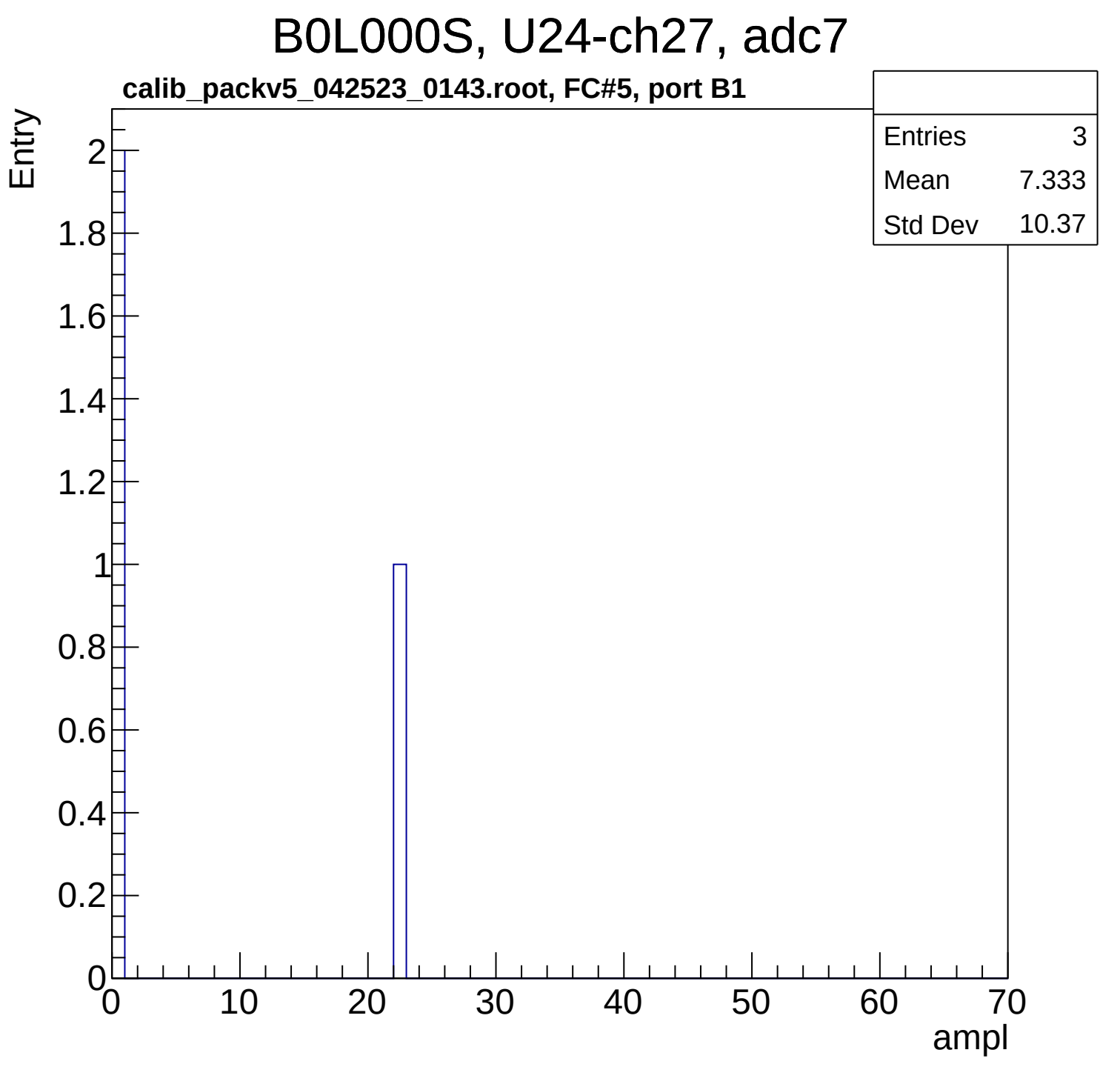
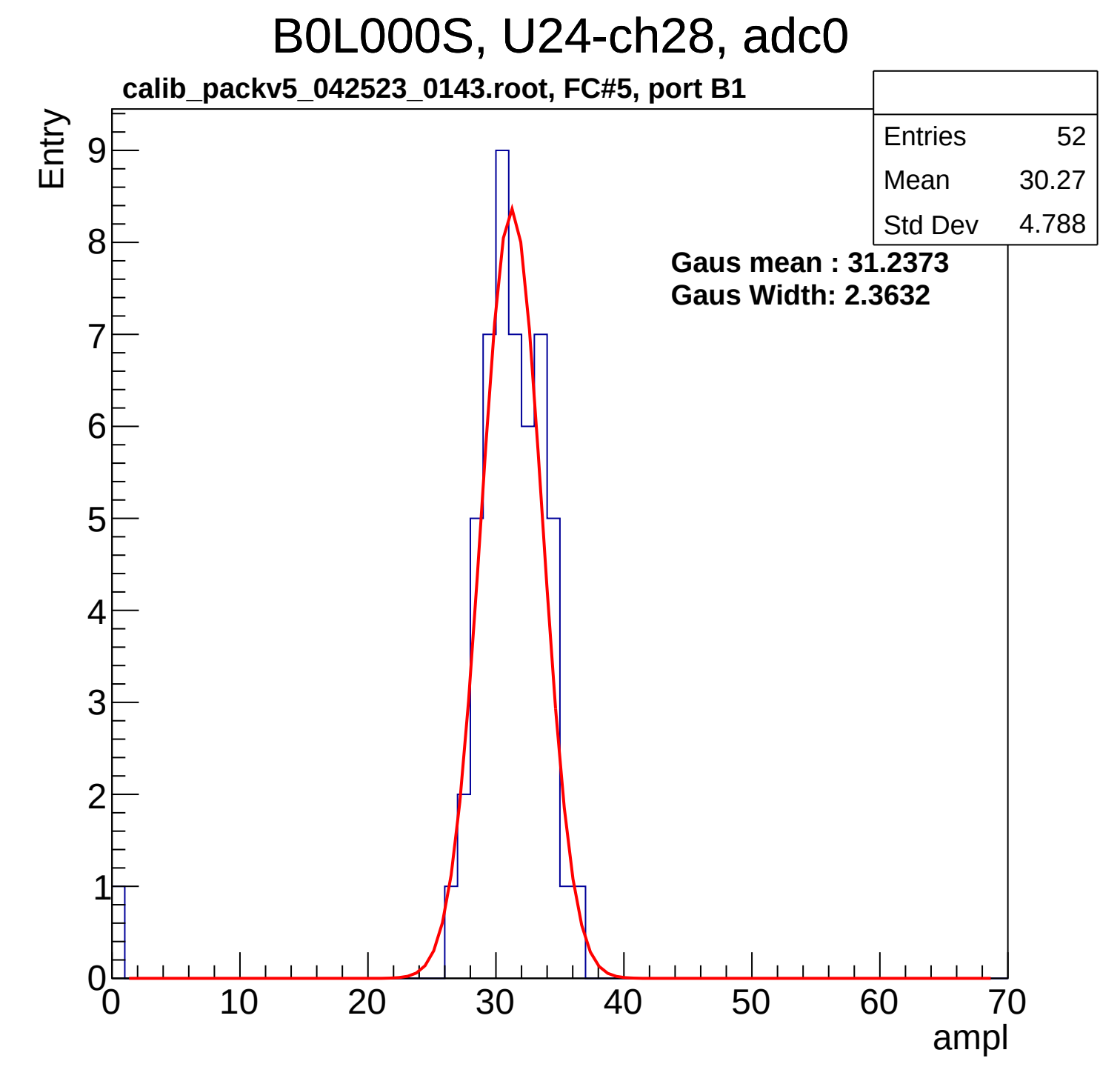
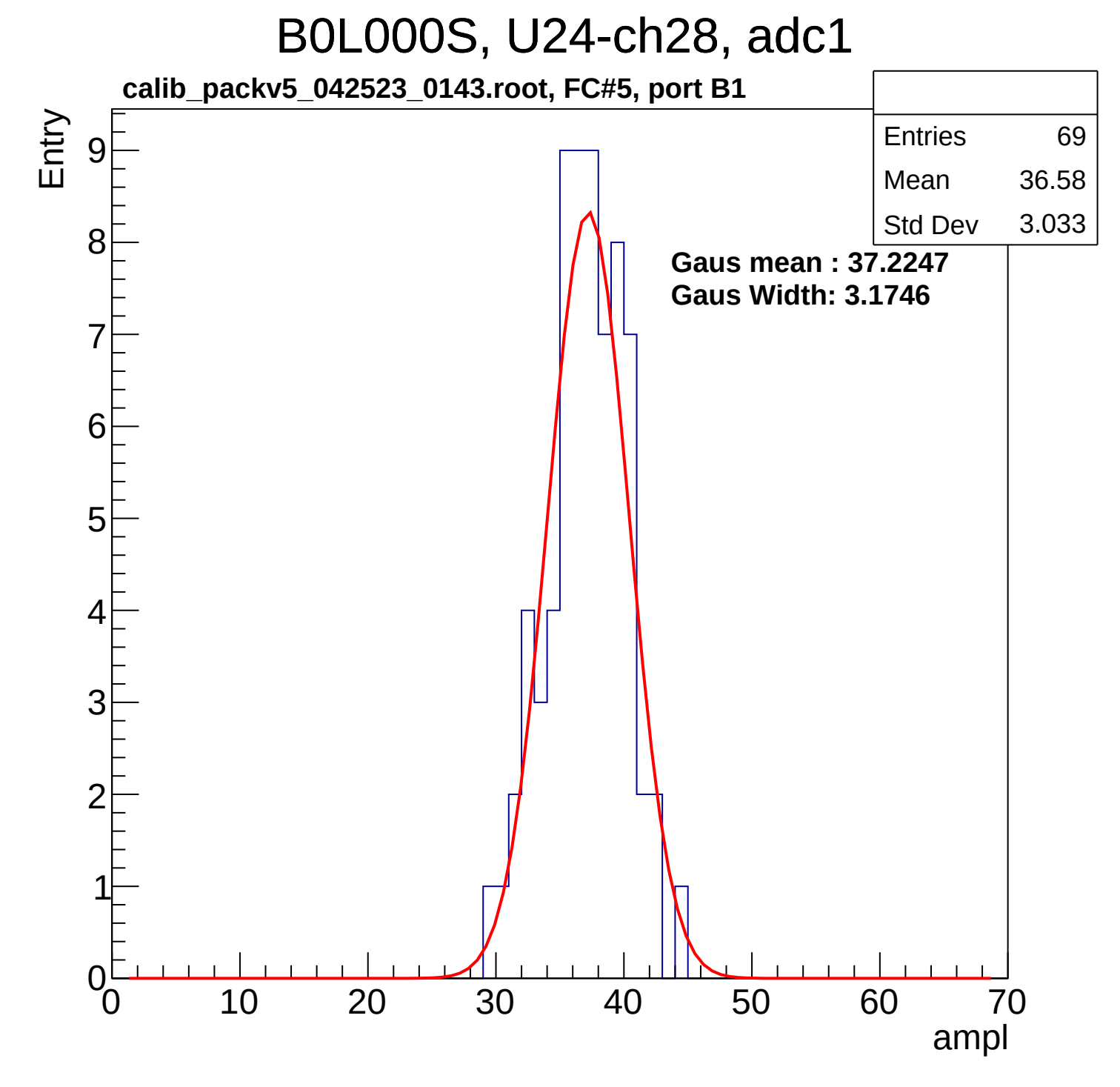

B0L000S, U24-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 30.27
Std Dev 4.788
Gaus mean : 31.2373
Gaus Width: 2.3632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.58
Std Dev 3.033
Gaus mean : 37.2247
Gaus Width: 3.1746
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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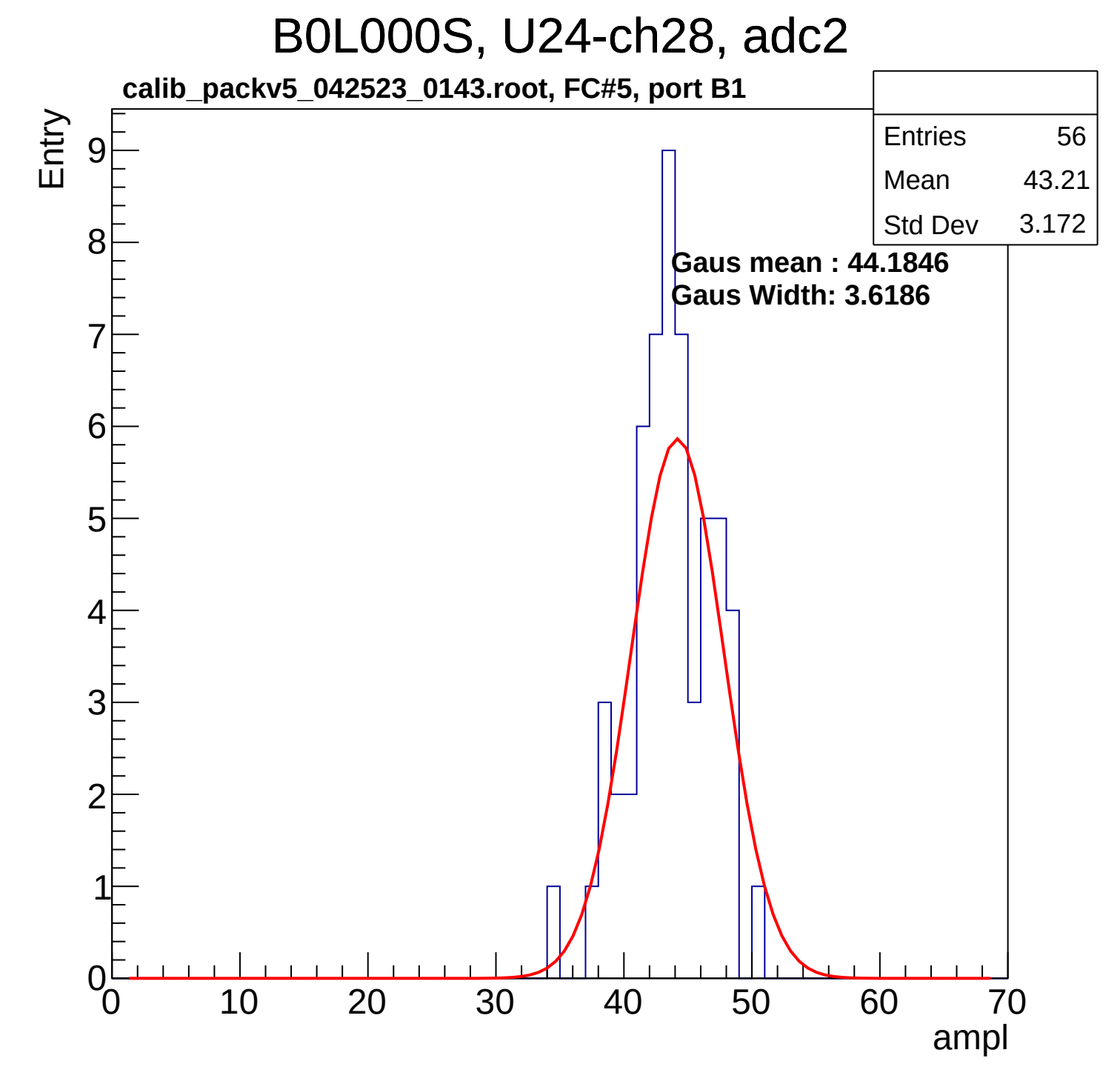
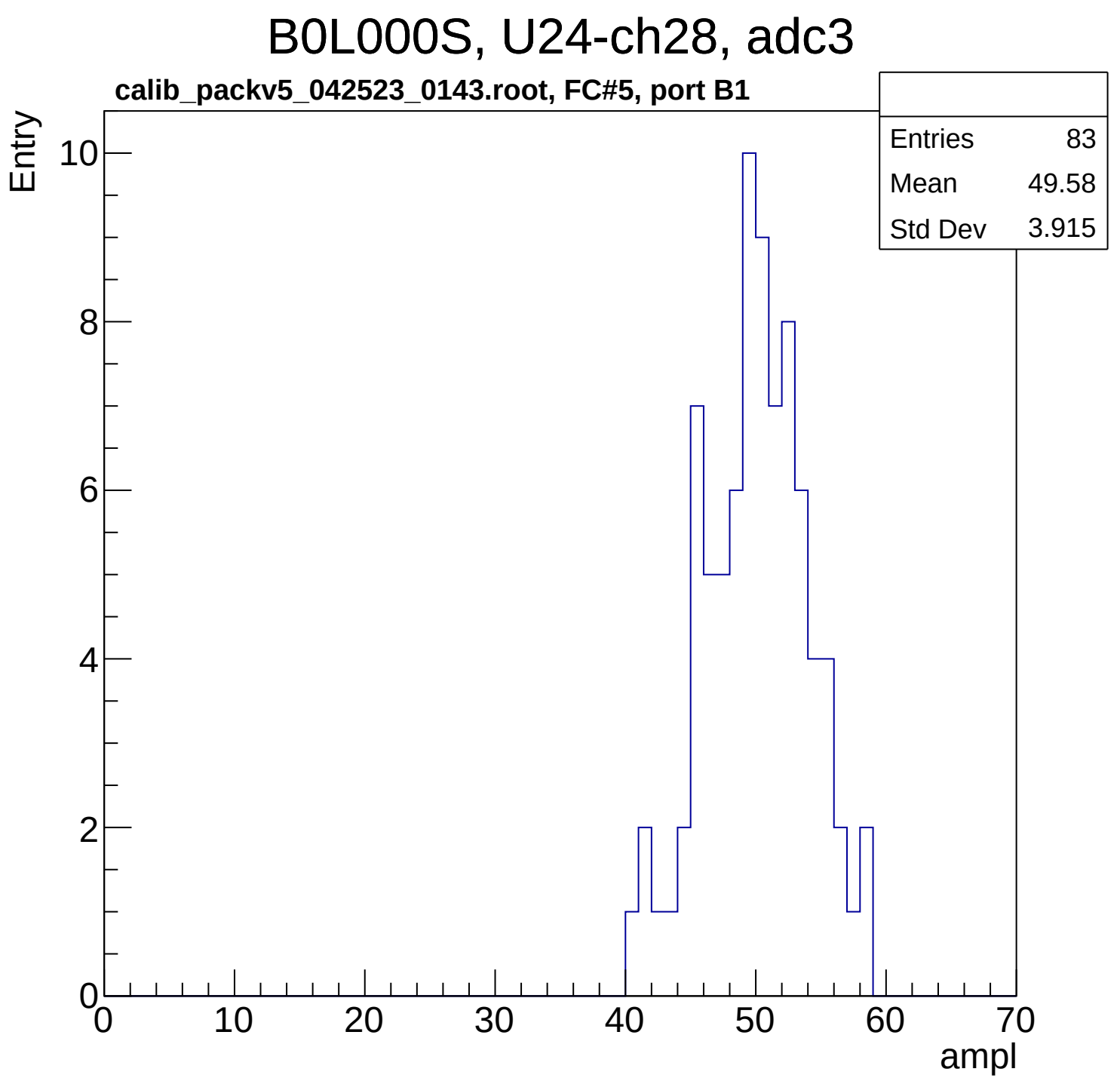
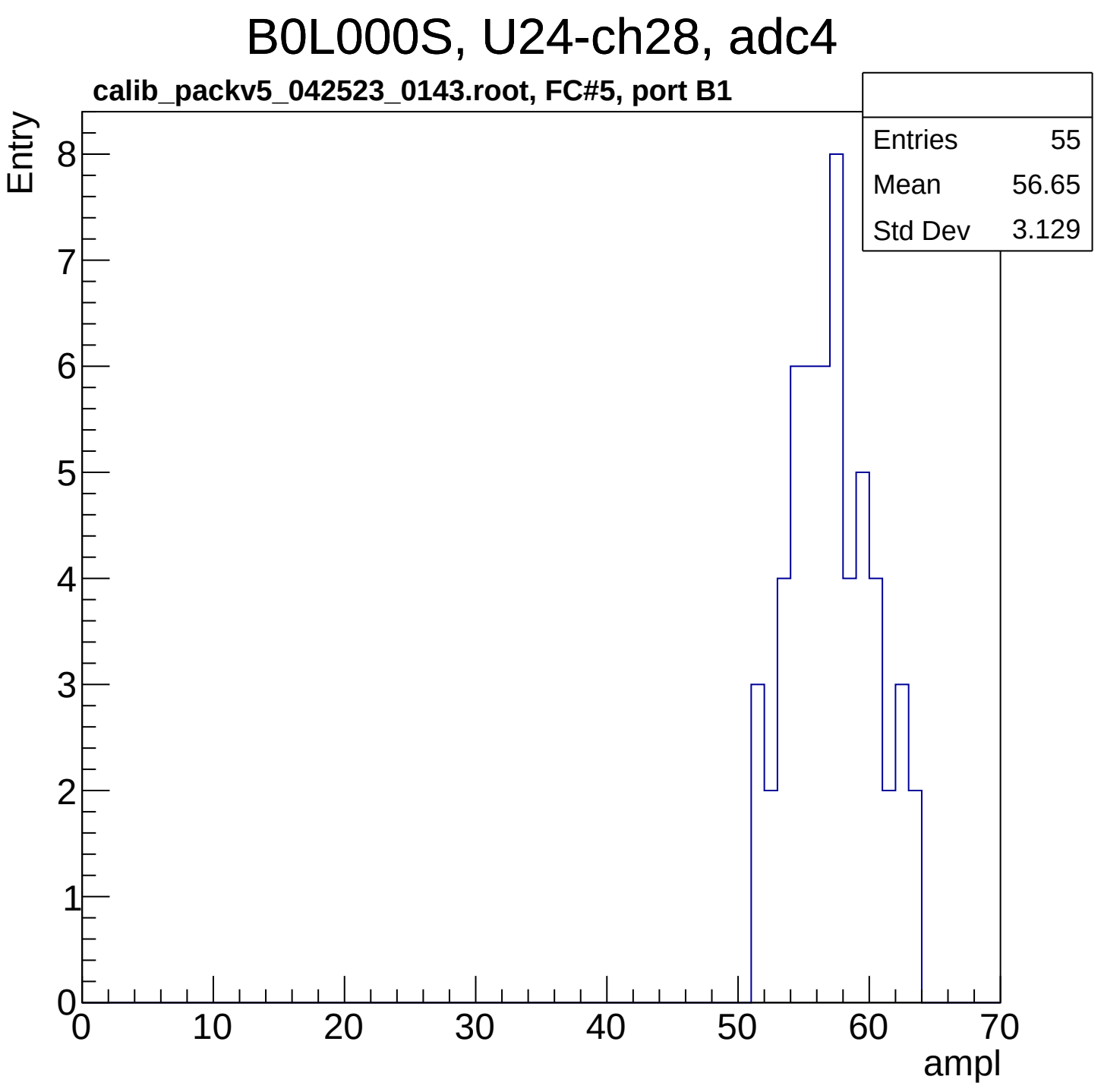

B0L000S, U24-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.21
Std Dev 3.172
Gaus mean : 44.1846
Gaus Width: 3.6186
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 49.58
Std Dev 3.915
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.65
Std Dev 3.129
Entry
ampl
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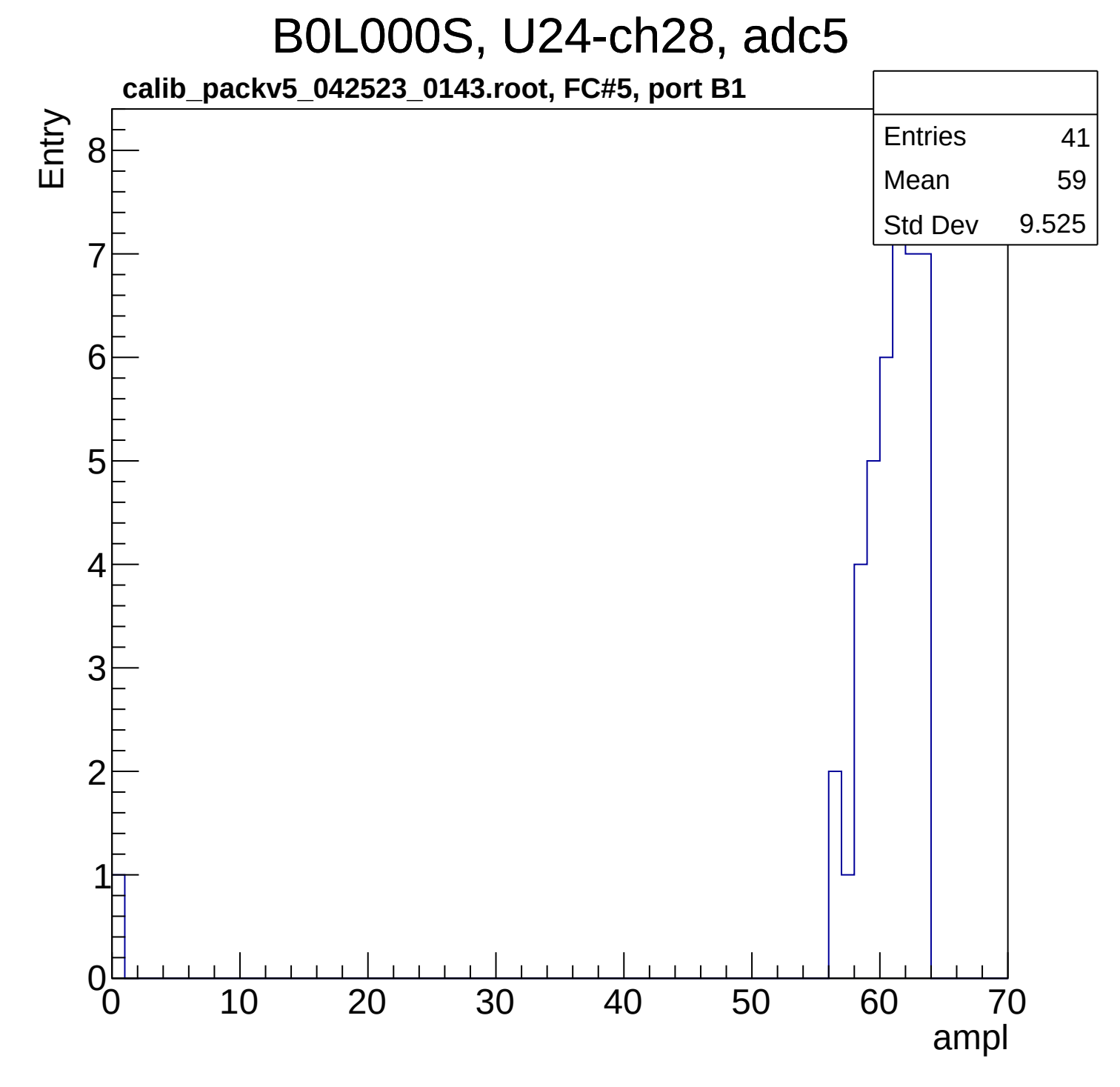
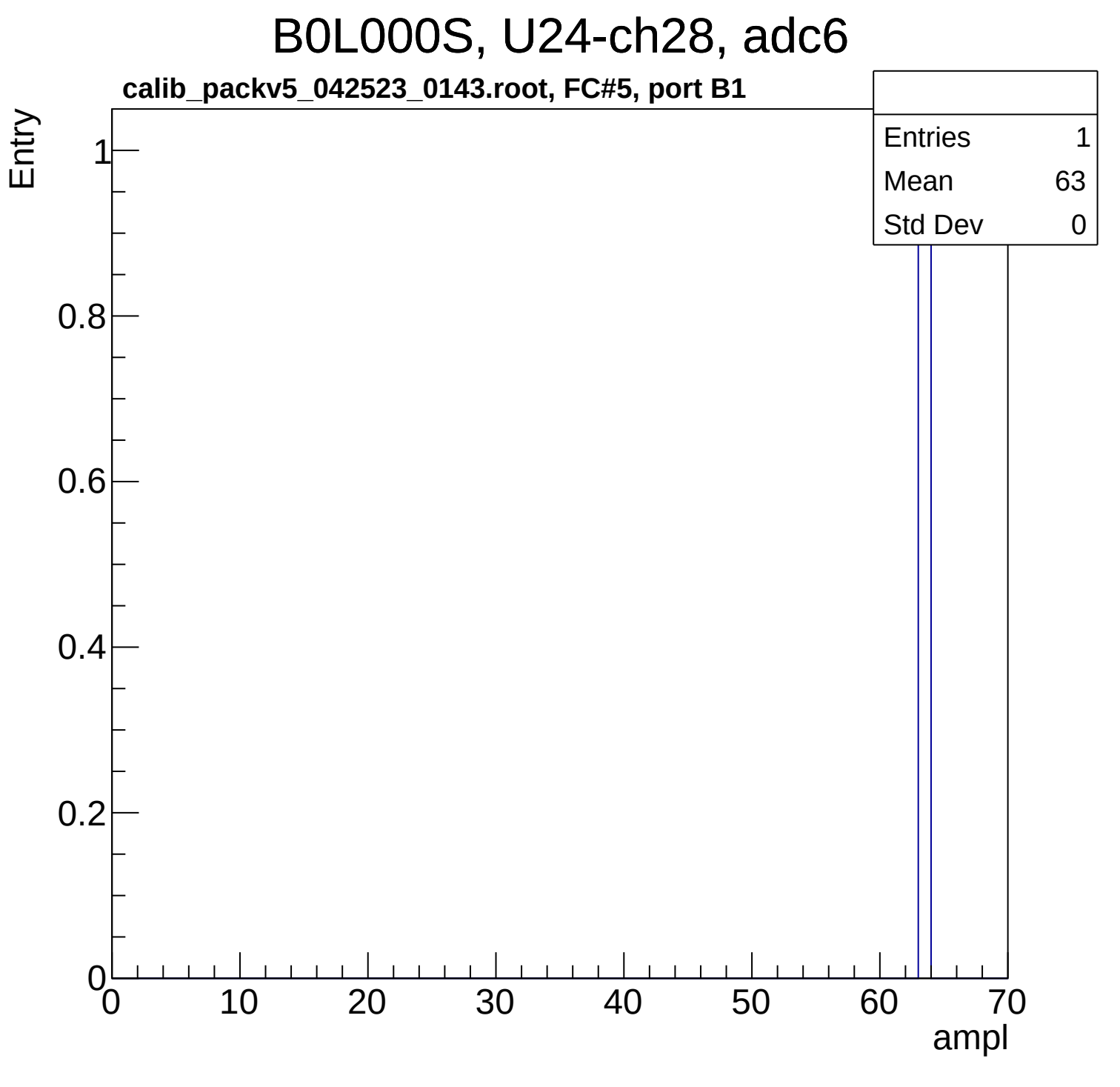
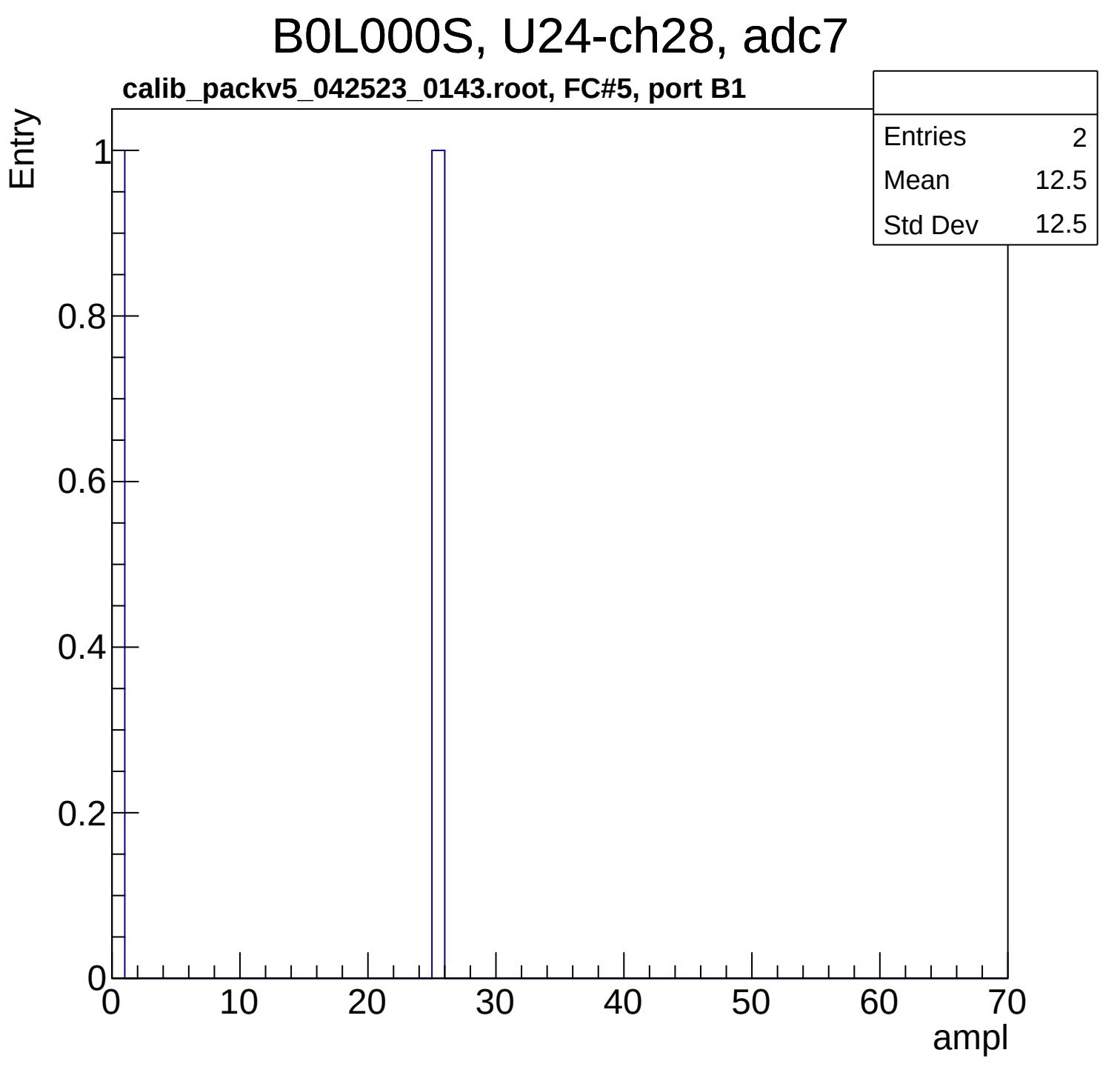
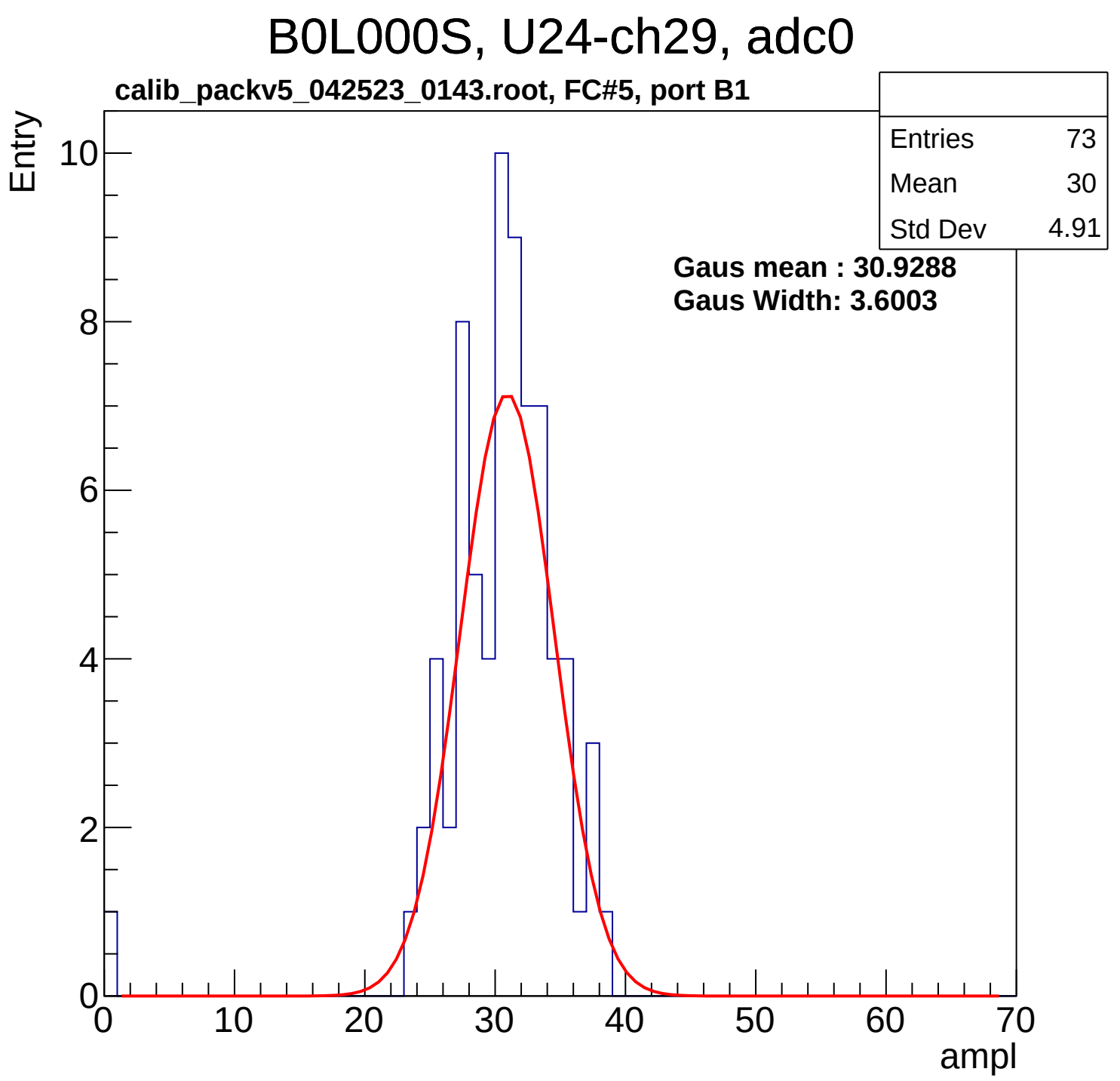
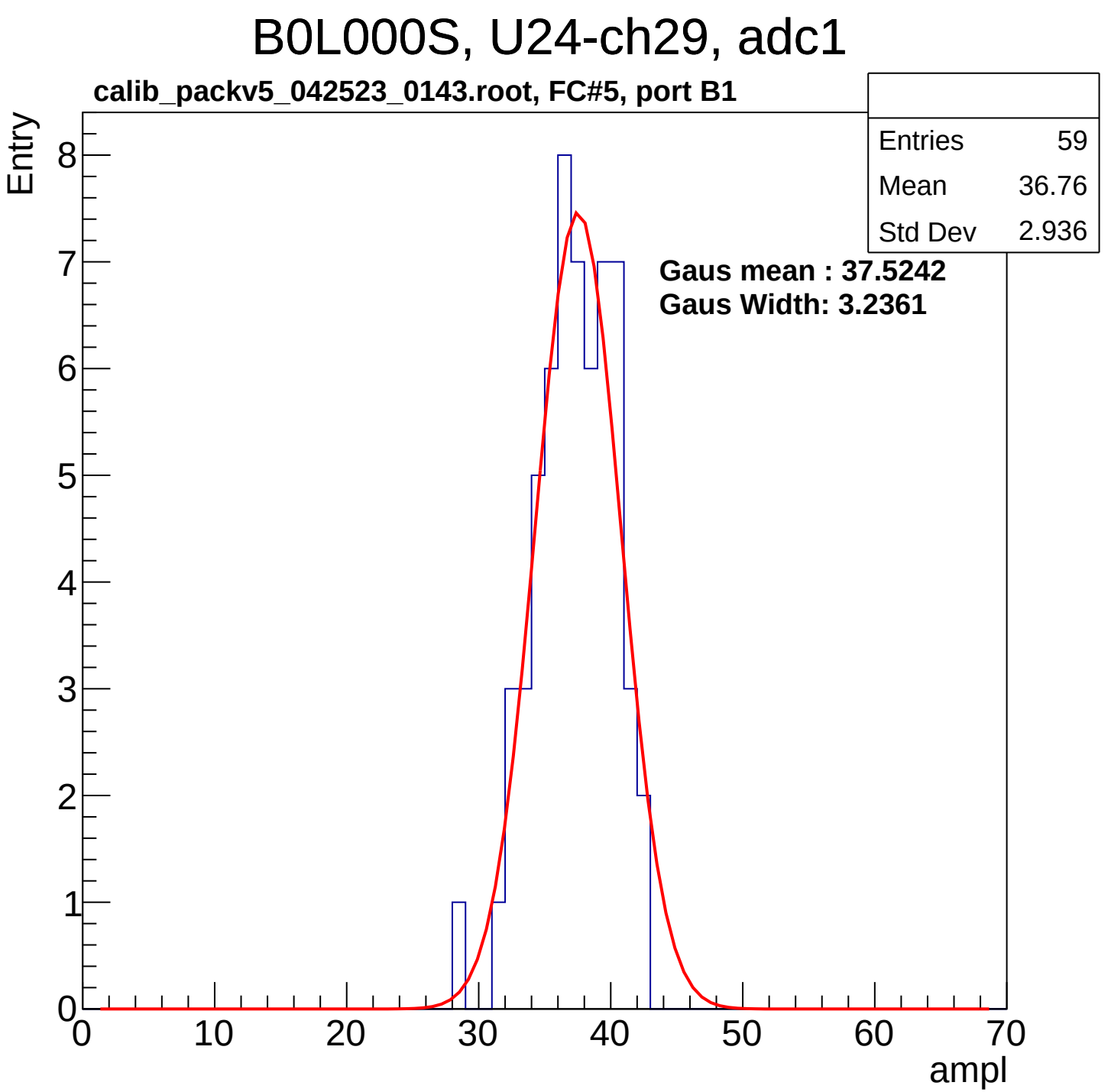
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

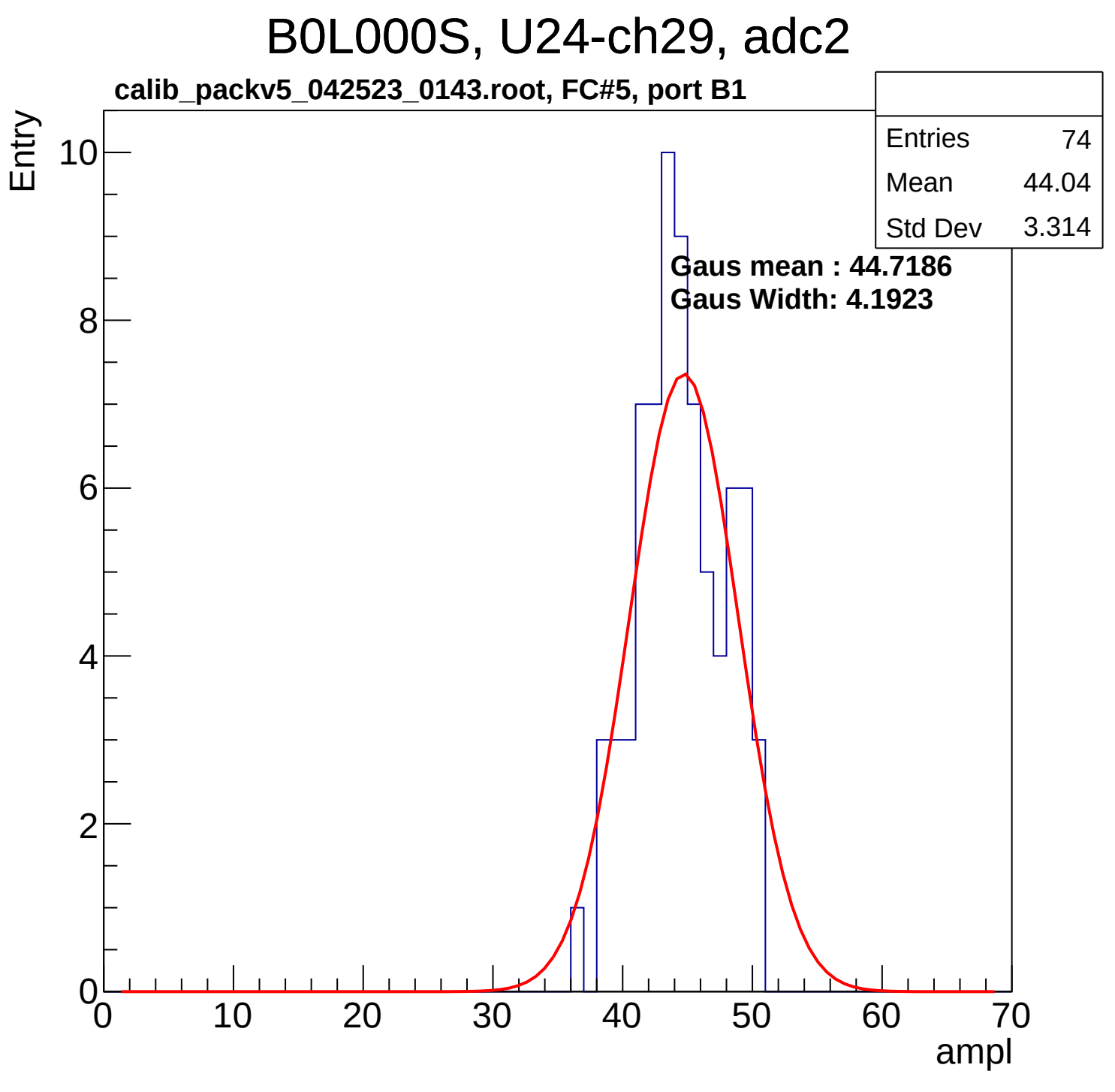

B0L000S, U24-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59
Std Dev 9.525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

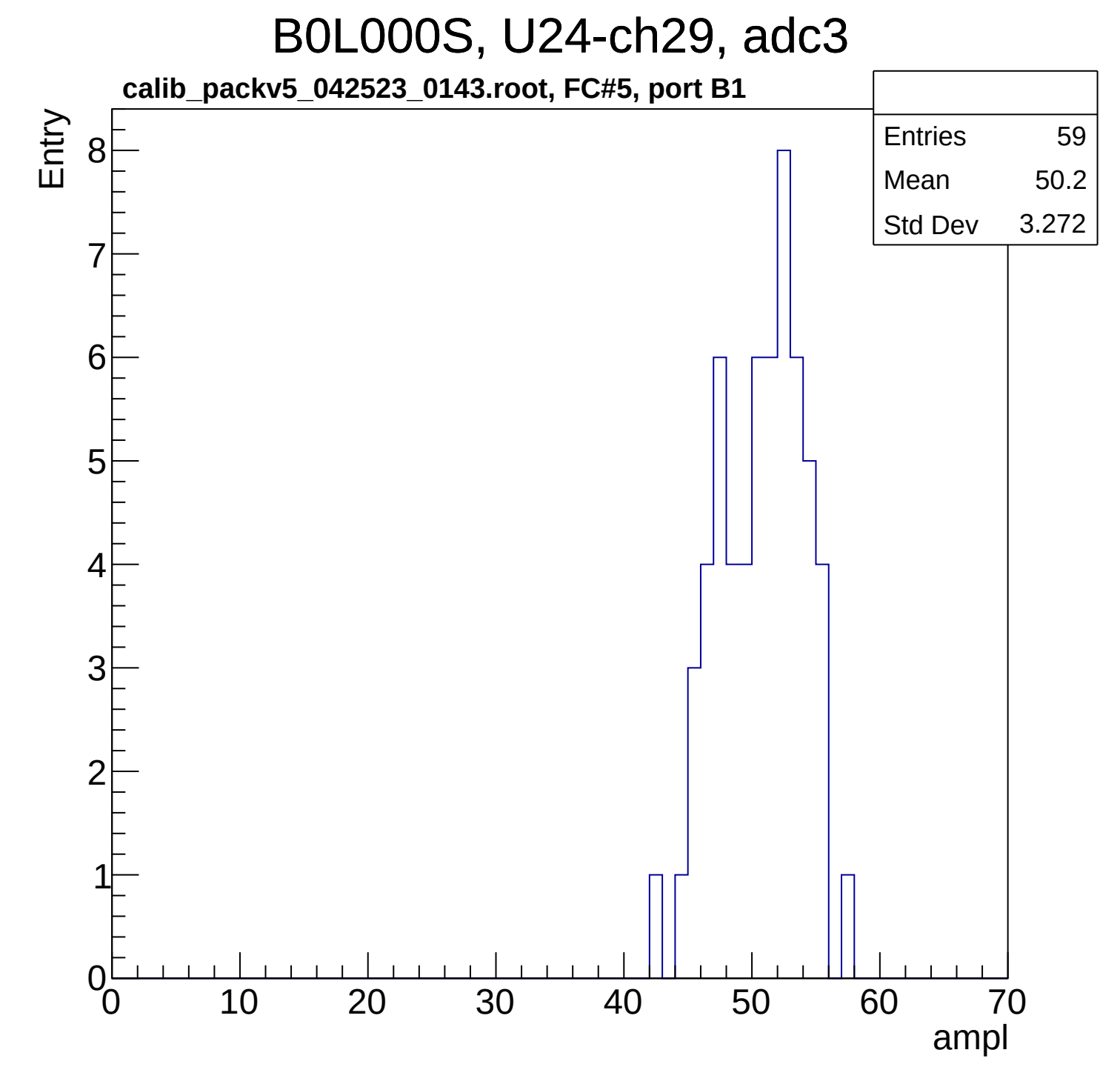

B0L000S, U24-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30
Std Dev 4.91
Gaus mean : 30.9288
Gaus Width: 3.6003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 36.76
Std Dev 2.936
Gaus mean : 37.5242
Gaus Width: 3.2361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 44.04
Std Dev 3.314
Gaus mean : 44.7186
Gaus Width: 4.1923
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 50.2
Std Dev 3.272
Entry
ampl
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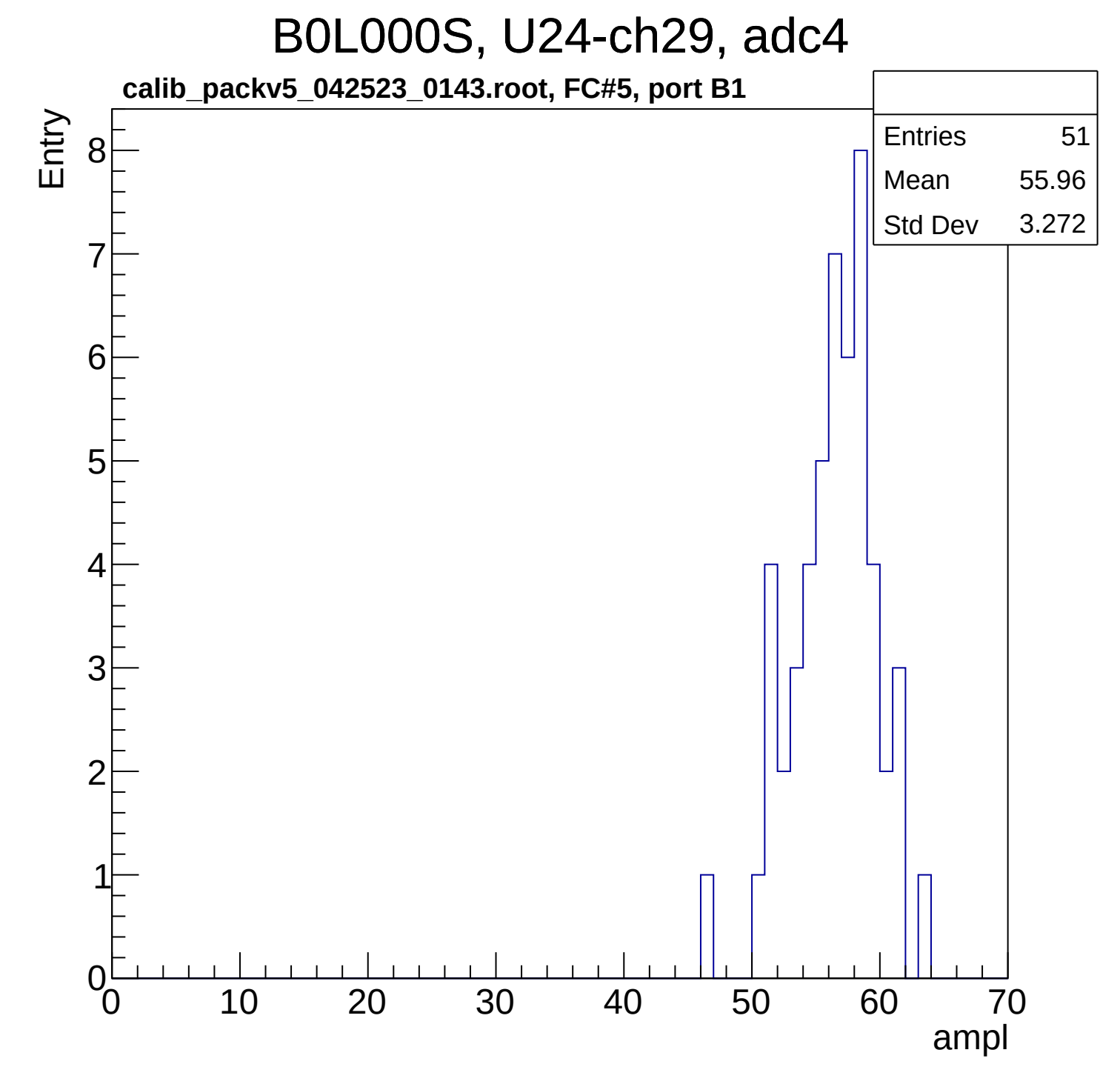
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 55.96
Std Dev 3.272
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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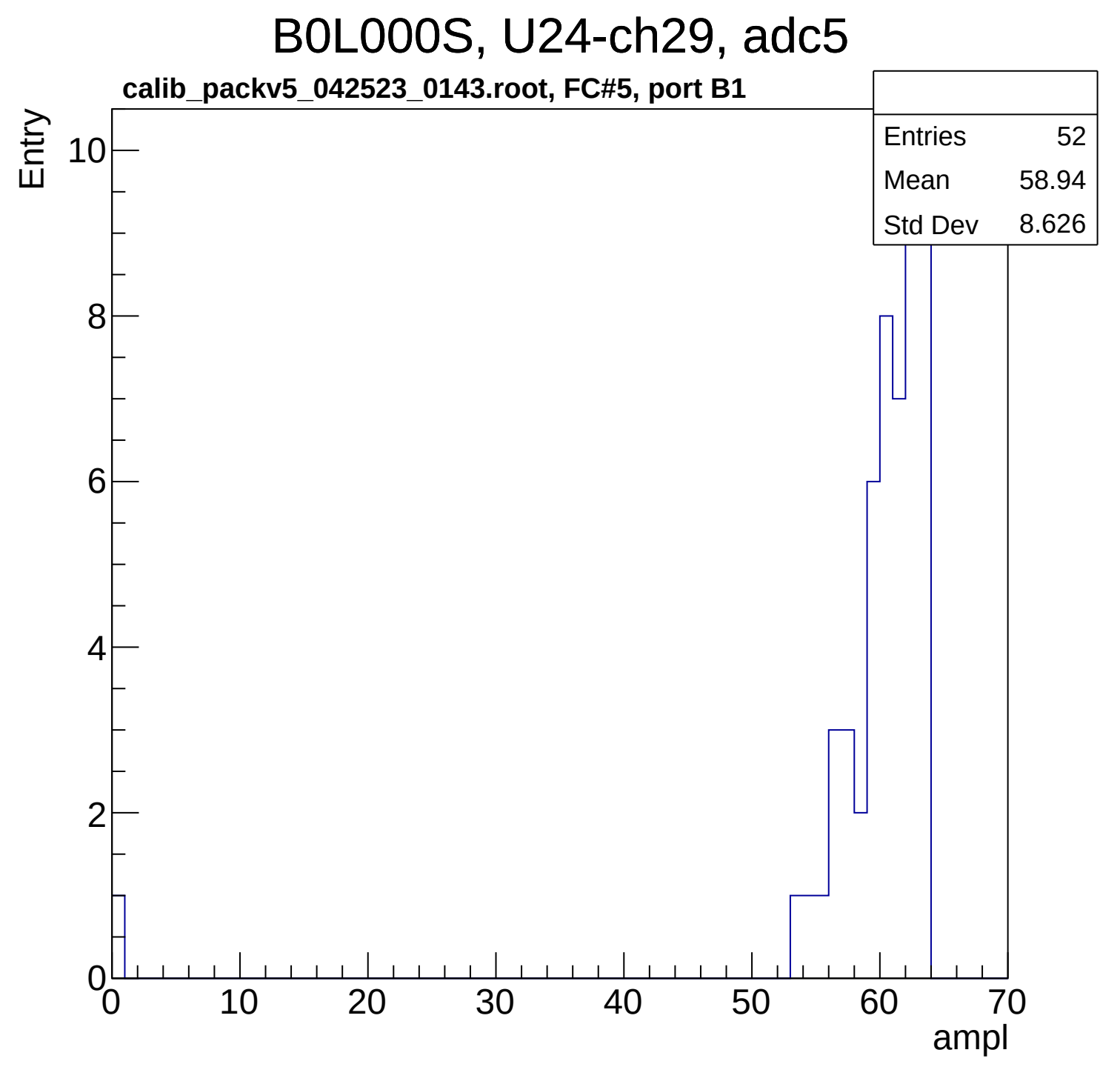
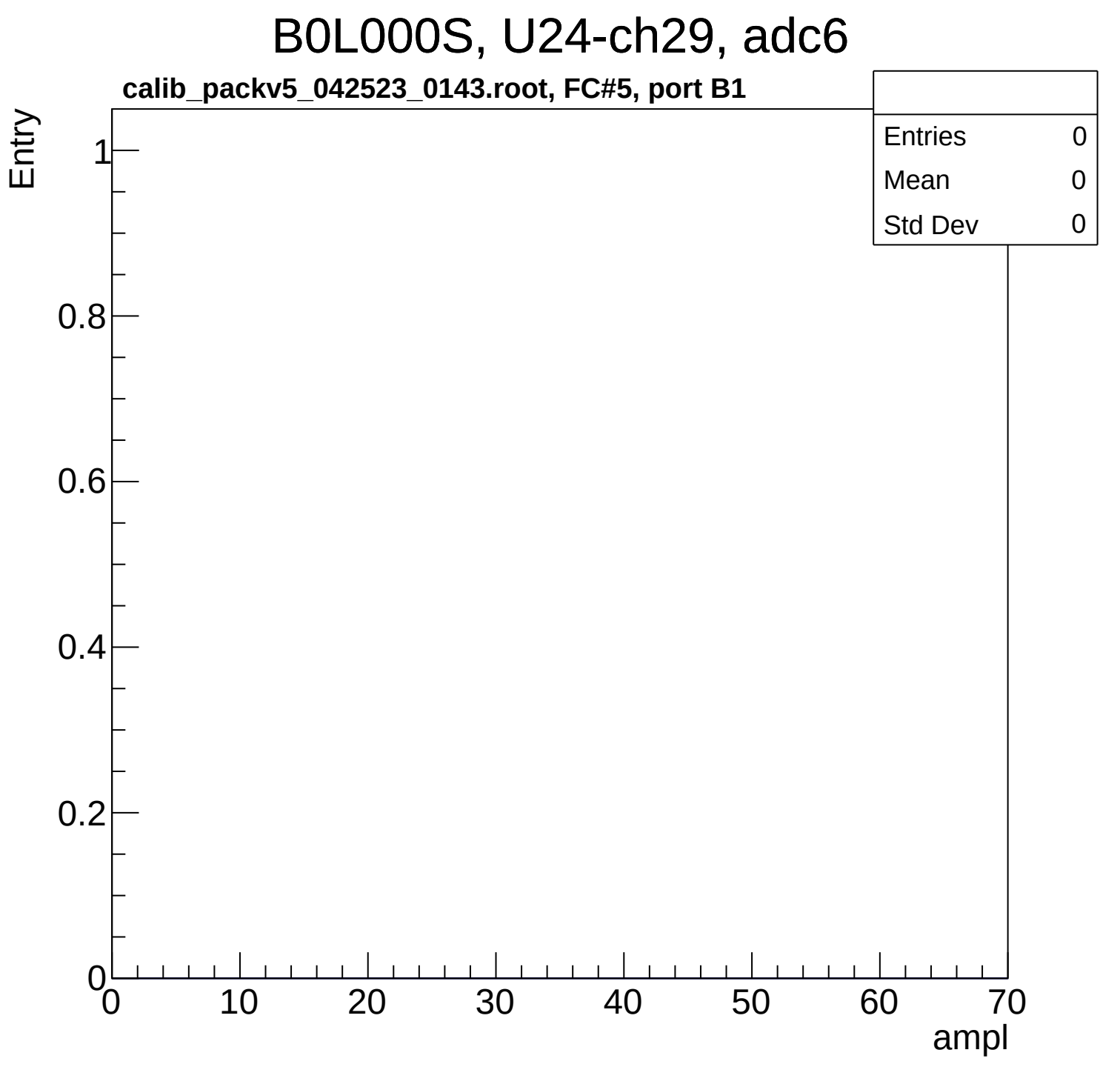
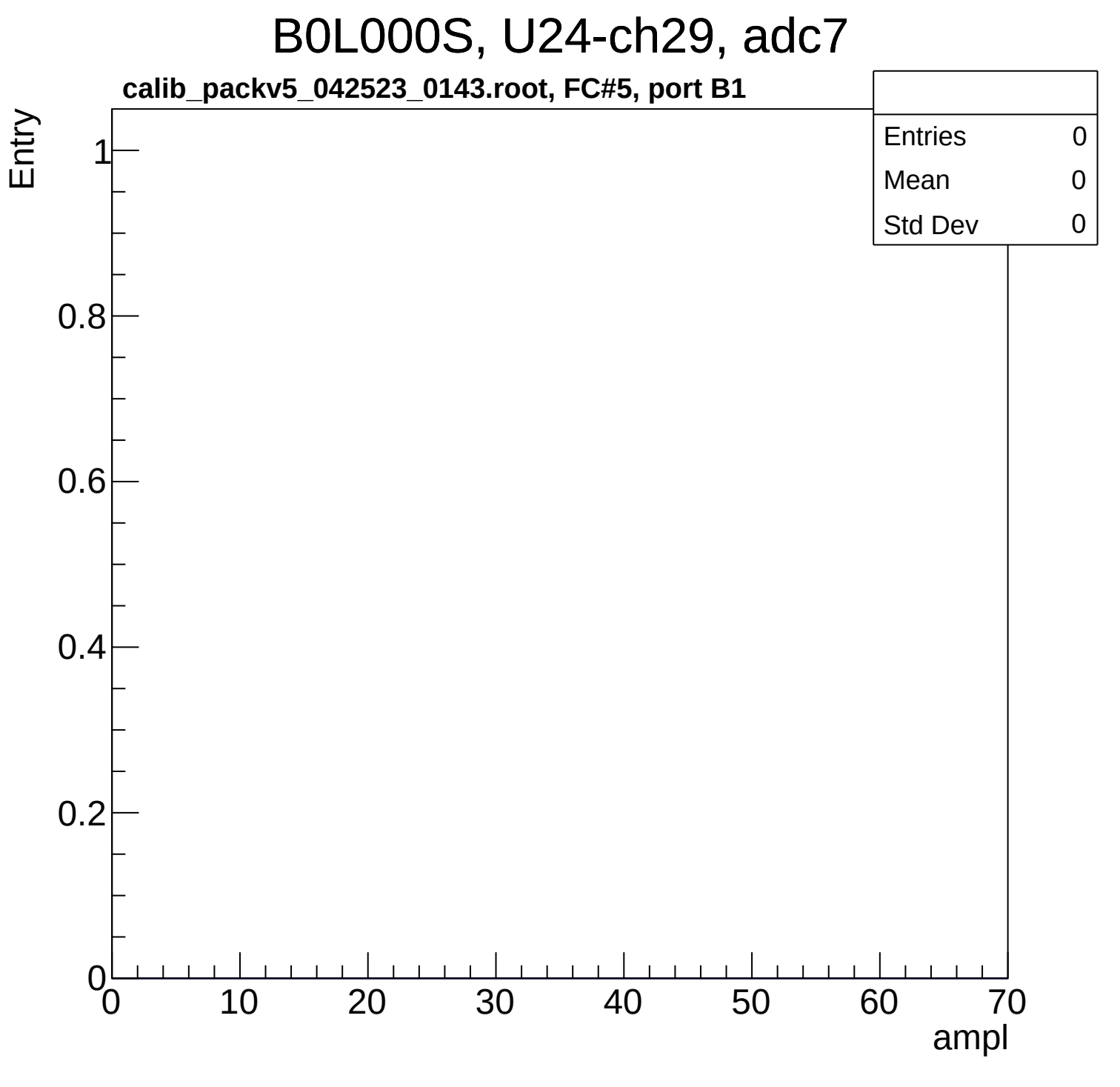

B0L000S, U24-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.94
Std Dev 8.626
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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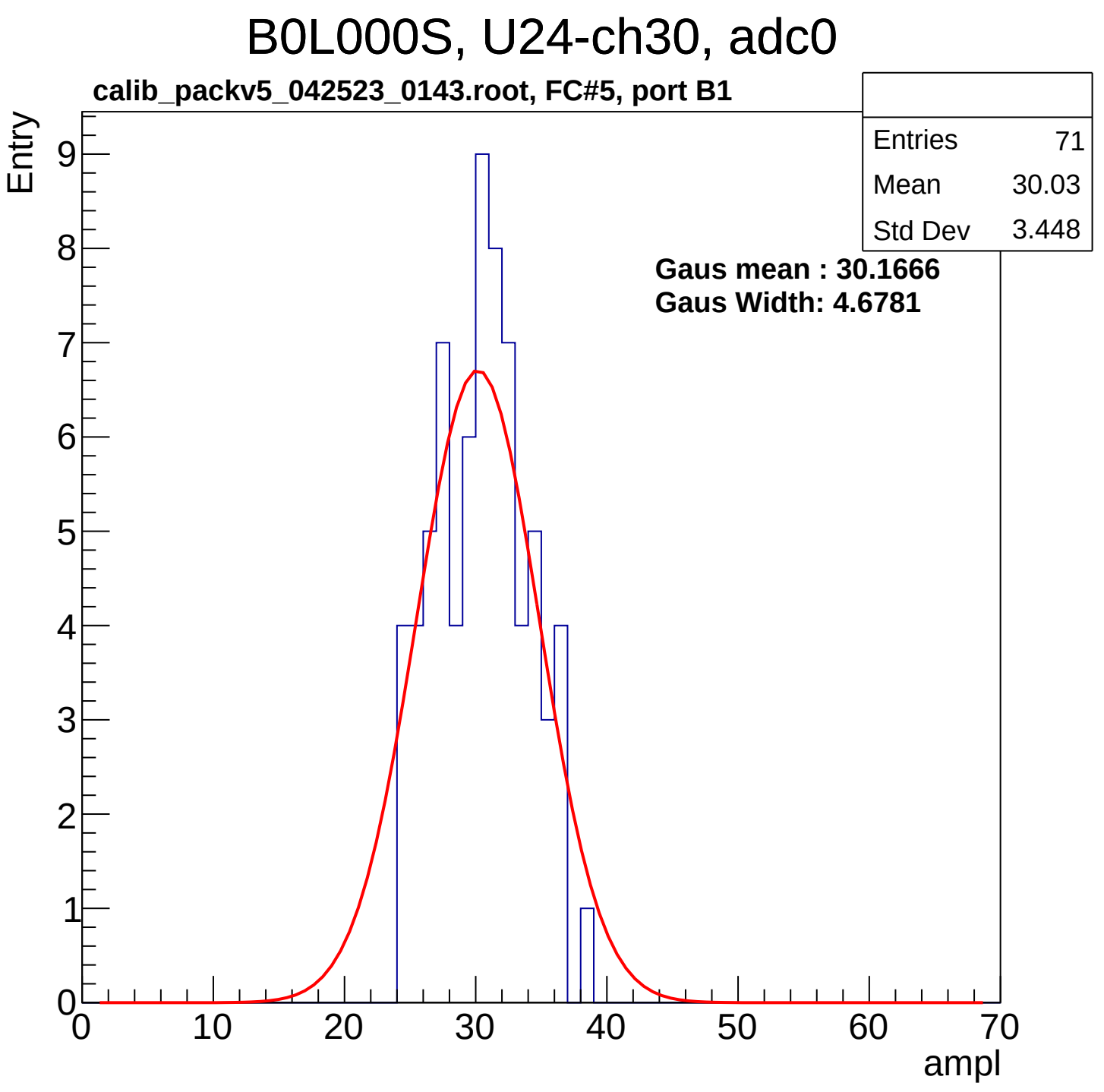
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.03
Std Dev 3.448
Gaus mean : 30.1666
Gaus Width: 4.6781
Entry
ampl
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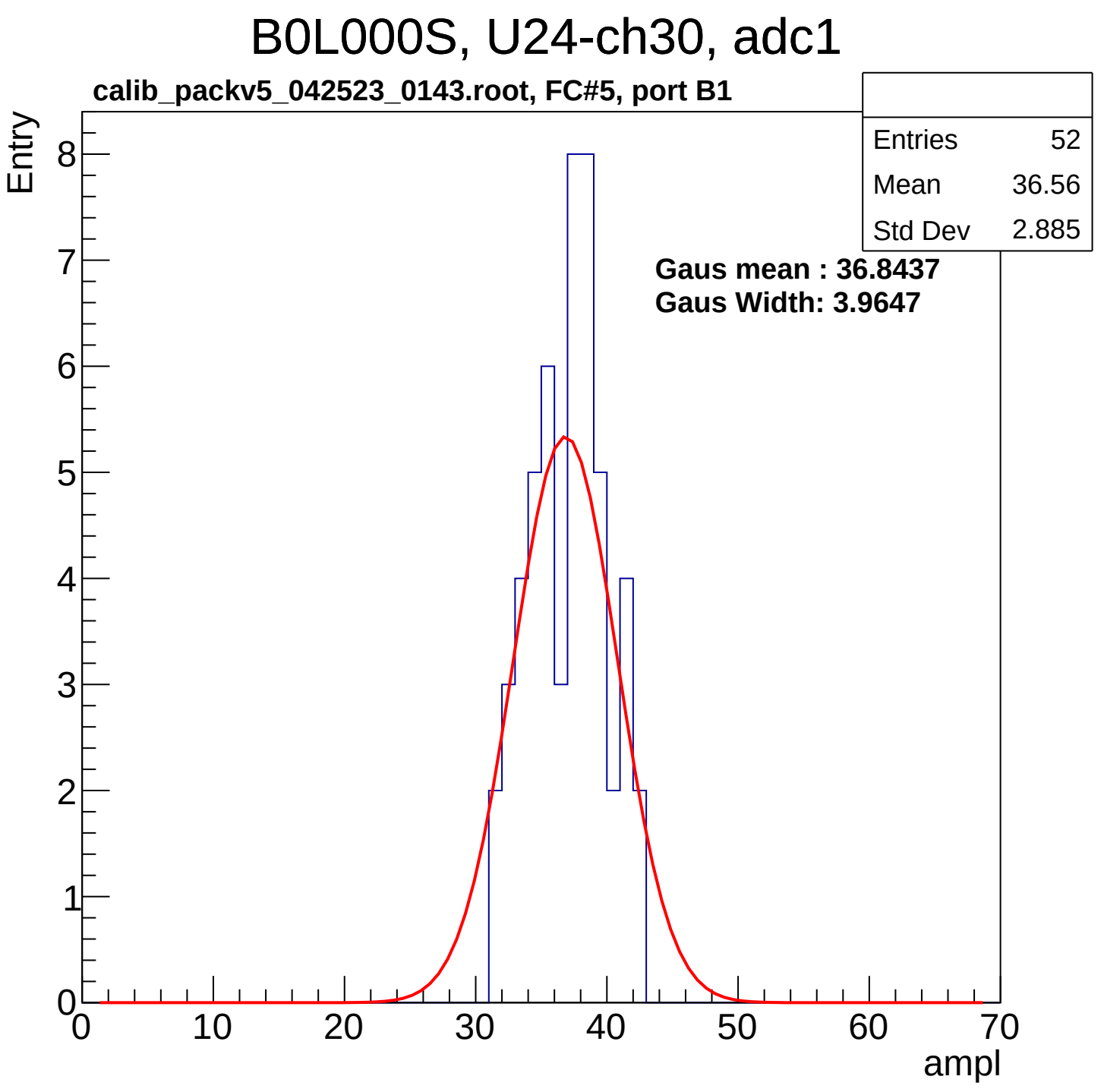
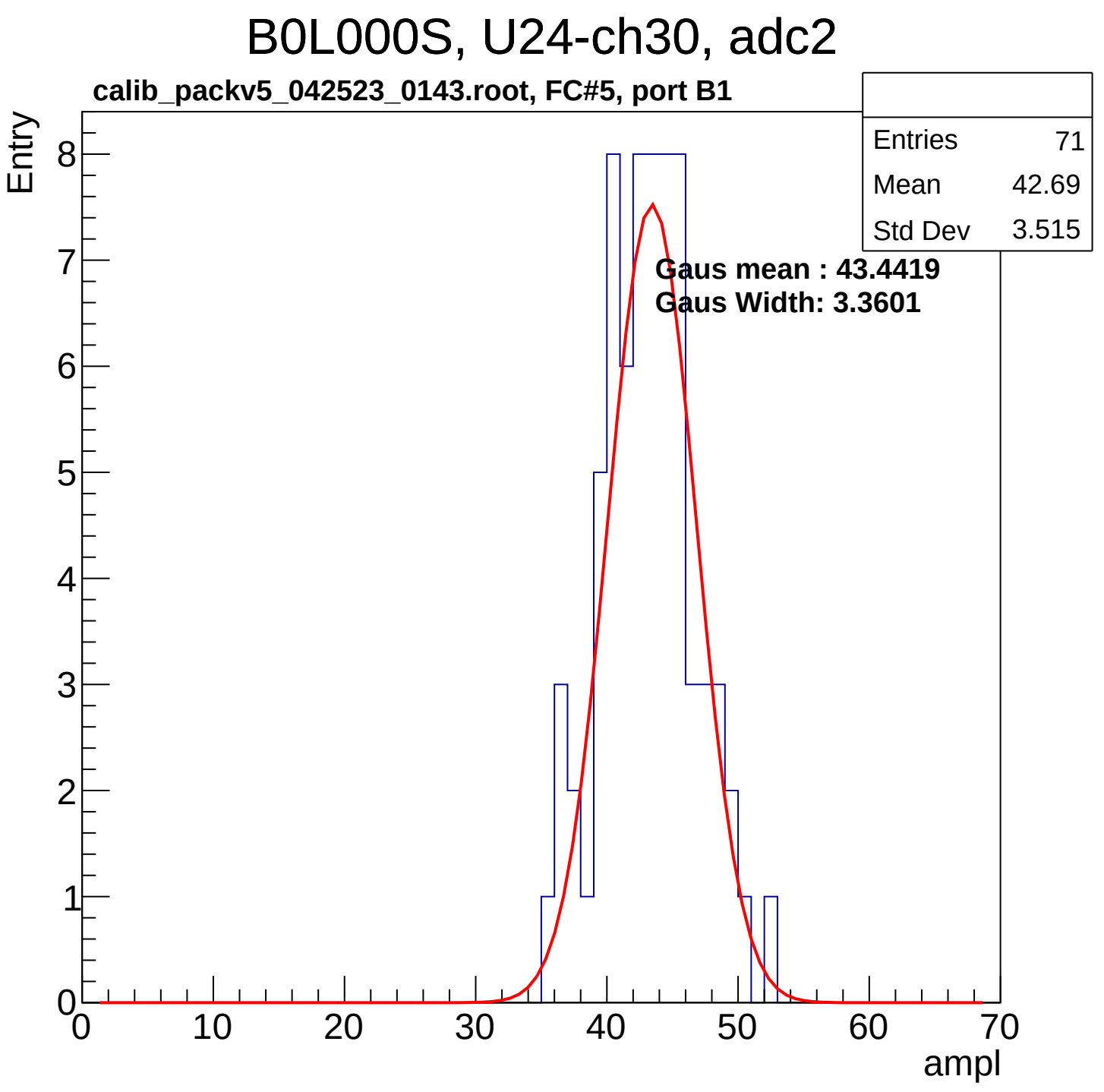
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 36.56
Std Dev 2.885
Gaus mean : 36.8437
Gaus Width: 3.9647
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.69
Std Dev 3.515
Gaus mean : 43.4419
Gaus Width: 3.3601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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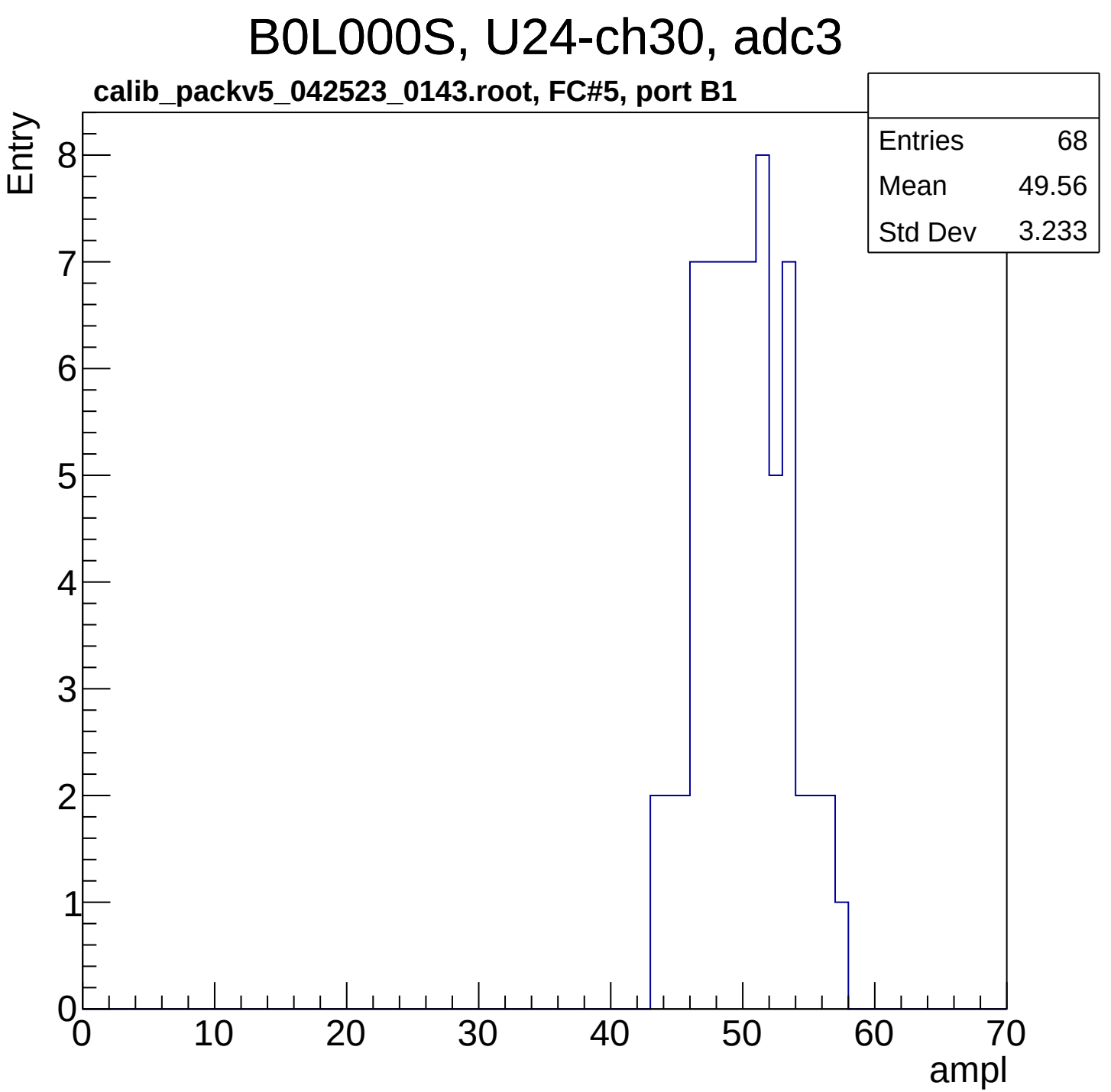
B0L000S, U24-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.56
Std Dev 3.233
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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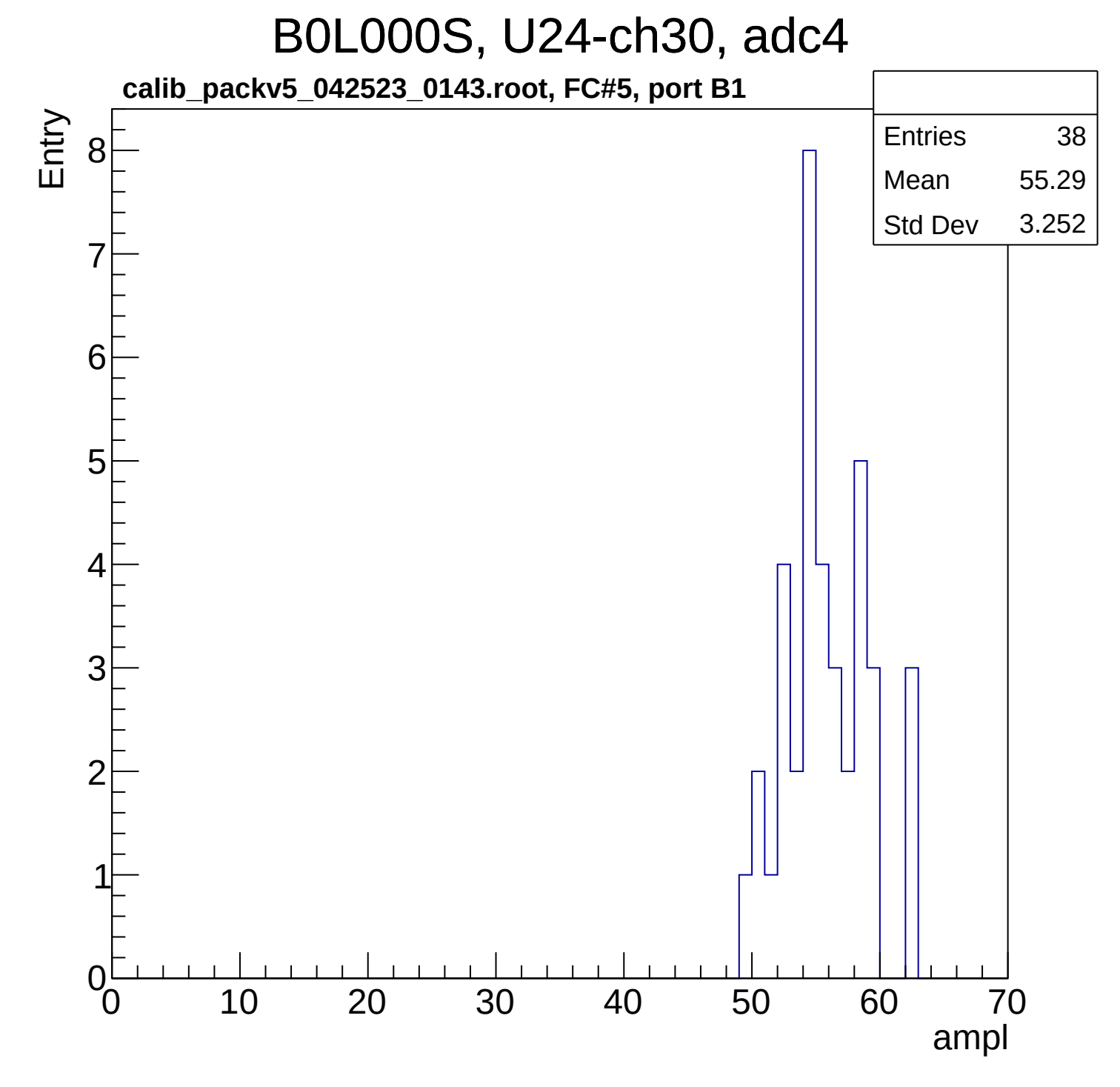
B0L000S, U24-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 55.29
Std Dev 3.252
Entry
ampl
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1
2
3
4
5
6
7
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10
20
30
40
50
60
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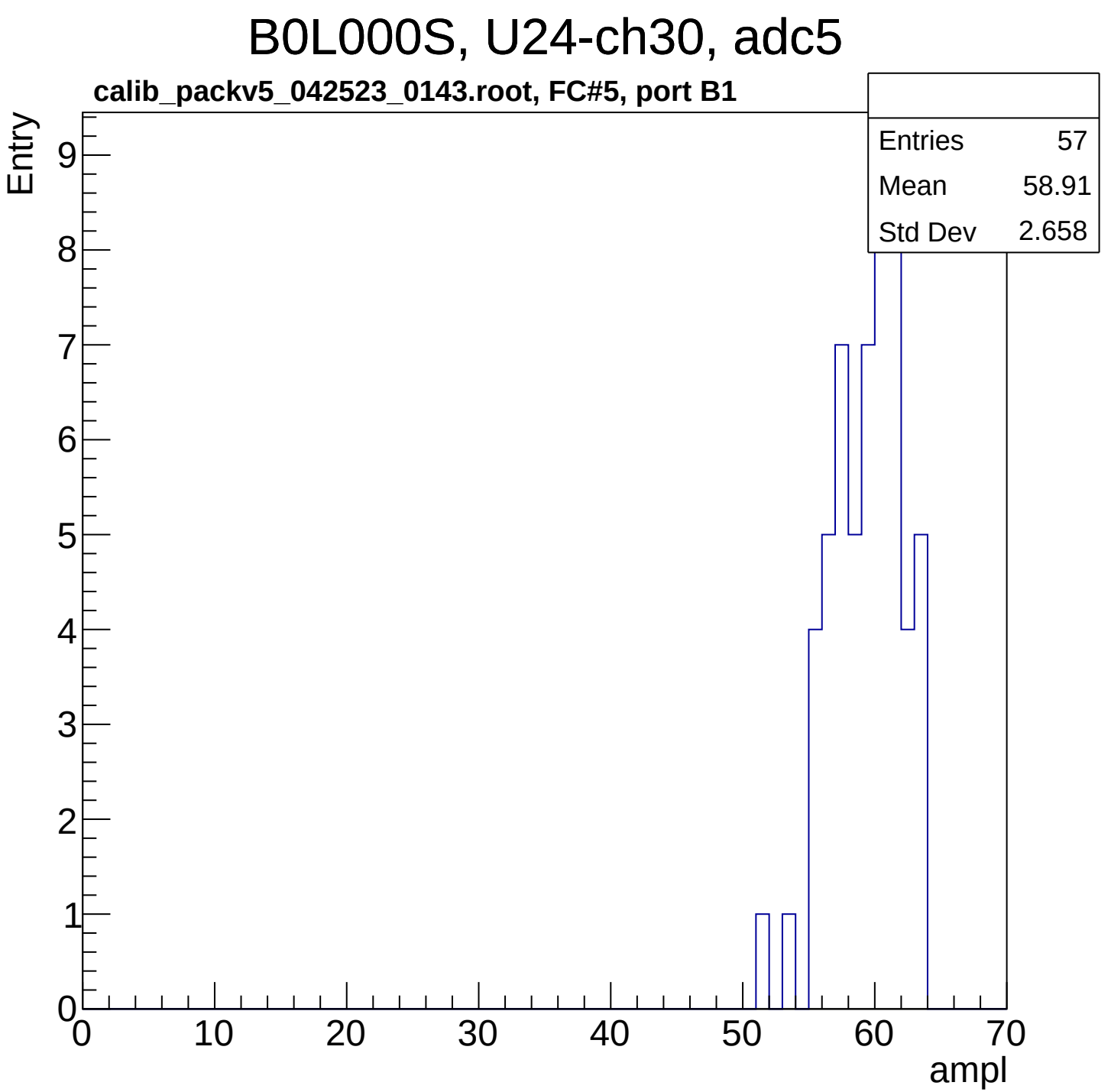
B0L000S, U24-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.91
Std Dev 2.658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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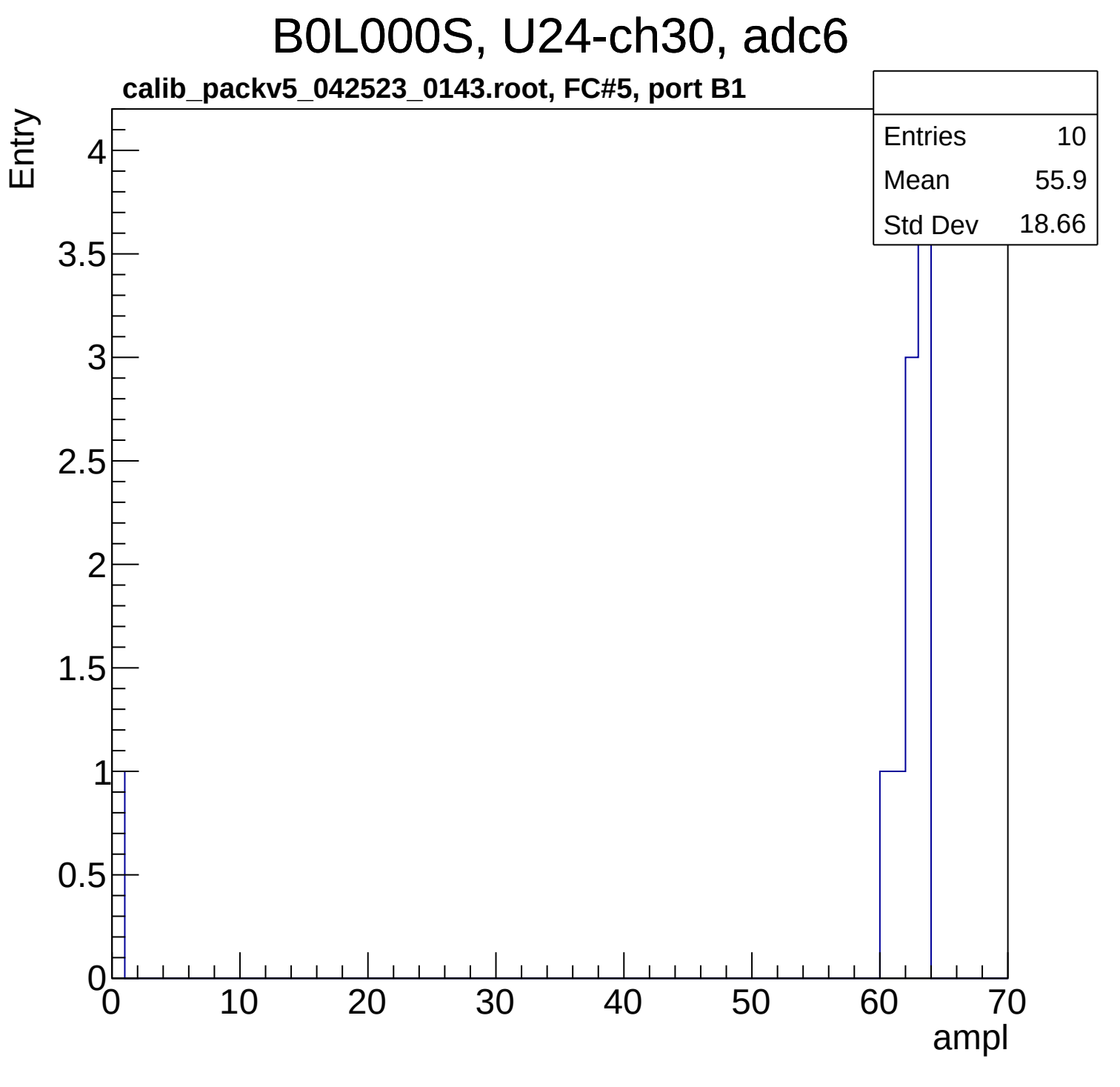
B0L000S, U24-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.9
Std Dev 18.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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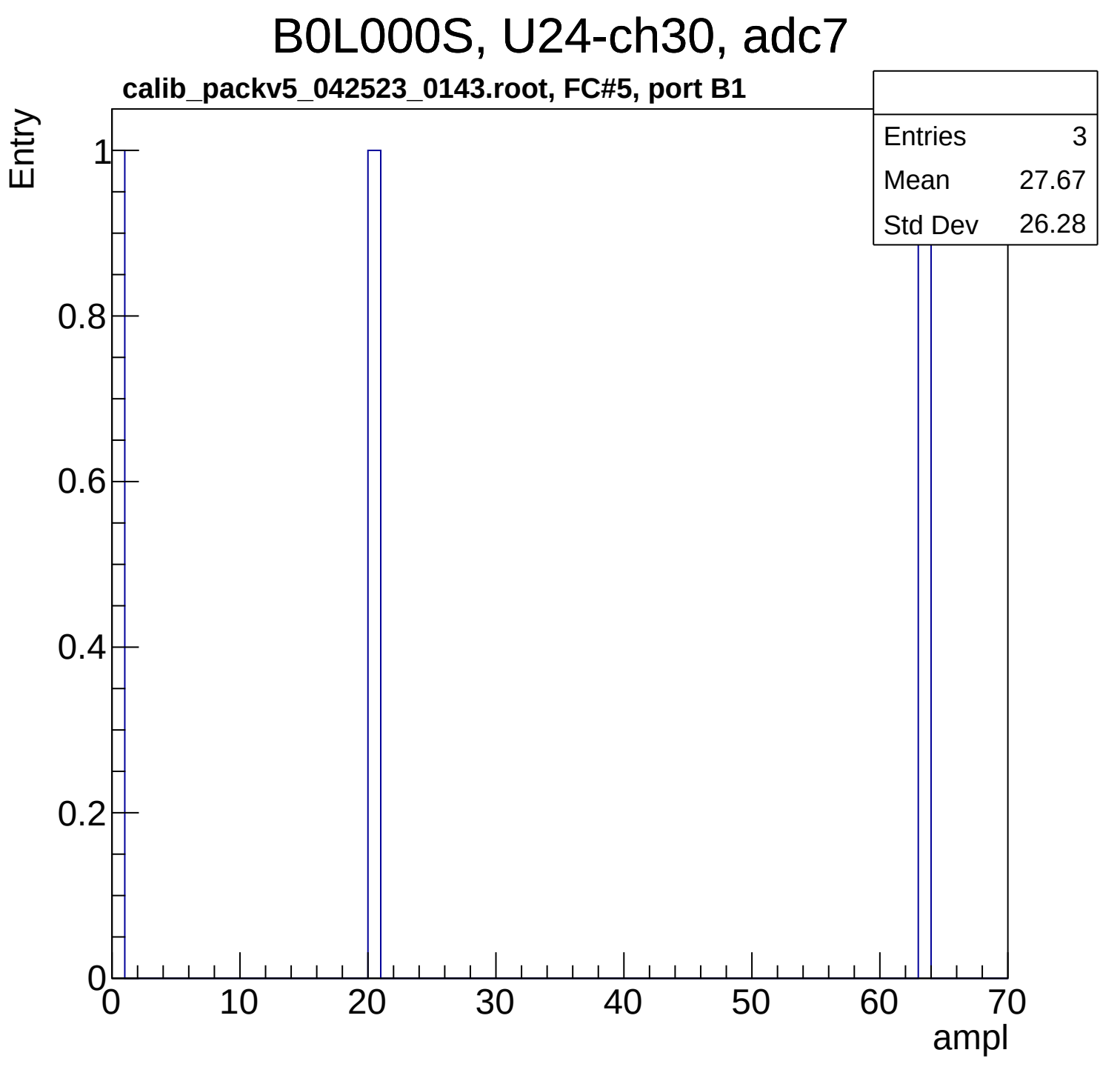
B0L000S, U24-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27.67
Std Dev 26.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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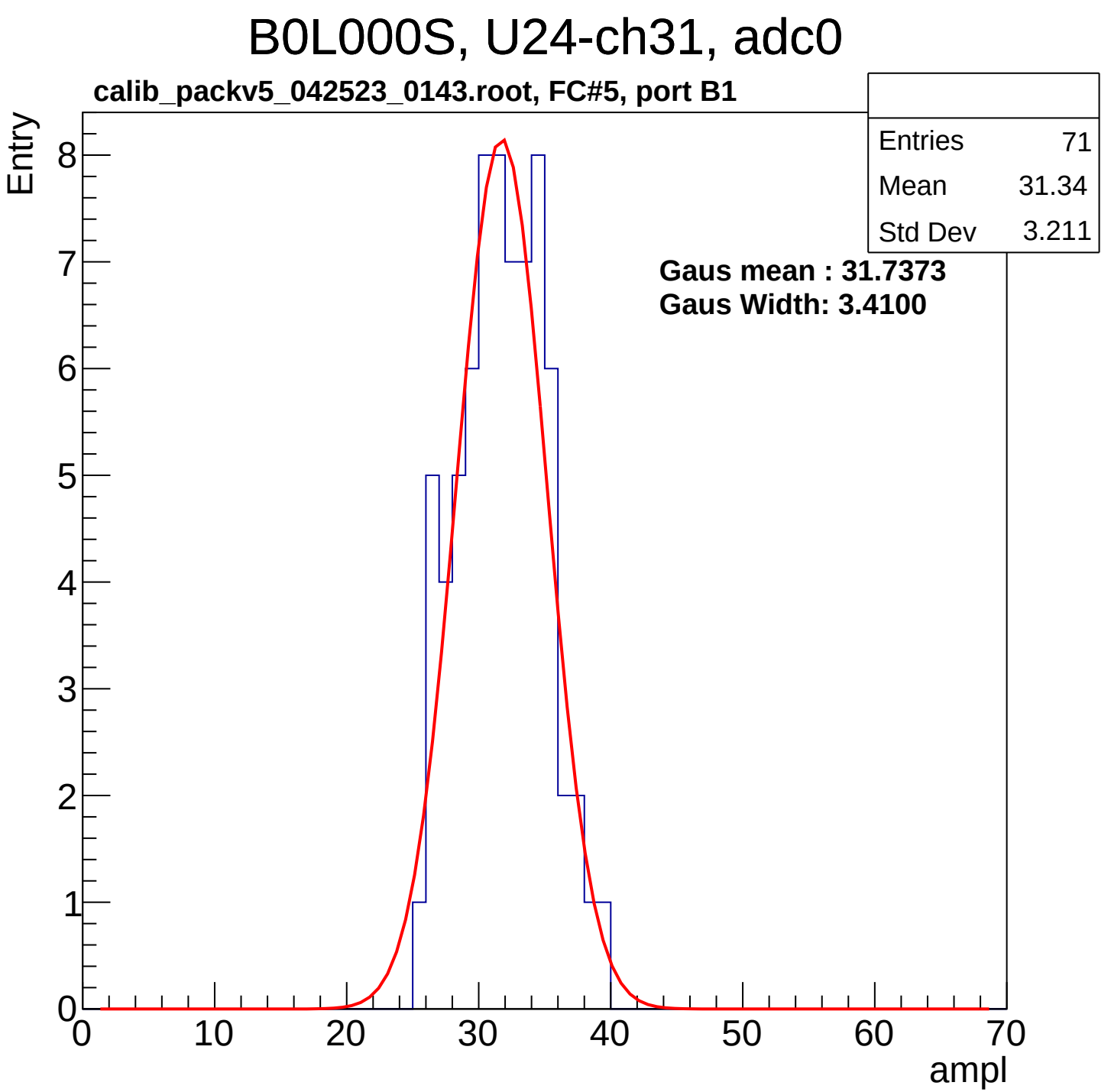
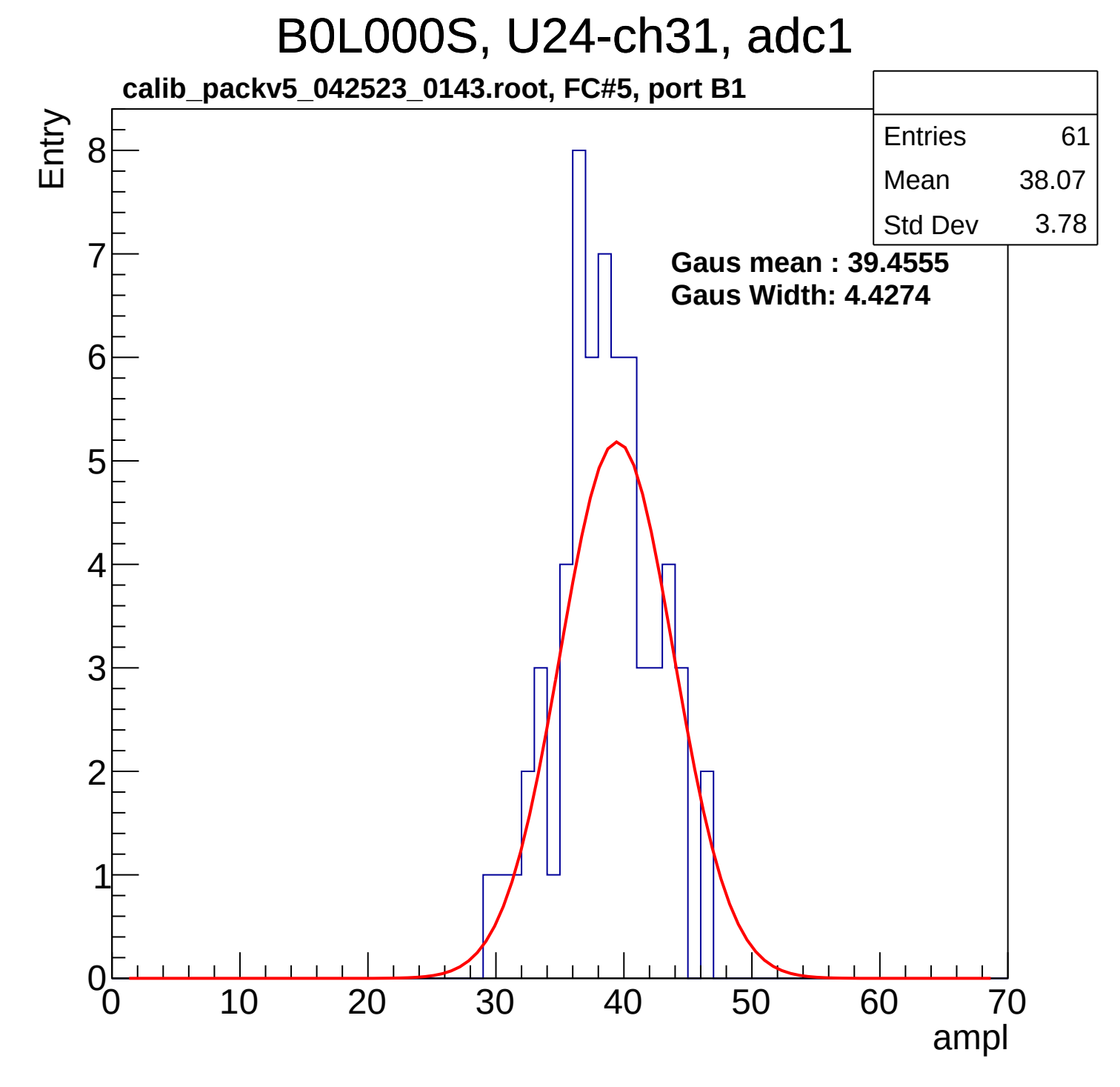

B0L000S, U24-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 31.34
Std Dev 3.211
Gaus mean : 31.7373
Gaus Width: 3.4100
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 38.07
Std Dev 3.78
Gaus mean : 39.4555
Gaus Width: 4.4274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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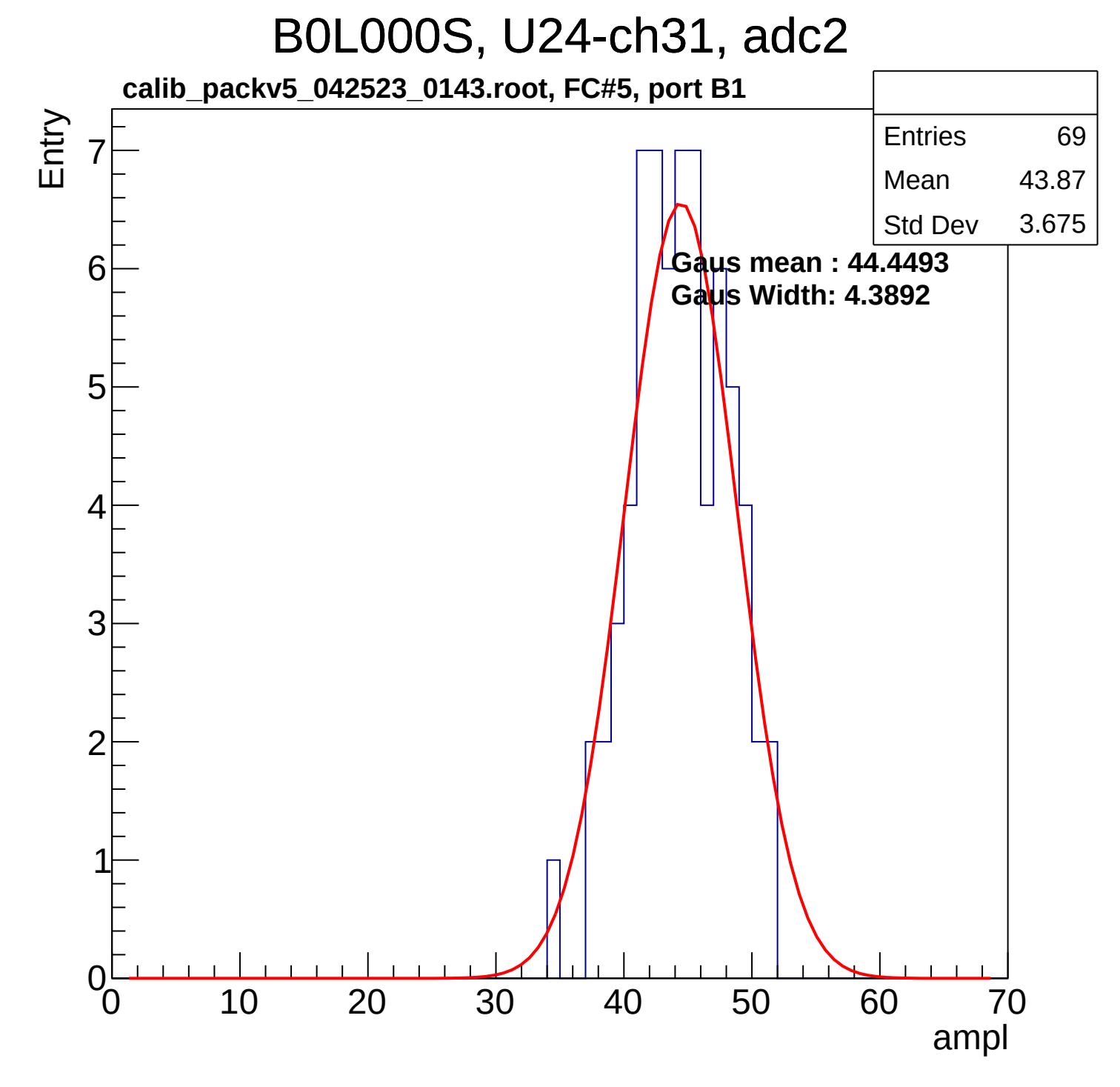
B0L000S, U24-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.87
Std Dev 3.675
Gaus mean : 44.4493
Gaus Width: 4.3892
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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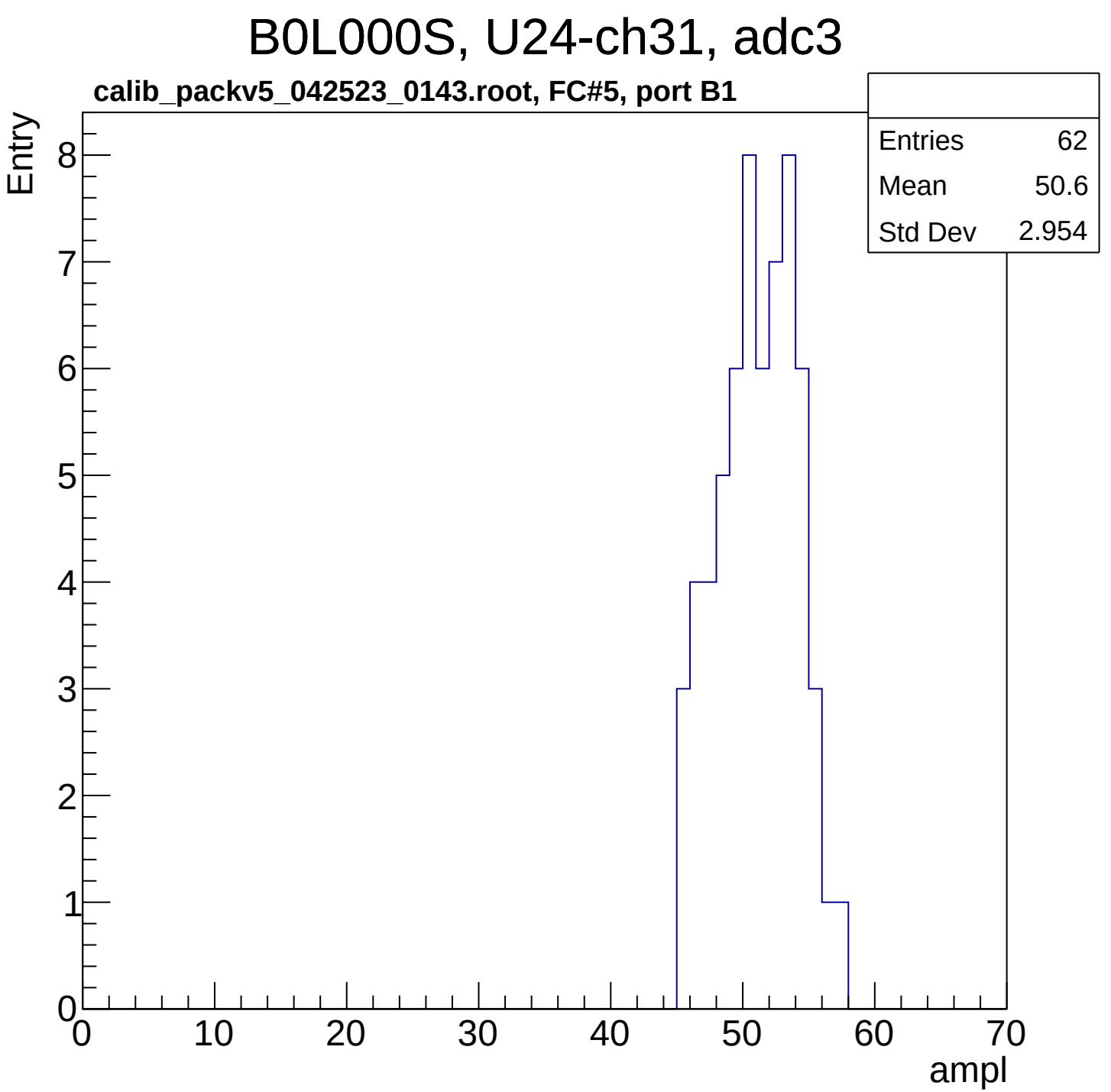
B0L000S, U24-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.6
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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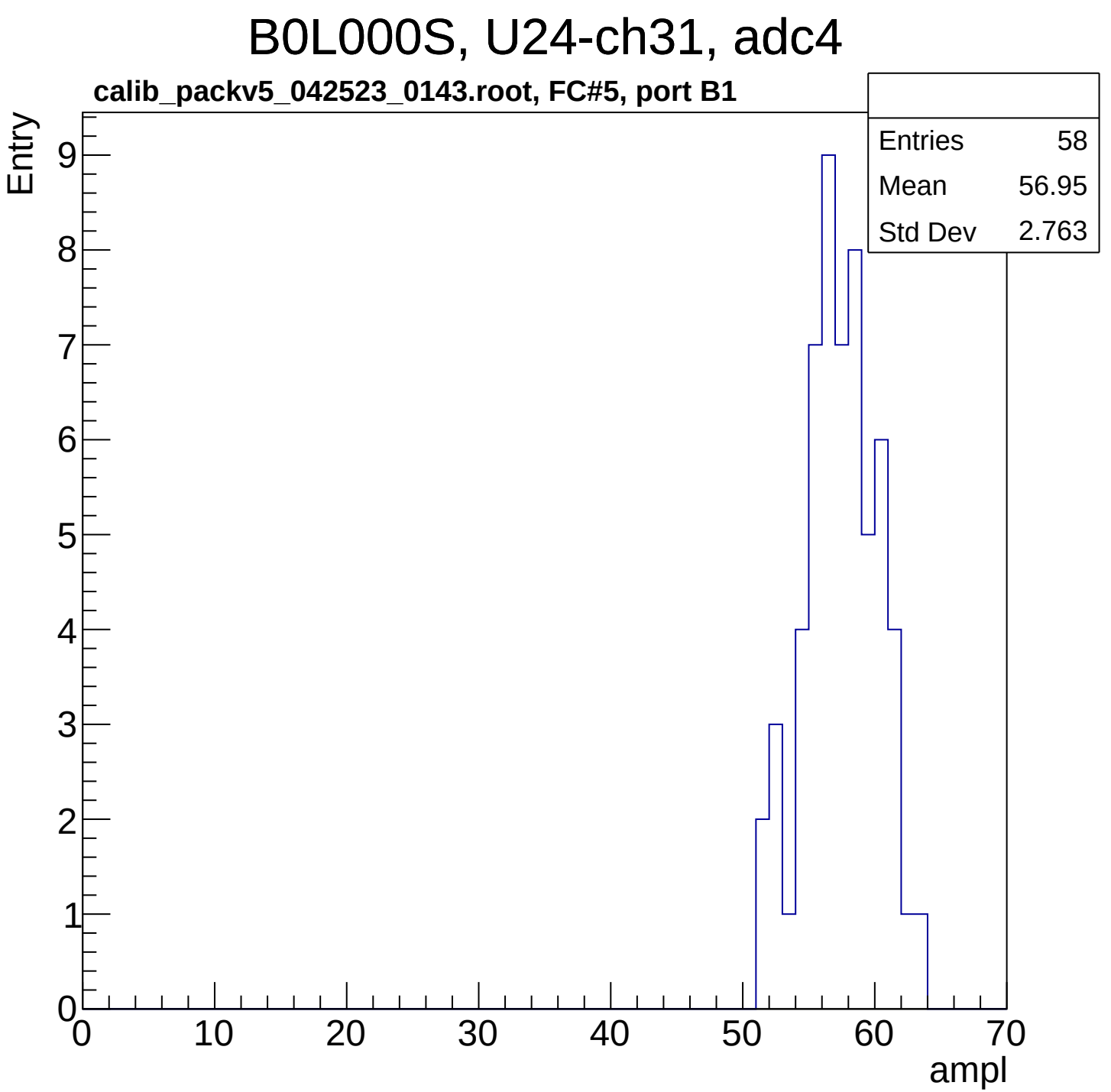
B0L000S, U24-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.95
Std Dev 2.763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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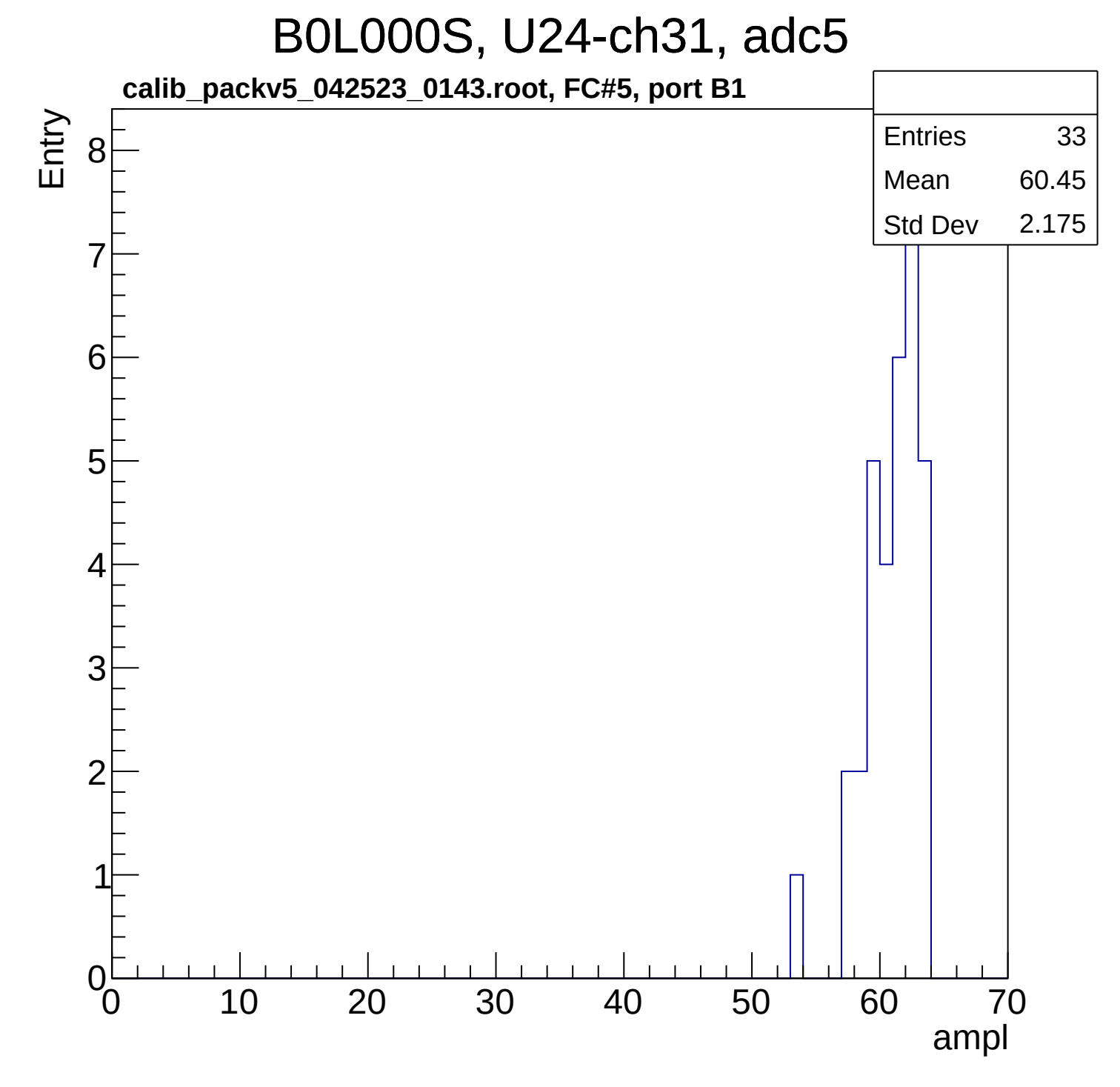
B0L000S, U24-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.45
Std Dev 2.175
Entry
ampl
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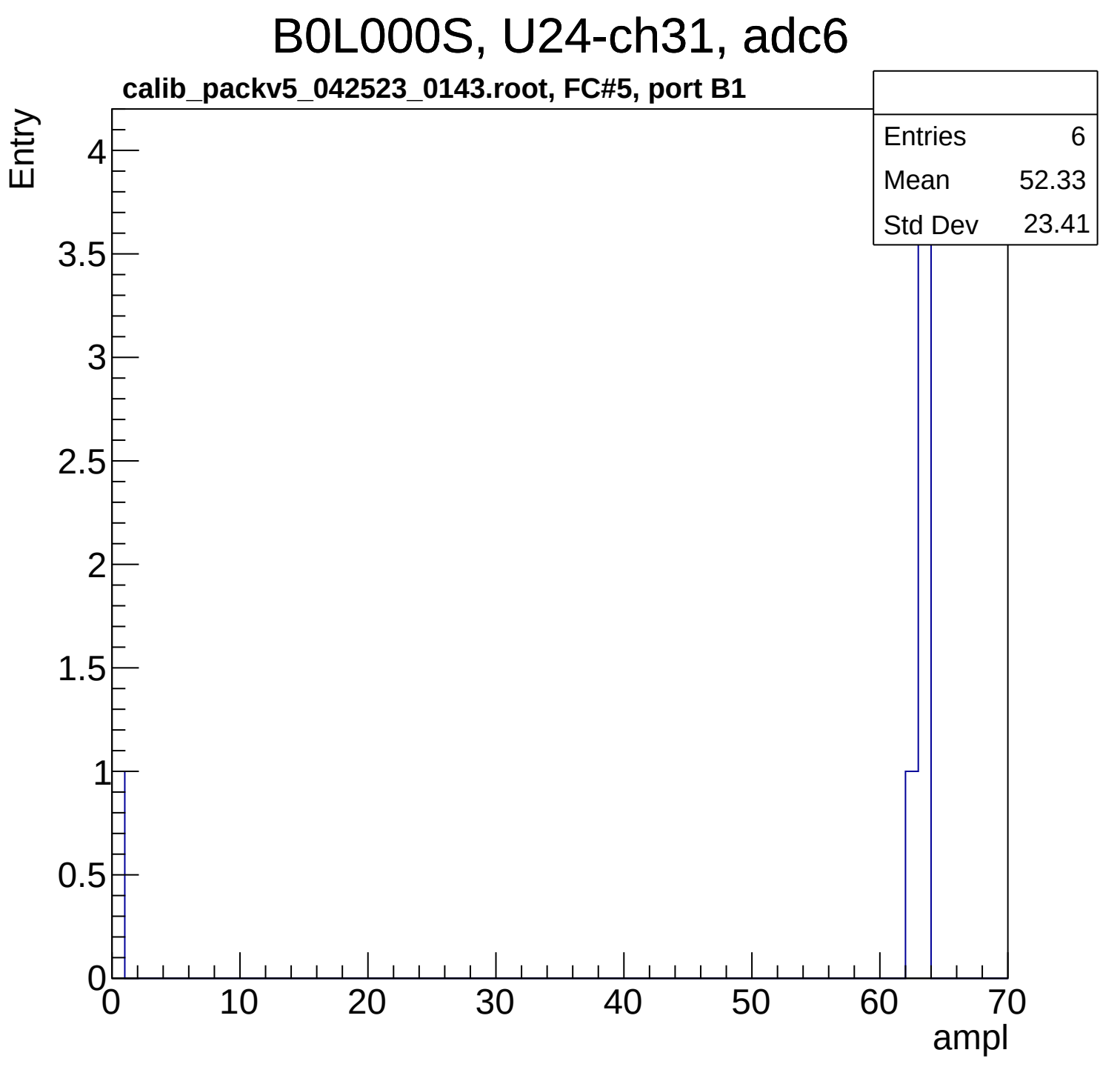
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 52.33
Std Dev 23.41
Entry
ampl
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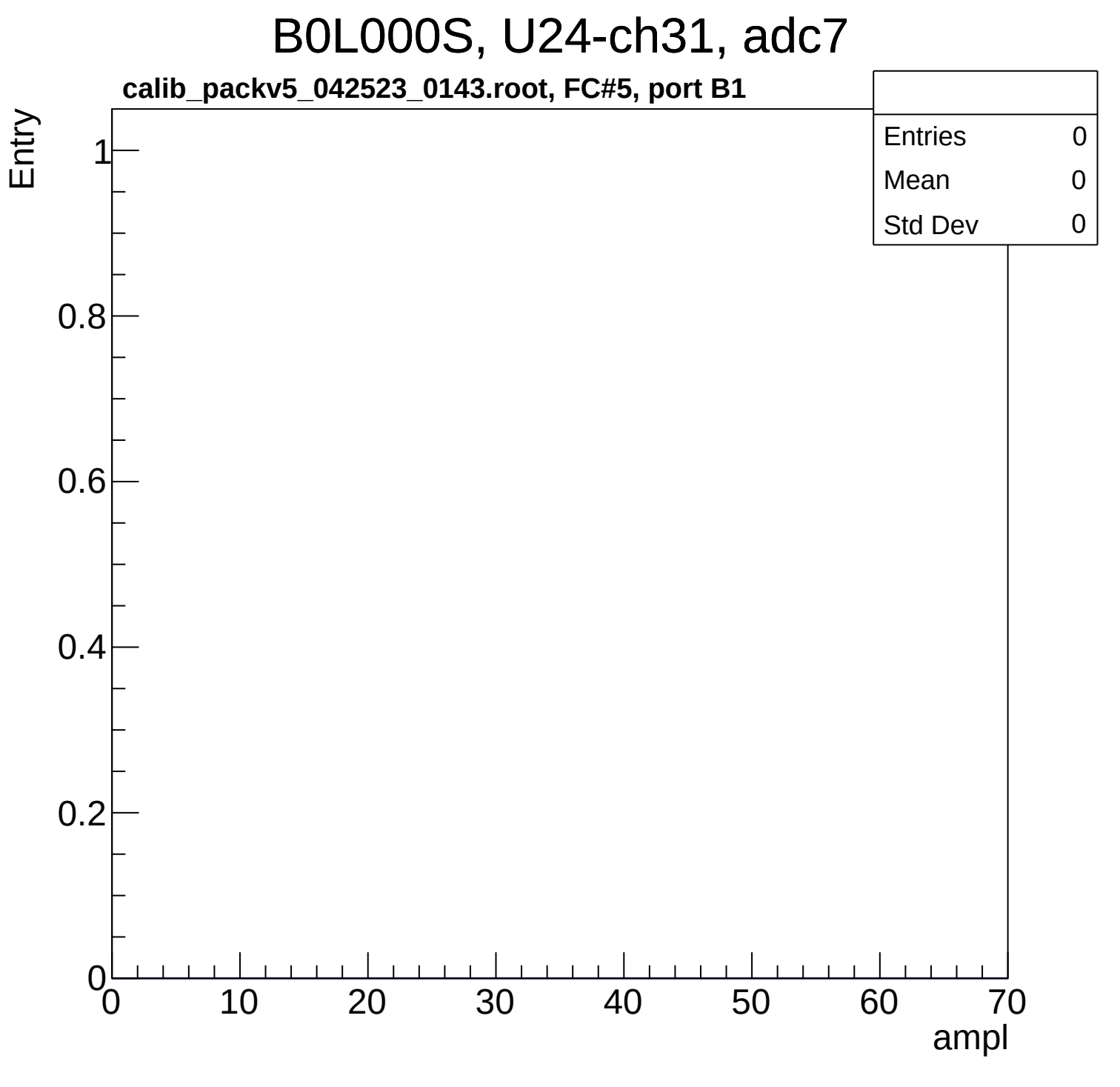
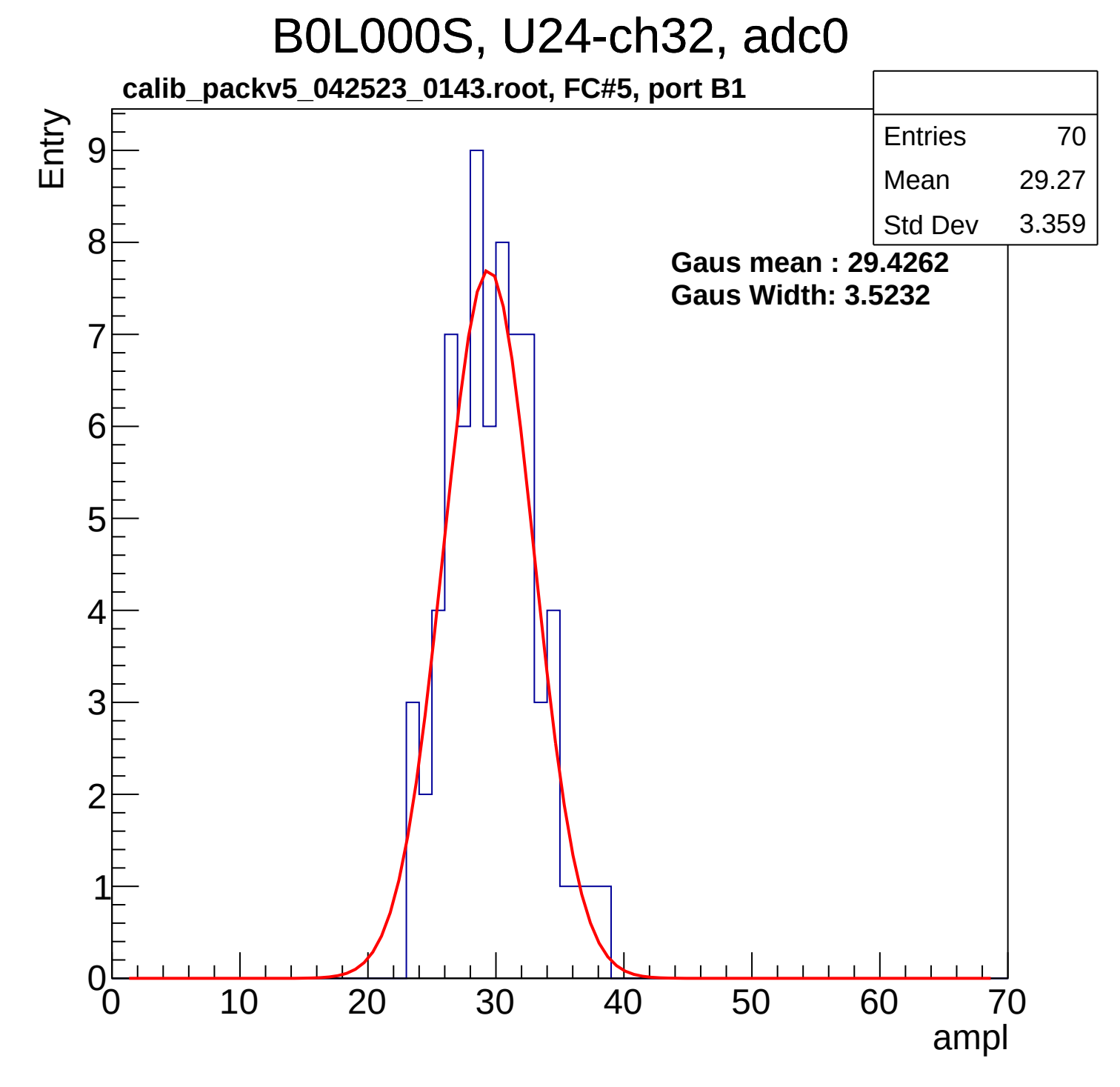
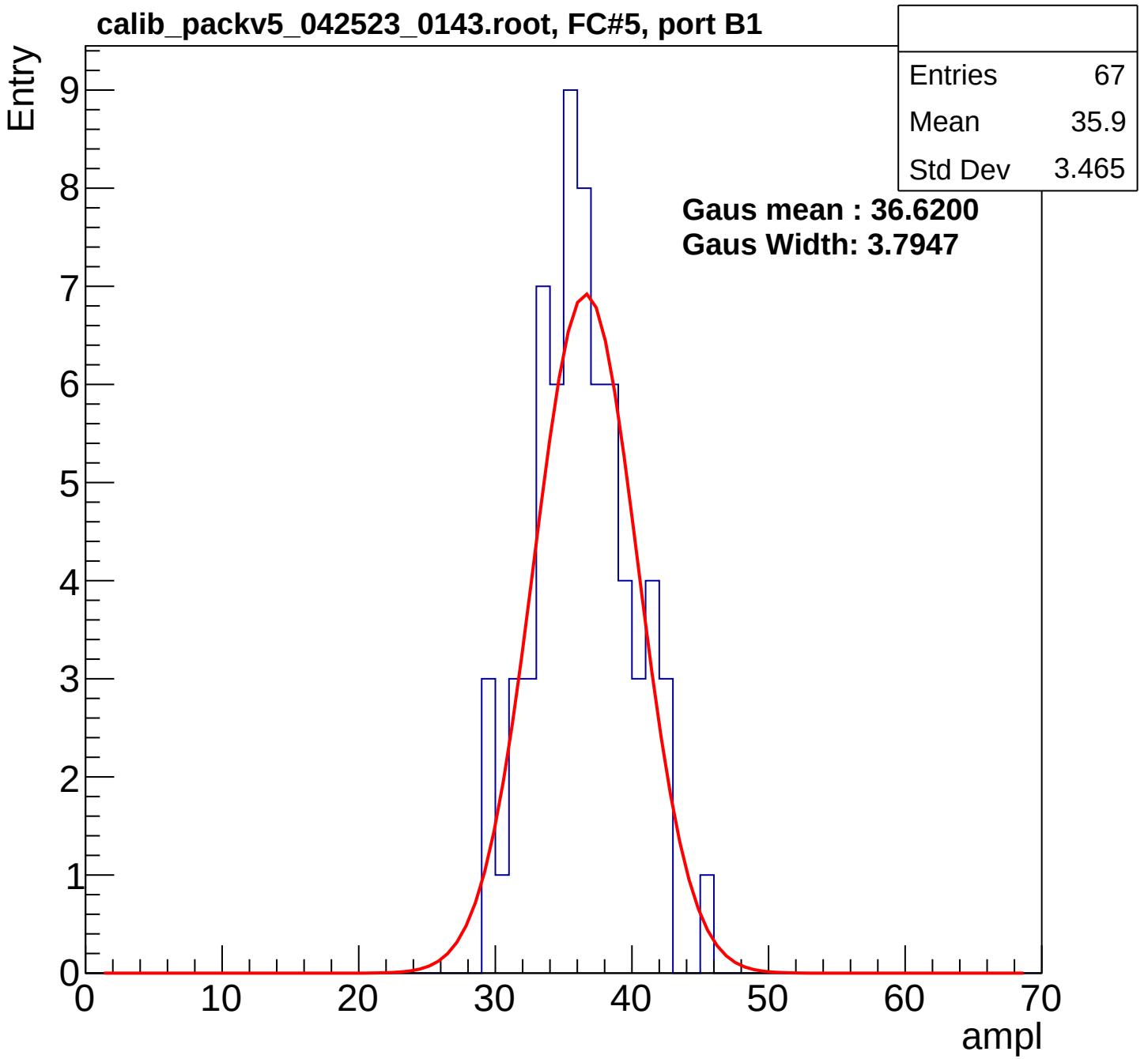
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.27
Std Dev 3.359
Gaus mean : 29.4262
Gaus Width: 3.5232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.9
Std Dev 3.465
Gaus mean : 36.6200
Gaus Width: 3.7947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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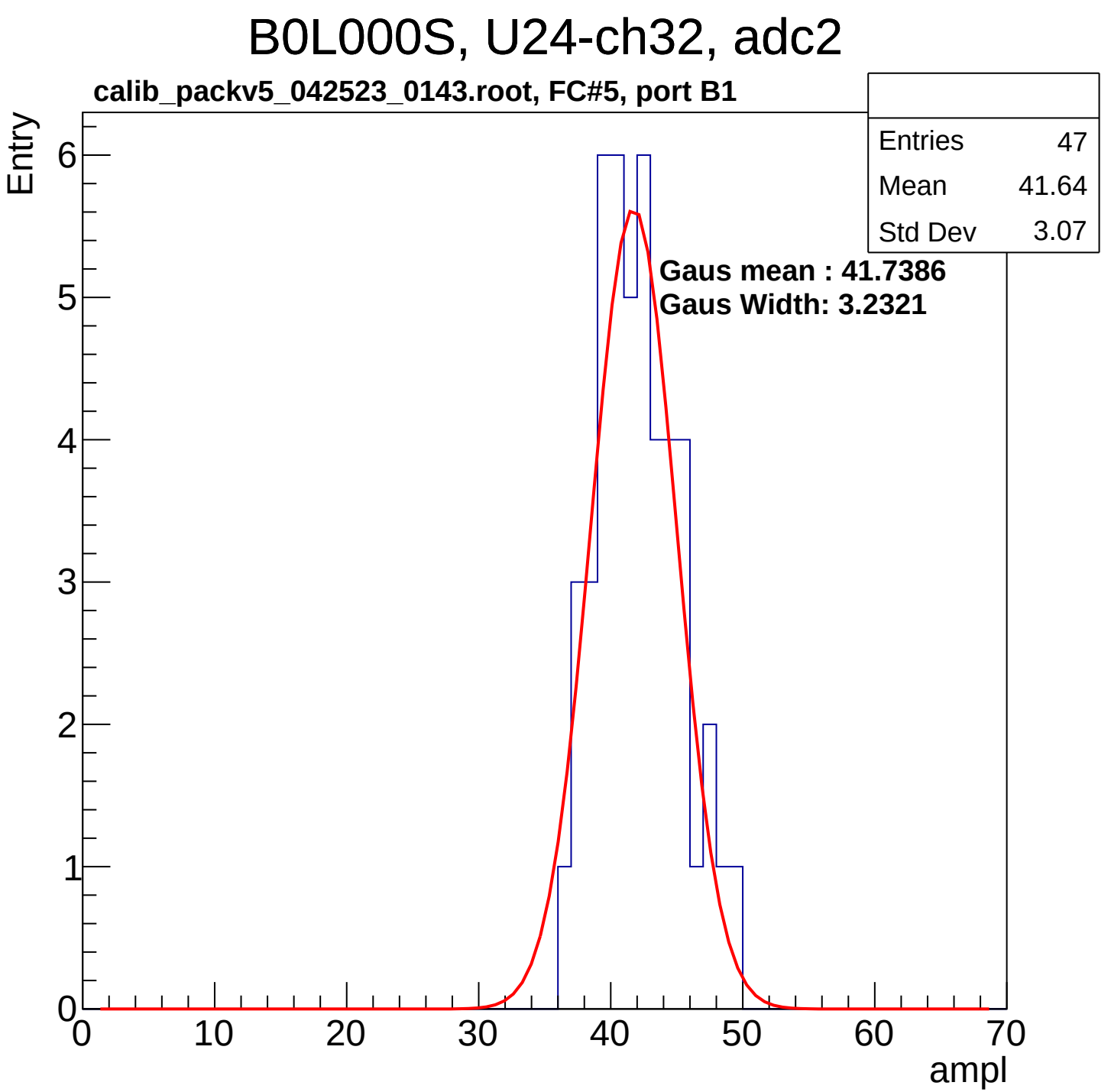
B0L000S, U24-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 41.64
Std Dev 3.07
Gaus mean : 41.7386
Gaus Width: 3.2321
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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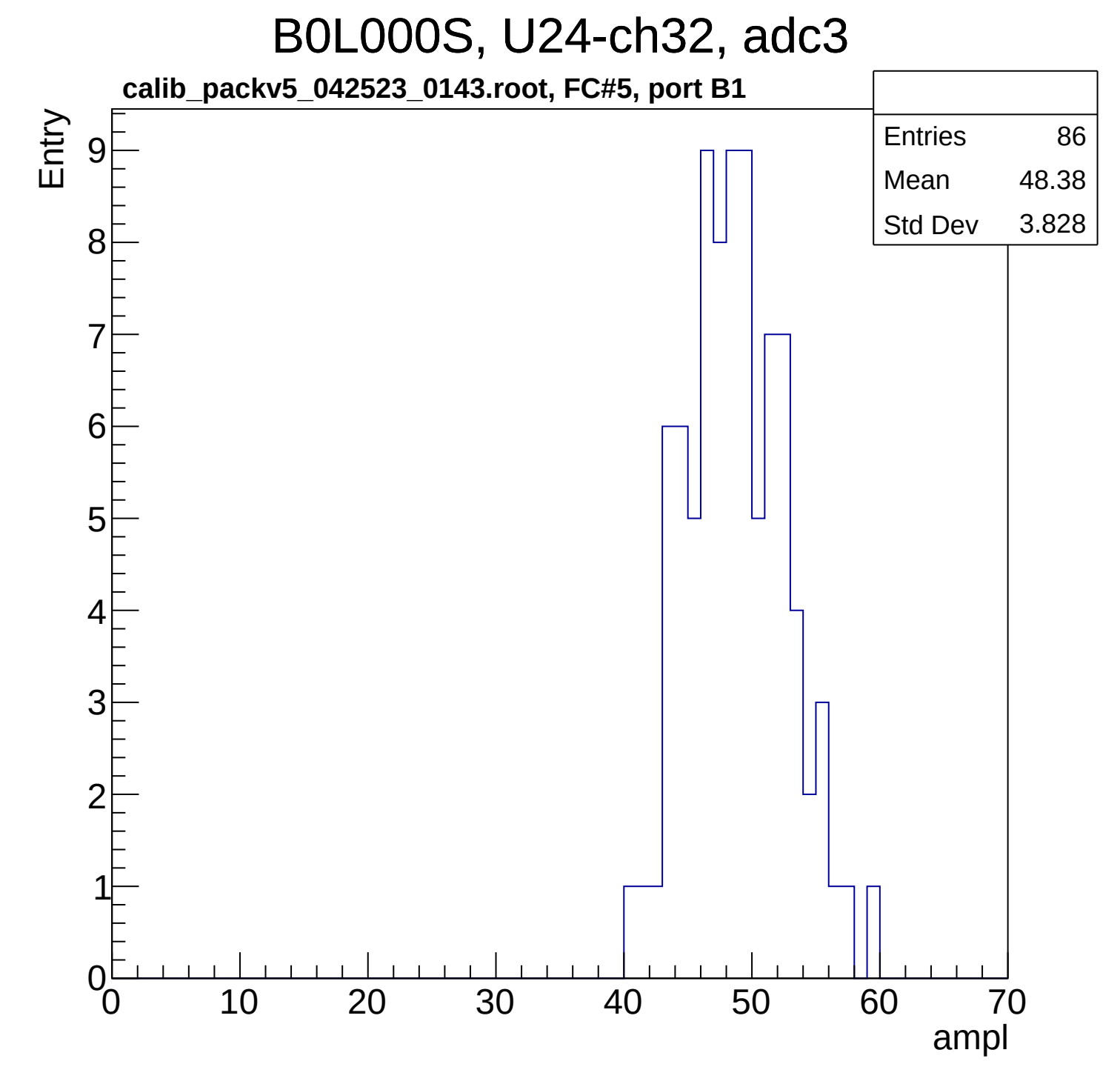
B0L000S, U24-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 48.38
Std Dev 3.828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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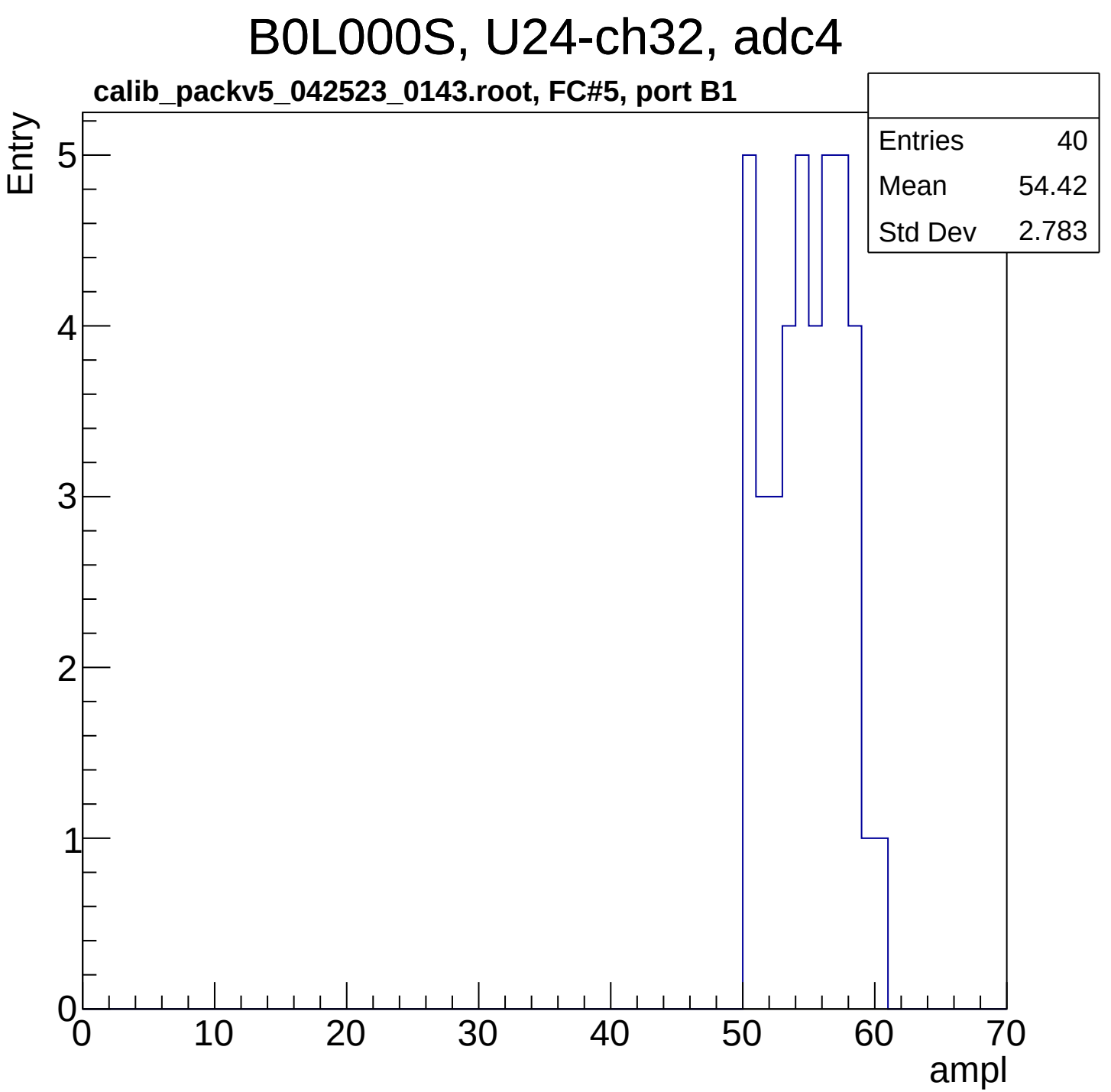
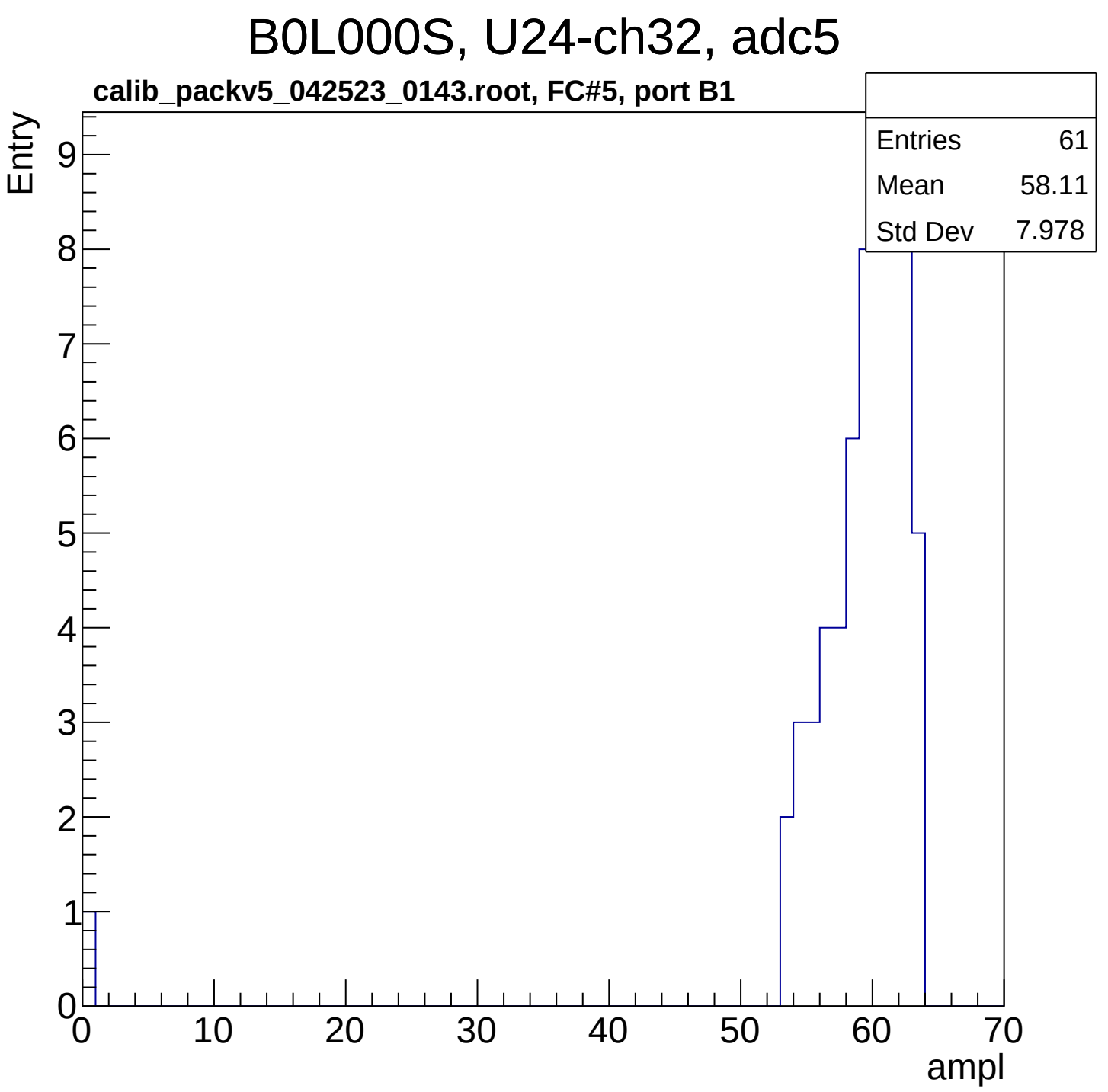

B0L000S, U24-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 54.42
Std Dev 2.783
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 58.11
Std Dev 7.978
Entry
ampl
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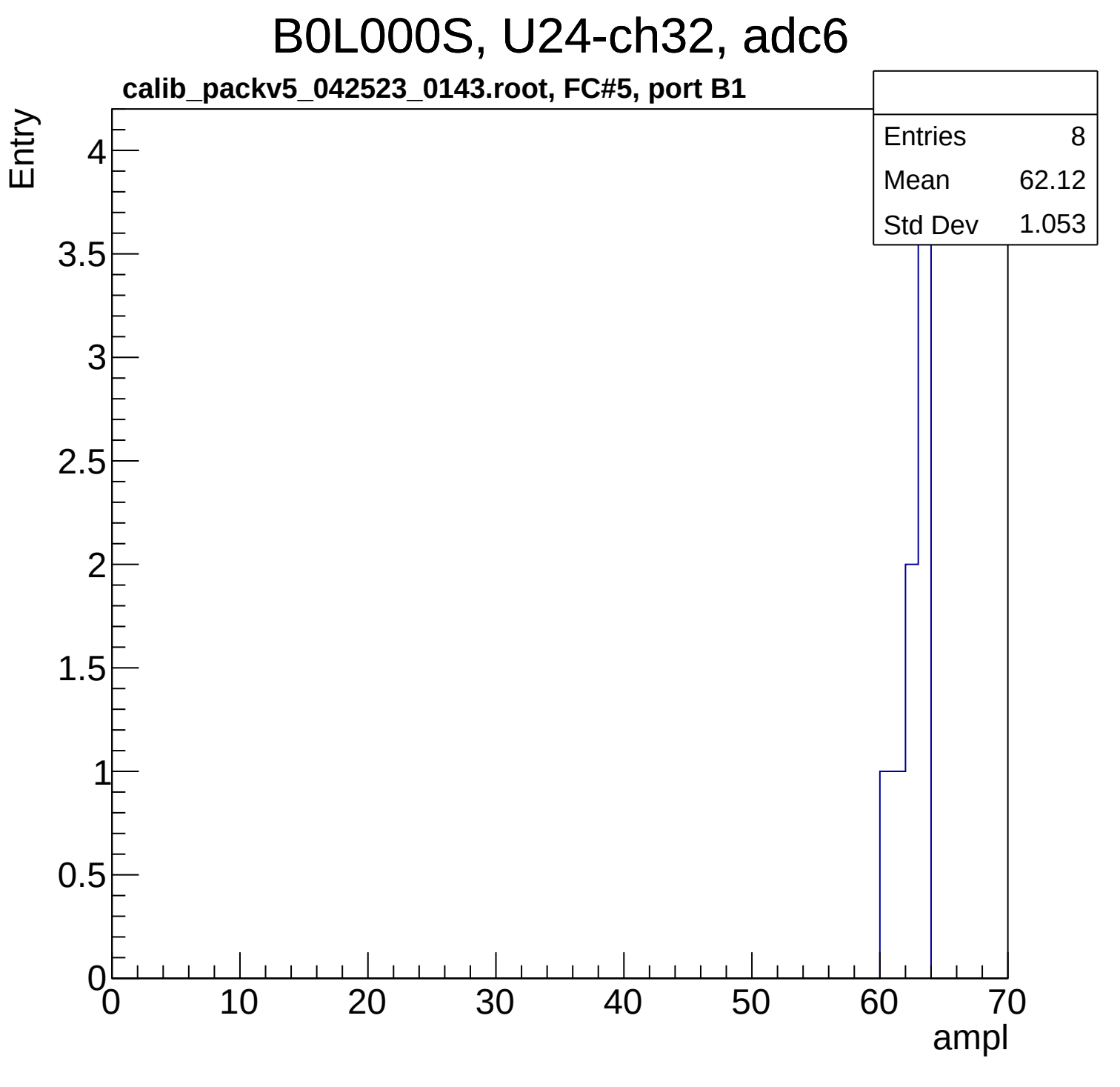
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
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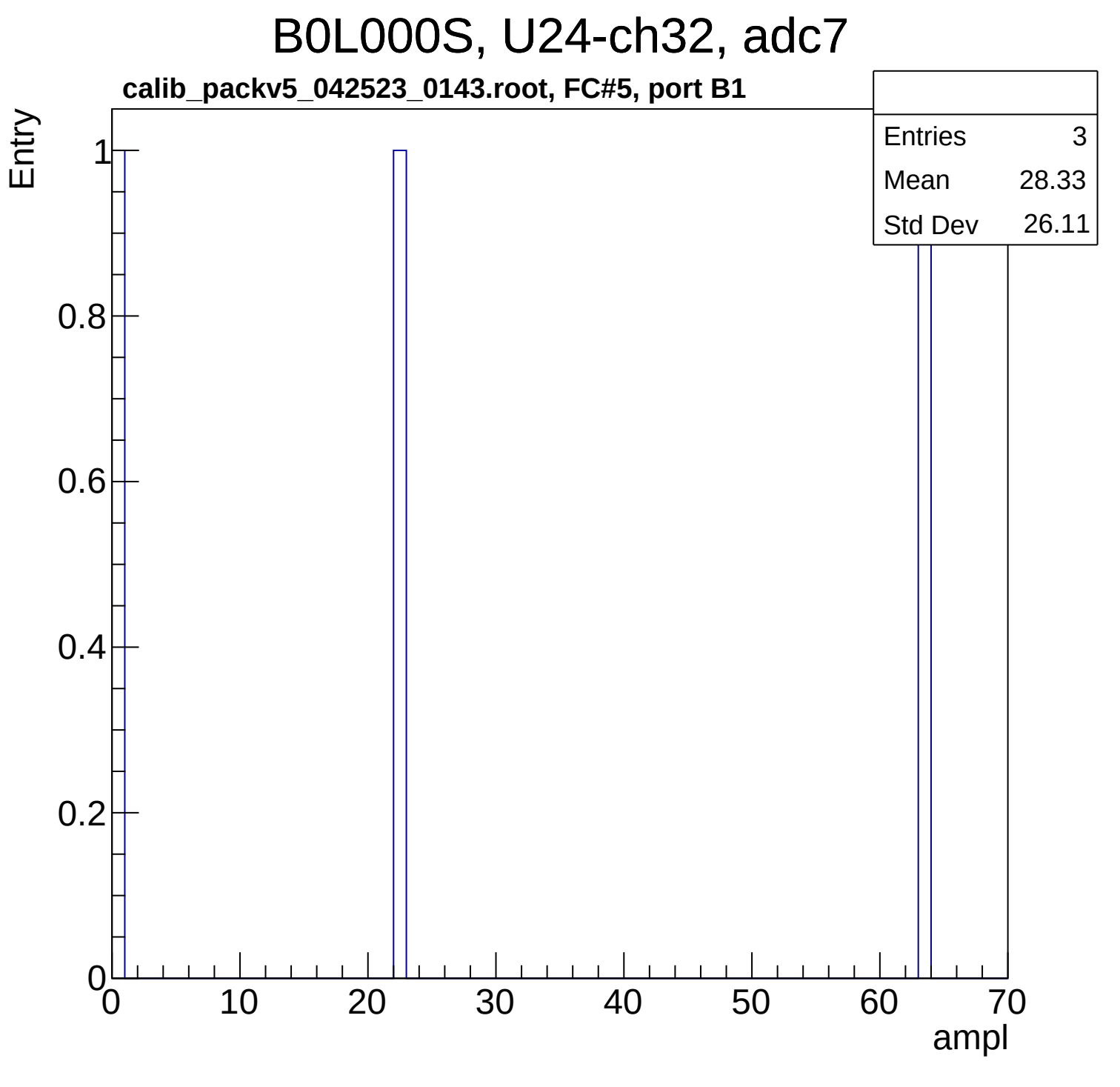
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28.33
Std Dev 26.11
Entry
ampl
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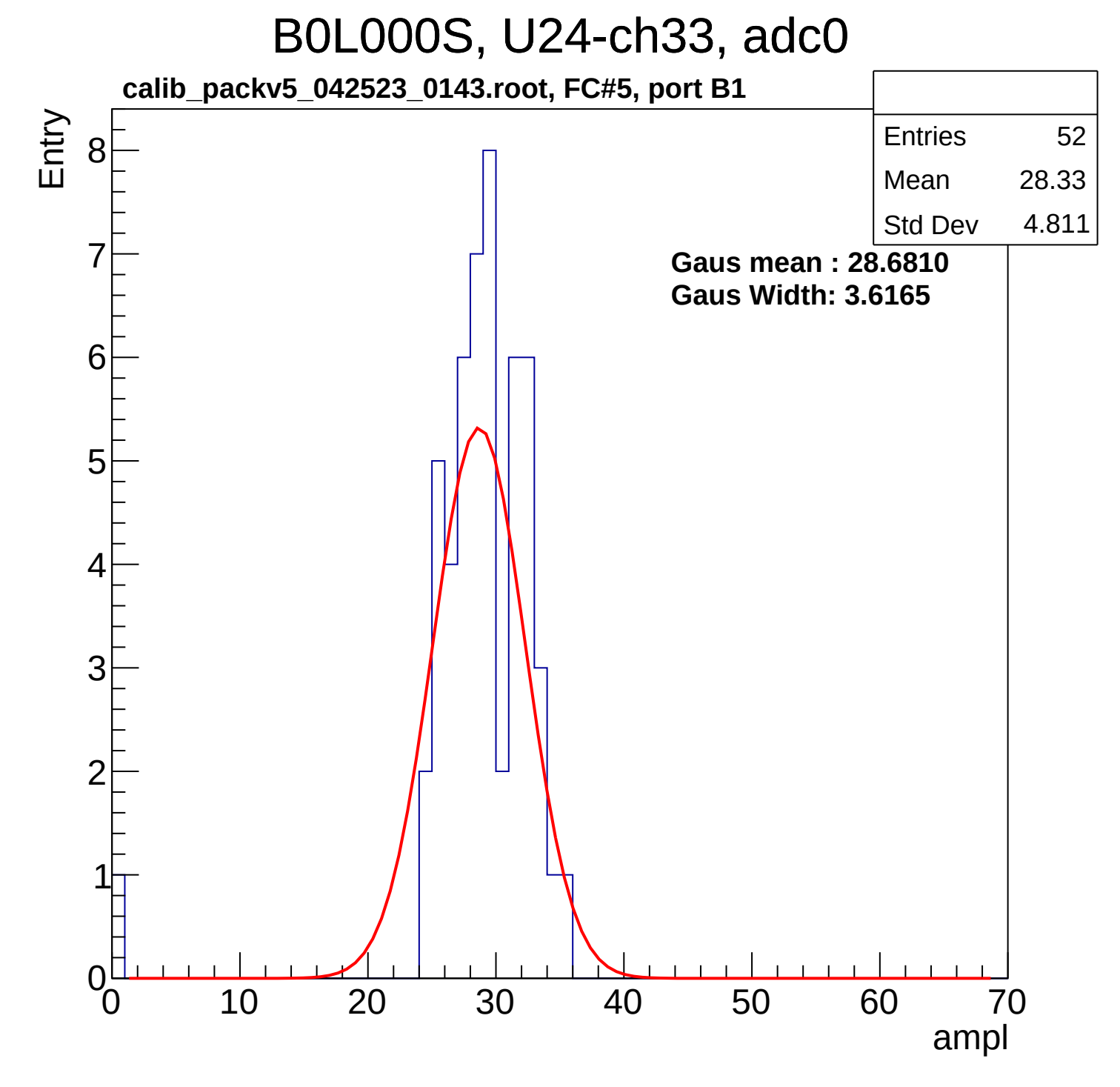
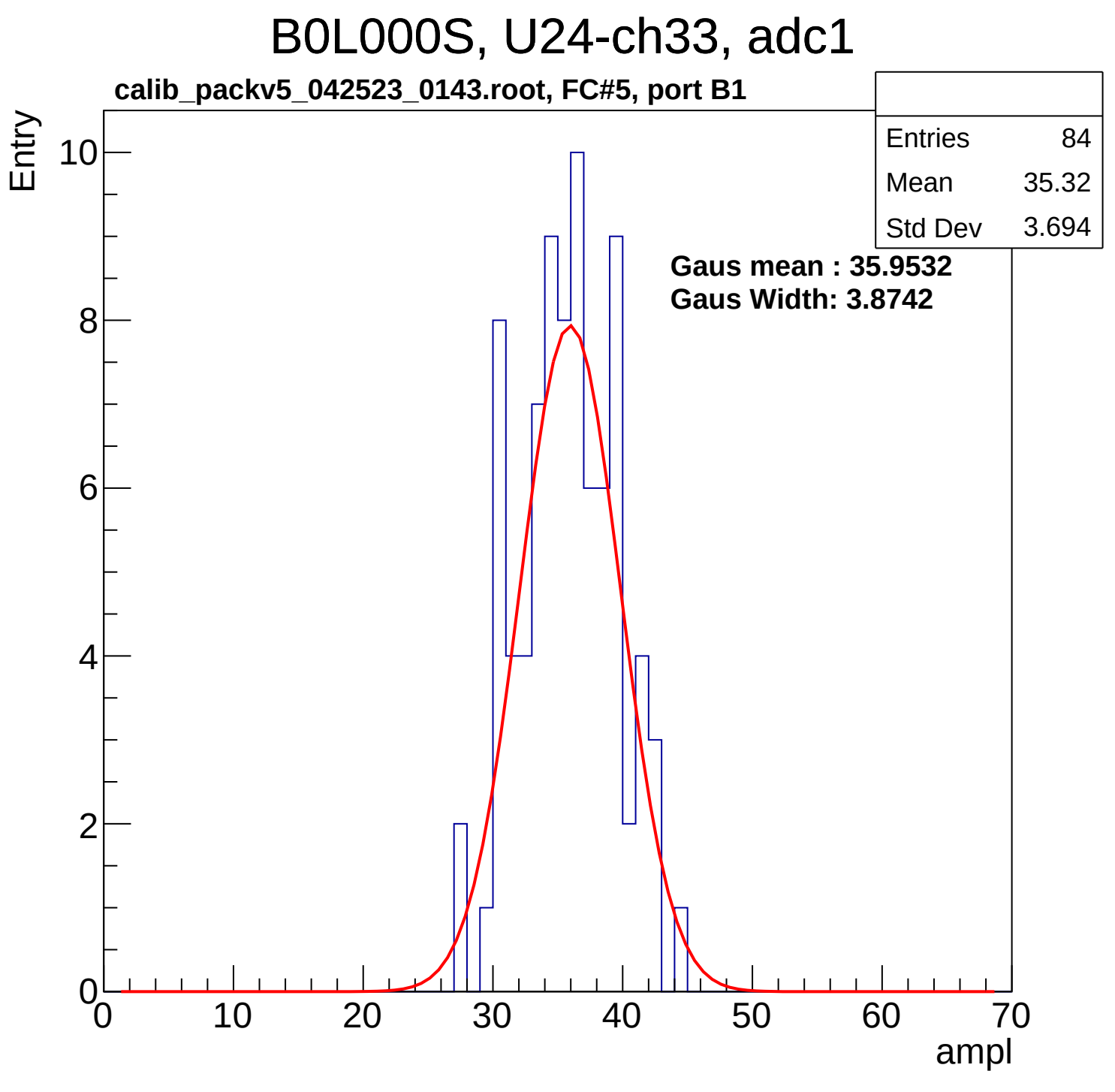
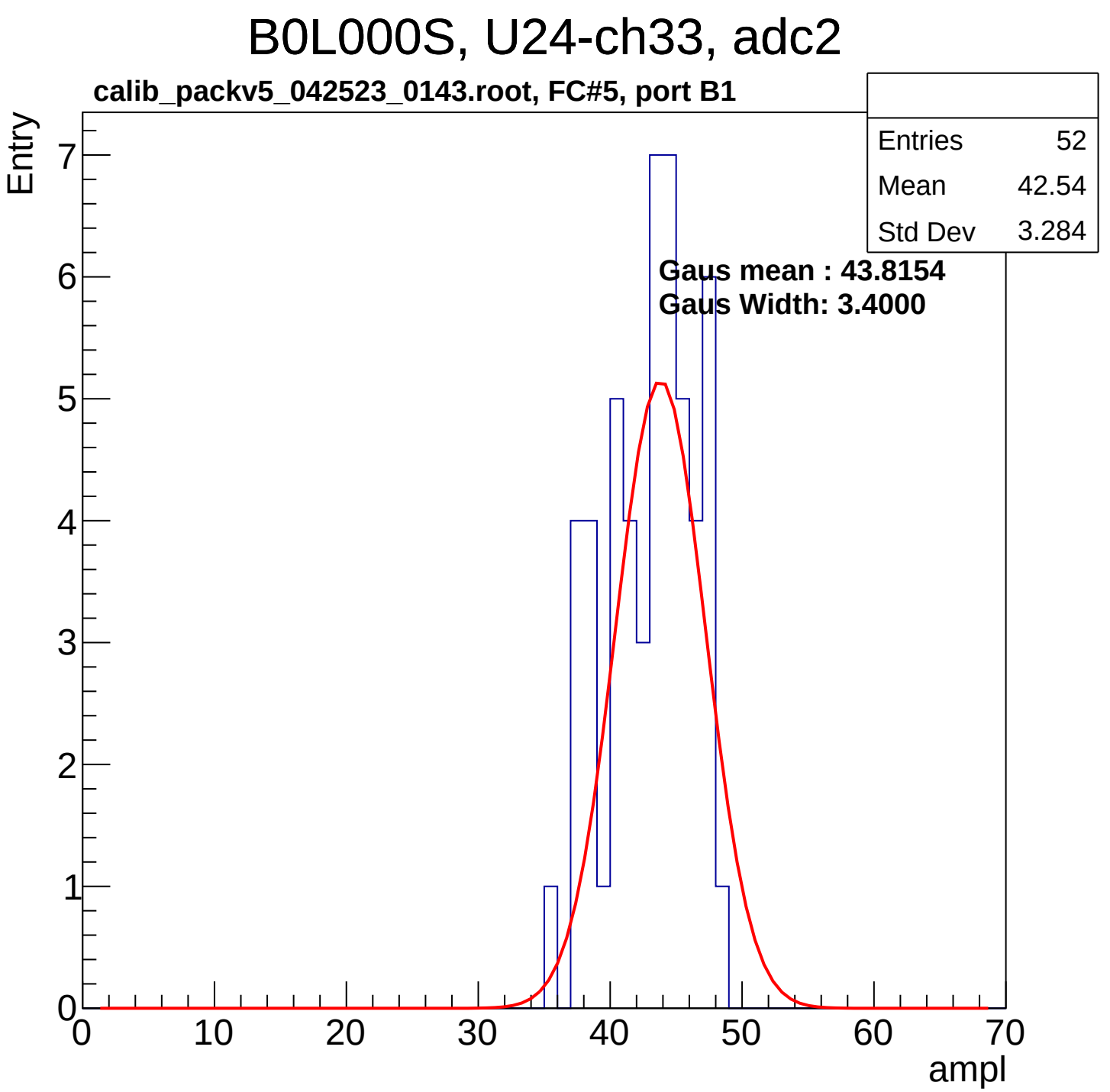
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U24-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 28.33
Std Dev 4.811
Gaus mean : 28.6810
Gaus Width: 3.6165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 35.32
Std Dev 3.694
Gaus mean : 35.9532
Gaus Width: 3.8742
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.54
Std Dev 3.284
Gaus mean : 43.8154
Gaus Width: 3.4000
Entry
ampl
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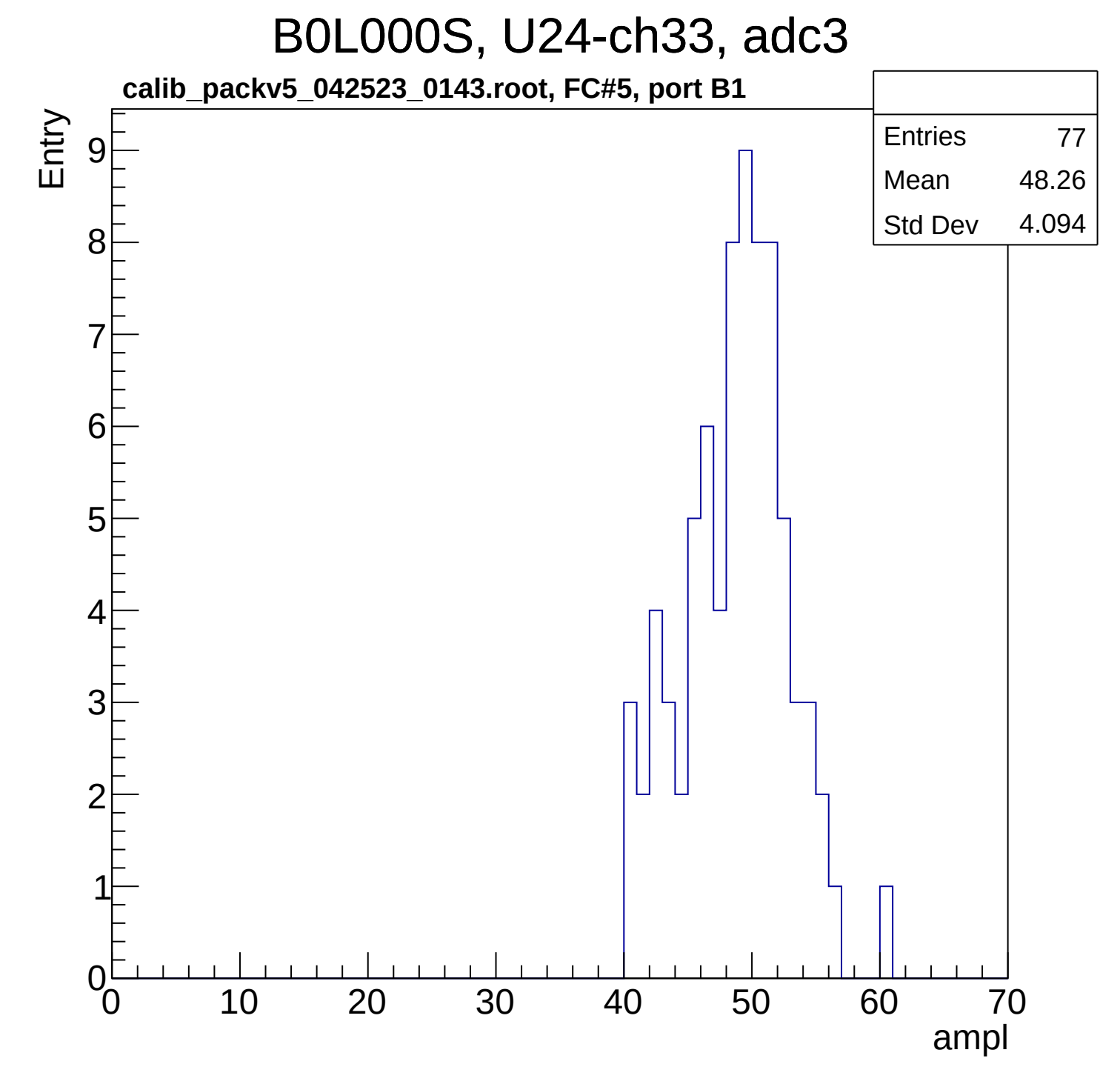
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 48.26
Std Dev 4.094
Entry
ampl
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3
4
5
6
7
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60
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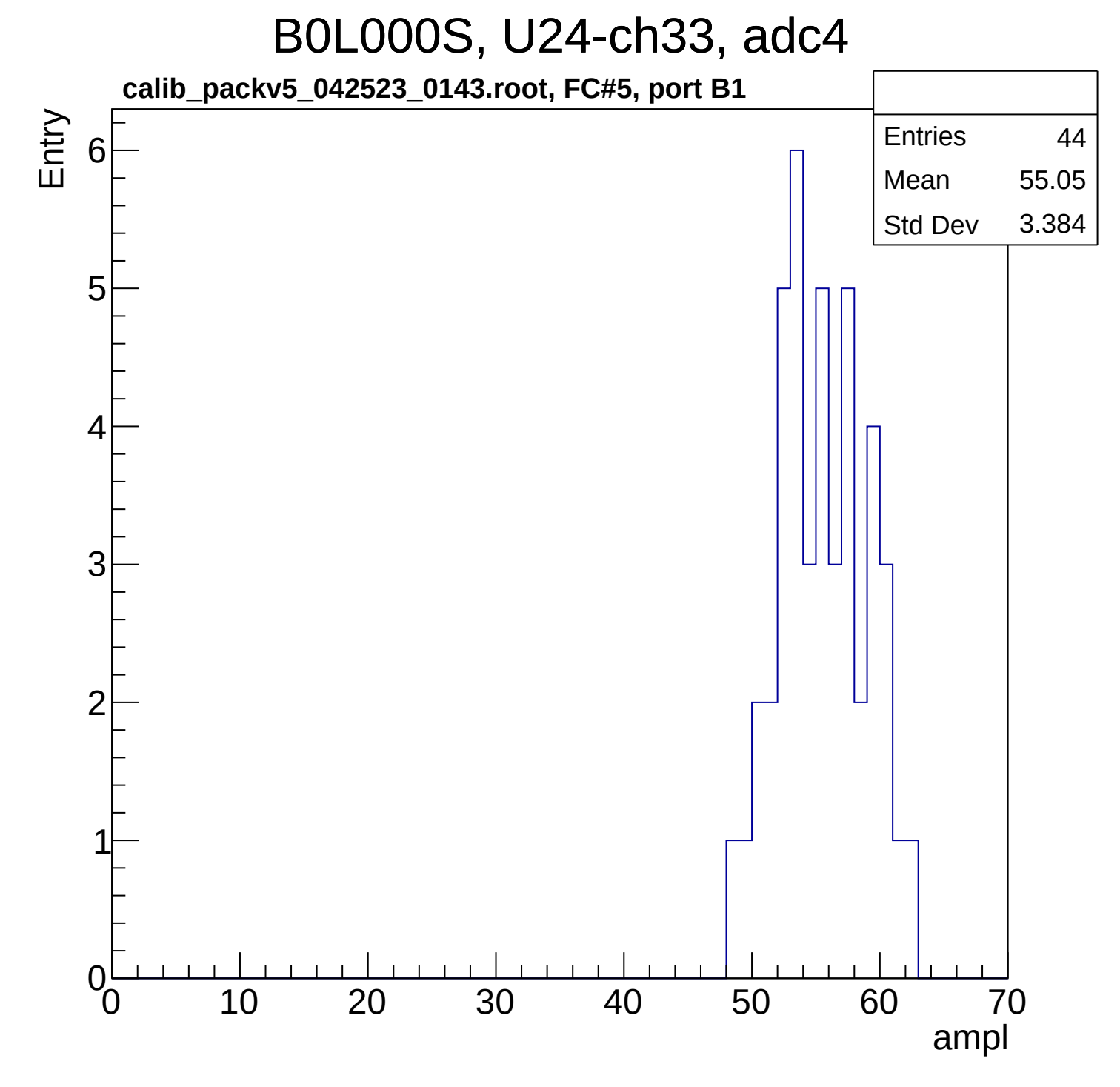
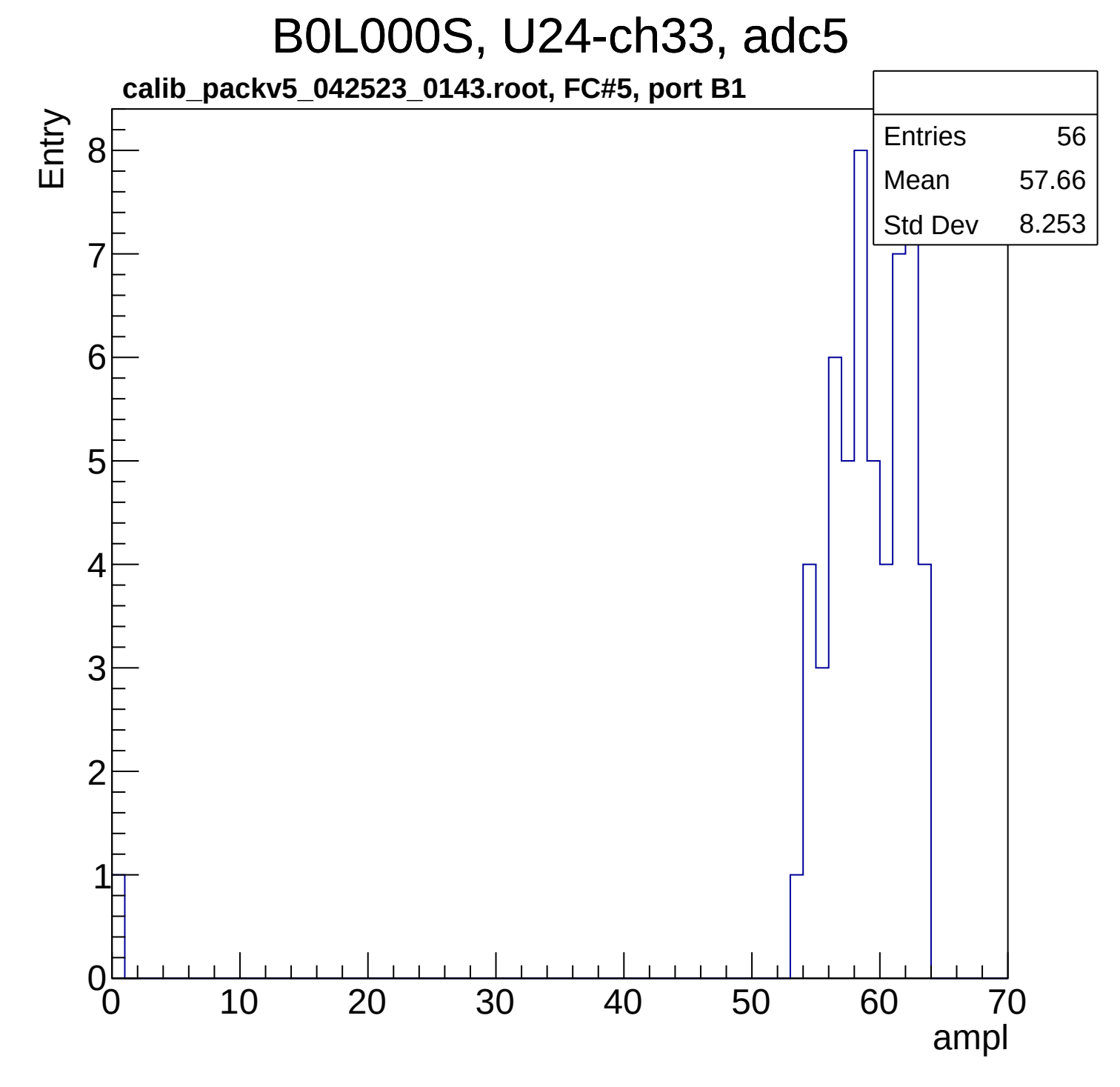

B0L000S, U24-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 55.05
Std Dev 3.384
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.66
Std Dev 8.253
Entry
ampl
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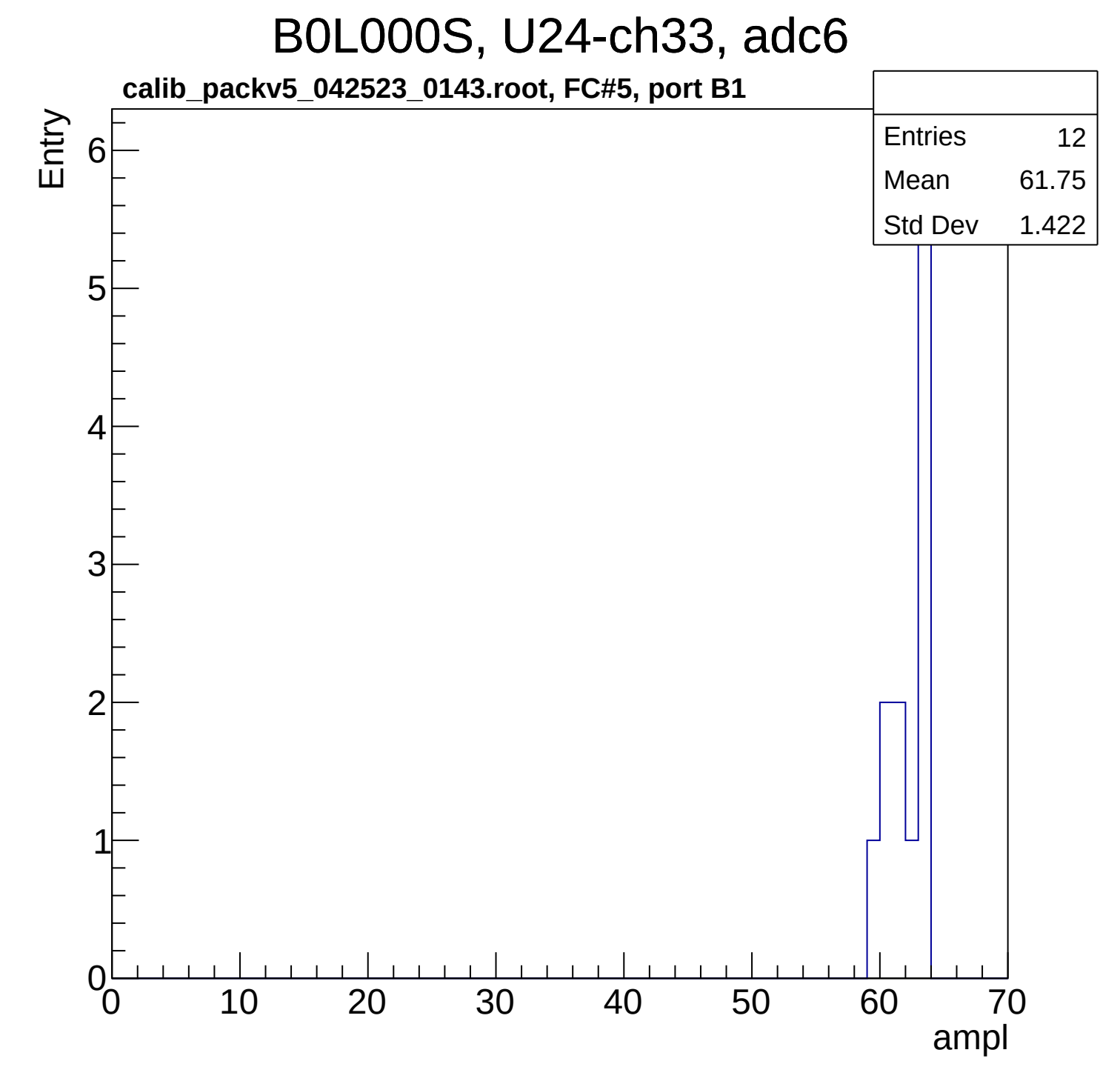
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.75
Std Dev 1.422
Entry
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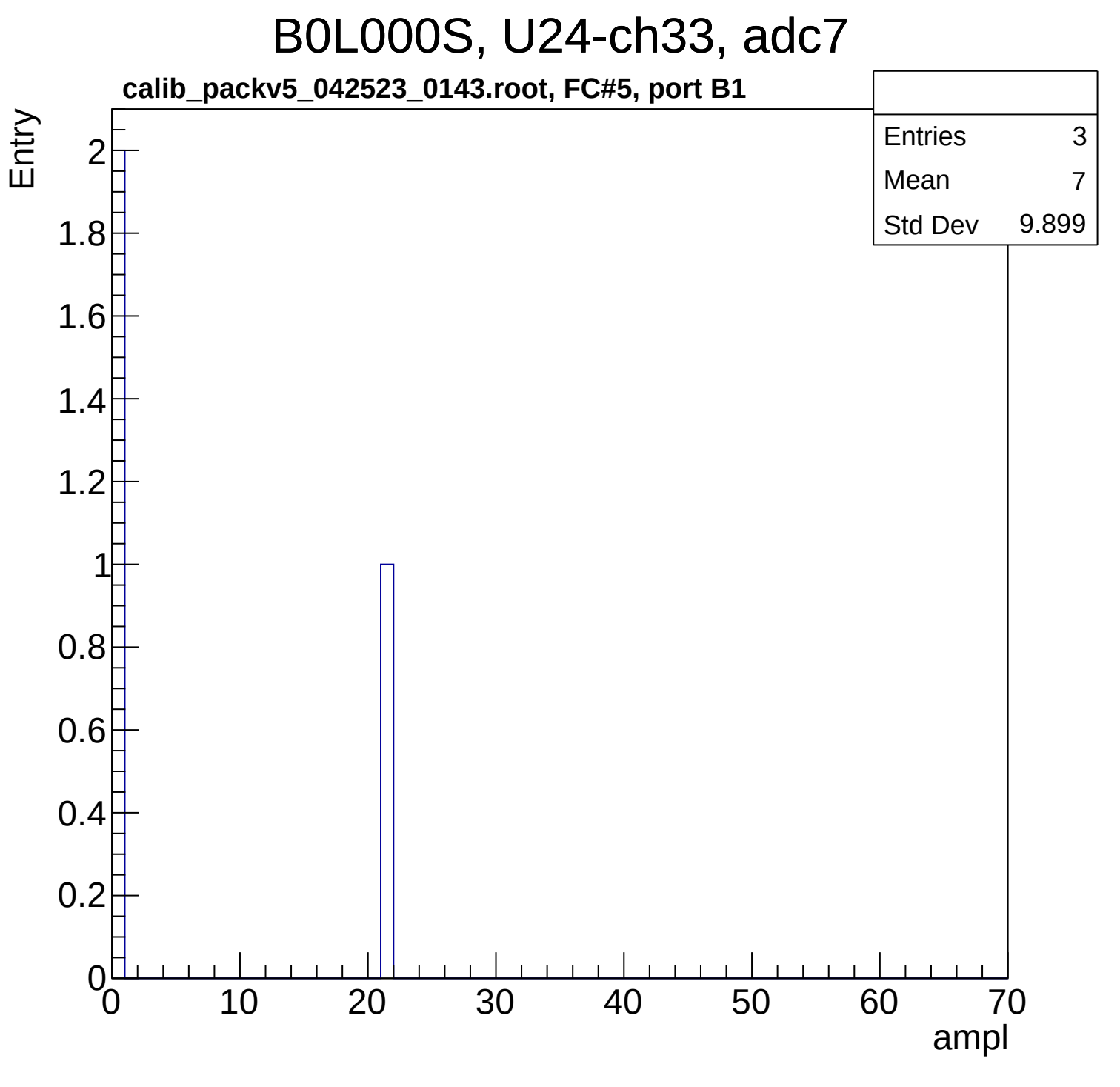
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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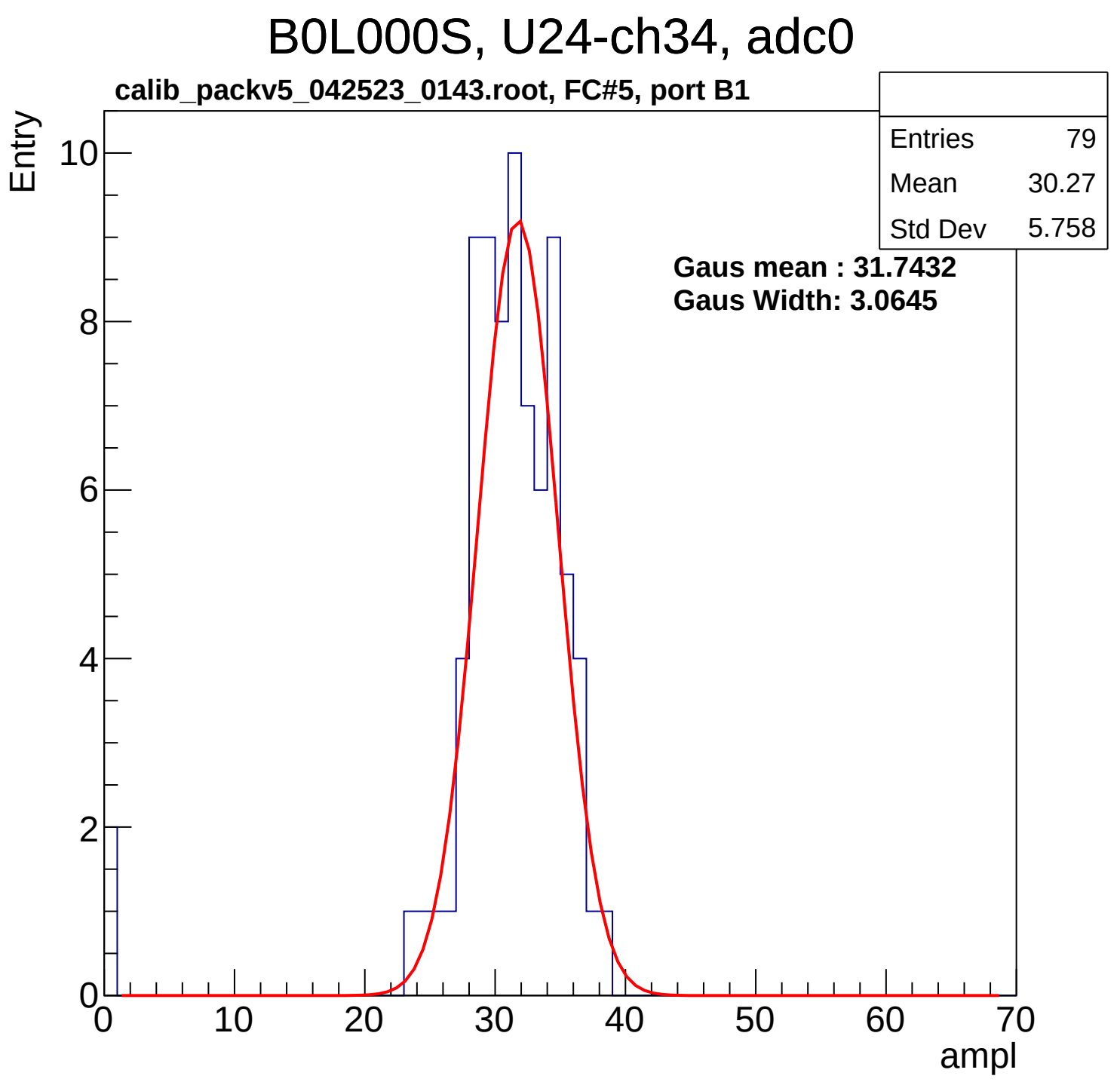
40
50
60
70


B0L000S, U24-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 30.27
Std Dev 5.758
Gaus mean : 31.7432
Gaus Width: 3.0645
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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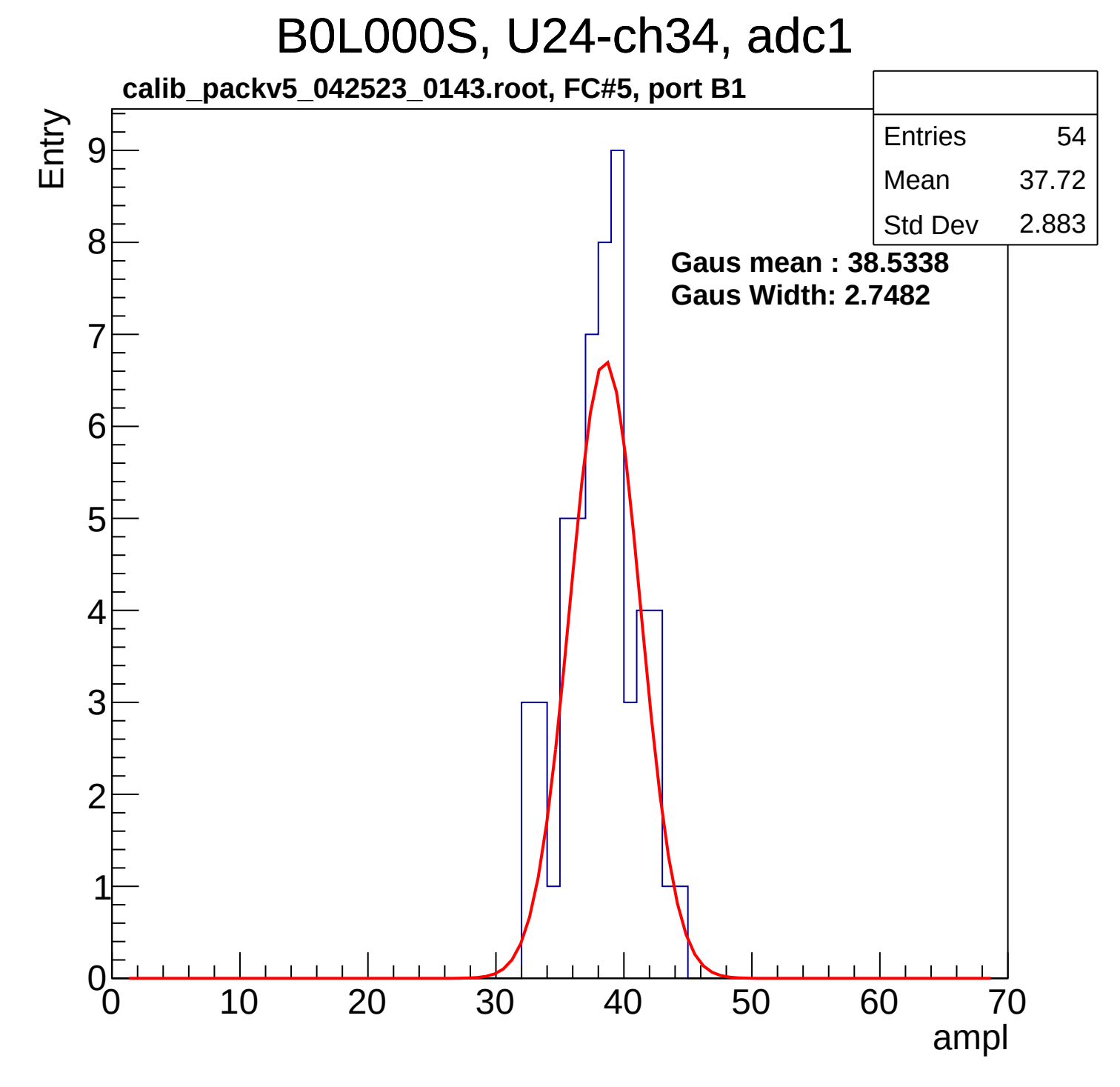
B0L000S, U24-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 37.72
Std Dev 2.883
Gaus mean : 38.5338
Gaus Width: 2.7482
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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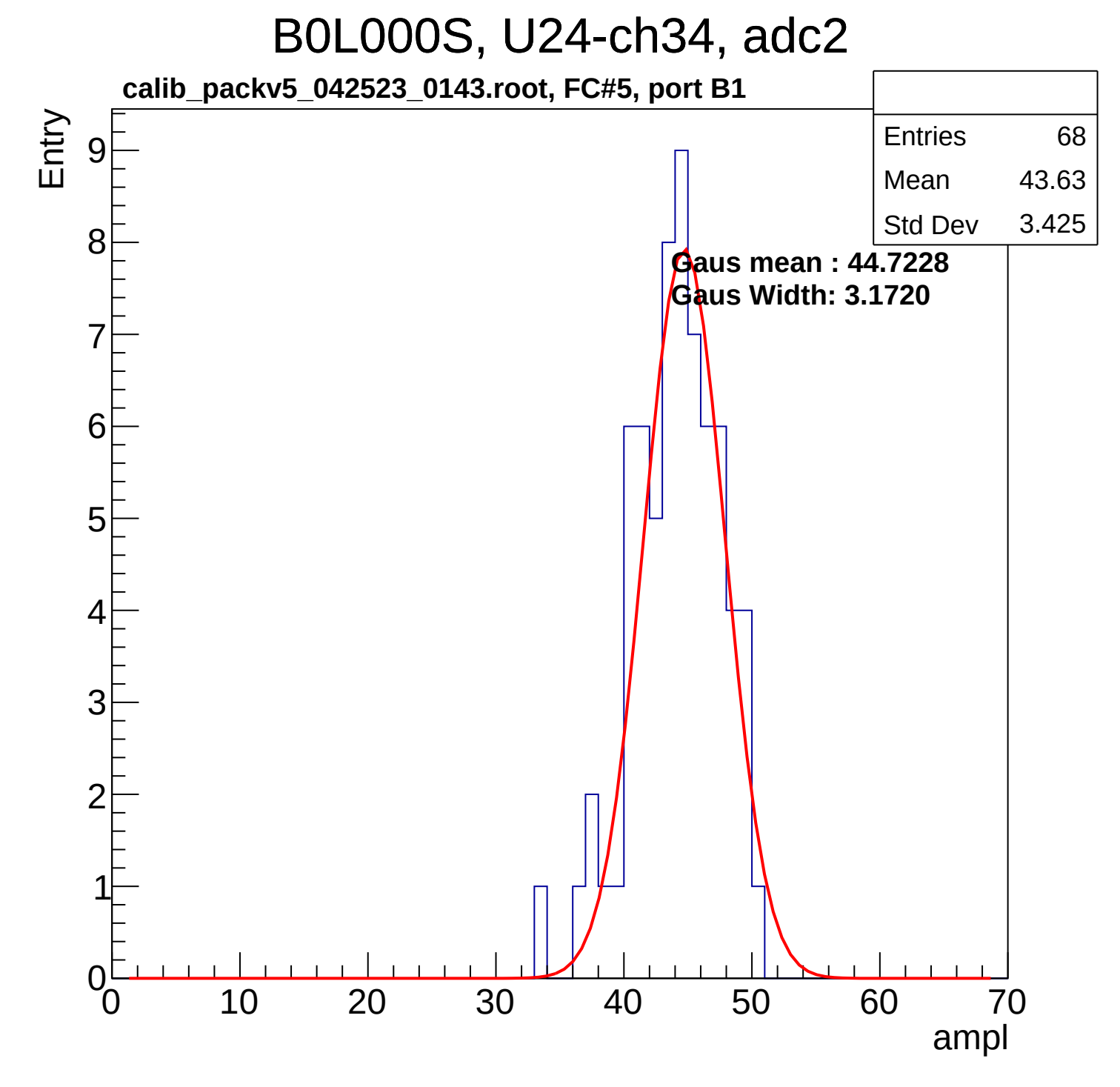
B0L000S, U24-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.63
Std Dev 3.425
Gaus mean : 44.7228
Gaus Width: 3.1720
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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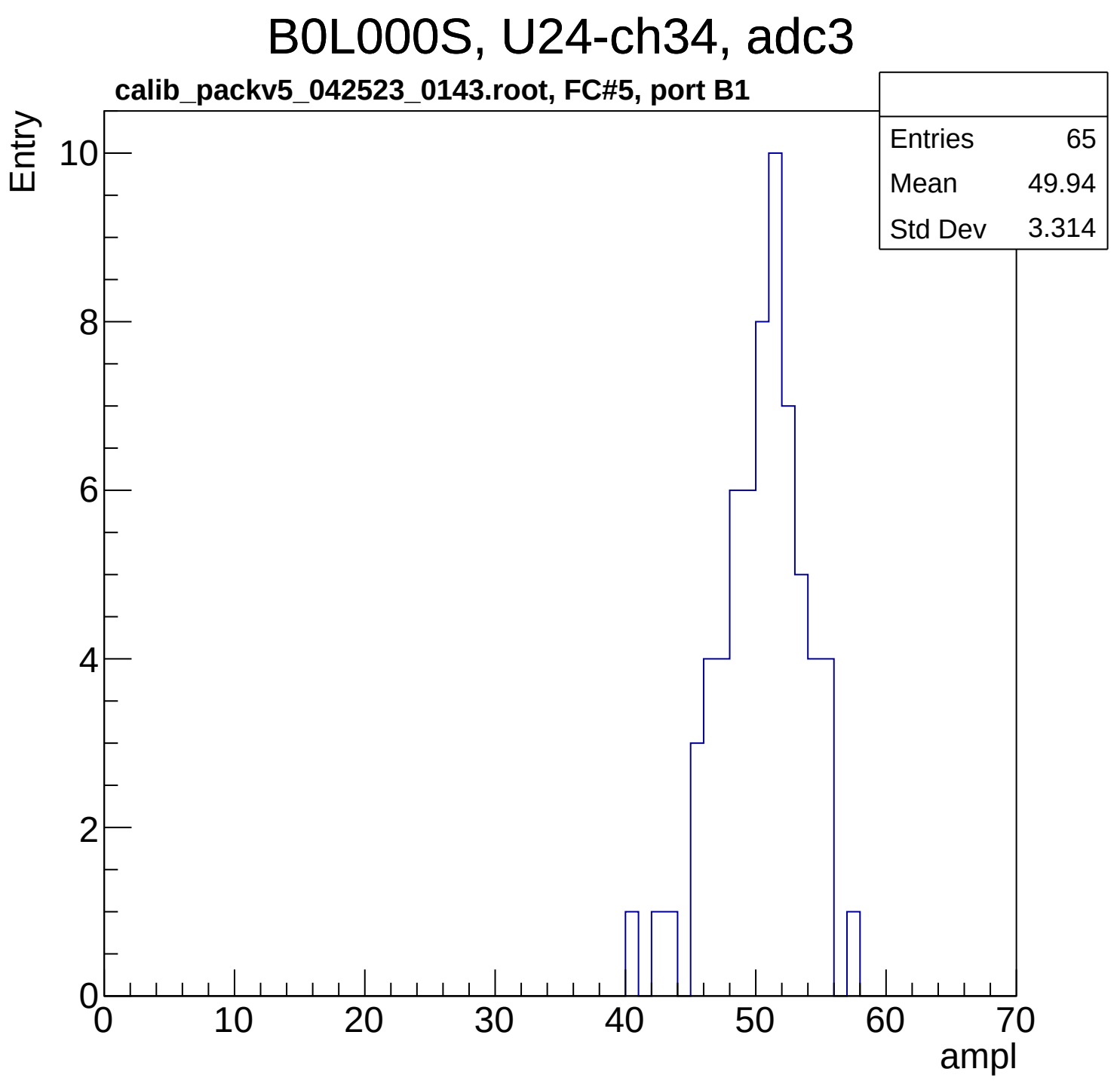
60
70


B0L000S, U24-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.94
Std Dev 3.314
Entry
ampl
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4
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0
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40
50
60
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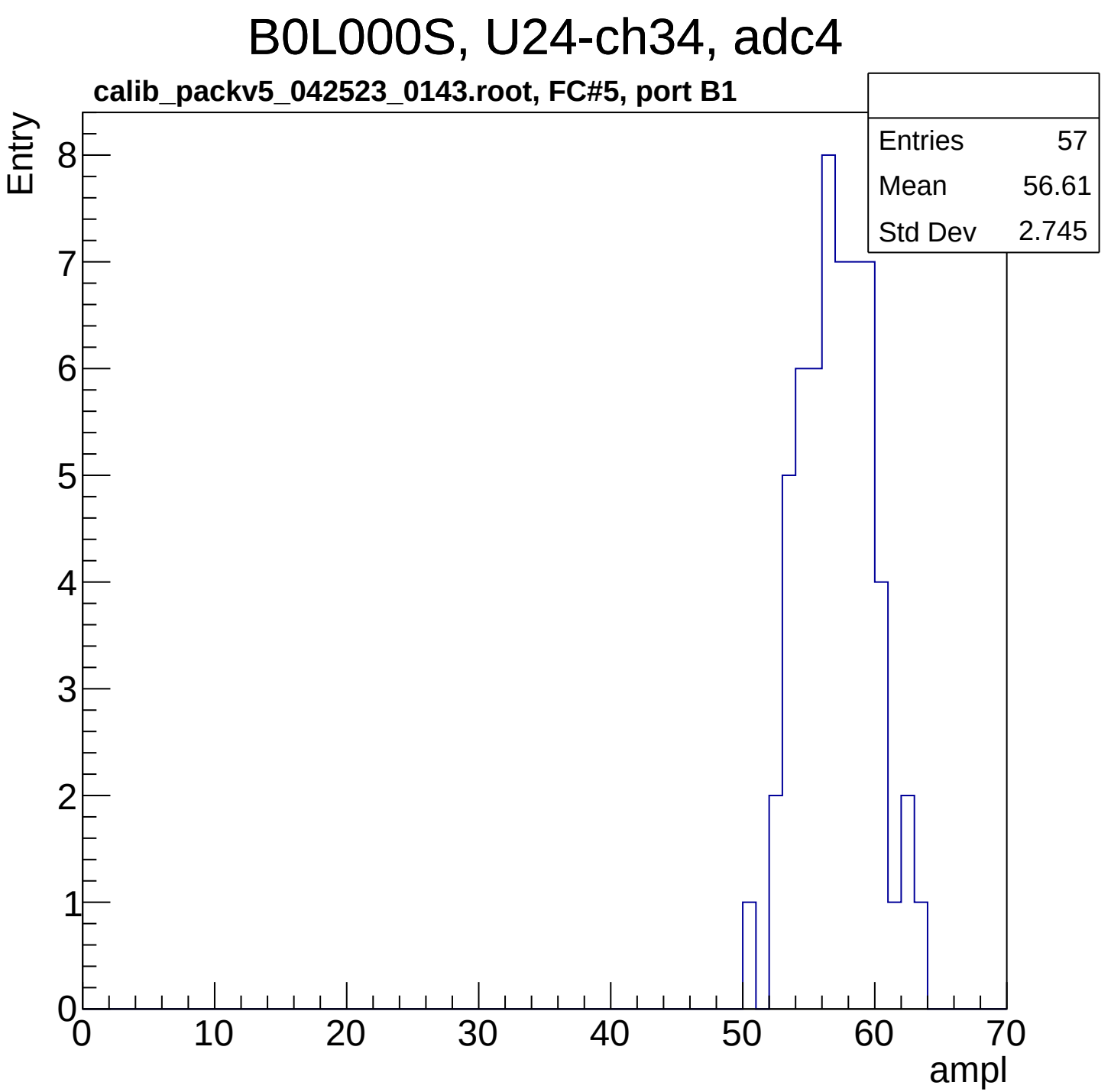
B0L000S, U24-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 56.61
Std Dev 2.745
Entry
ampl
0
1
2
3
4
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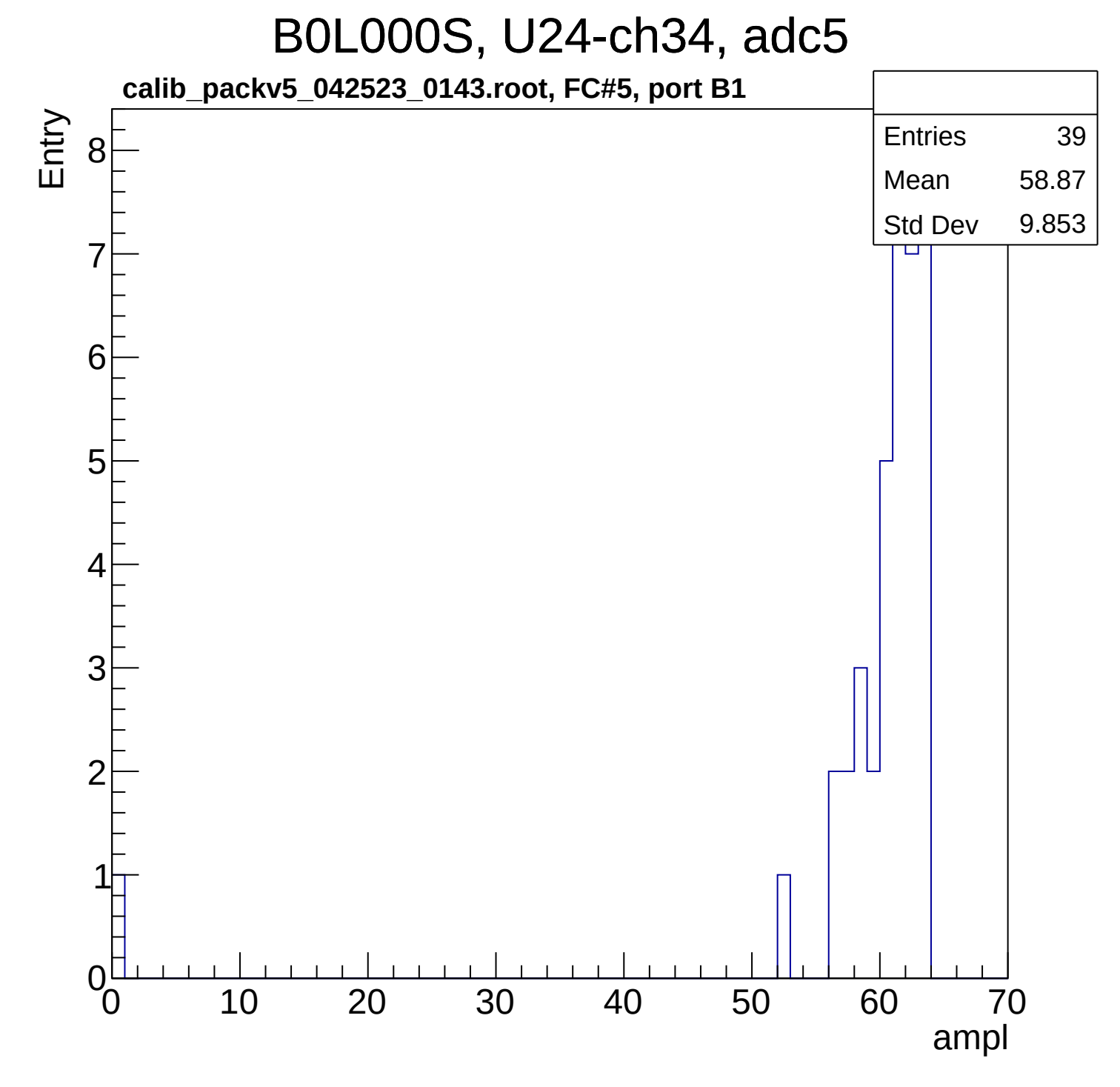
B0L000S, U24-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.87
Std Dev 9.853
Entry
ampl
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2
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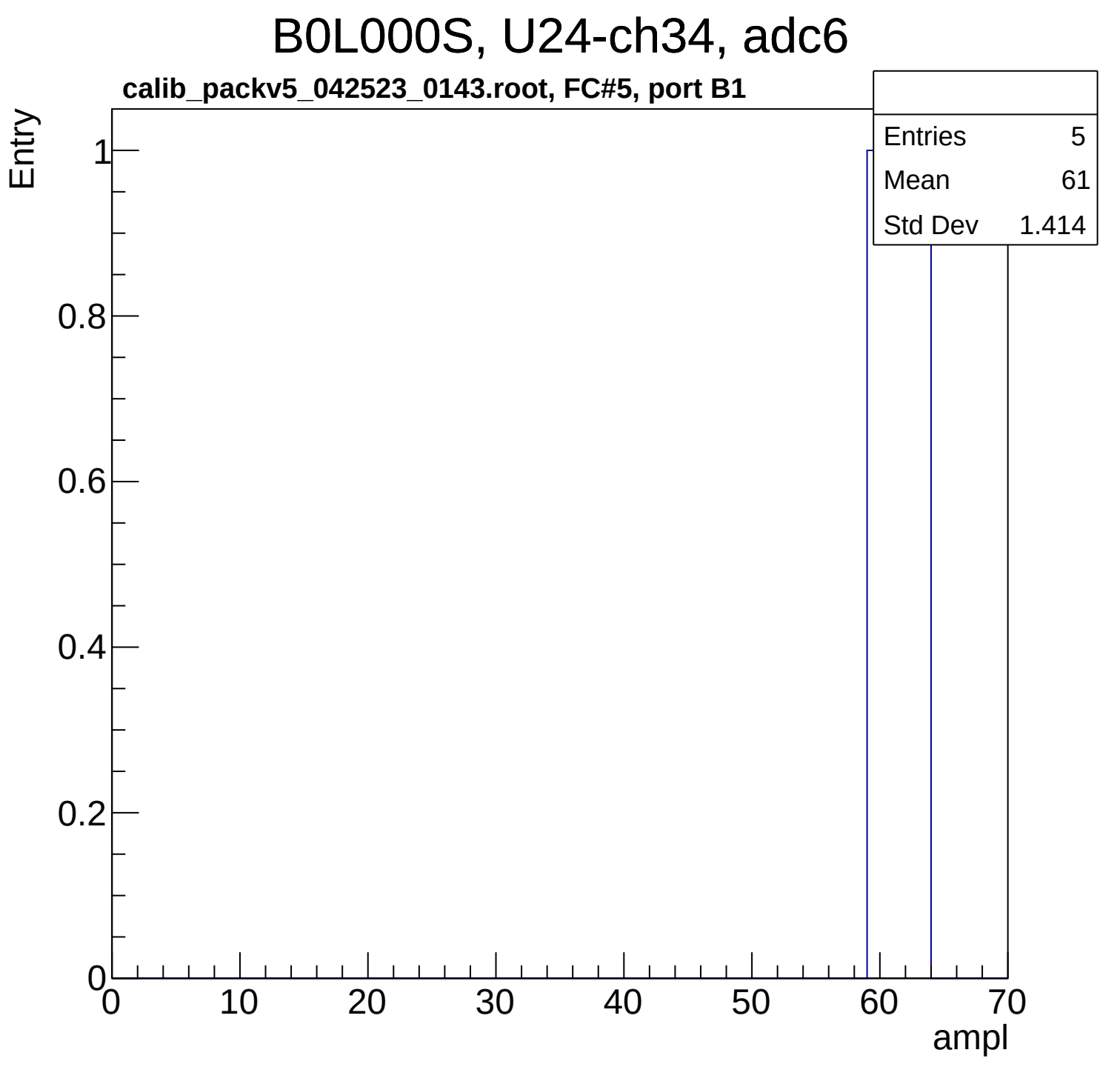
B0L000S, U24-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61
Std Dev 1.414
Entry
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0.6
0.4
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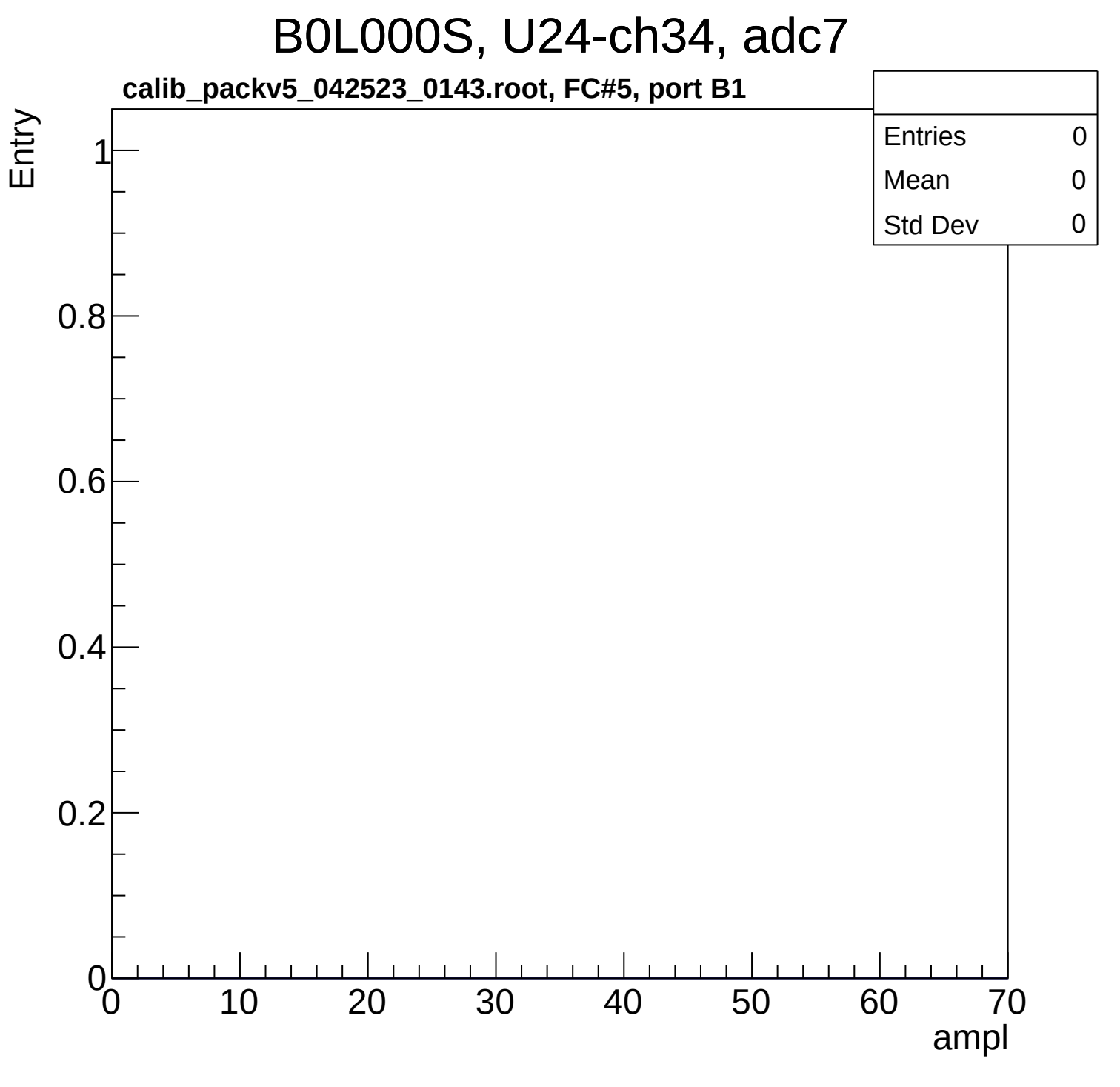
70
ampl


B0L000S, U24-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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60
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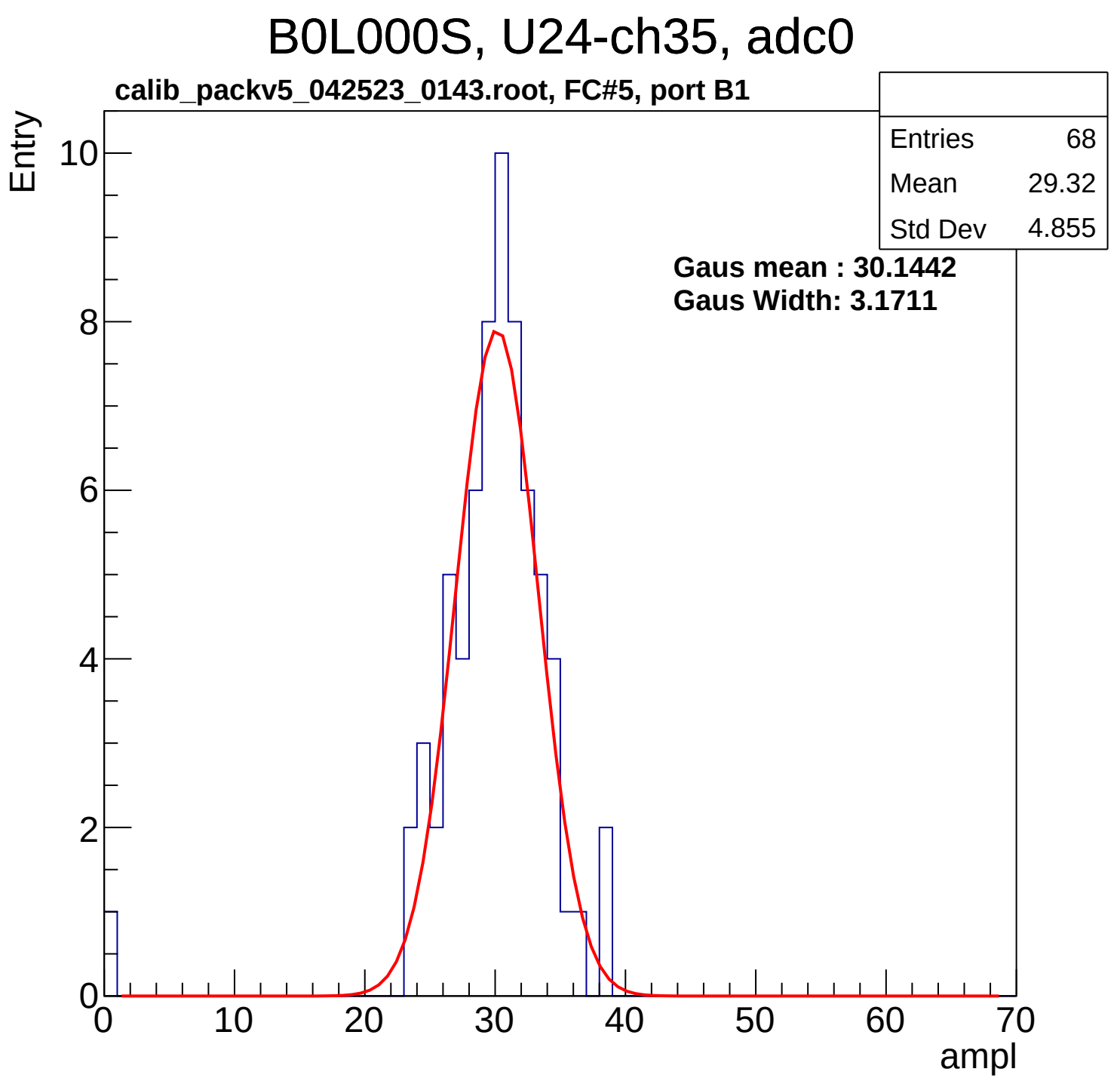
ampl


B0L000S, U24-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.32
Std Dev 4.855
Gaus mean : 30.1442
Gaus Width: 3.1711
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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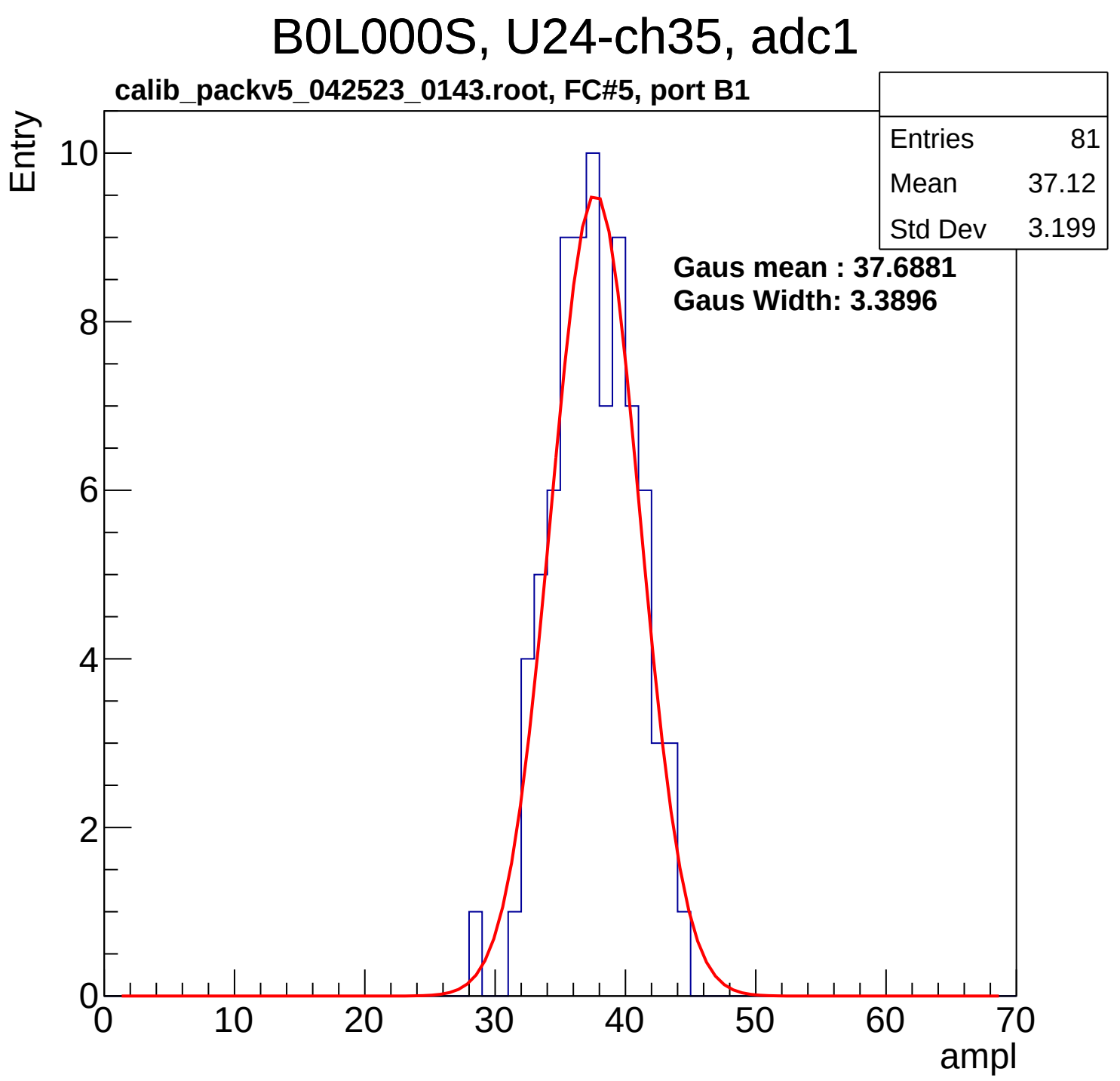
B0L000S, U24-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.12
Std Dev 3.199
Gaus mean : 37.6881
Gaus Width: 3.3896
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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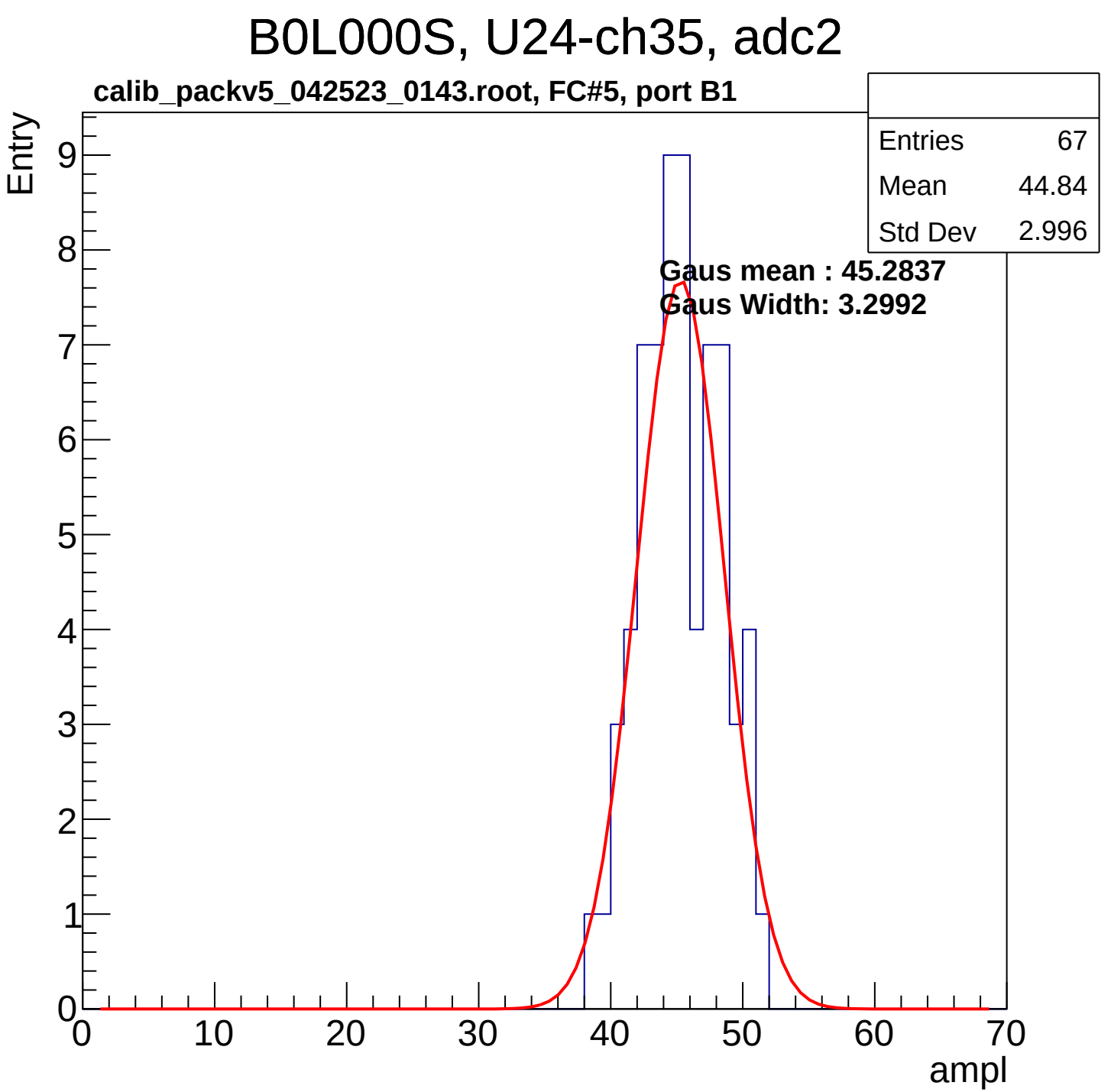
B0L000S, U24-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.84
Std Dev 2.996
Gaus mean : 45.2837
Gaus Width: 3.2992
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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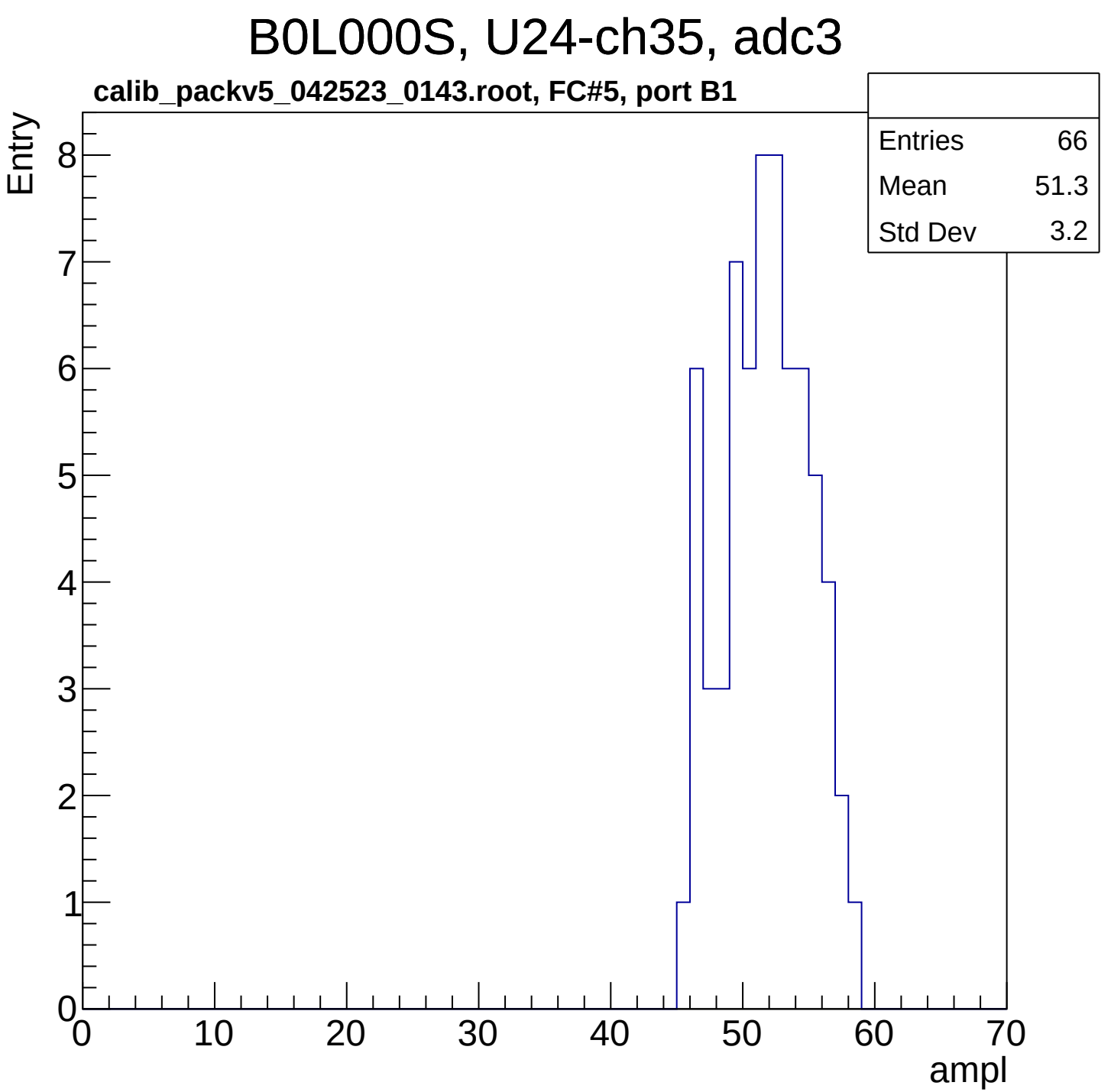
B0L000S, U24-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.3
Std Dev 3.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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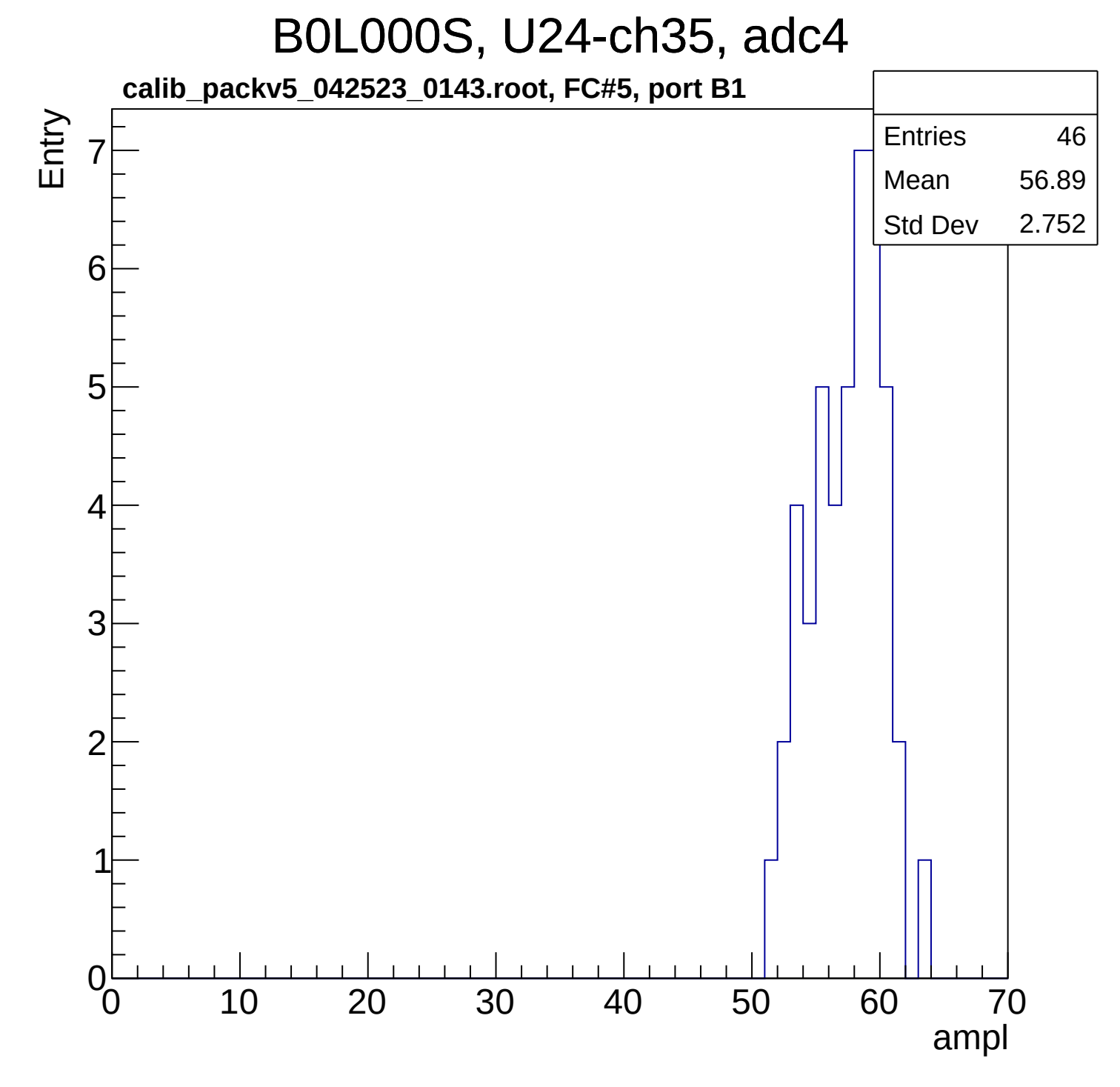
B0L000S, U24-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.89
Std Dev 2.752
Entry
ampl
0
1
2
3
4
5
6
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0
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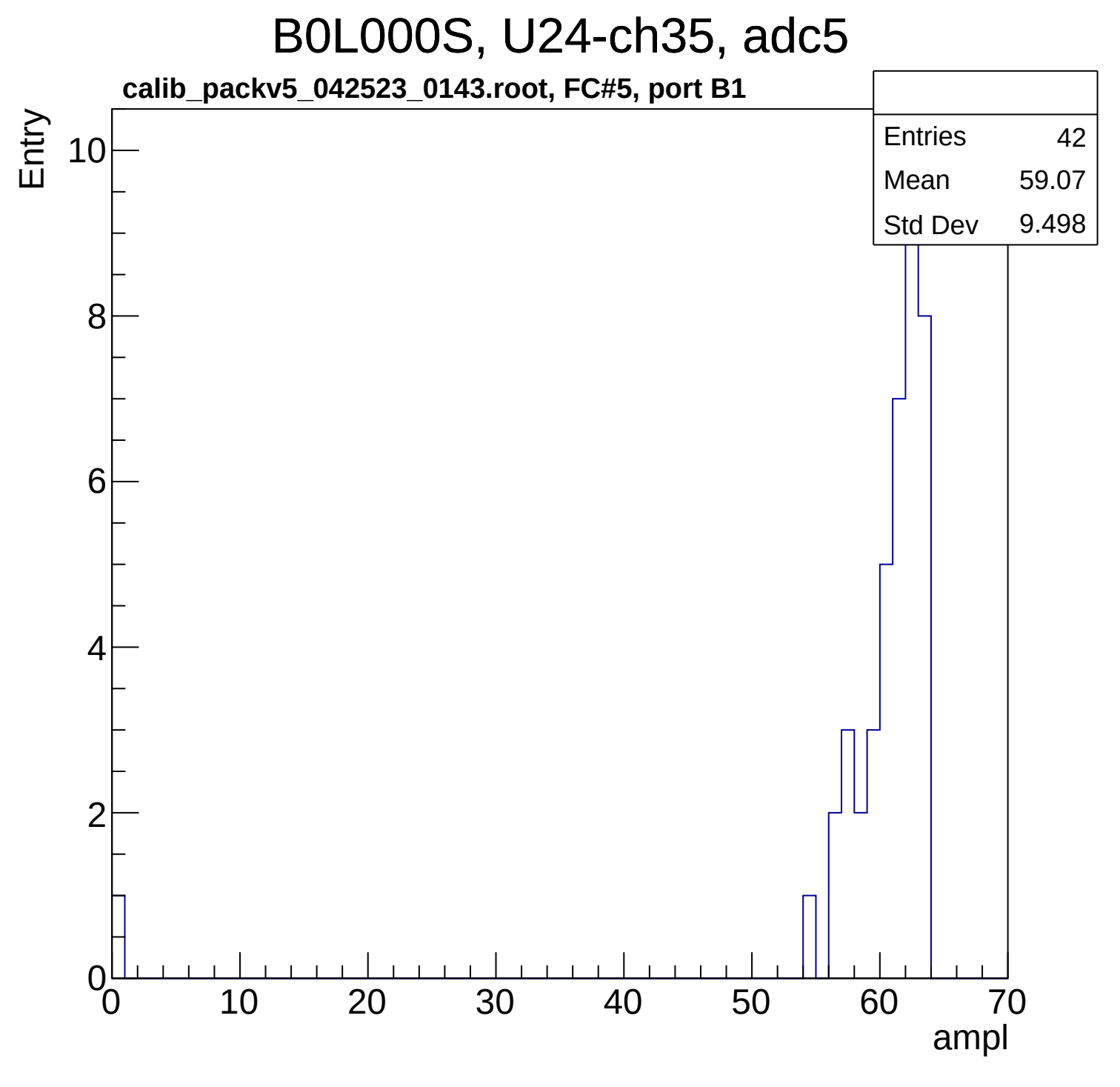
B0L000S, U24-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.07
Std Dev 9.498
Entry
0
2
4
6
8
10
0
10
20
30
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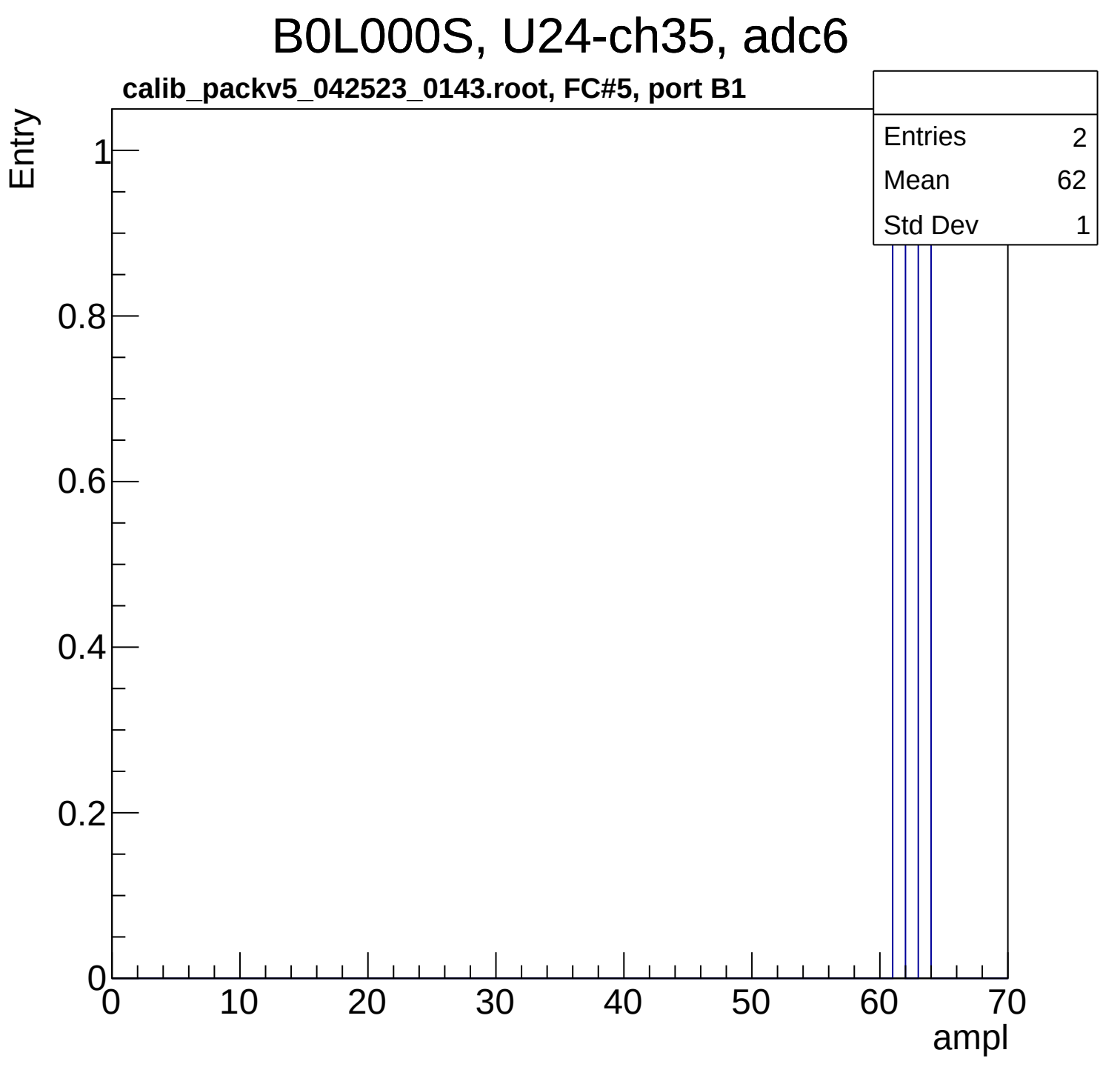
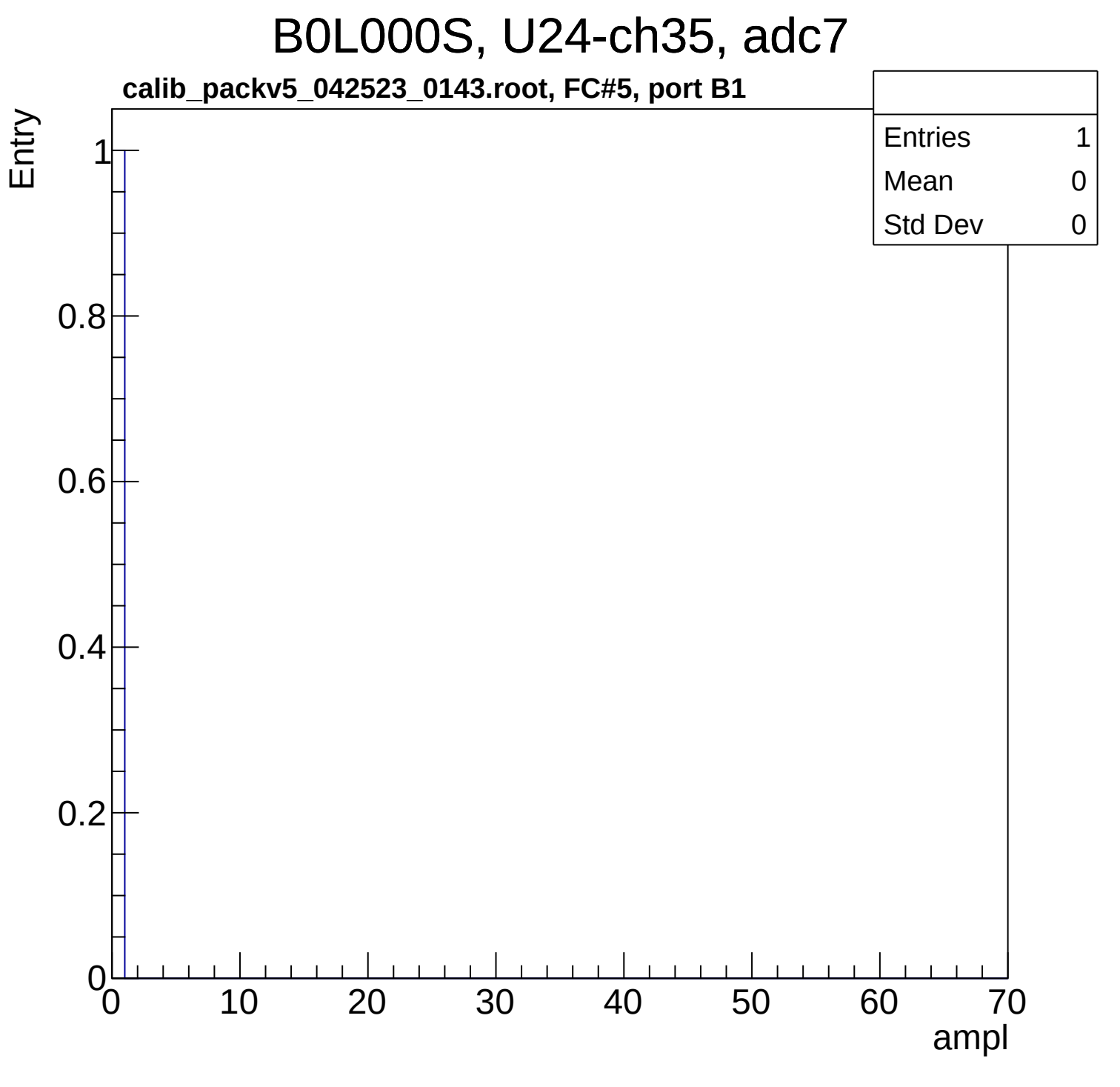
50
60
70
ampl


B0L000S, U24-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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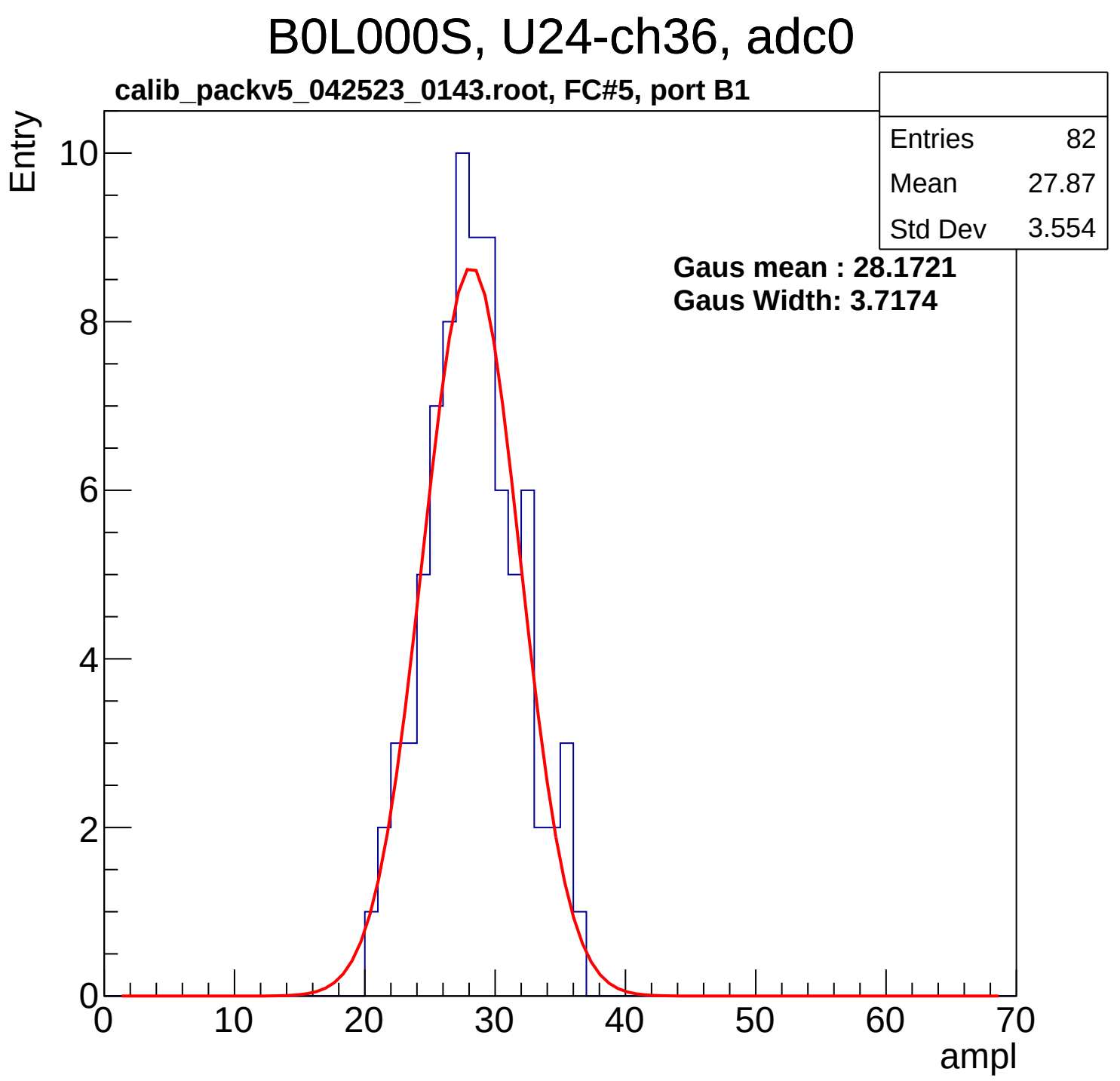
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 27.87
Std Dev 3.554
Gaus mean : 28.1721
Gaus Width: 3.7174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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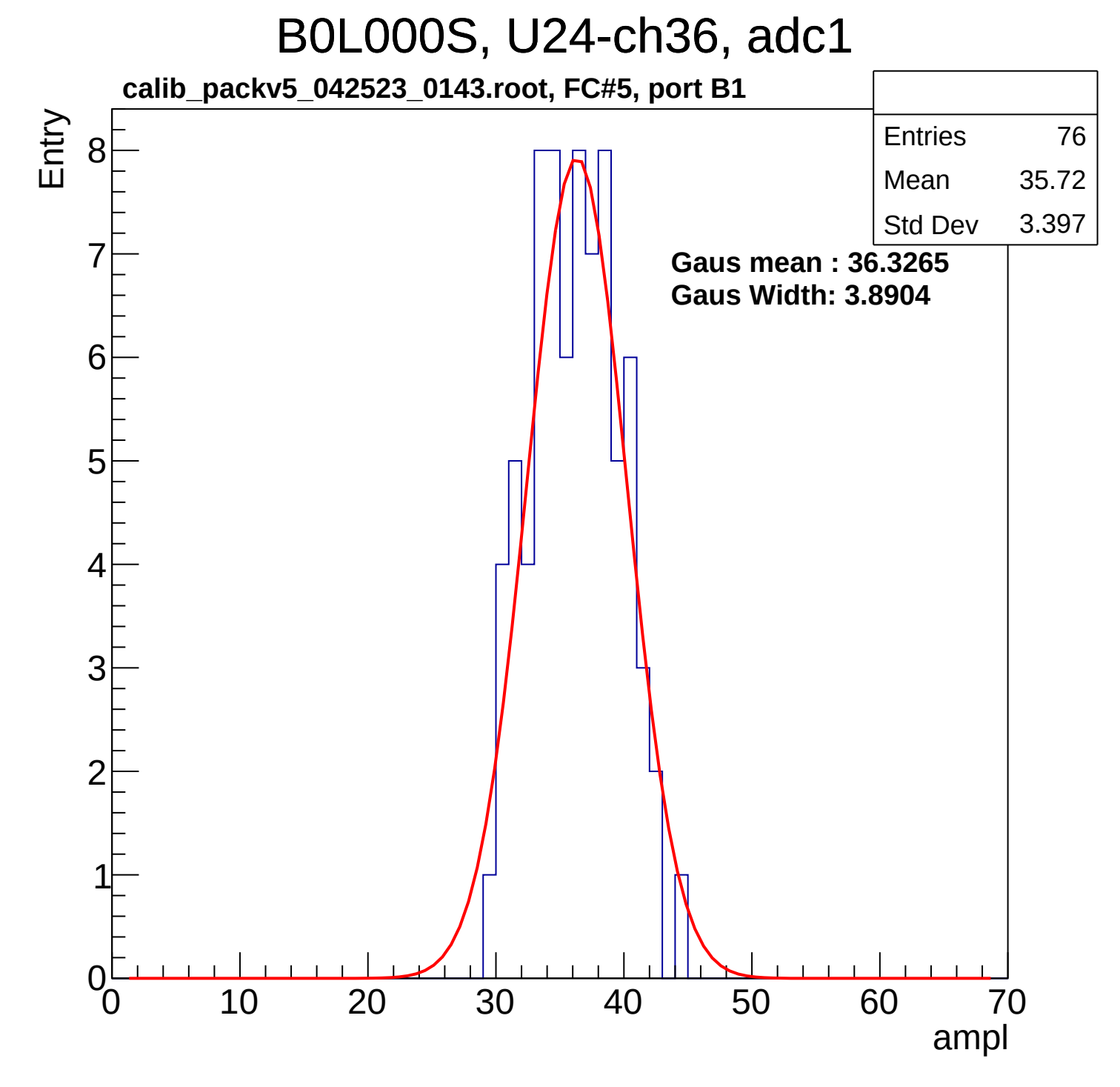
B0L000S, U24-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.72
Std Dev 3.397
Gaus mean : 36.3265
Gaus Width: 3.8904
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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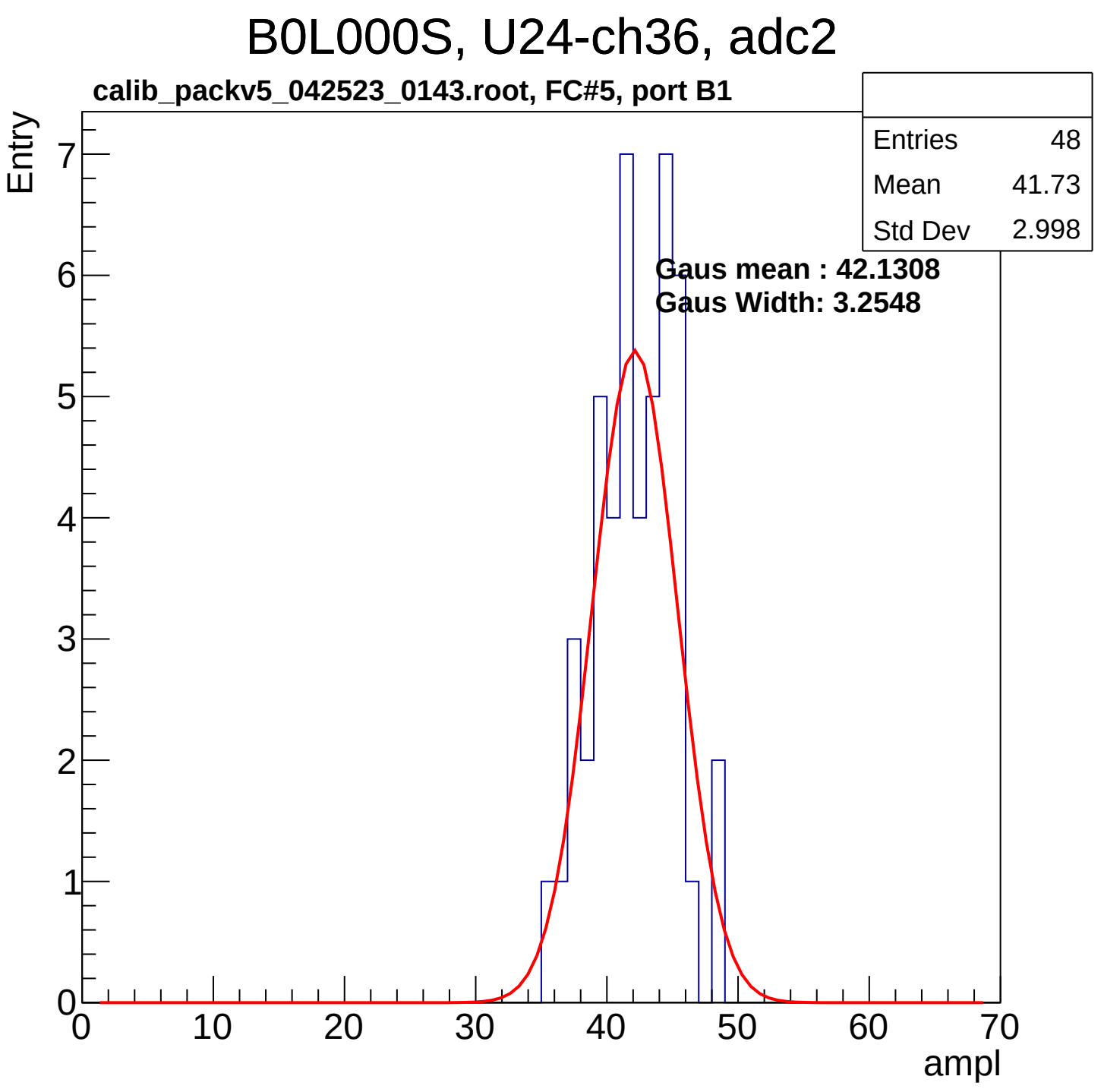
B0L000S, U24-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 41.73
Std Dev 2.998
Gaus mean : 42.1308
Gaus Width: 3.2548
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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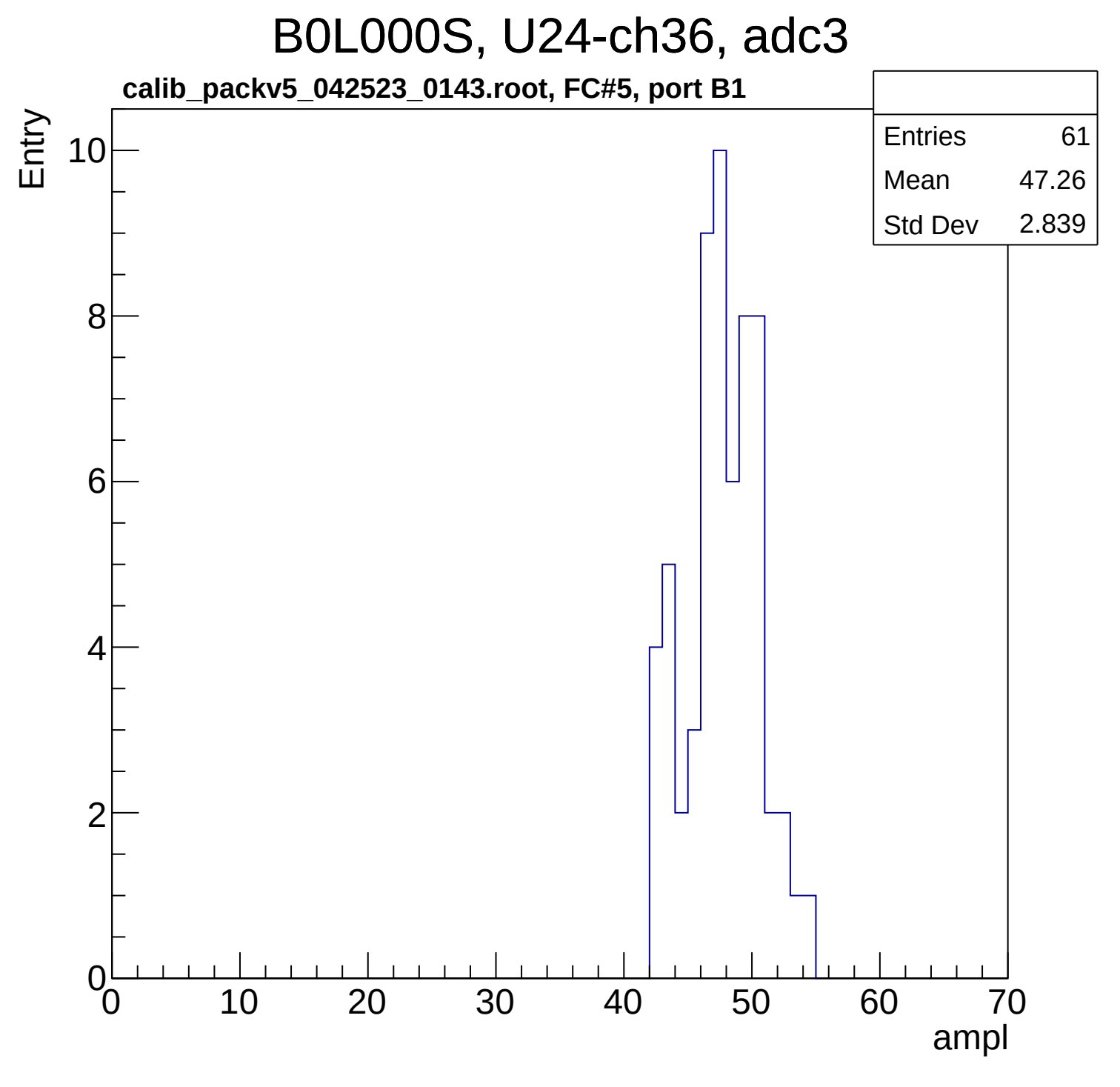
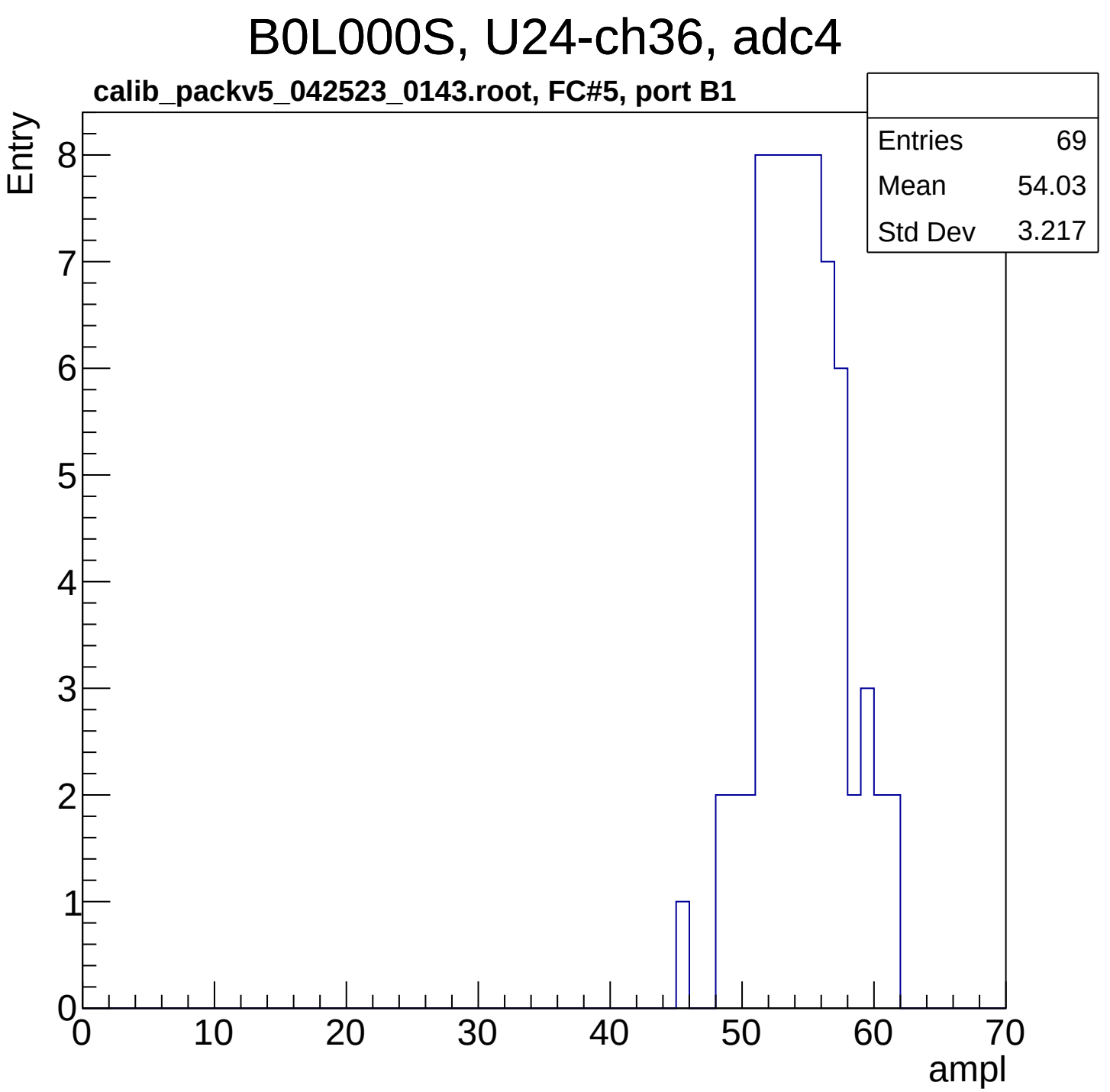

B0L000S, U24-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.26
Std Dev 2.839
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 54.03
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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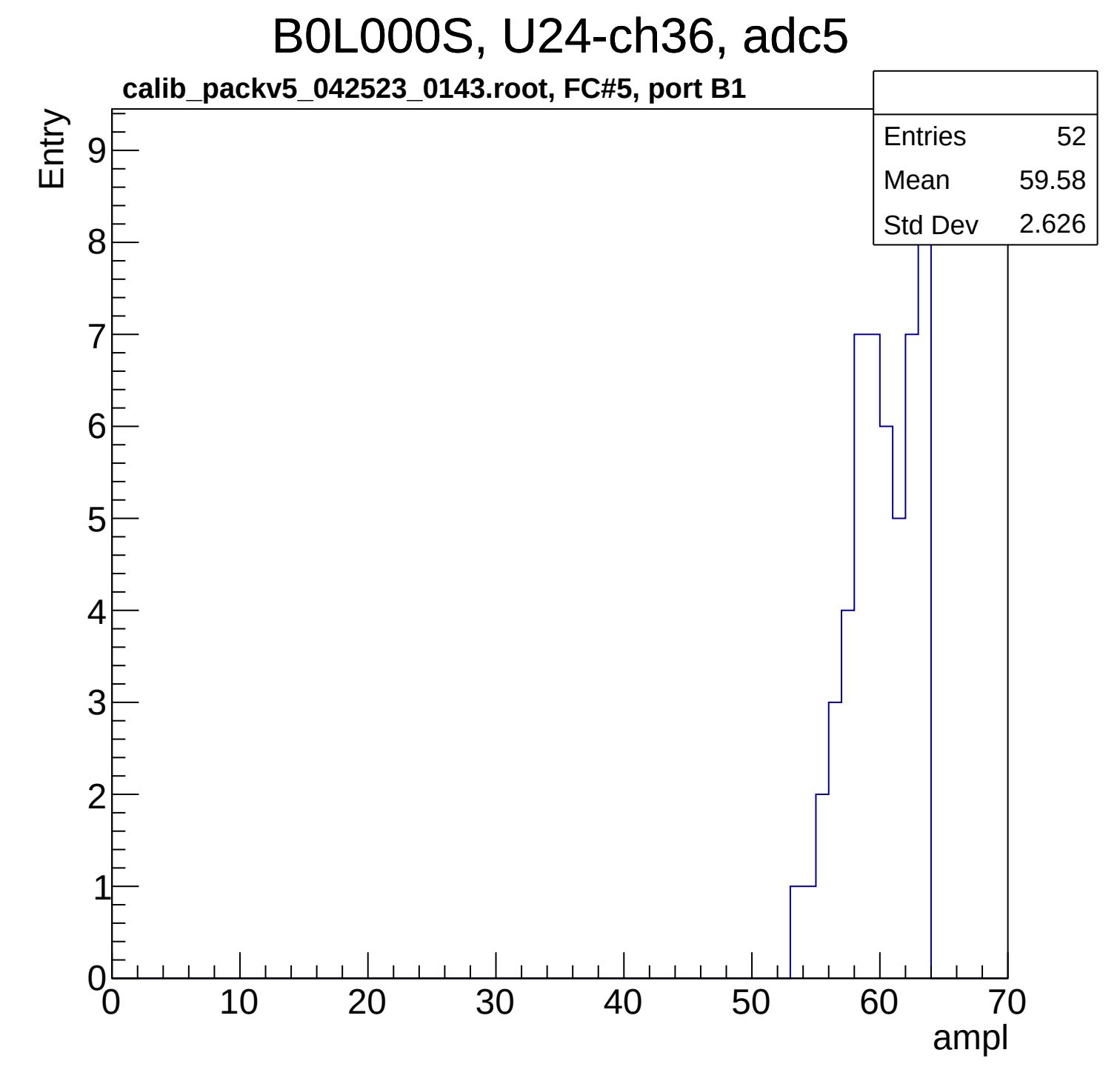
B0L000S, U24-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.58
Std Dev 2.626
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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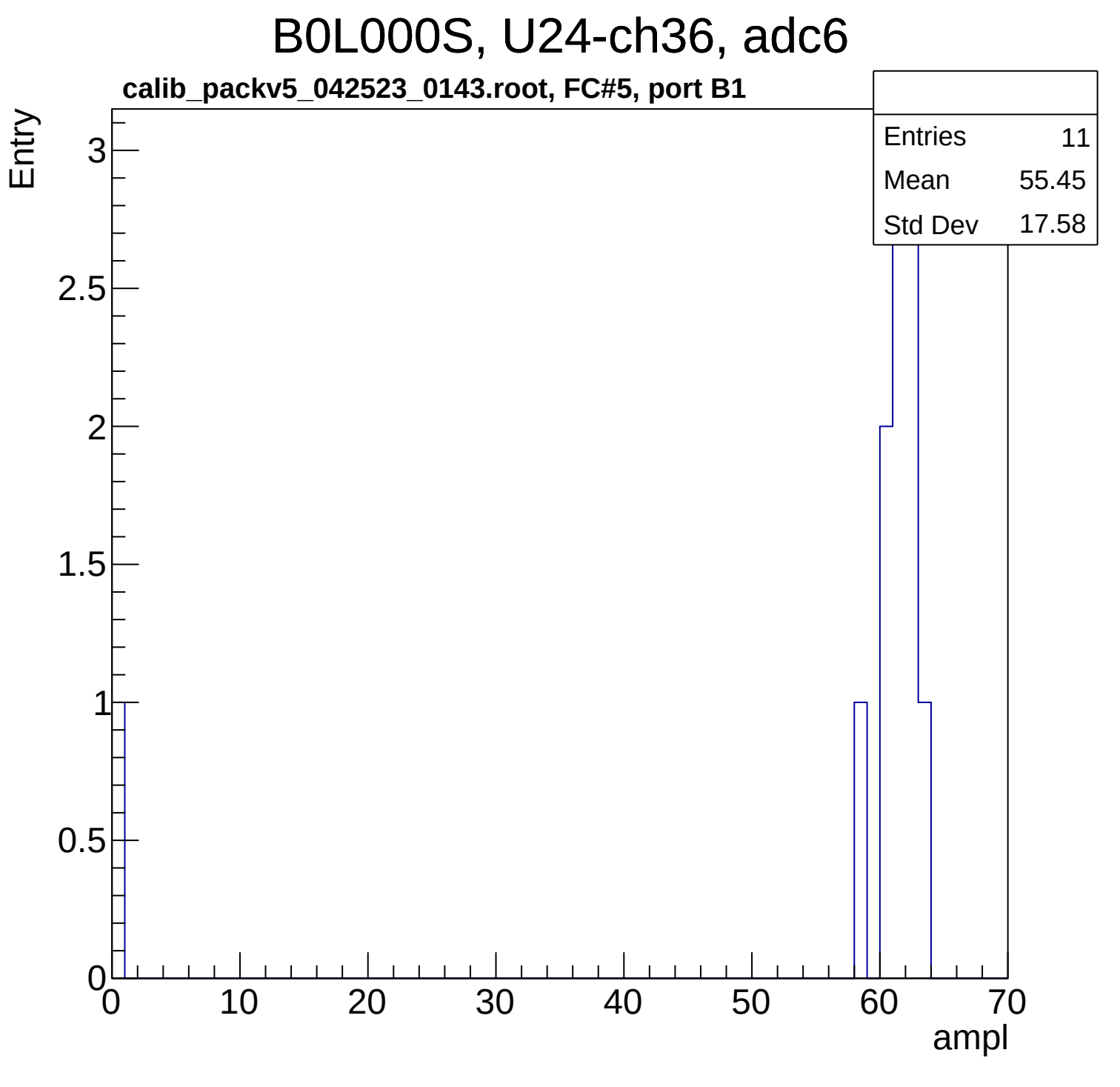
B0L000S, U24-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 55.45
Std Dev 17.58
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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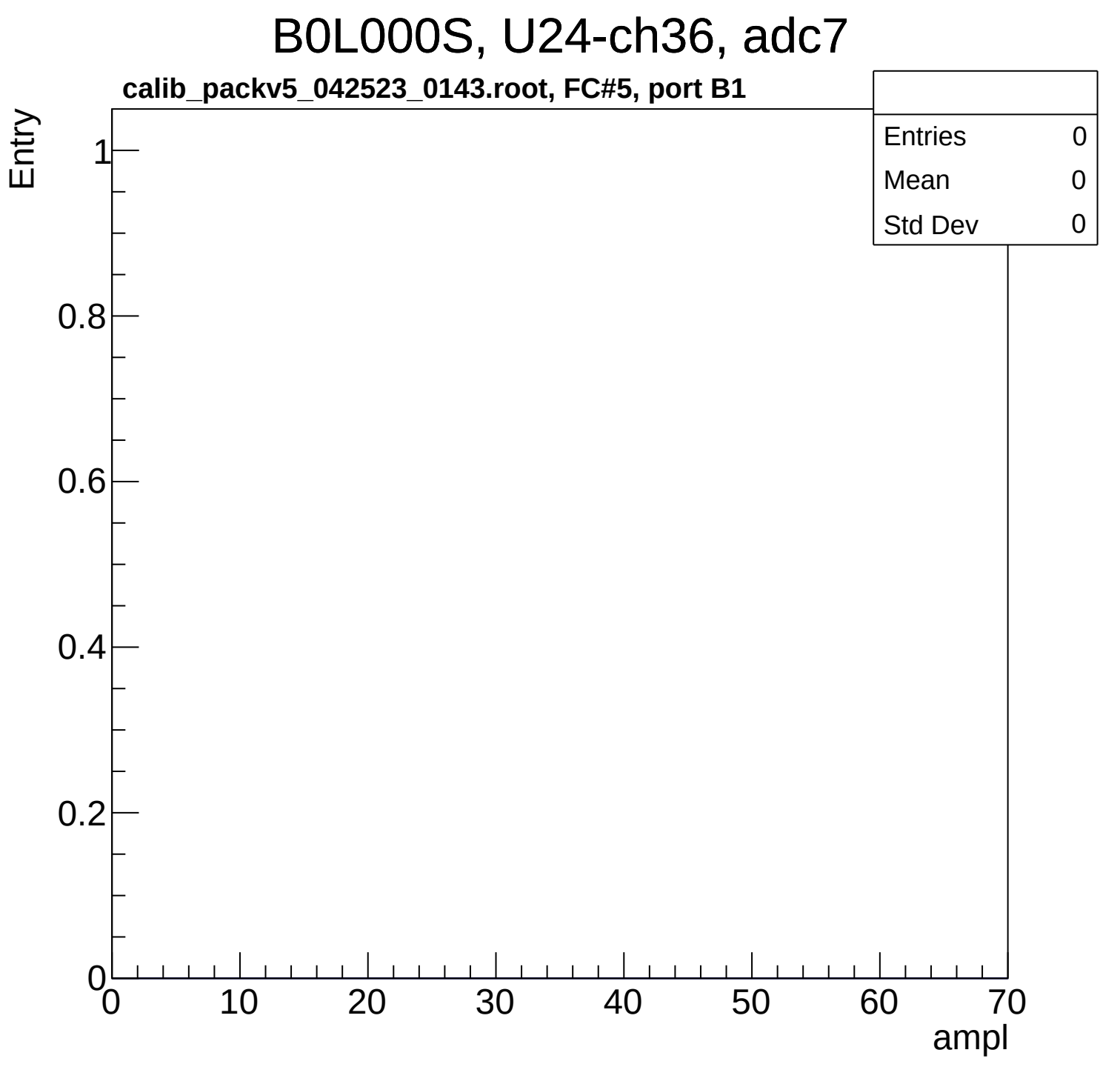
60
70


B0L000S, U24-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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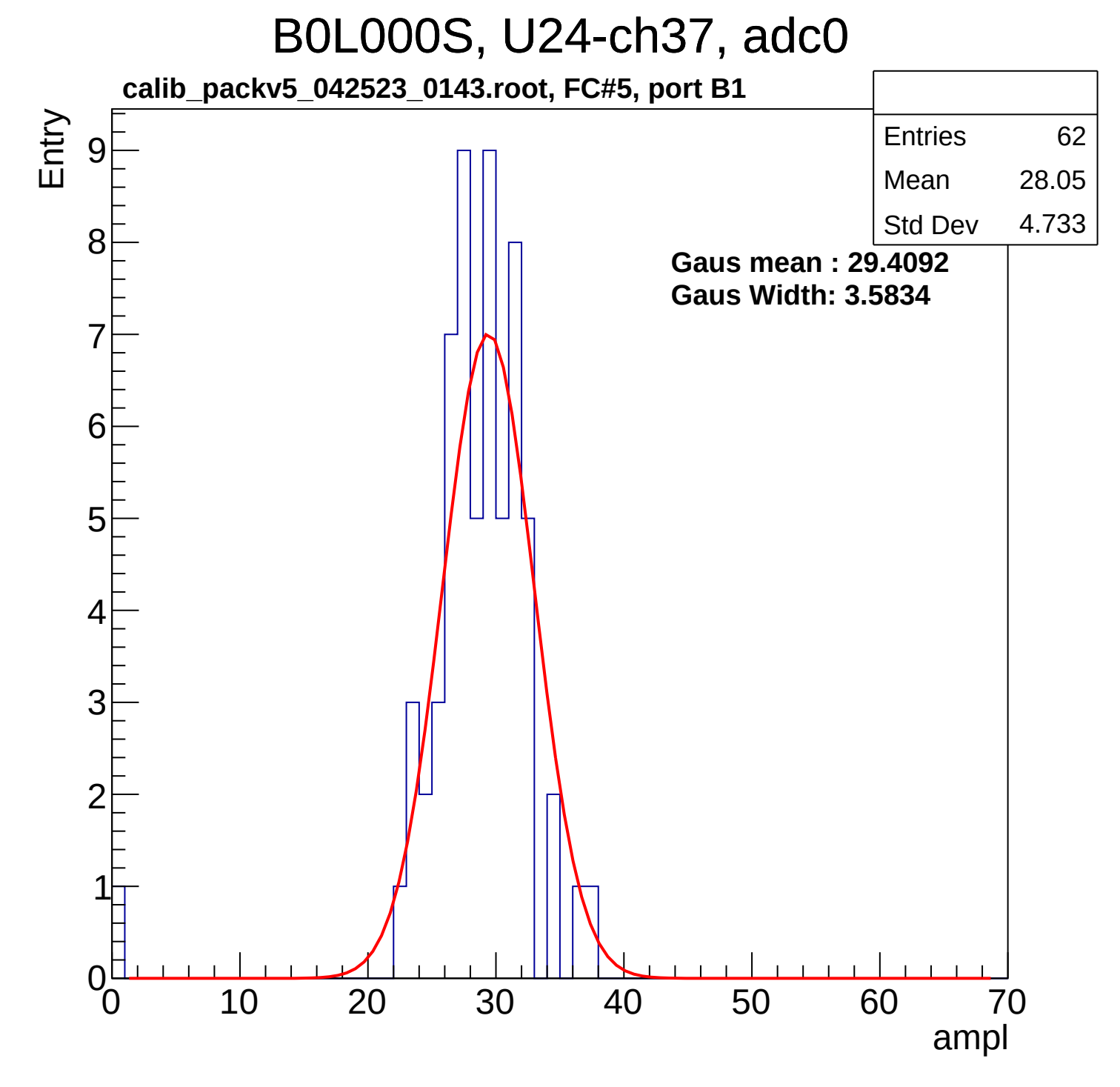
50
60
70
ampl


B0L000S, U24-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 28.05
Std Dev 4.733
Gaus mean : 29.4092
Gaus Width: 3.5834
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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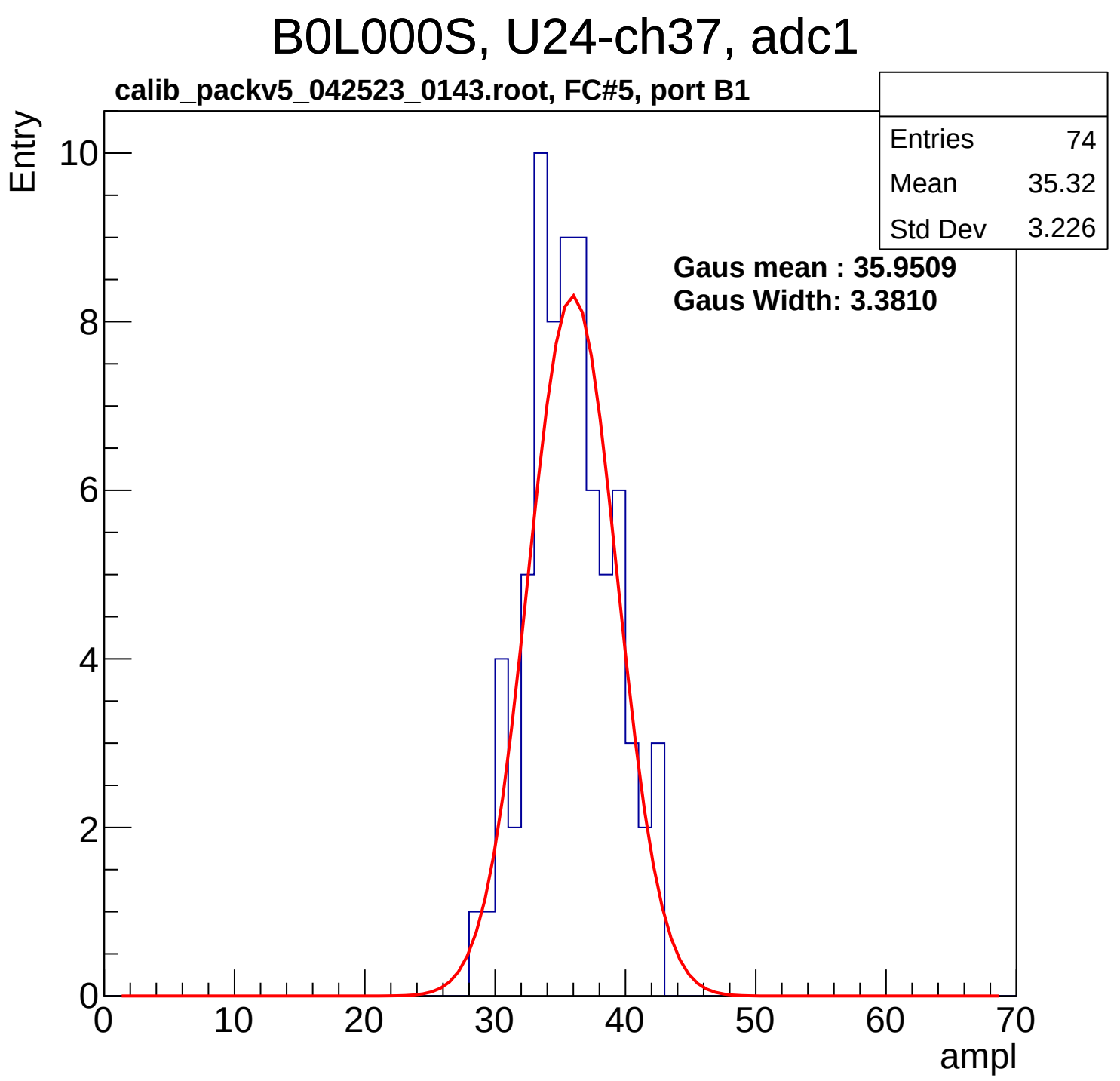
B0L000S, U24-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.32
Std Dev 3.226
Gaus mean : 35.9509
Gaus Width: 3.3810
Entry
ampl
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2
4
6
8
10
0
10
20
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40
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60
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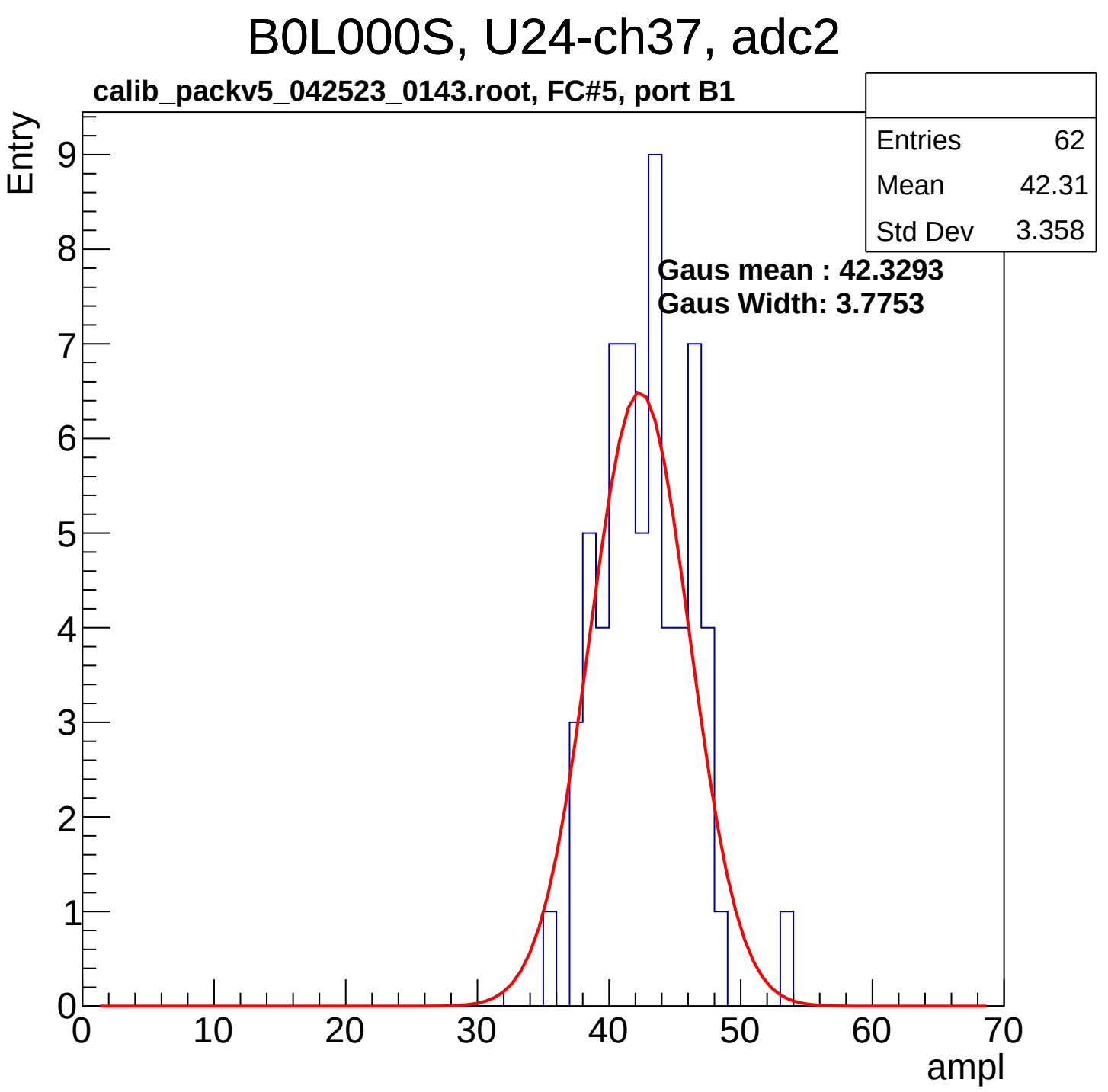
B0L000S, U24-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 42.31
Std Dev 3.358
Gaus mean : 42.3293
Gaus Width: 3.7753
Entry
ampl
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20
30
40
50
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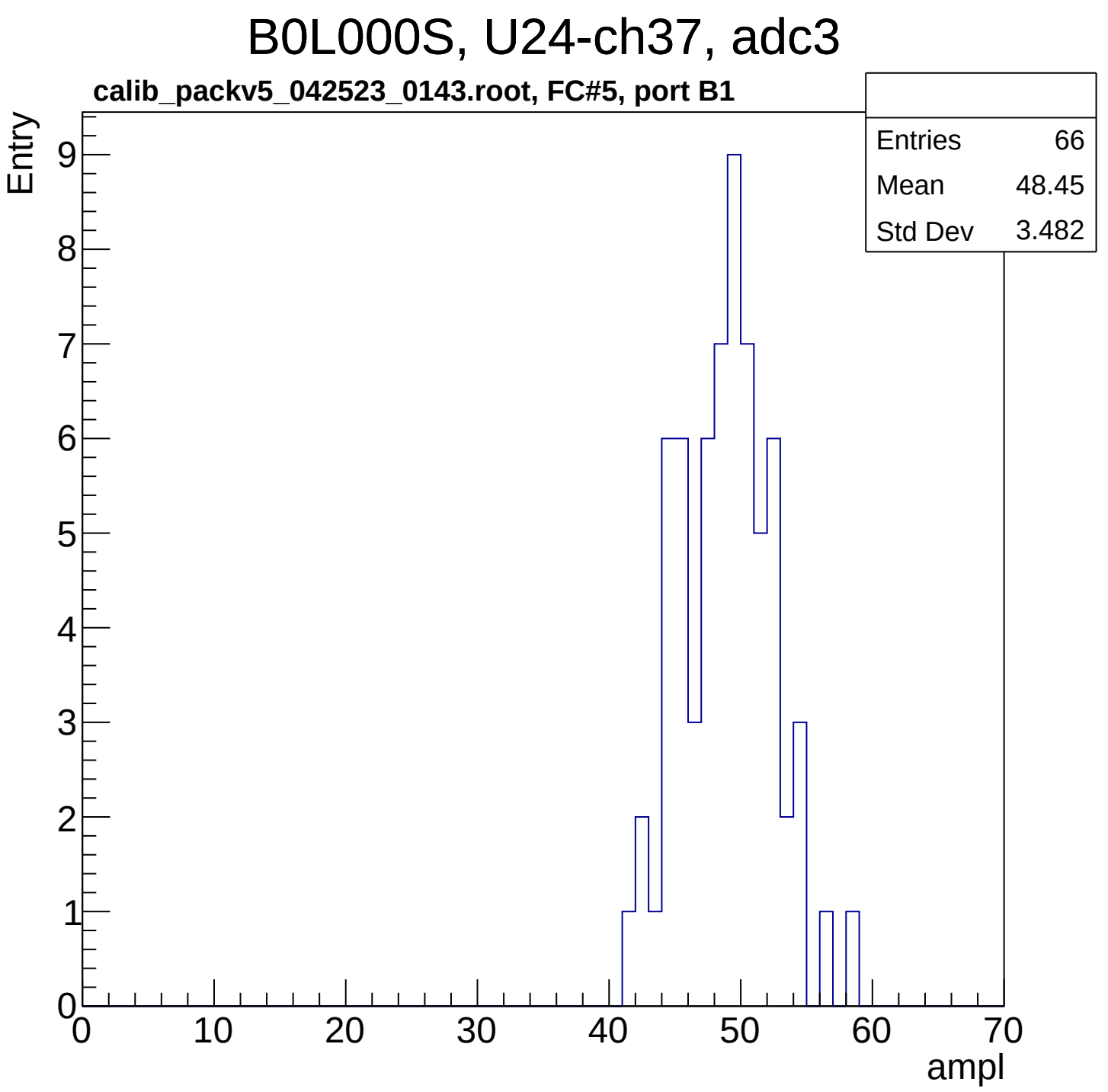
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 48.45
Std Dev 3.482
Entry
ampl
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6
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8
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0
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60
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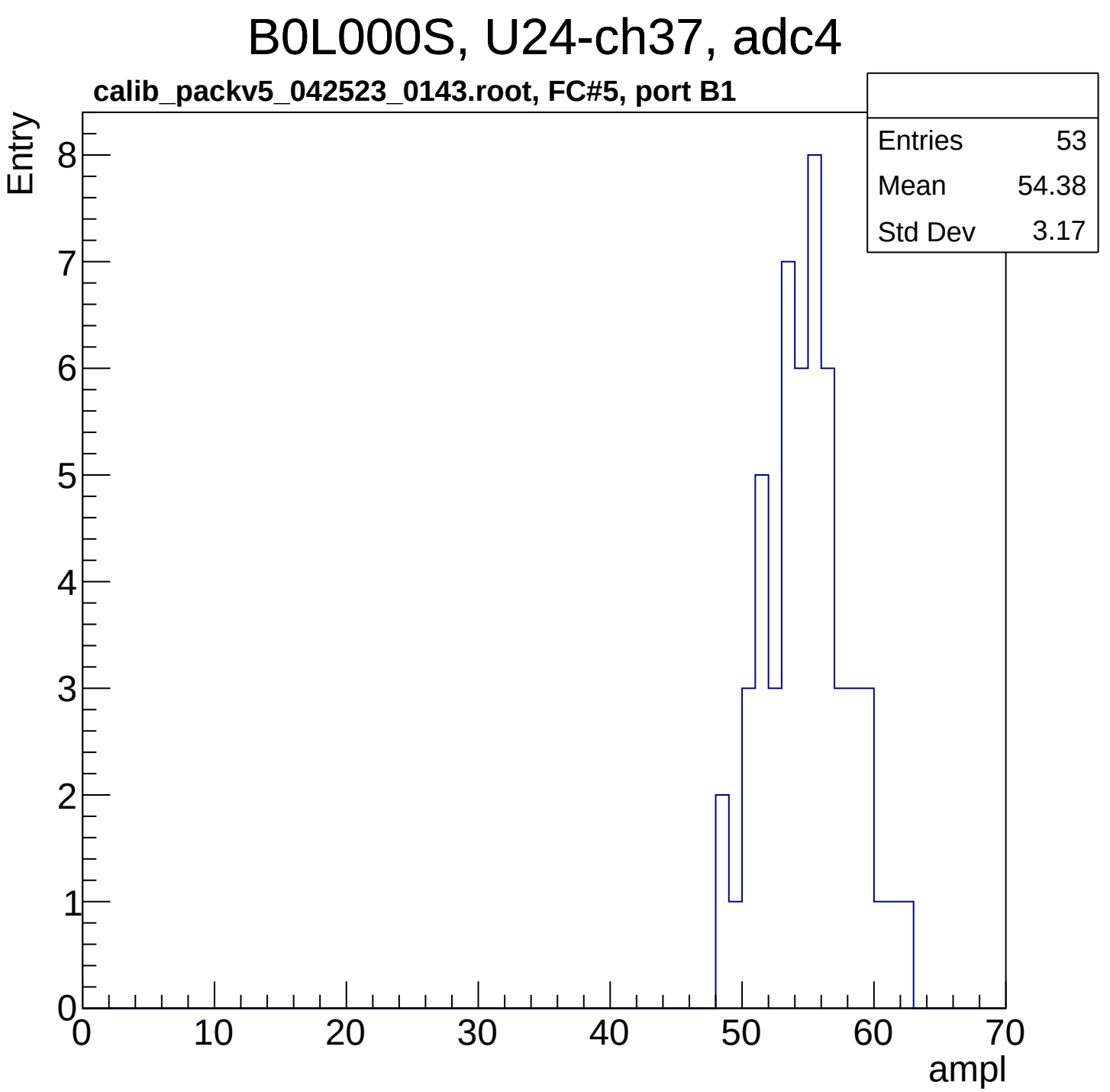
B0L000S, U24-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.38
Std Dev 3.17
Entry
ampl
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60
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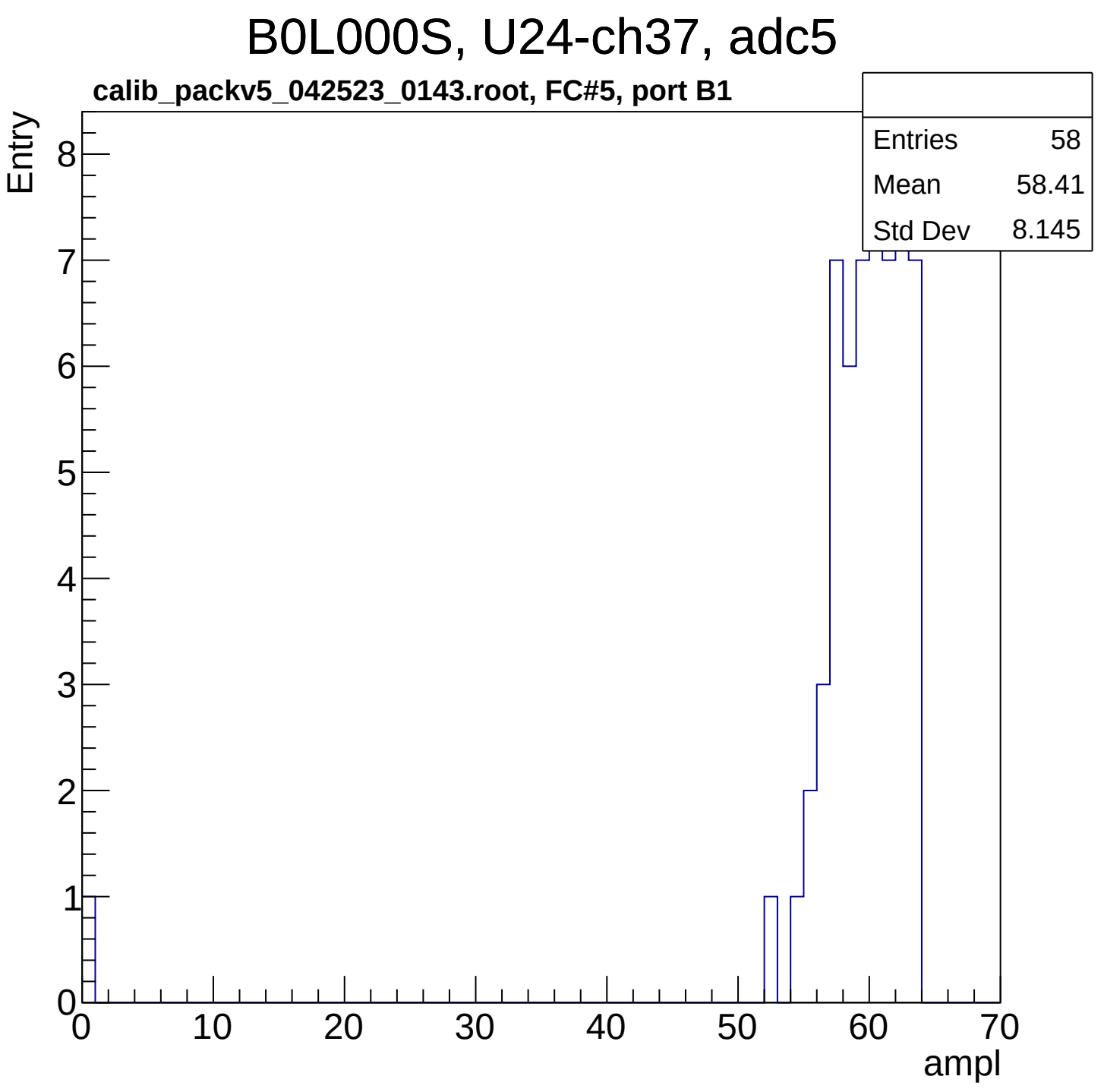
B0L000S, U24-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.41
Std Dev 8.145
Entry
ampl
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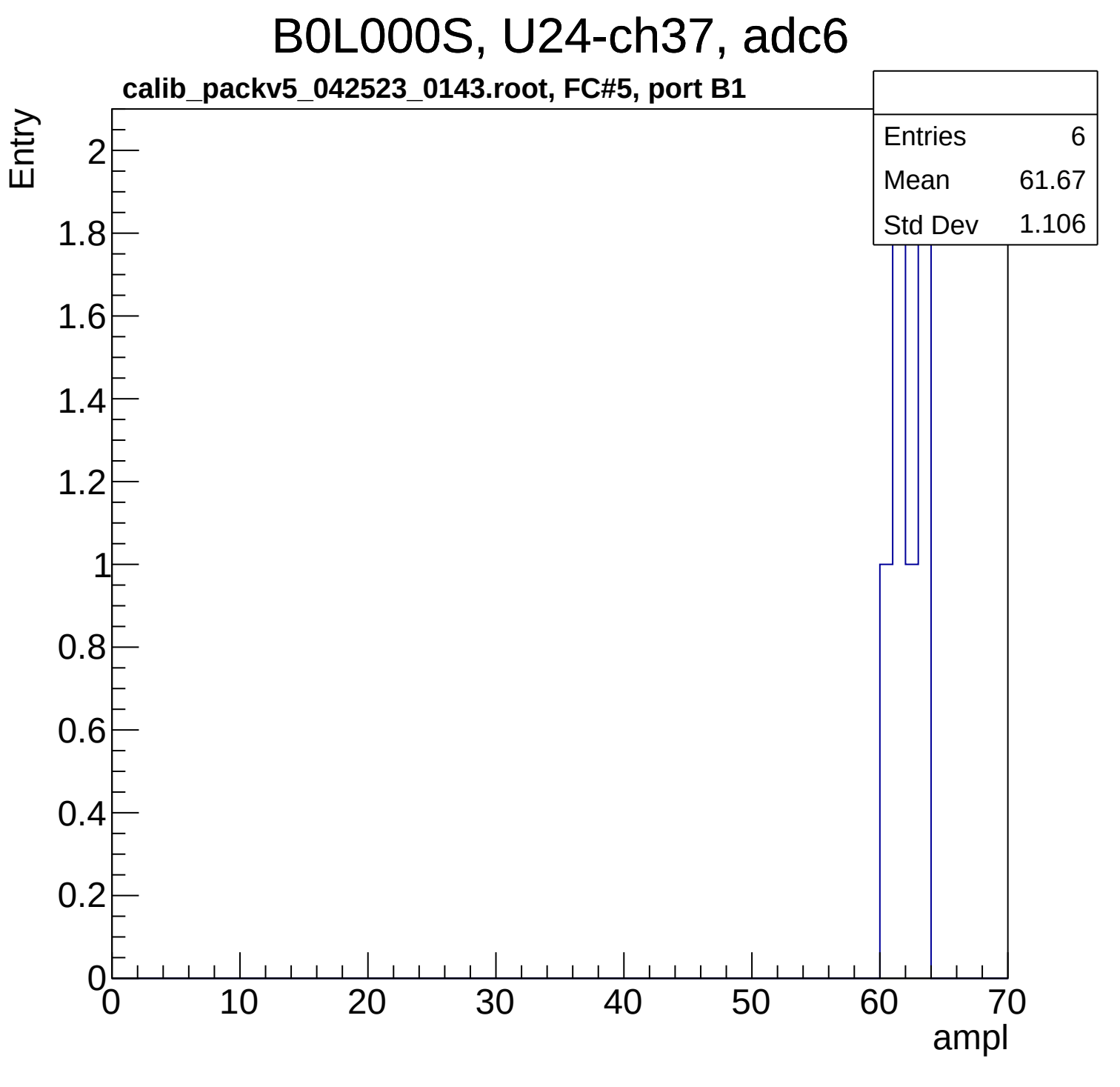
B0L000S, U24-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
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60
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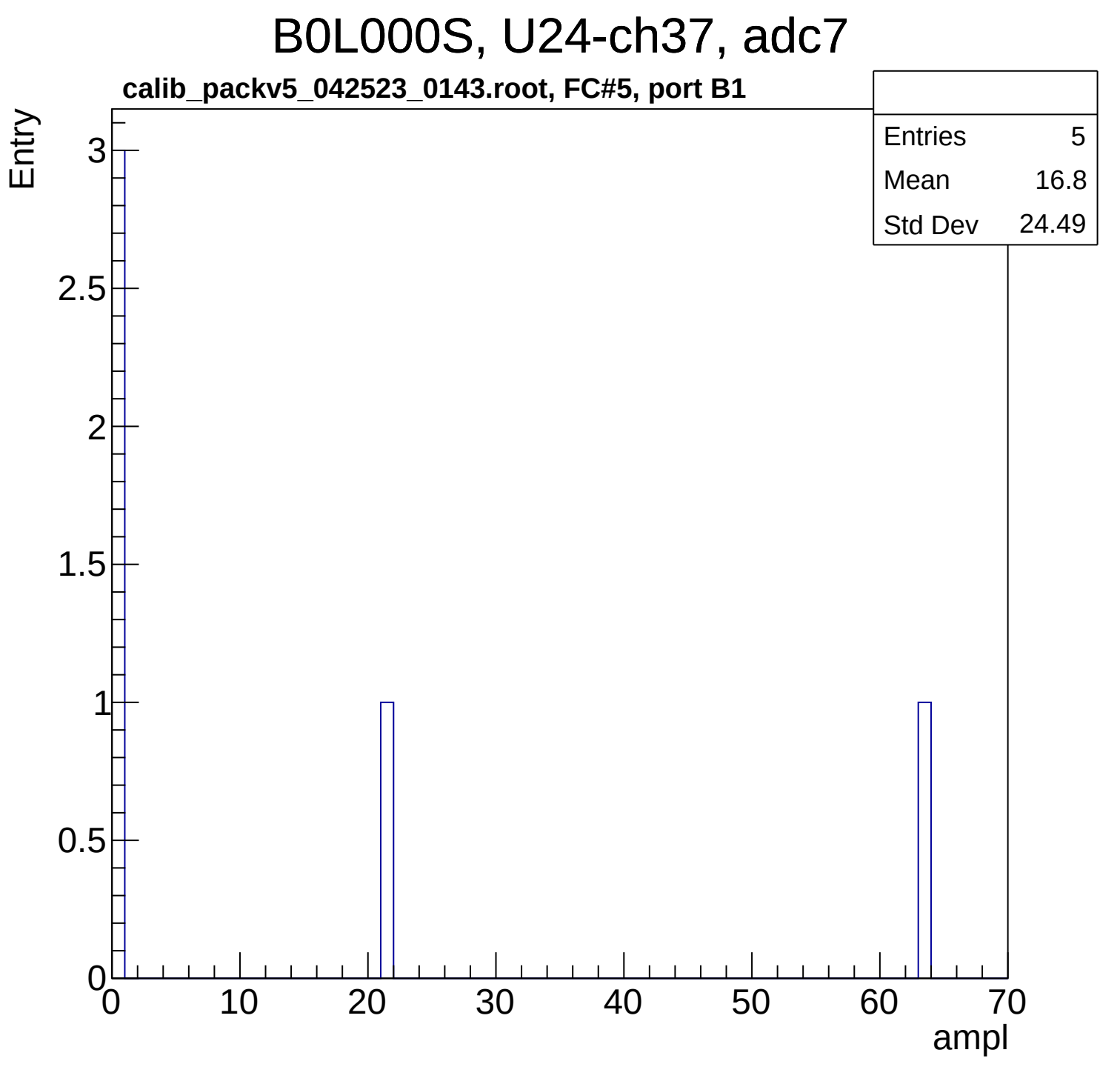
B0L000S, U24-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 16.8
Std Dev 24.49
Entry
ampl
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0.5
1
1.5
2
2.5
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0
10
20
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40
50
60
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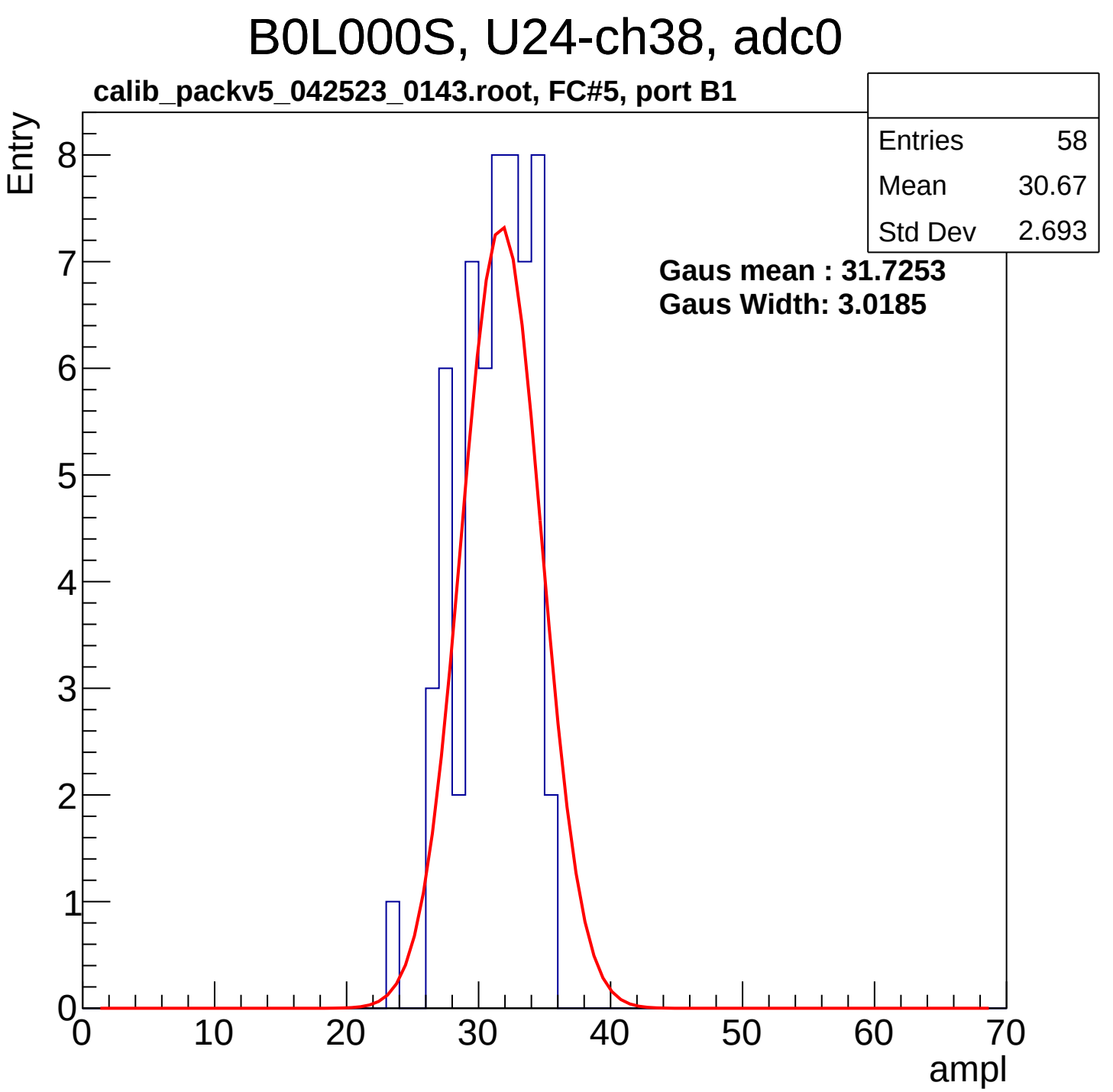
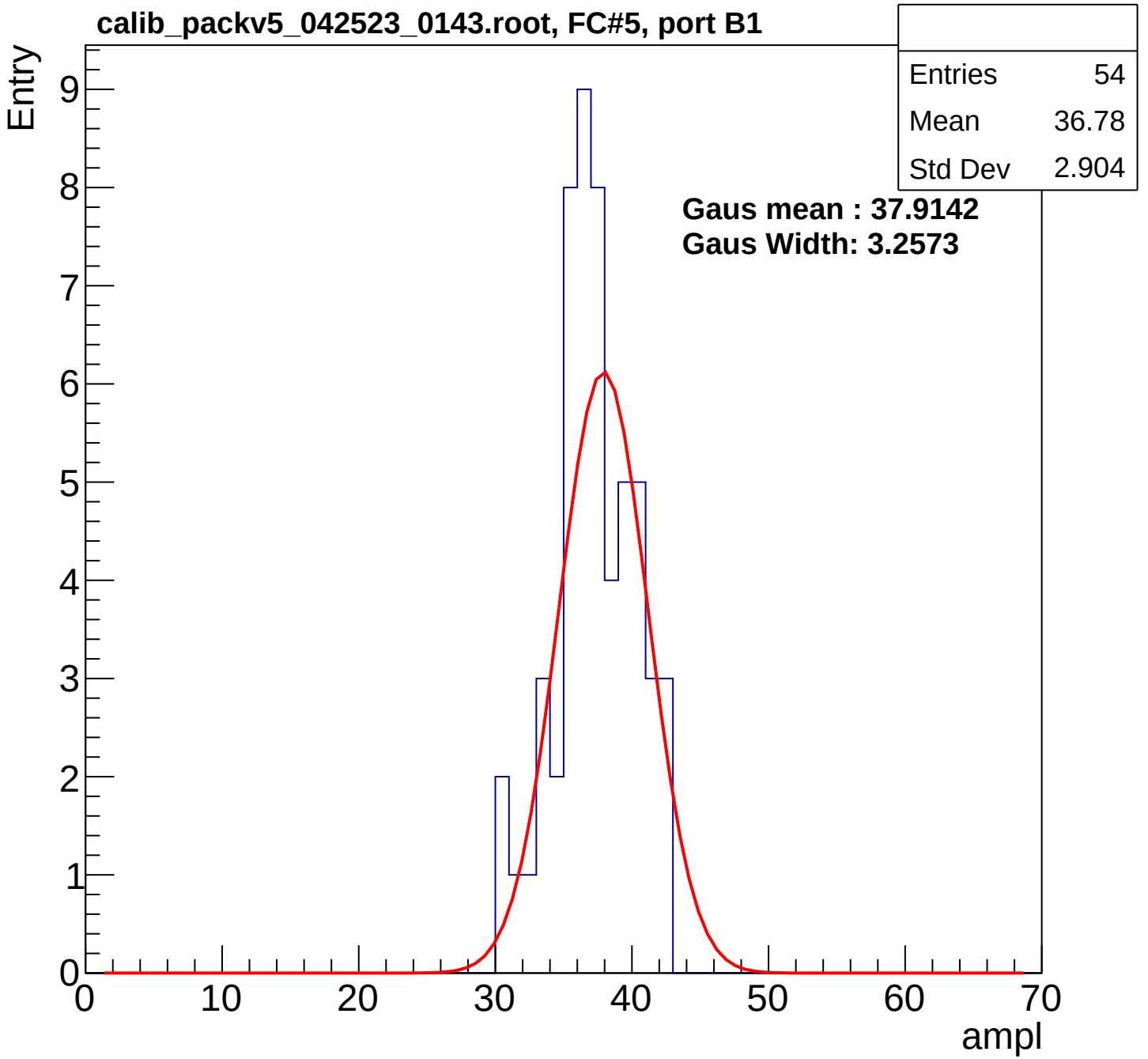

B0L000S, U24-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 30.67
Std Dev 2.693
Gaus mean : 31.7253
Gaus Width: 3.0185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 36.78
Std Dev 2.904
Gaus mean : 37.9142
Gaus Width: 3.2573
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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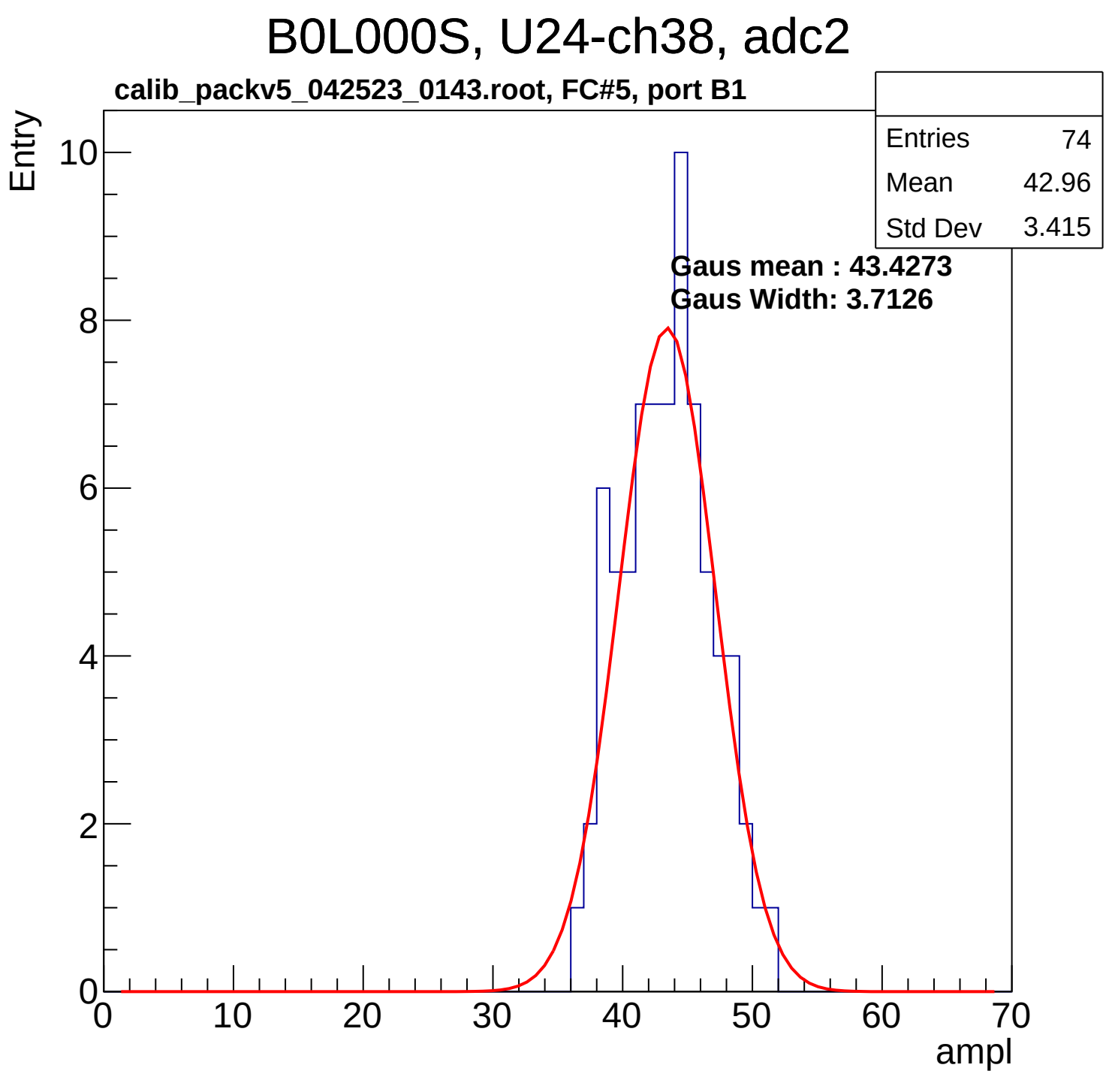
B0L000S, U24-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 42.96
Std Dev 3.415
Gaus mean : 43.4273
Gaus Width: 3.7126
Entry
ampl
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6
8
10
0
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20
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40
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60
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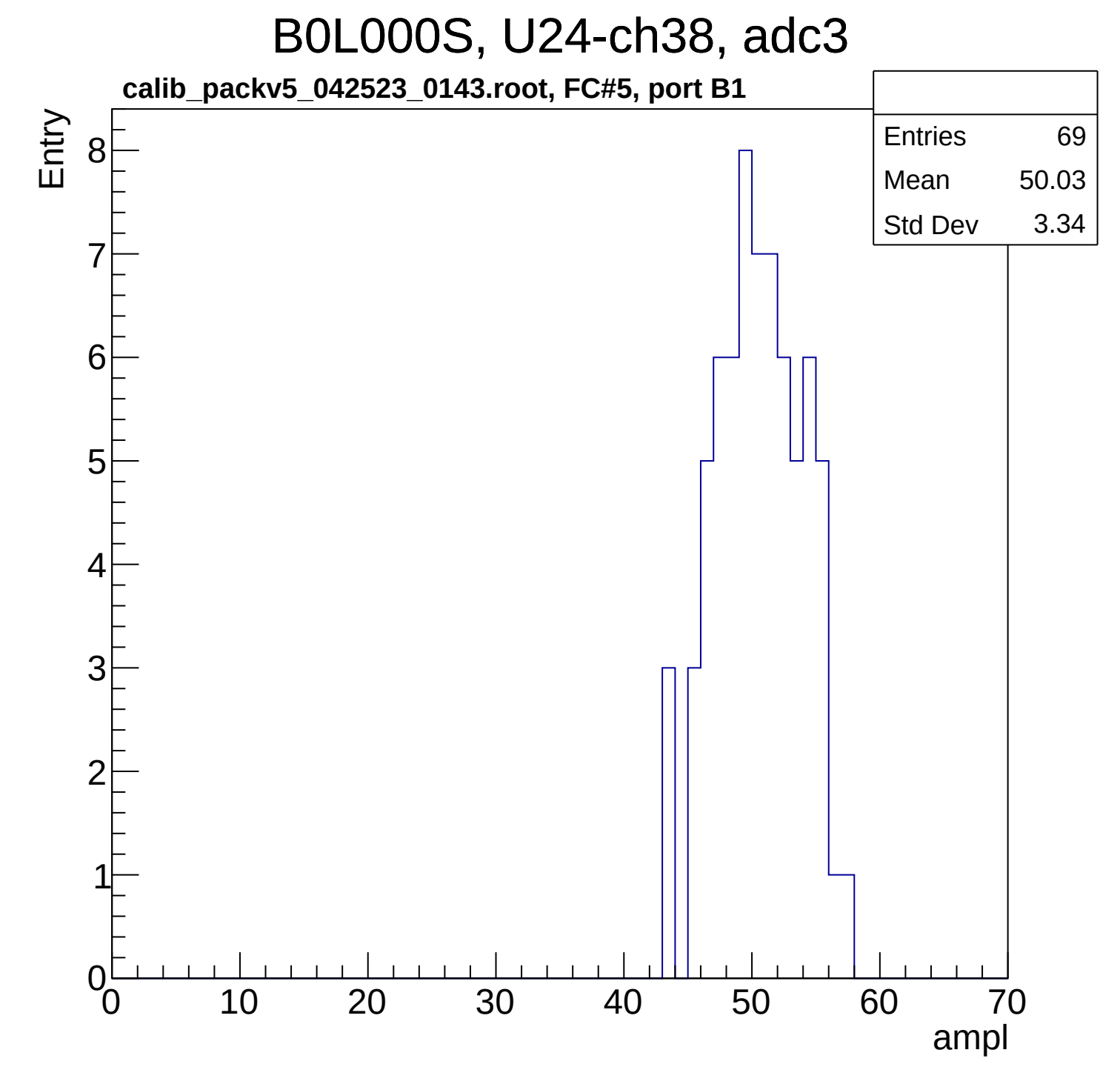
B0L000S, U24-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.03
Std Dev 3.34
Entry
ampl
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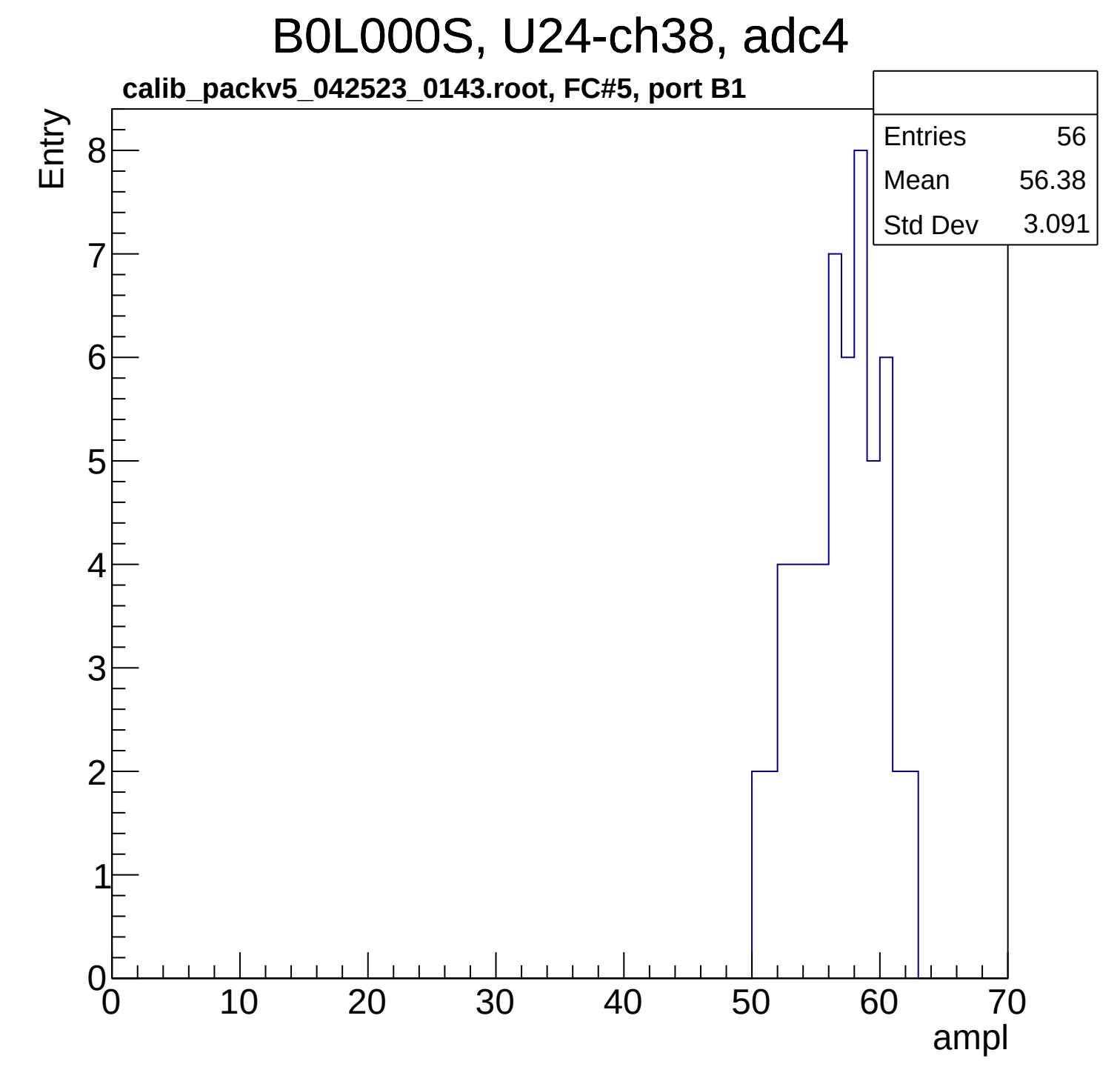
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.38
Std Dev 3.091
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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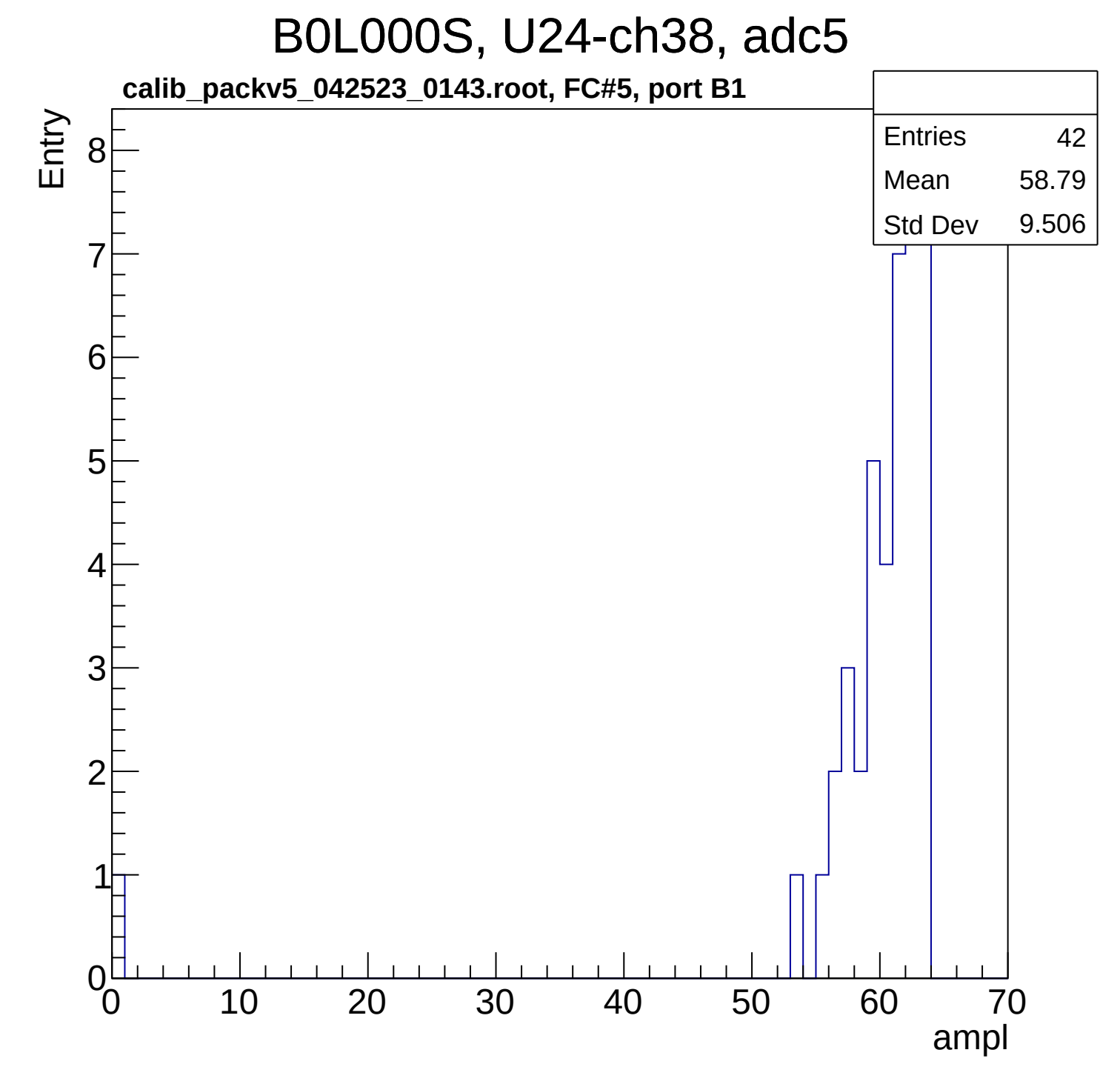
B0L000S, U24-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.79
Std Dev 9.506
Entry
ampl
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1
2
3
4
5
6
7
8
0
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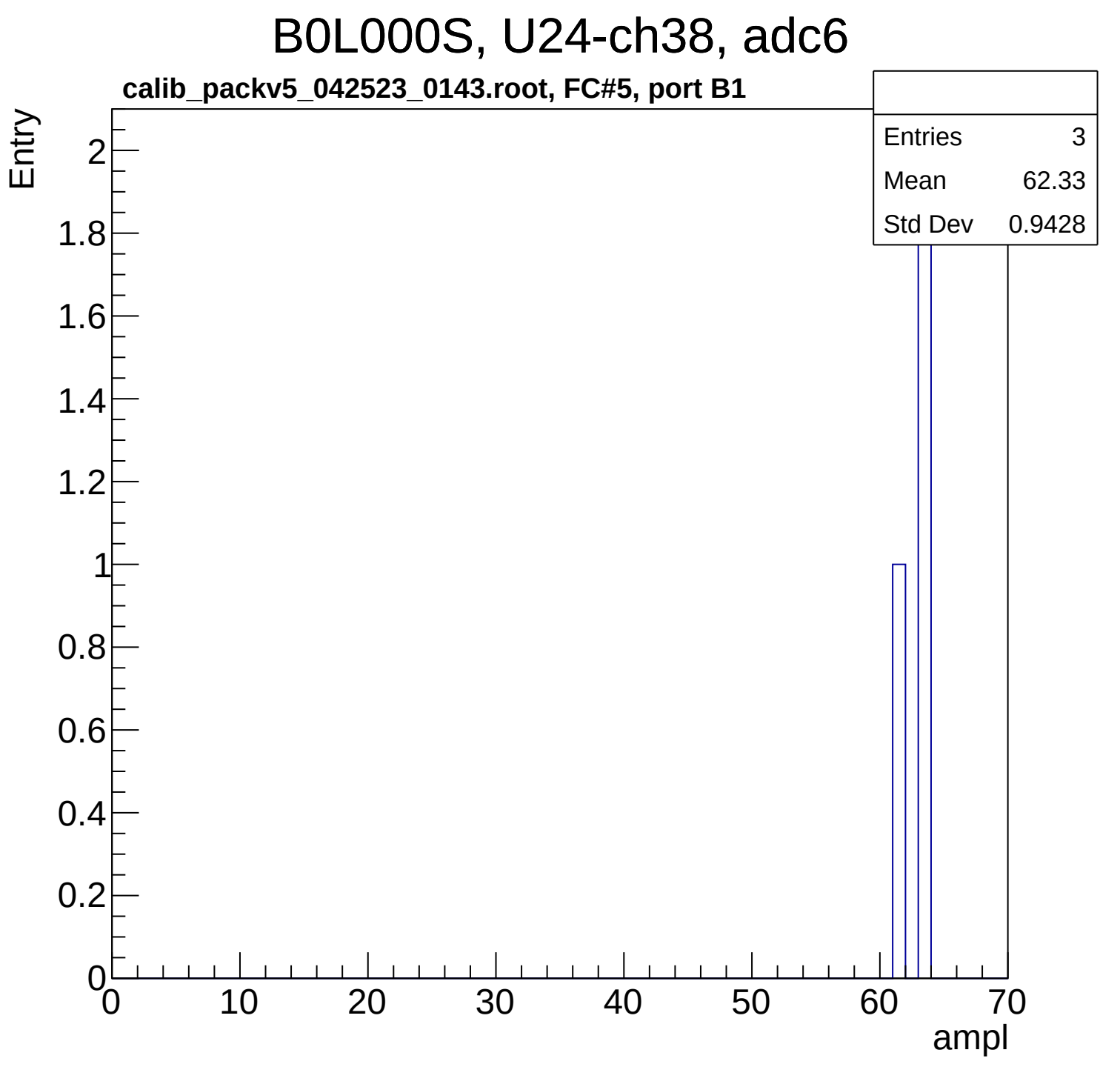
B0L000S, U24-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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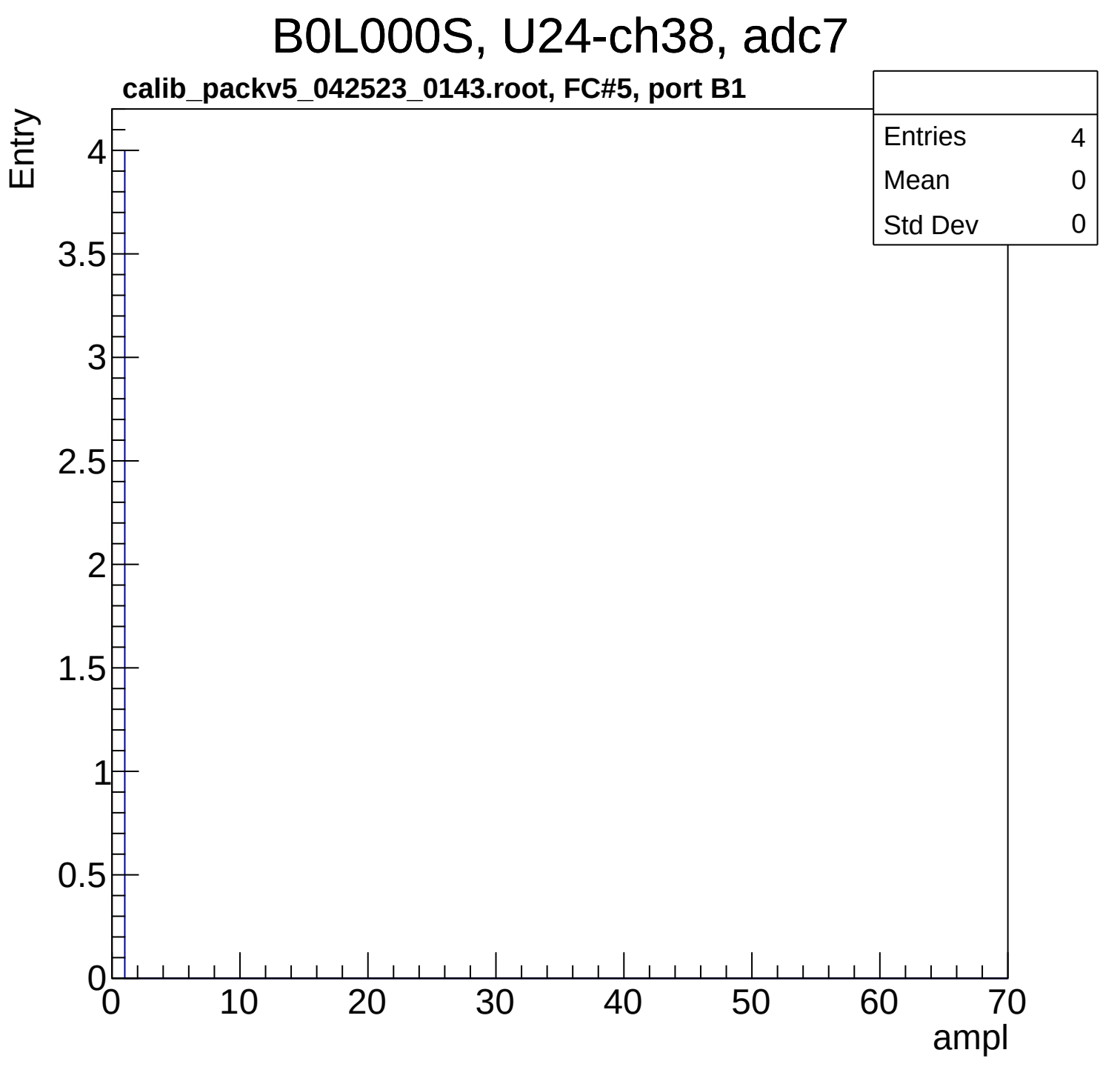
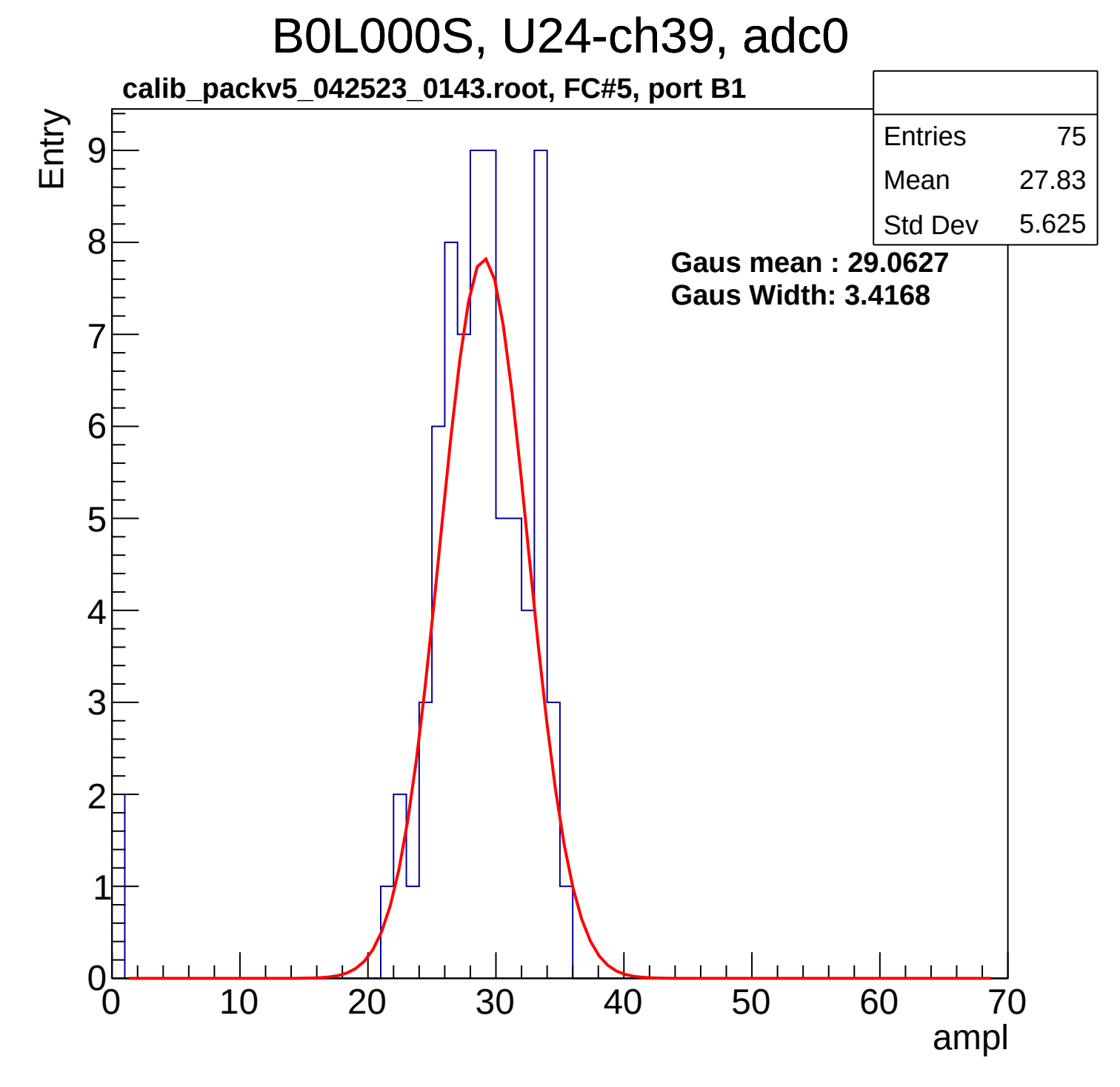

B0L000S, U24-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 27.83
Std Dev 5.625
Gaus mean : 29.0627
Gaus Width: 3.4168
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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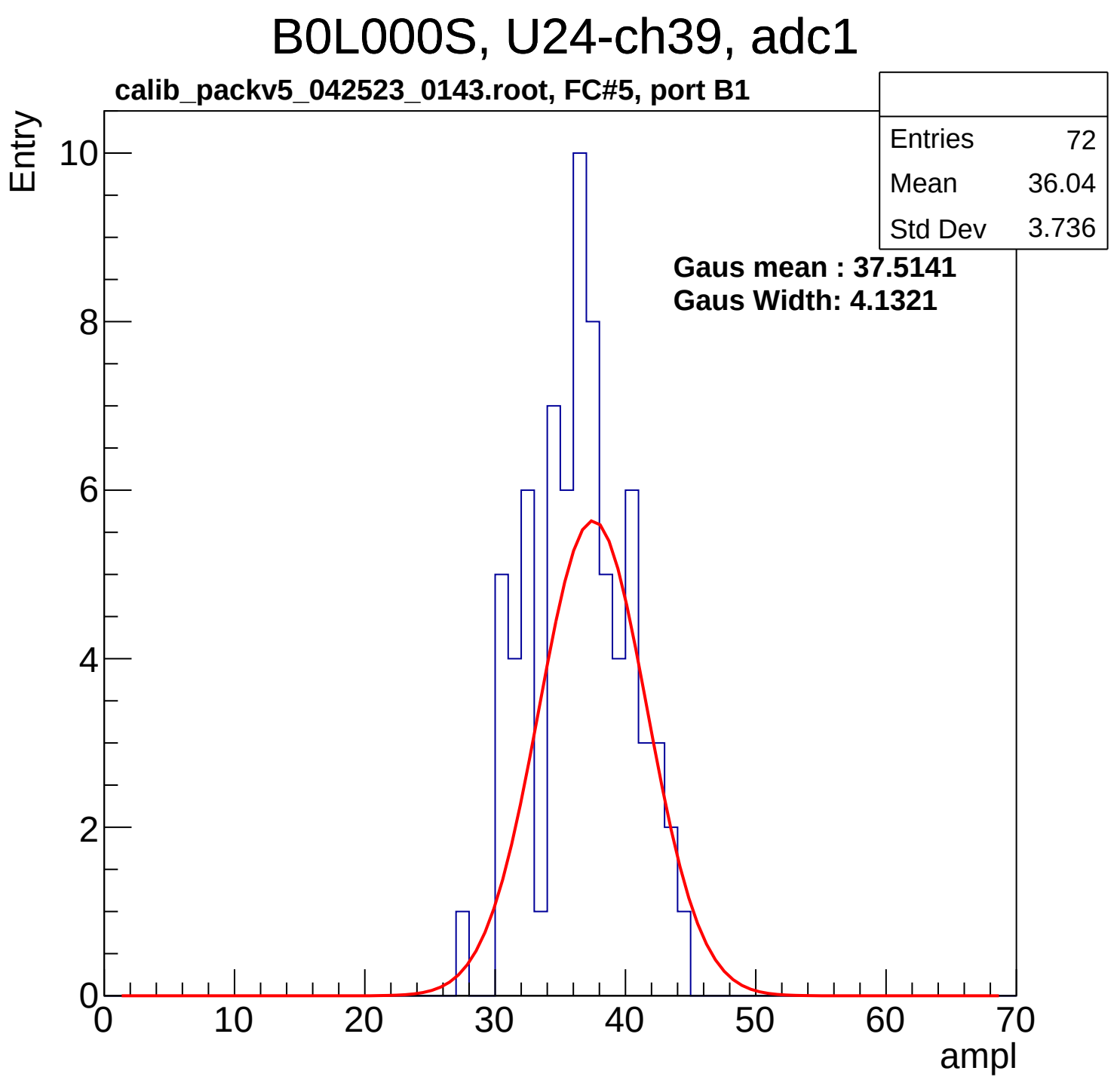
B0L000S, U24-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.04
Std Dev 3.736
Gaus mean : 37.5141
Gaus Width: 4.1321
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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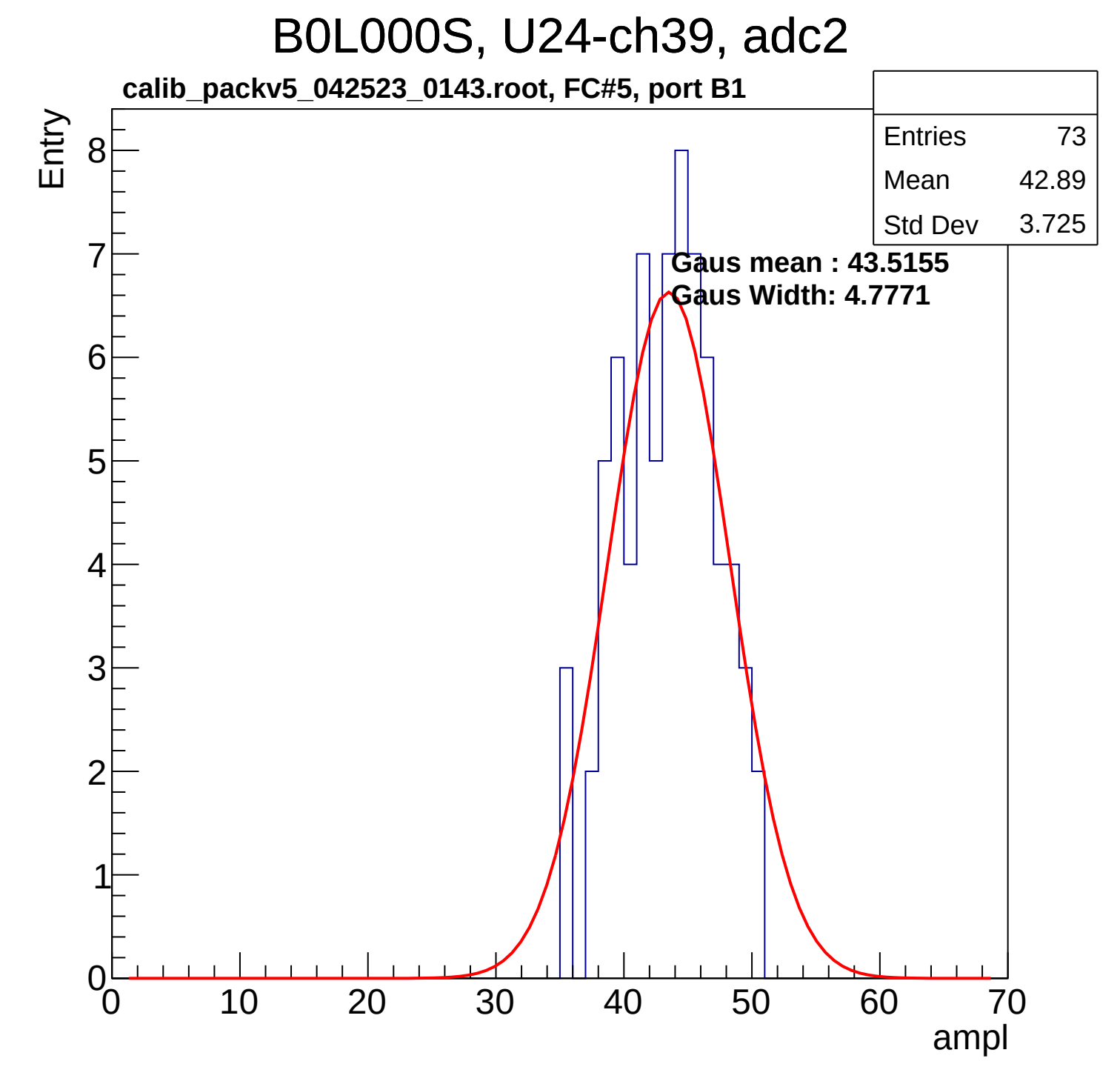
B0L000S, U24-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.89
Std Dev 3.725
Gaus mean : 43.5155
Gaus Width: 4.7771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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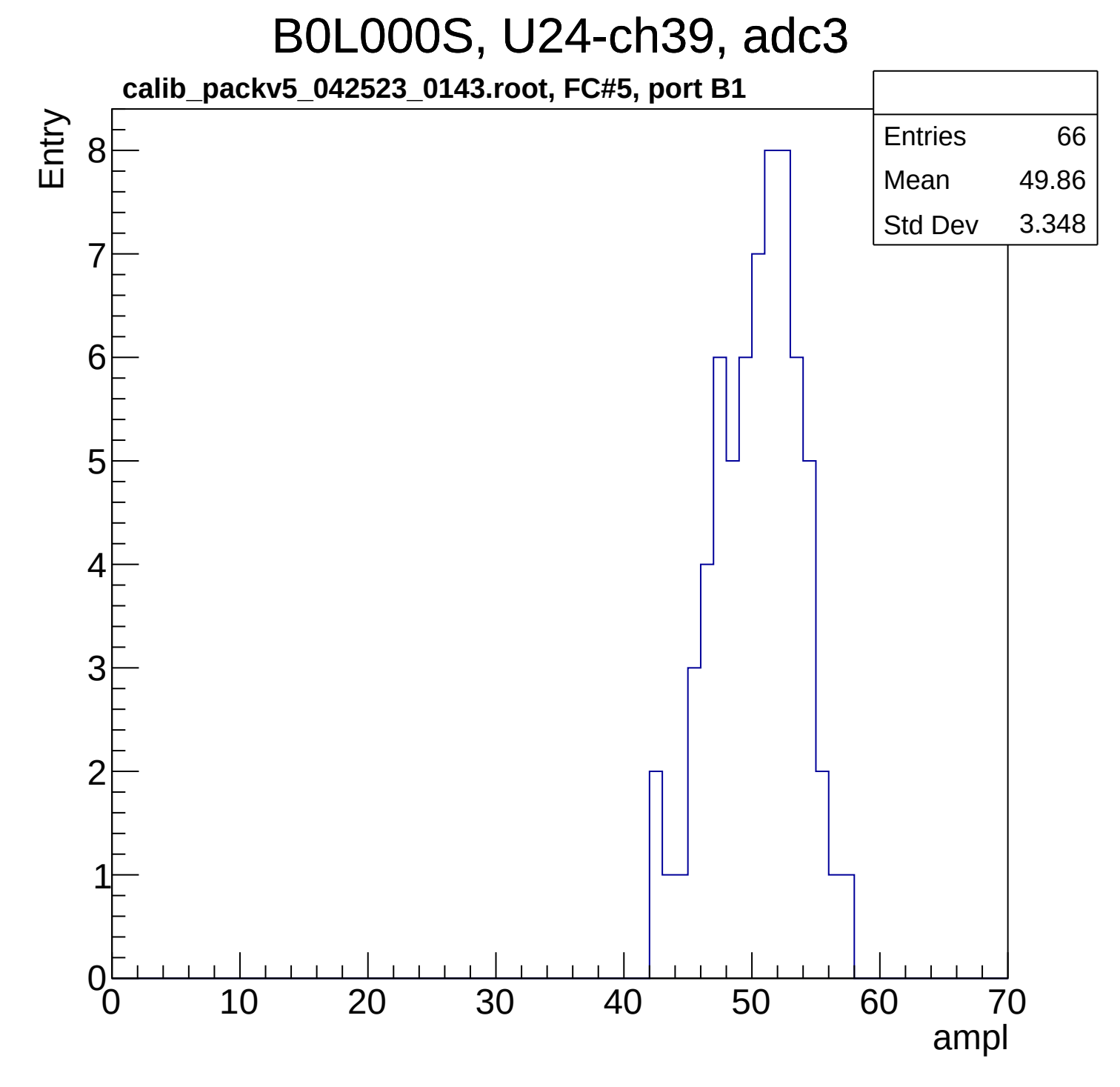
B0L000S, U24-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.86
Std Dev 3.348
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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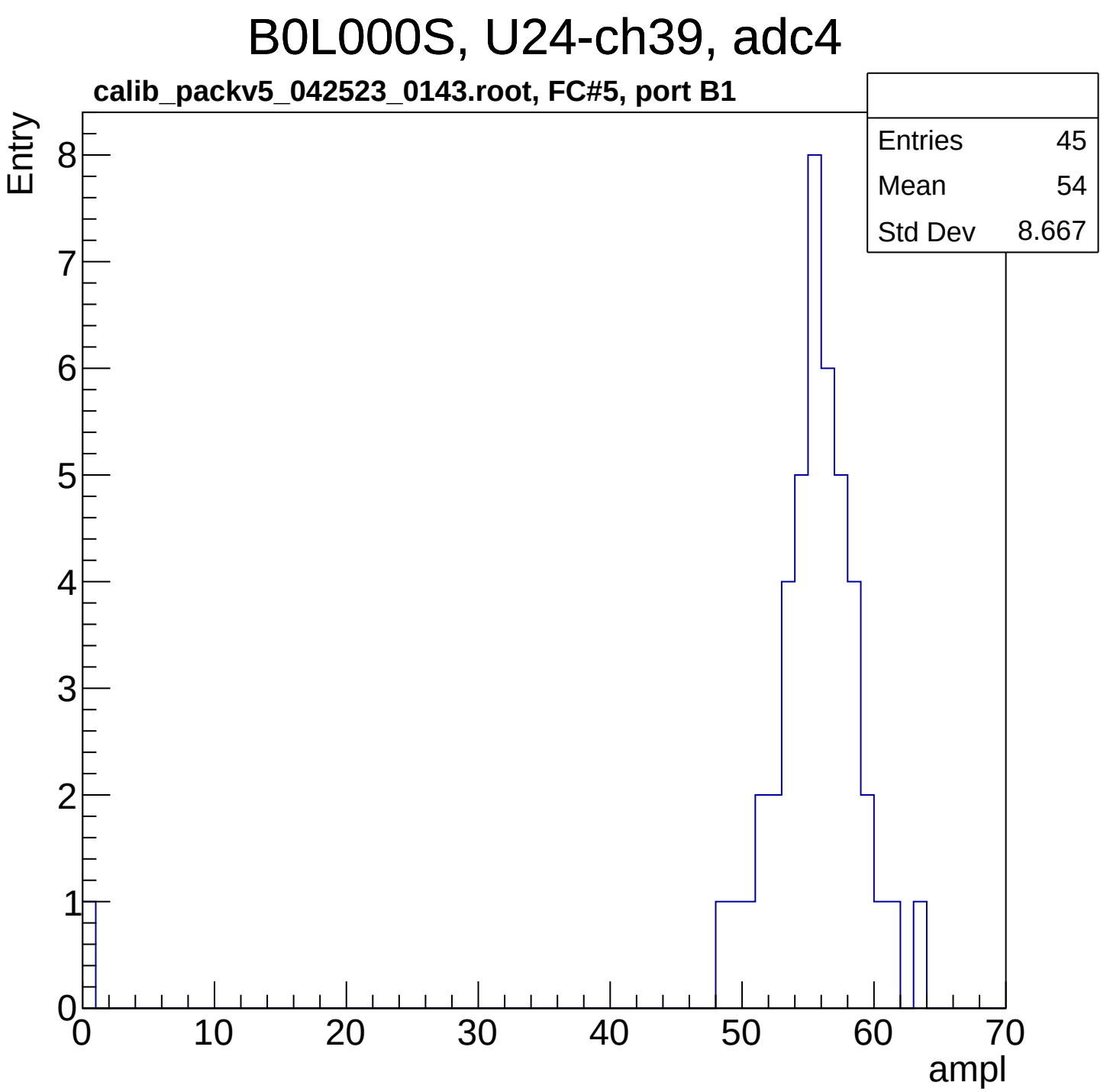
B0L000S, U24-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 54
Std Dev 8.667
Entry
0
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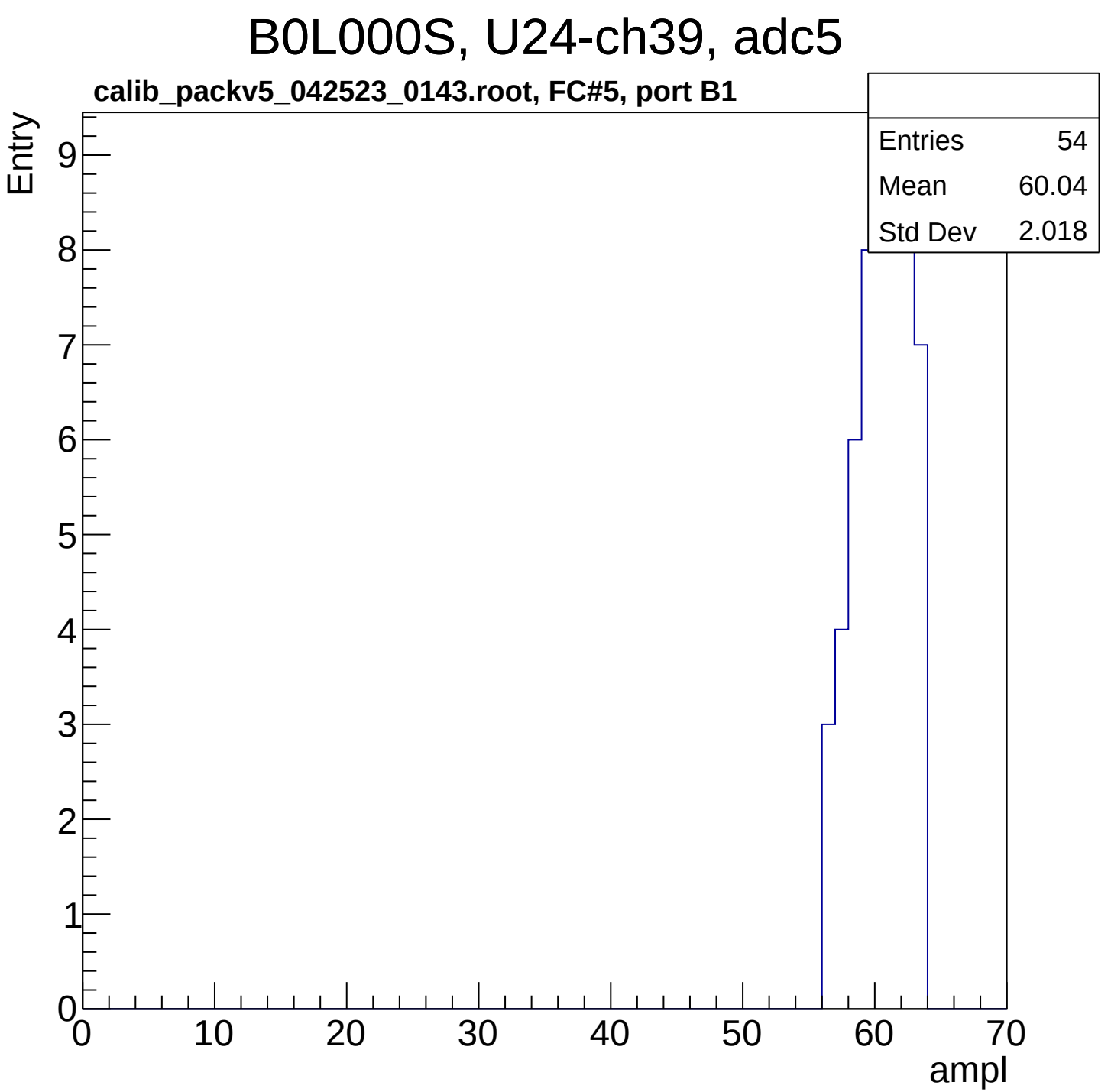
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 60.04
Std Dev 2.018
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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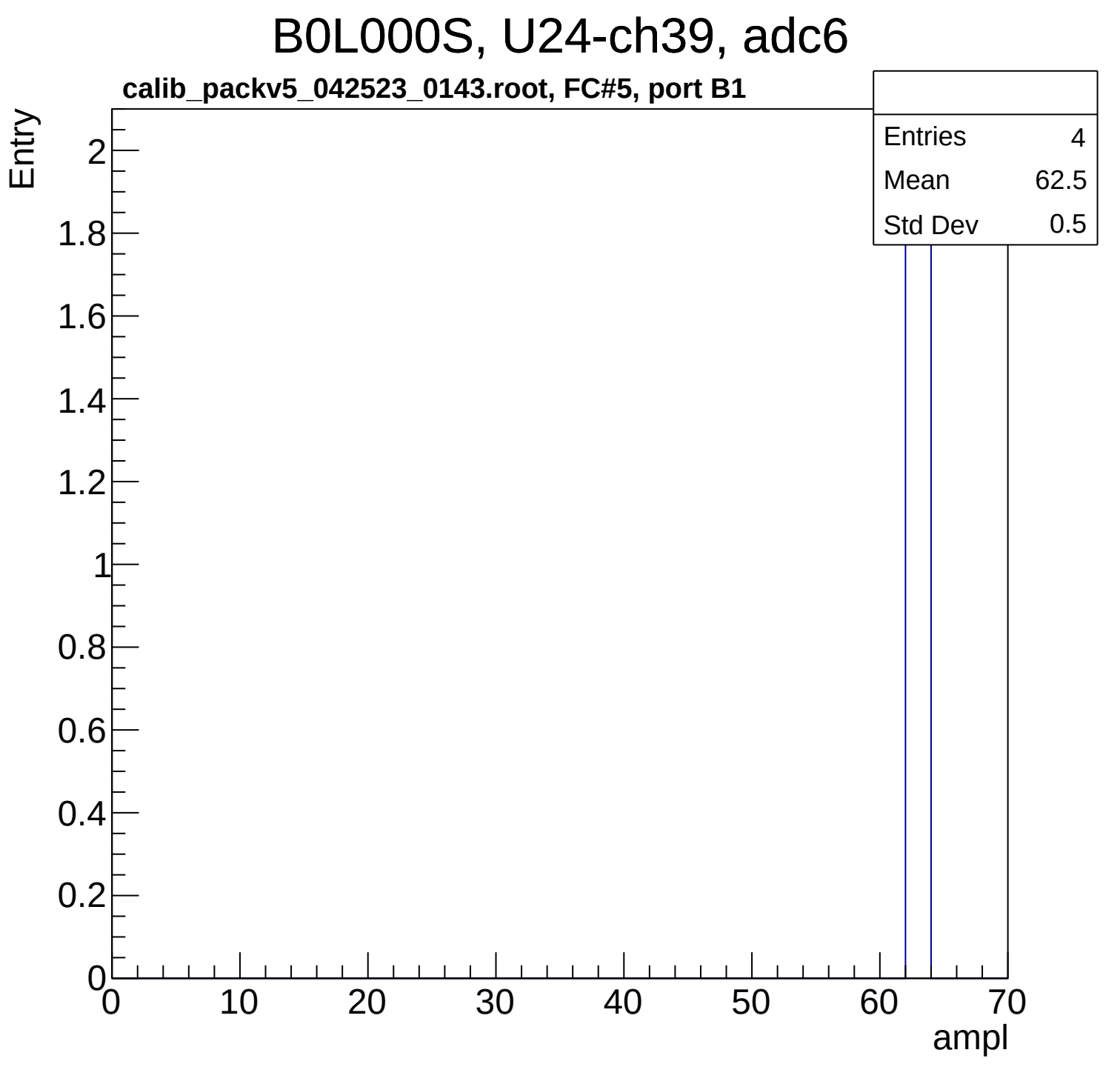
B0L000S, U24-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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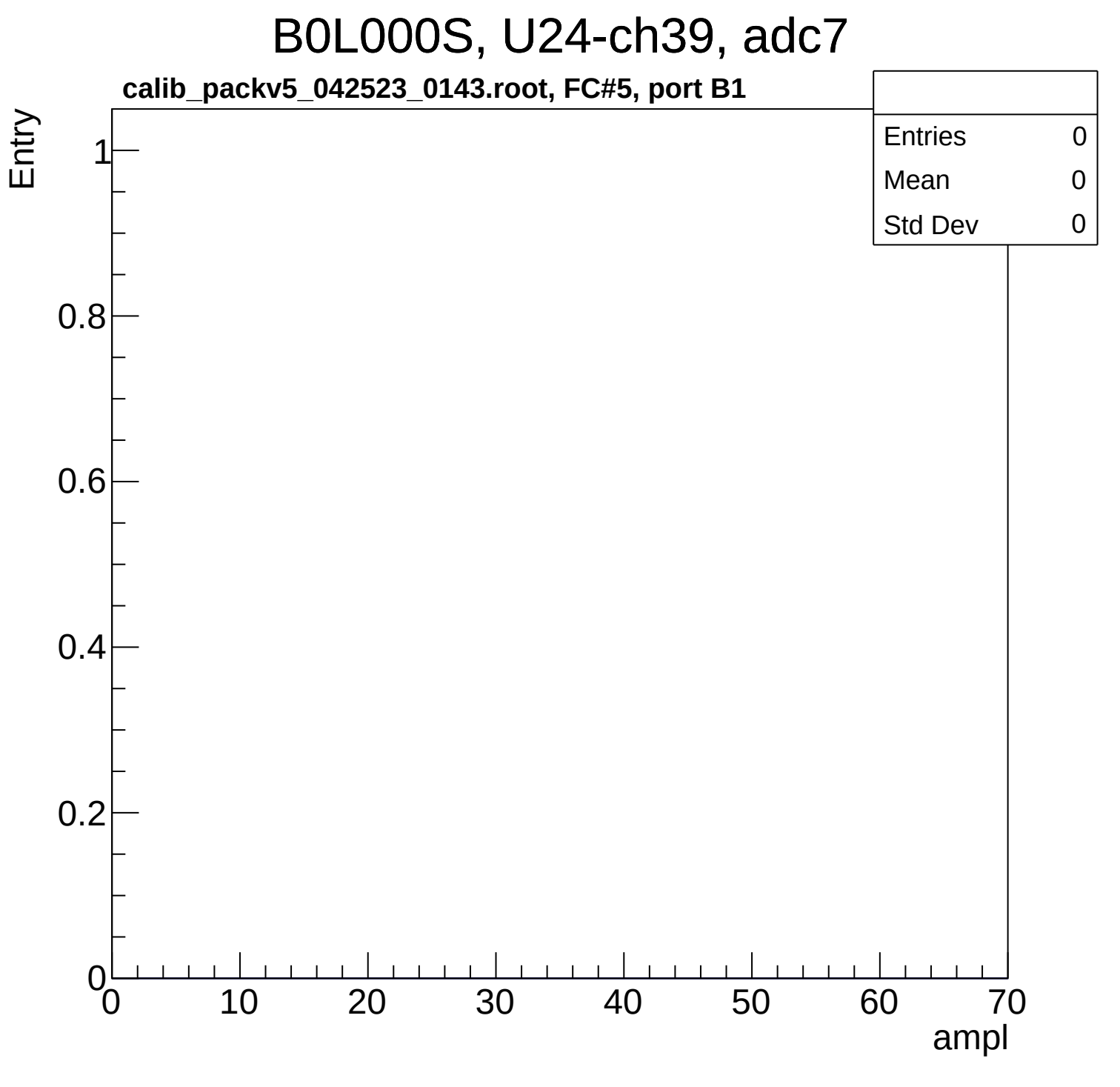
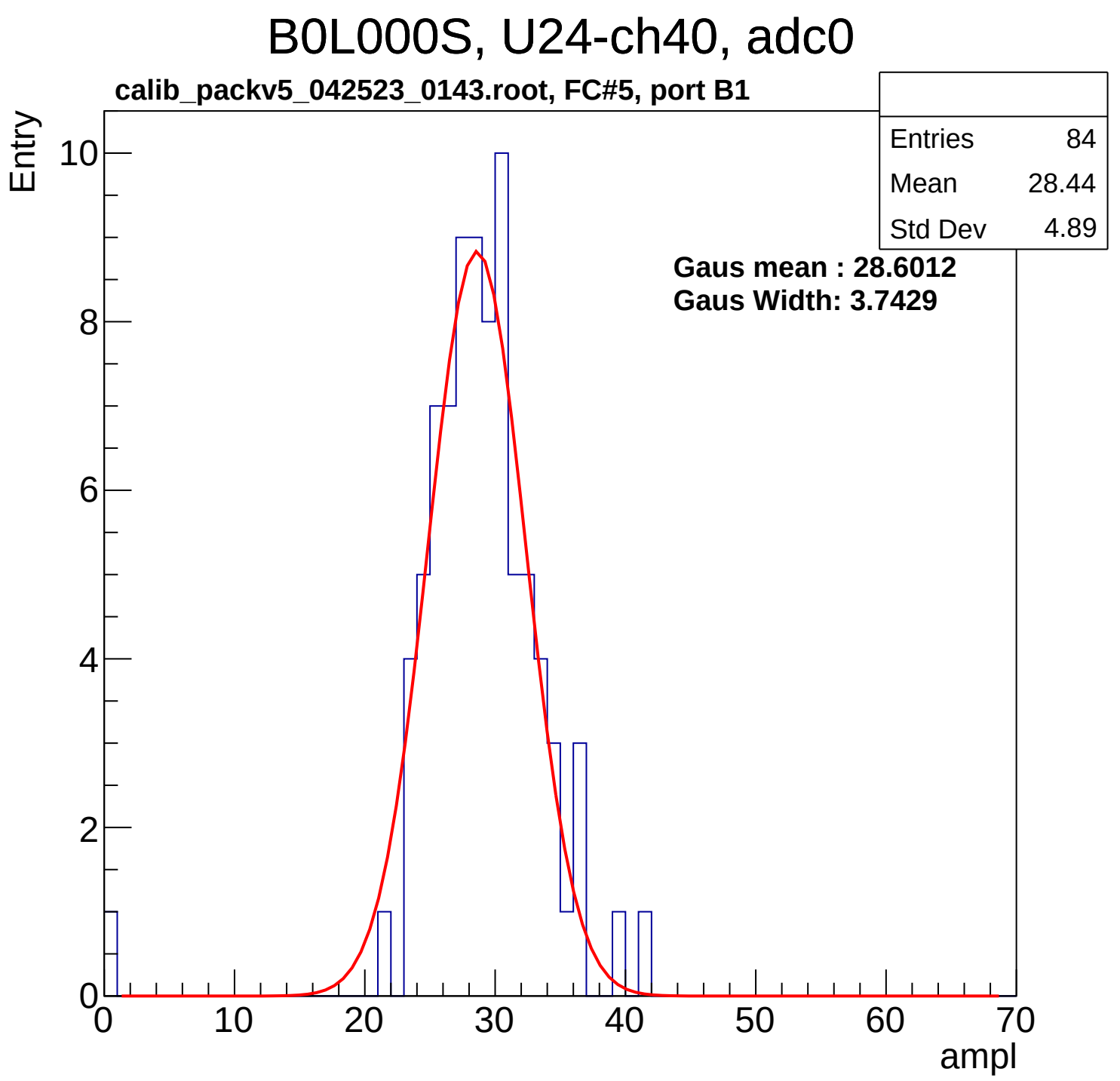

B0L000S, U24-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 28.44
Std Dev 4.89
Gaus mean : 28.6012
Gaus Width: 3.7429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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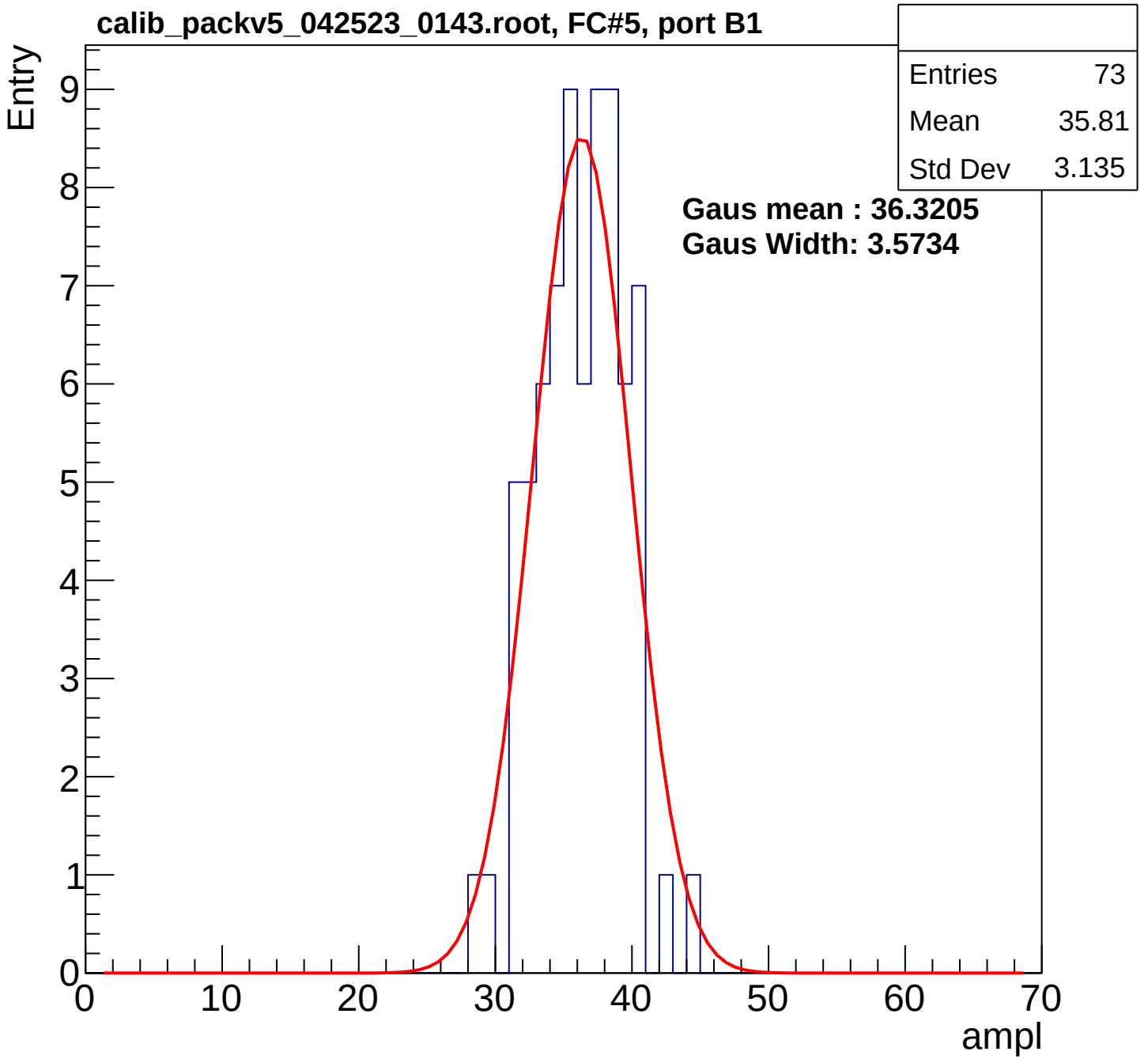
B0L000S, U24-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.81
Std Dev 3.135
Gaus mean : 36.3205
Gaus Width: 3.5734
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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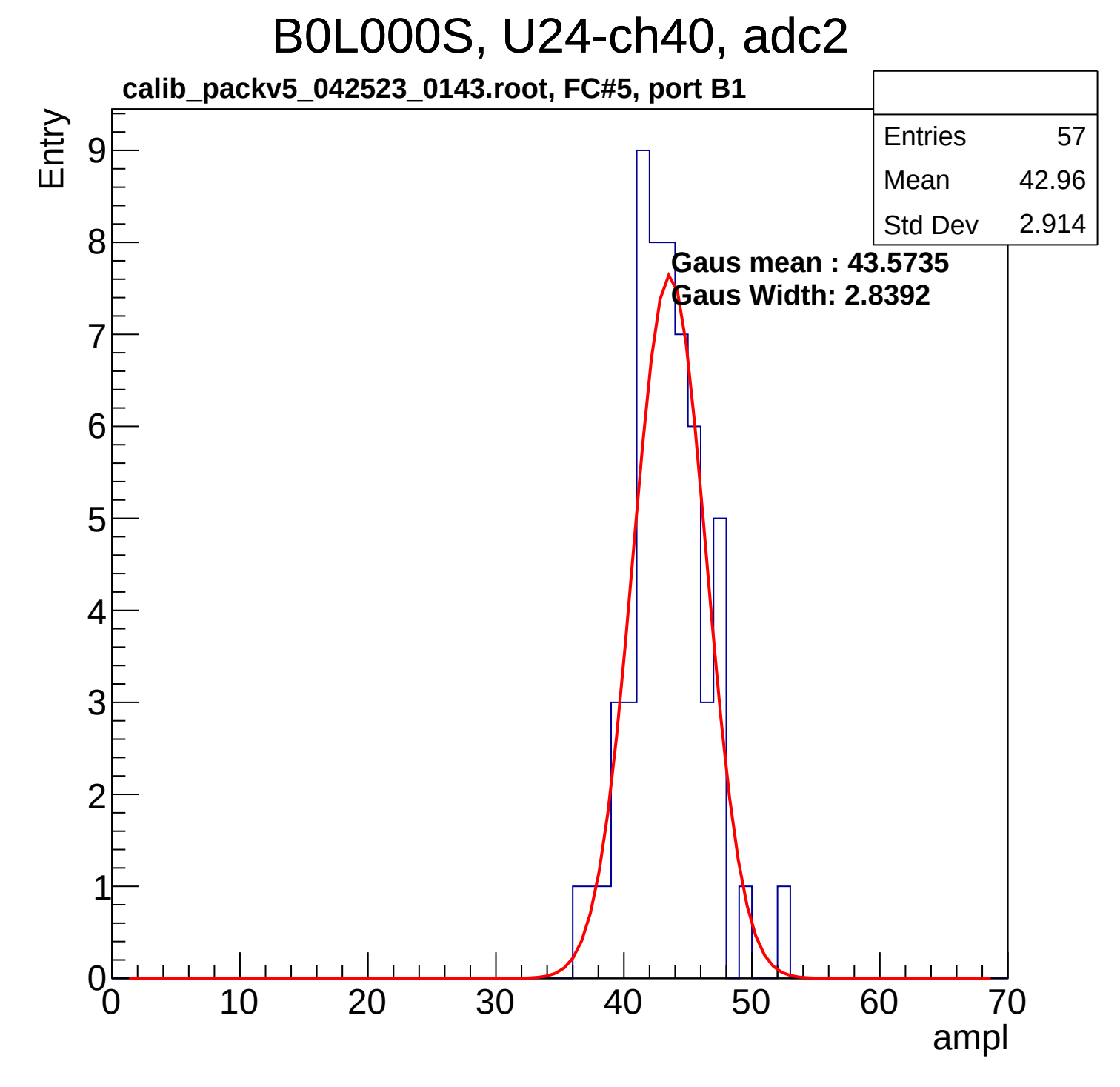
B0L000S, U24-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.96
Std Dev 2.914
Gaus mean : 43.5735
Gaus Width: 2.8392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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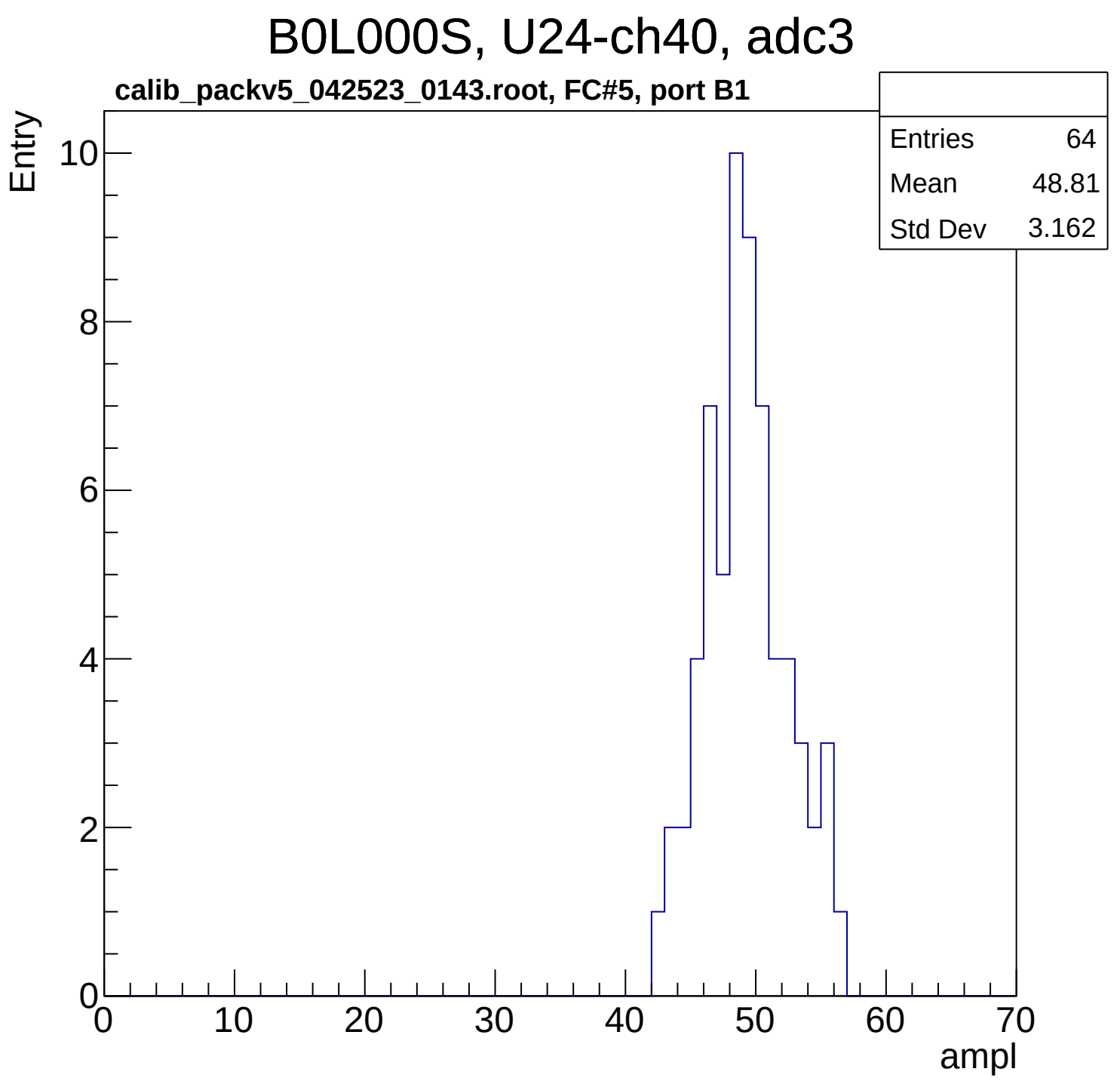
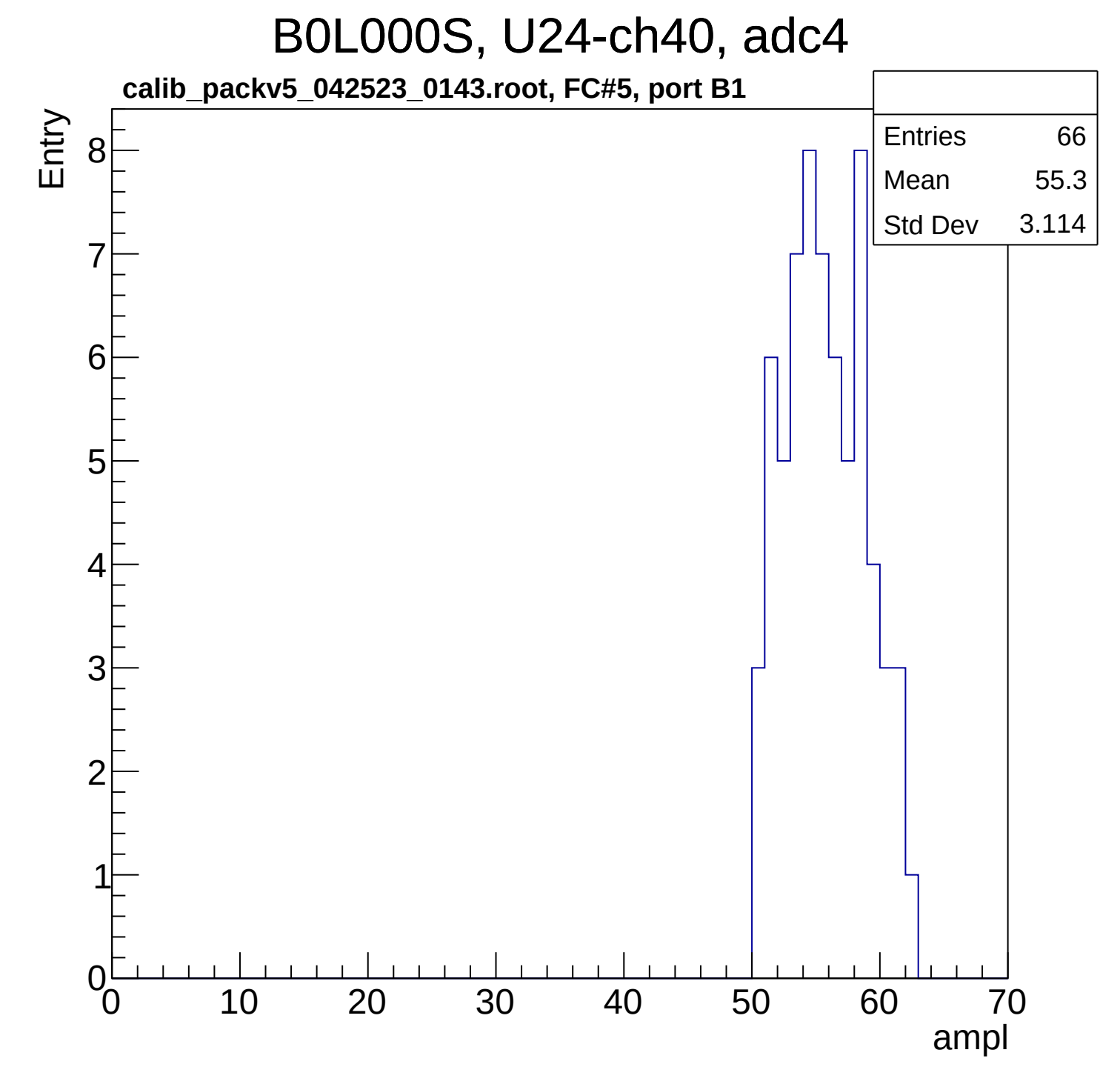

B0L000S, U24-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 48.81
Std Dev 3.162
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 55.3
Std Dev 3.114
Entry
ampl
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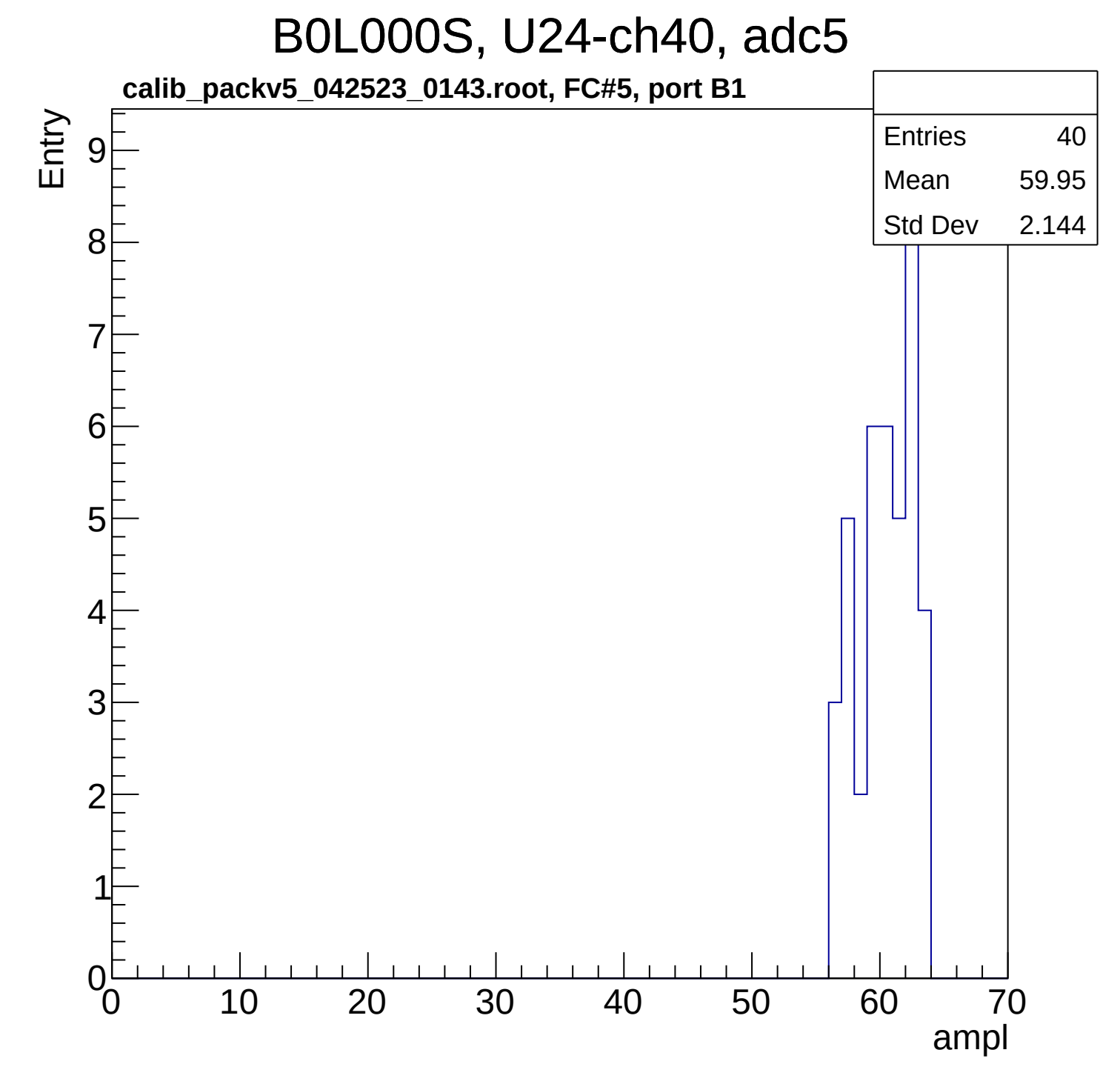
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.95
Std Dev 2.144
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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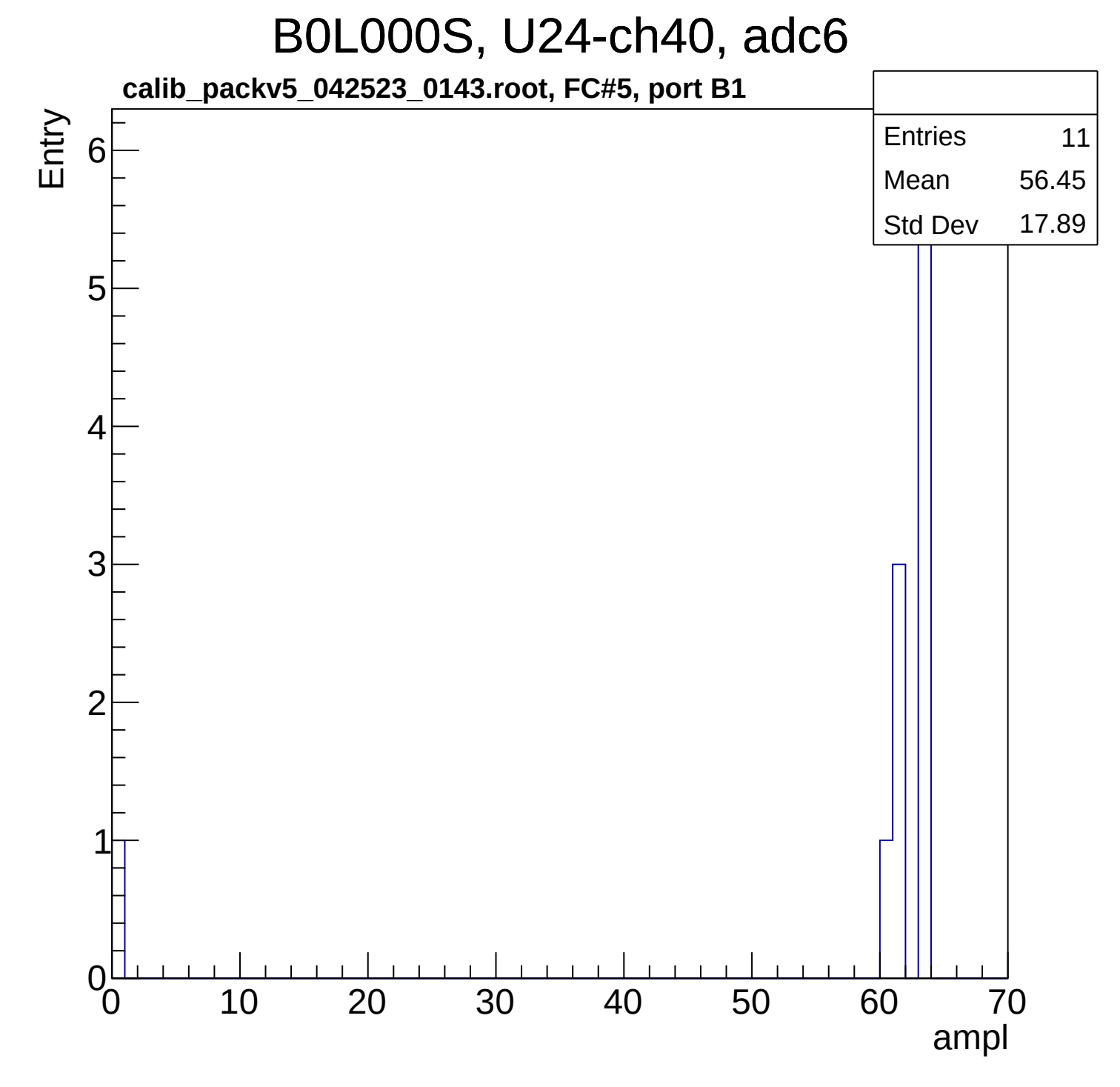
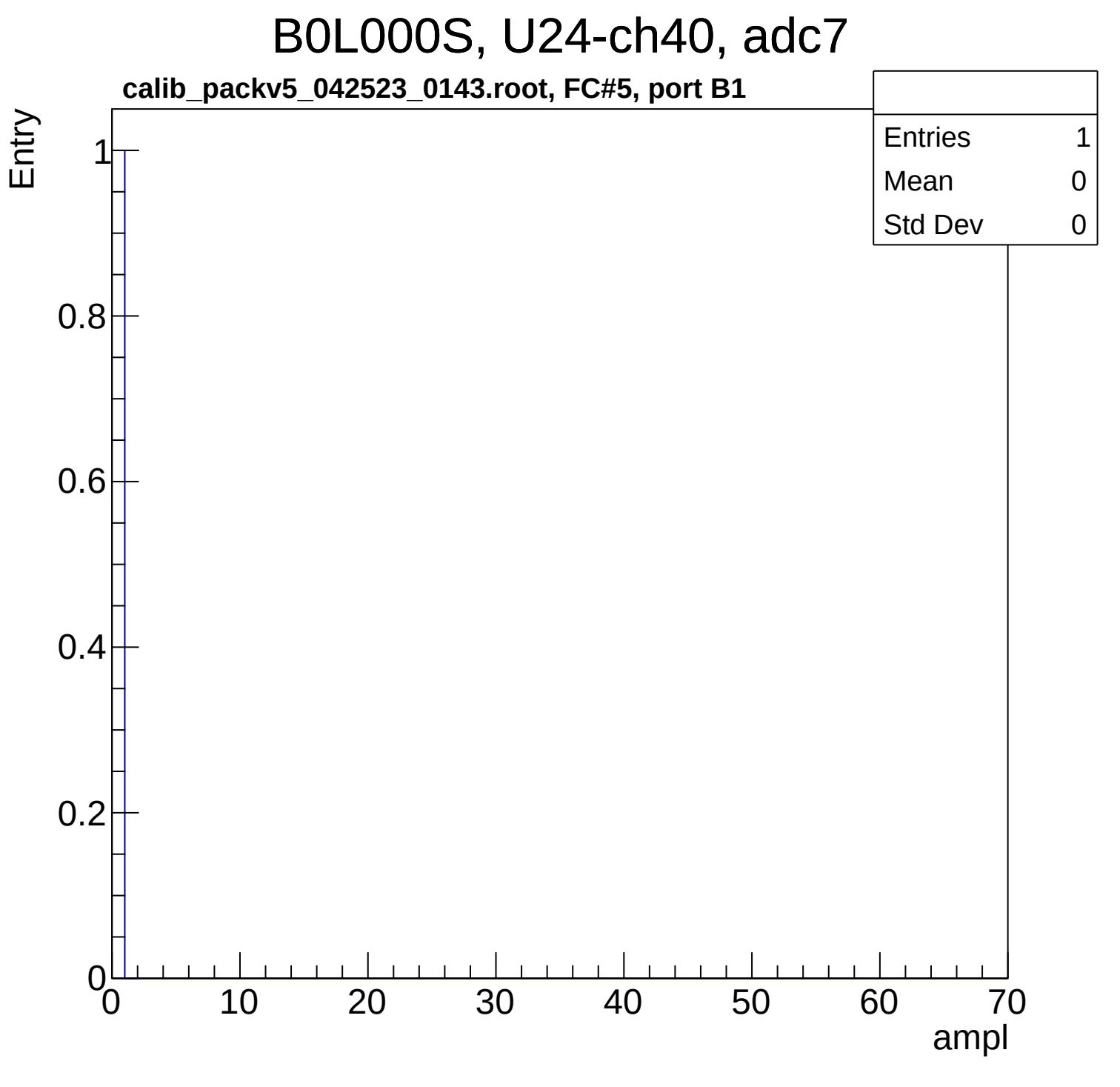
60
70


B0L000S, U24-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.45
Std Dev 17.89
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
20
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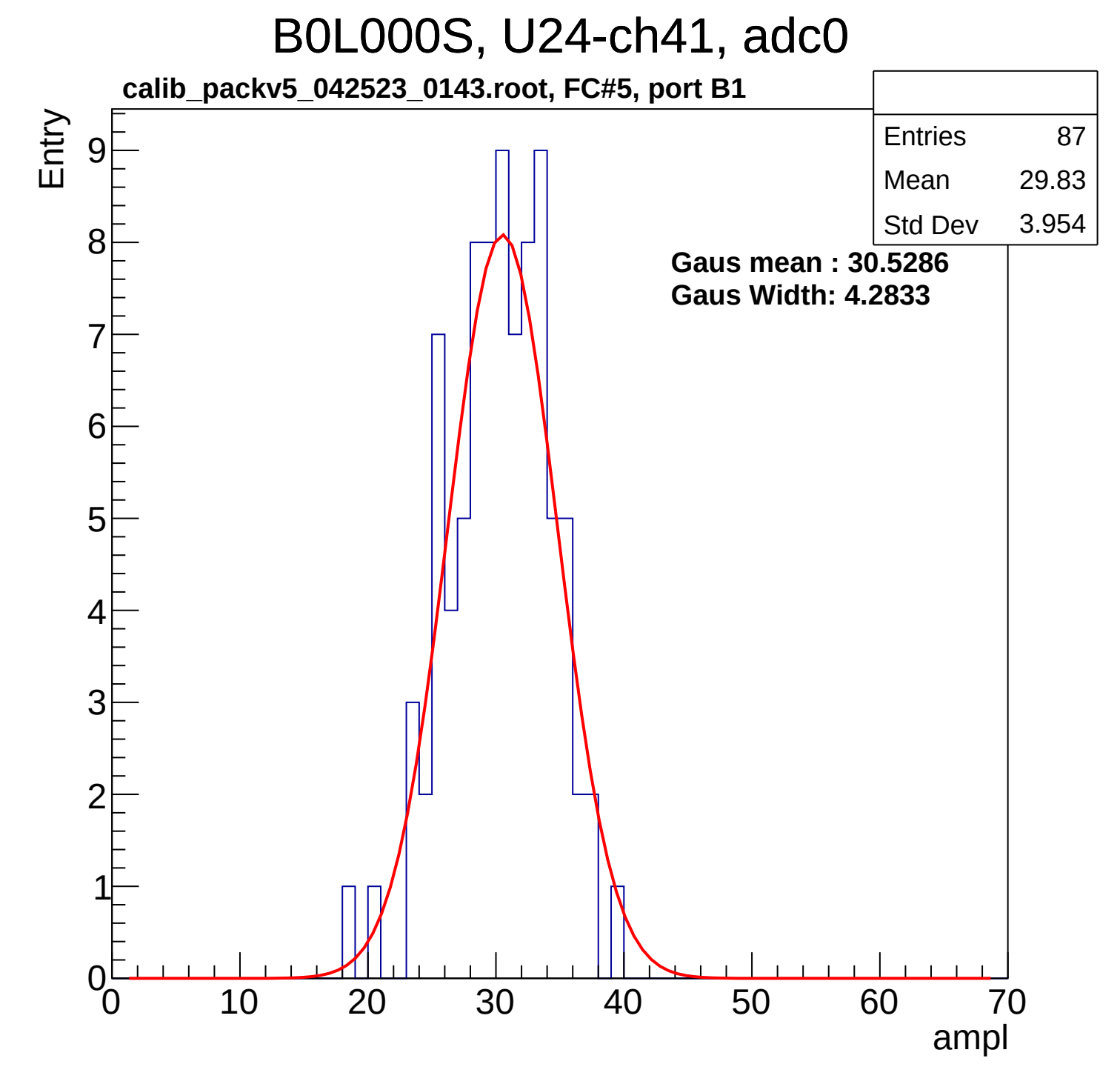
50
60
70
ampl


B0L000S, U24-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 29.83
Std Dev 3.954
Gaus mean : 30.5286
Gaus Width: 4.2833
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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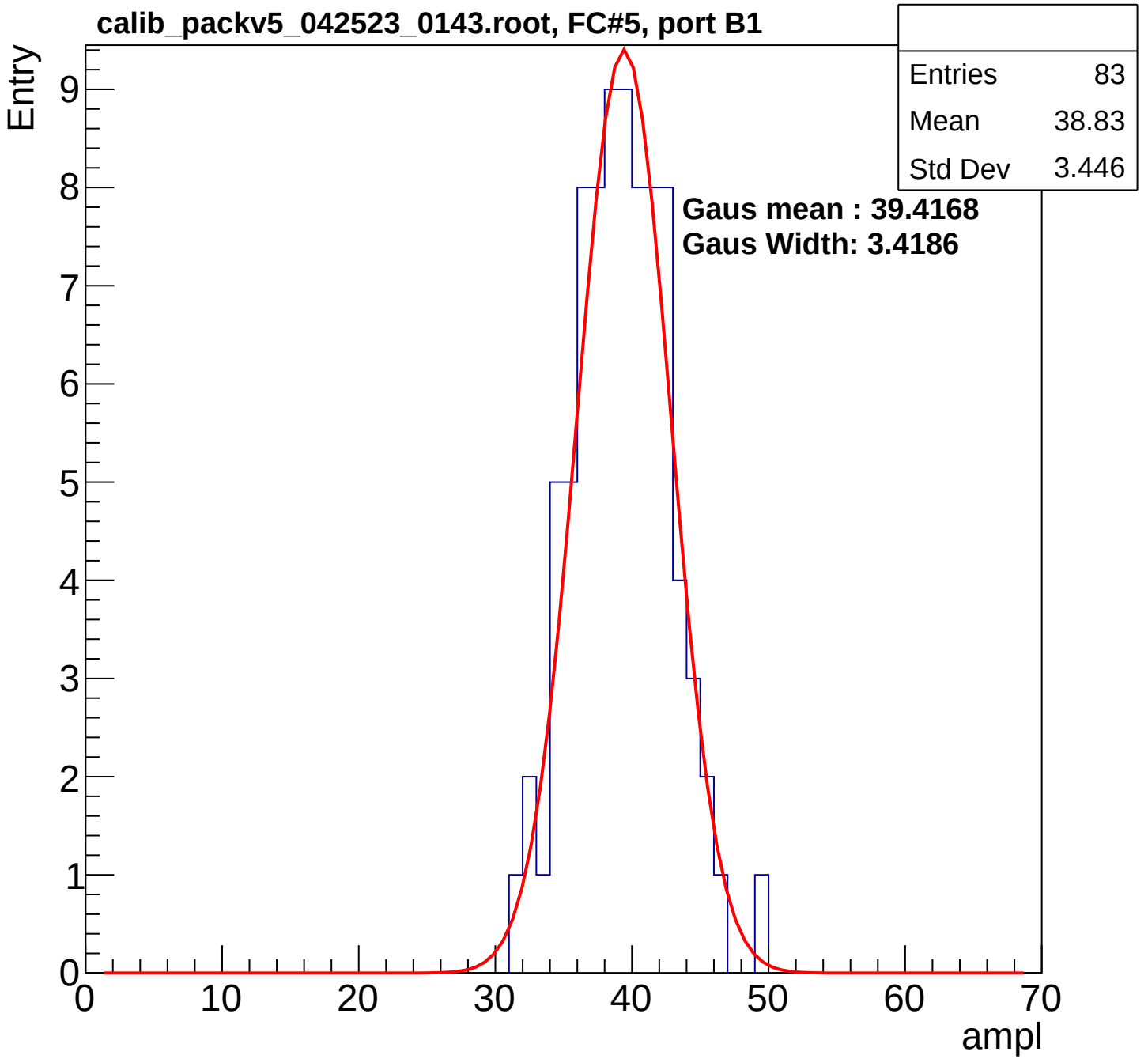
B0L000S, U24-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 38.83
Std Dev 3.446
Gaus mean : 39.4168
Gaus Width: 3.4186
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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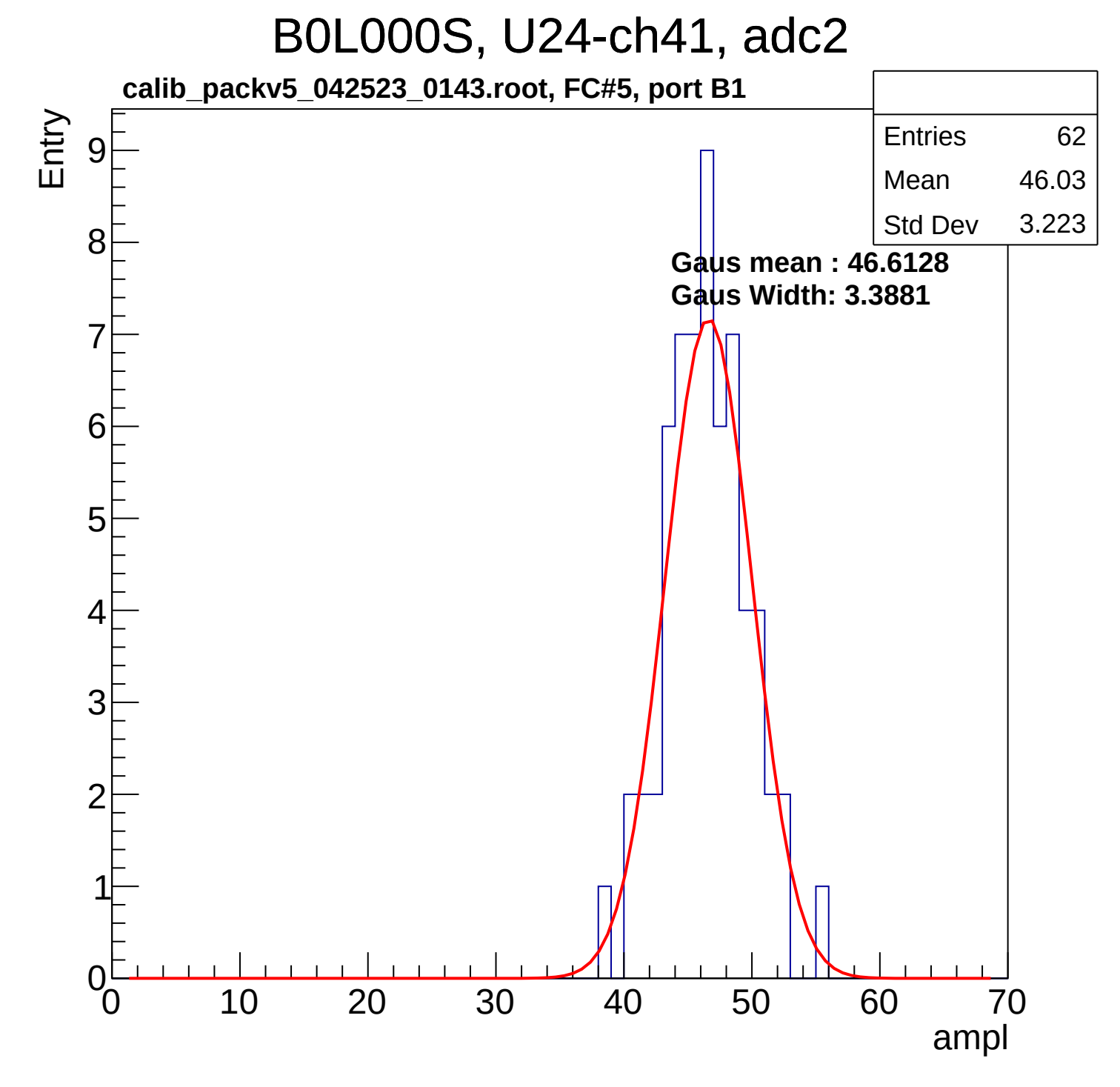
B0L000S, U24-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 46.03
Std Dev 3.223
Gaus mean : 46.6128
Gaus Width: 3.3881
Entry
ampl
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3
4
5
6
7
8
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0
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60
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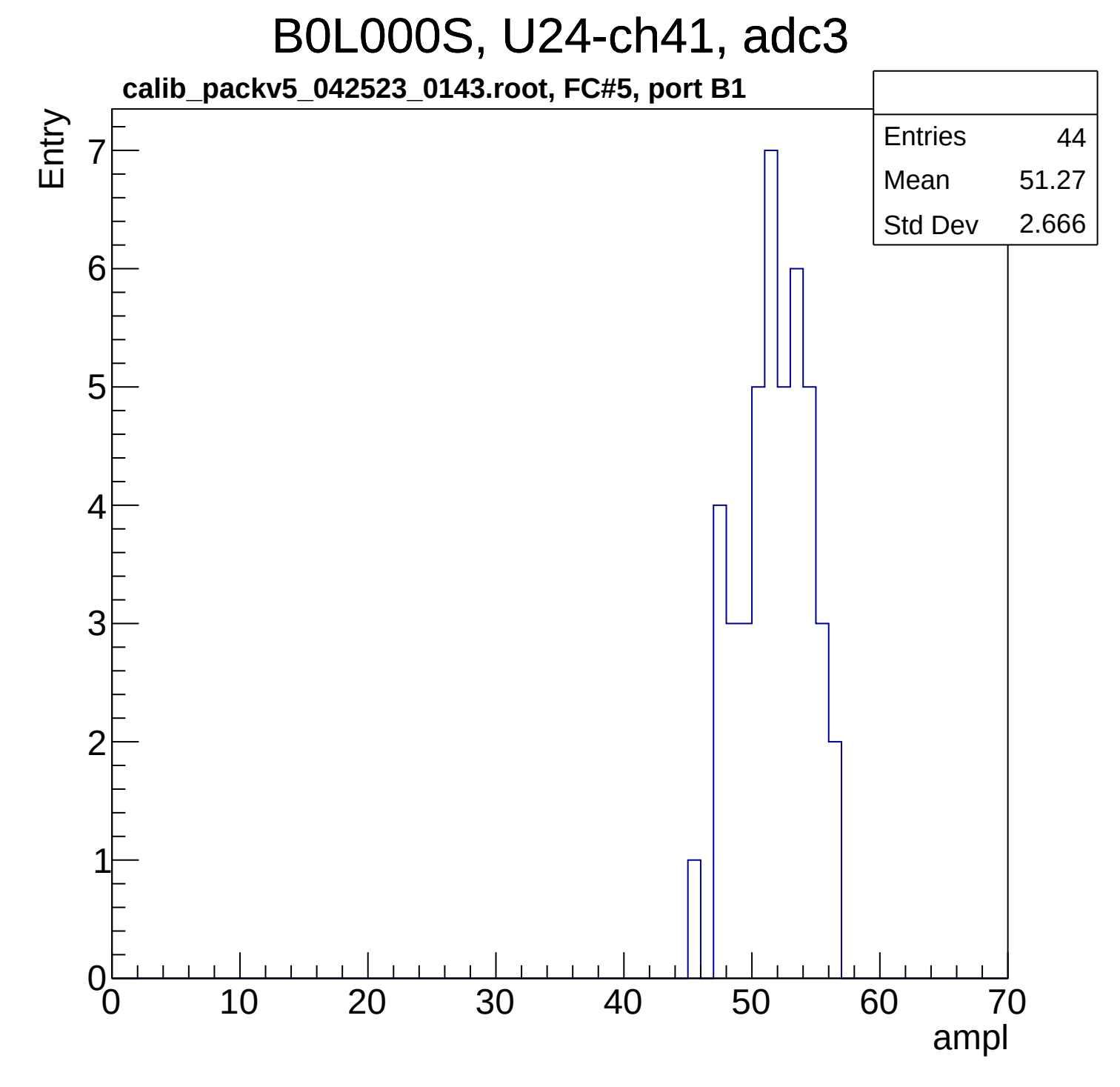
B0L000S, U24-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 51.27
Std Dev 2.666
Entry
ampl
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3
4
5
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0
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20
30
40
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60
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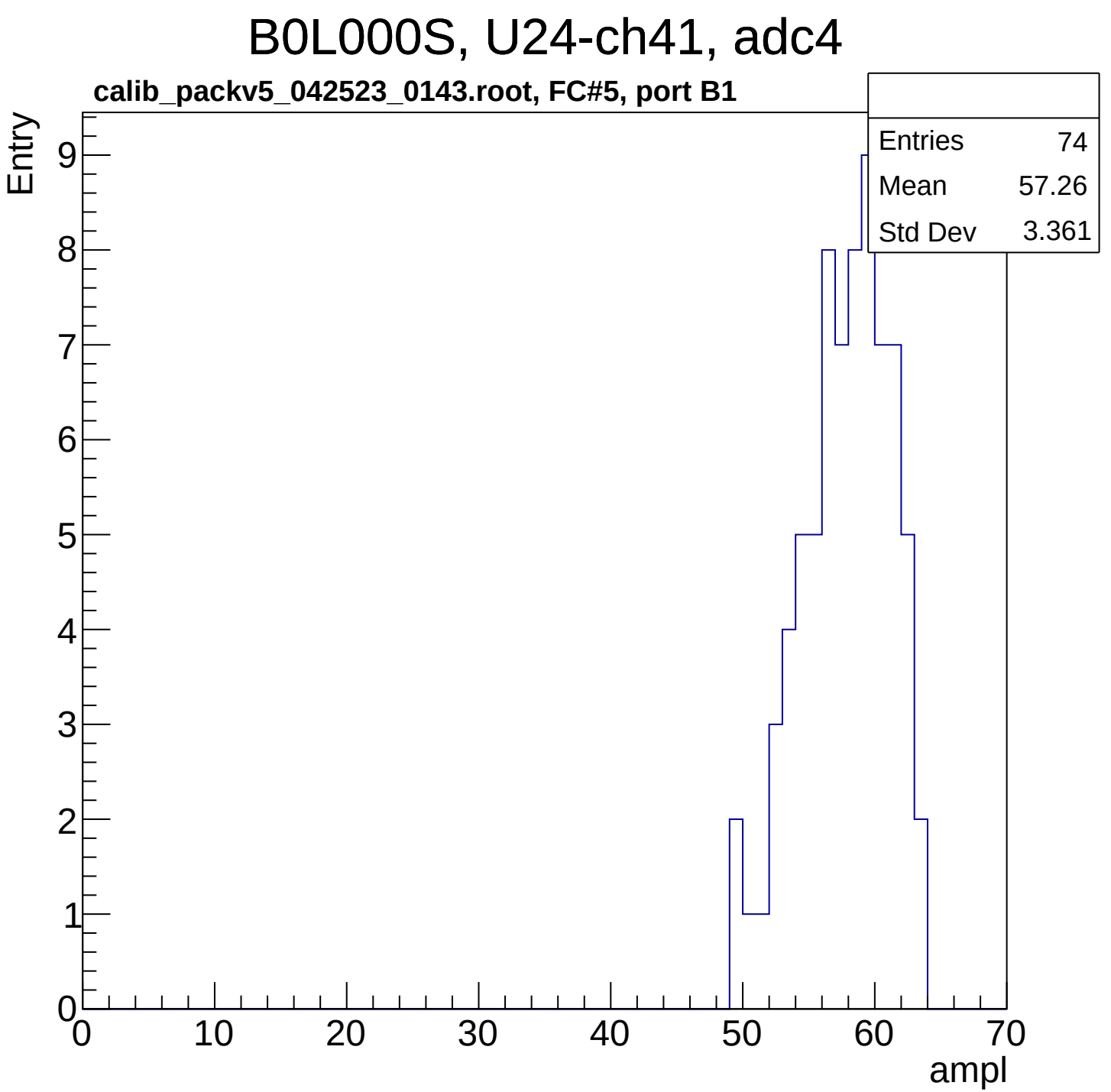
B0L000S, U24-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 57.26
Std Dev 3.361
Entry
ampl
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3
4
5
6
7
8
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0
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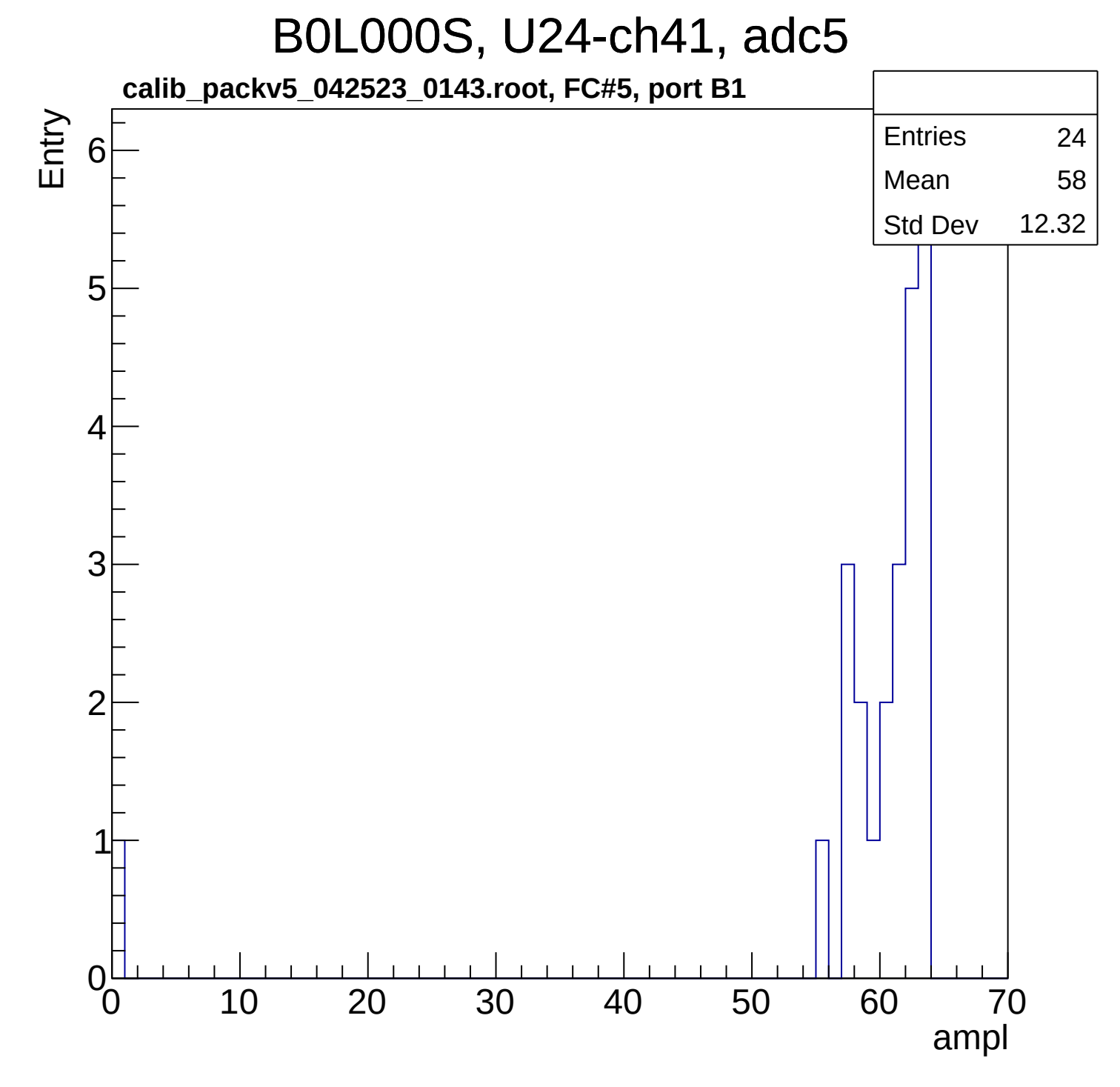
B0L000S, U24-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58
Std Dev 12.32
Entry
ampl
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2
3
4
5
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0
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20
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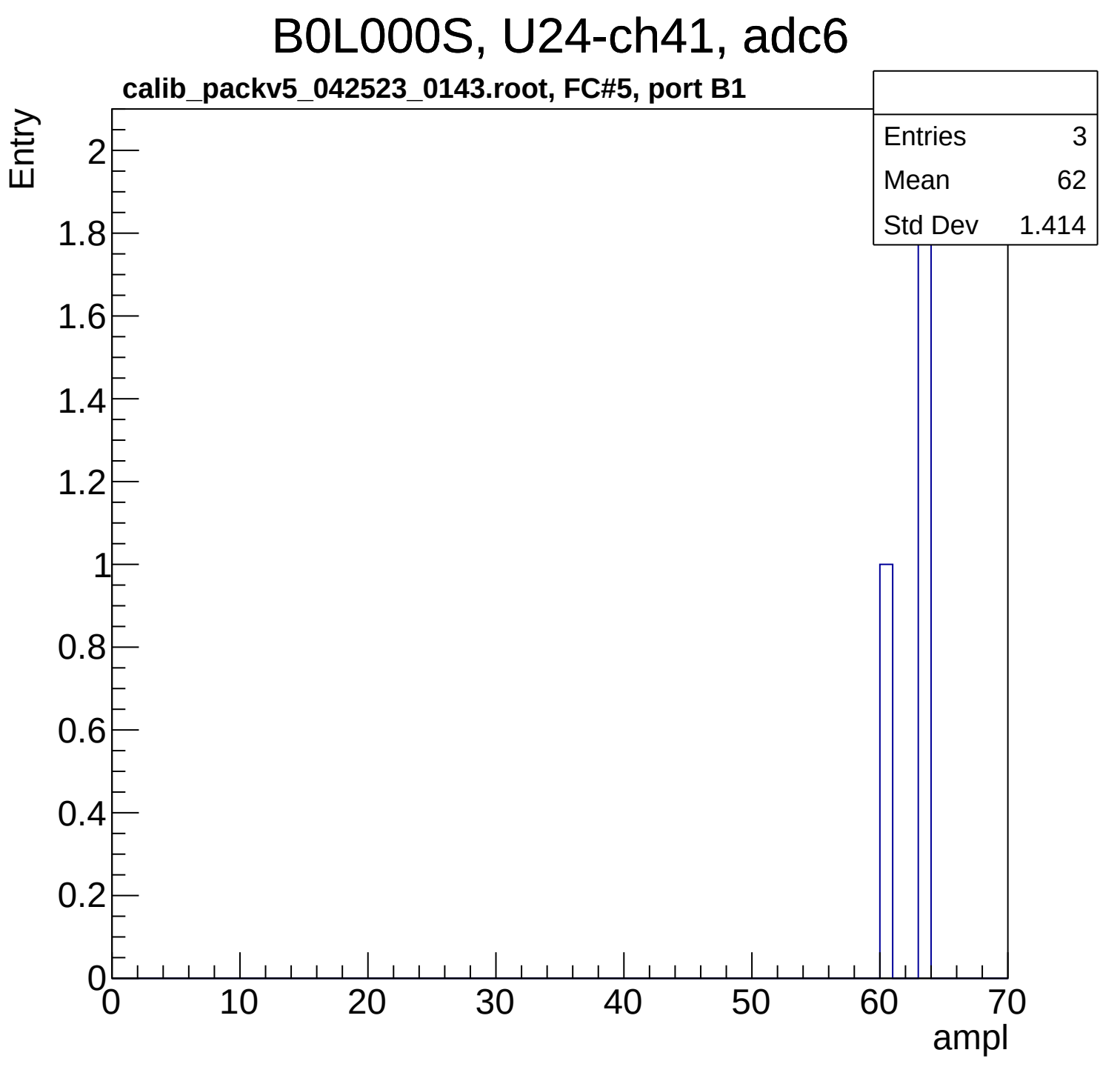
40
50
60
70


B0L000S, U24-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
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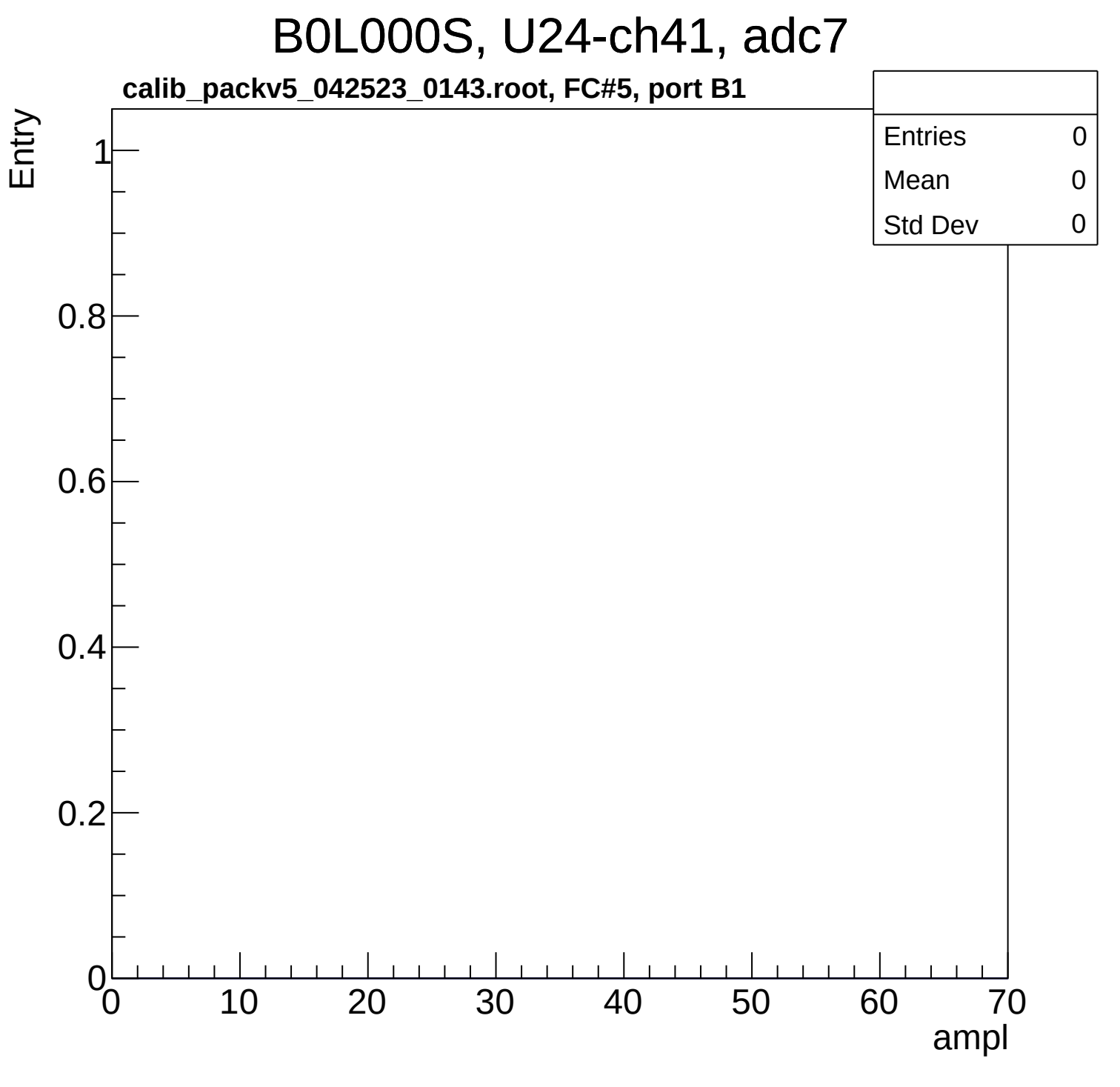
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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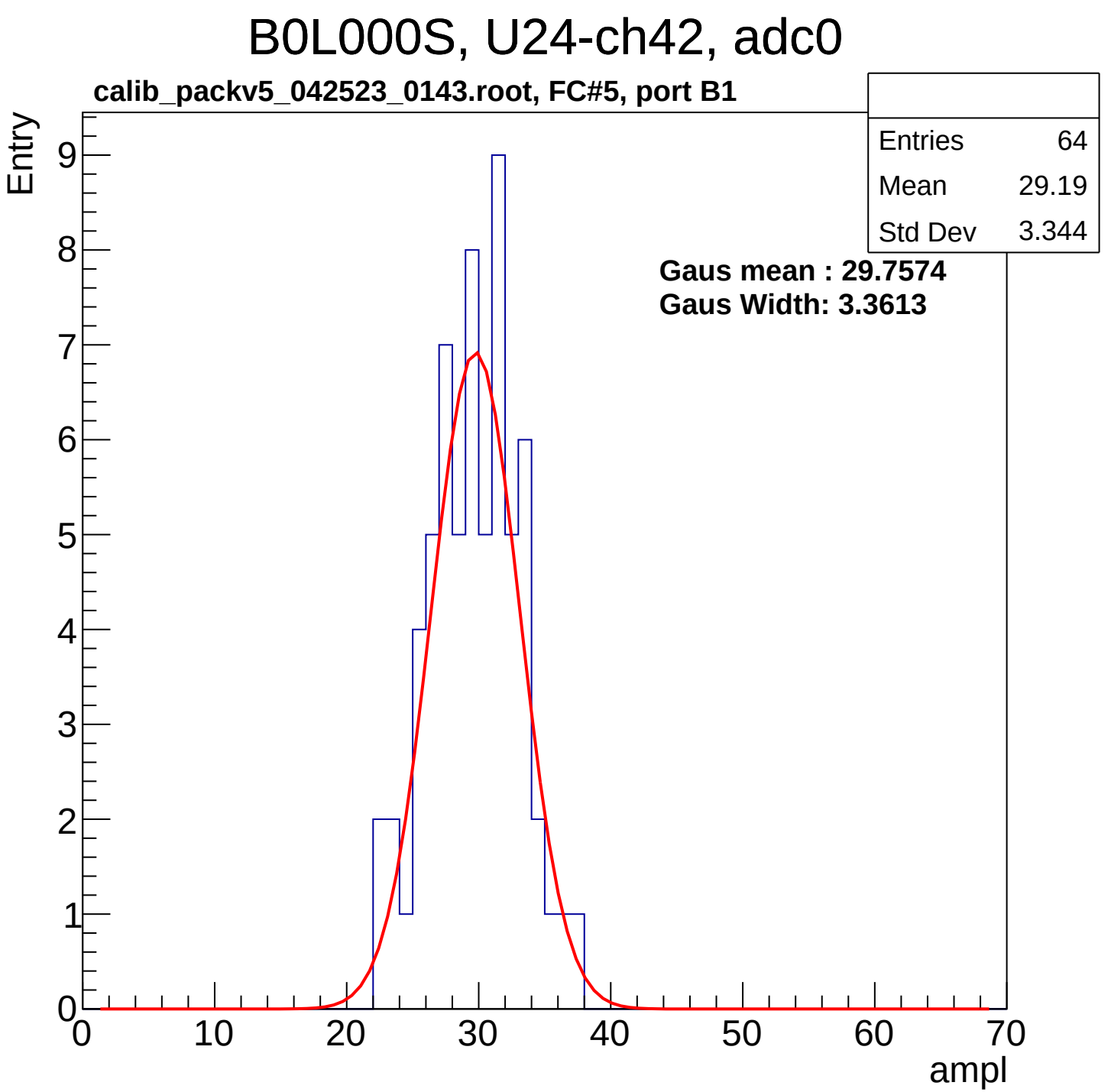
60
70
ampl


B0L000S, U24-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.19
Std Dev 3.344
Gaus mean : 29.7574
Gaus Width: 3.3613
Entry
ampl
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3
4
5
6
7
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0
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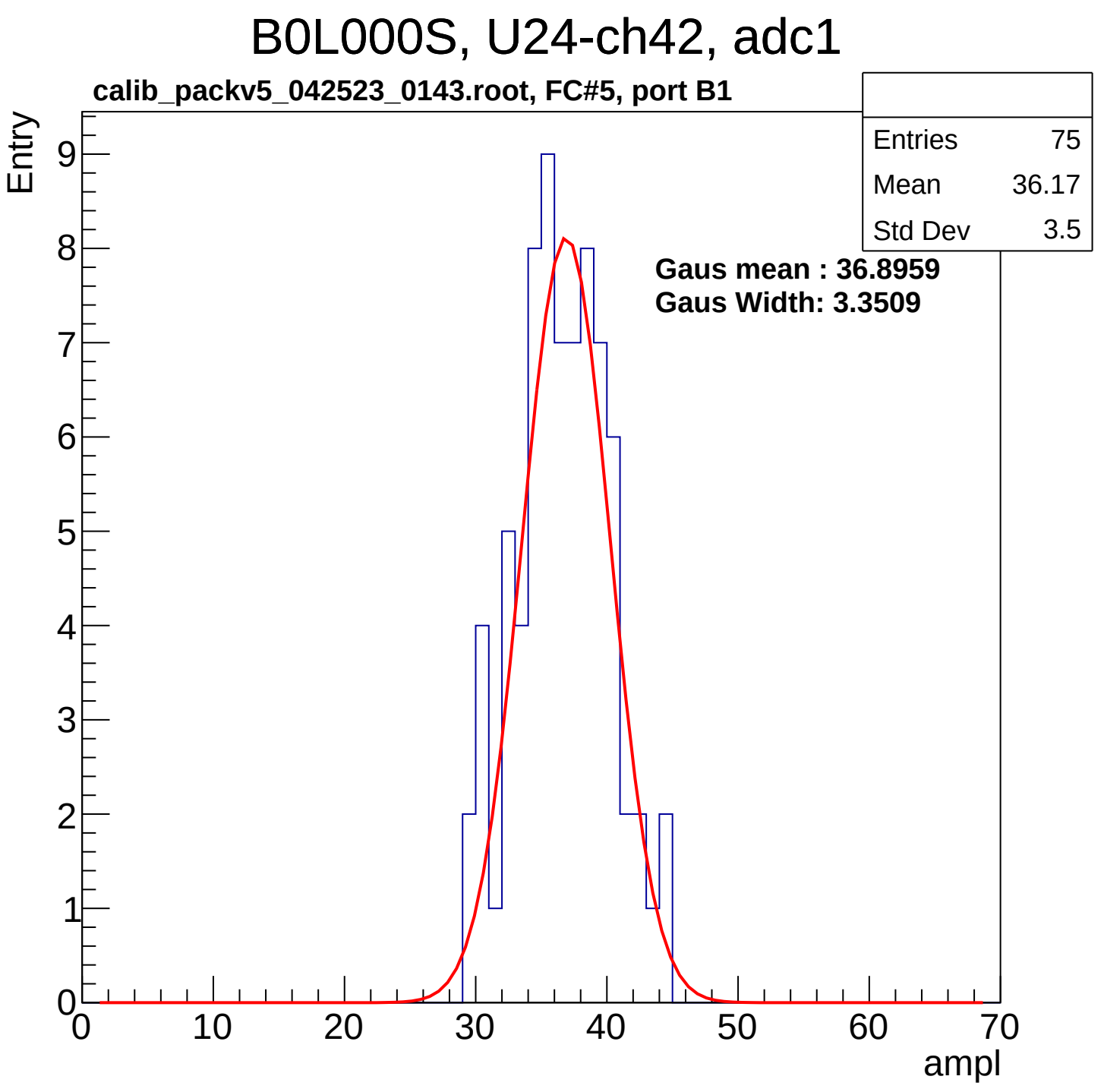
B0L000S, U24-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.17
Std Dev 3.5
Gaus mean : 36.8959
Gaus Width: 3.3509
Entry
ampl
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2
3
4
5
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7
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0
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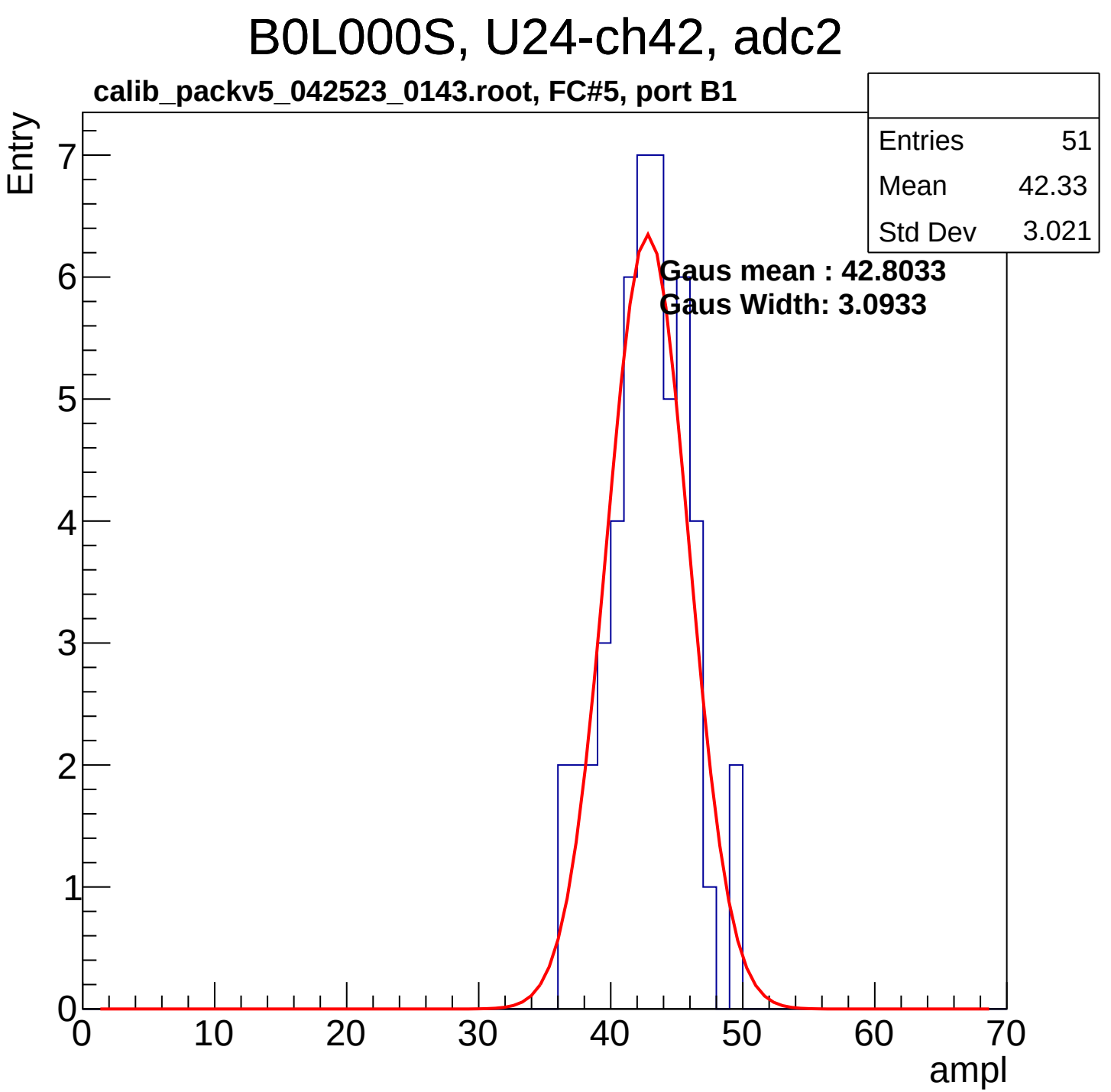
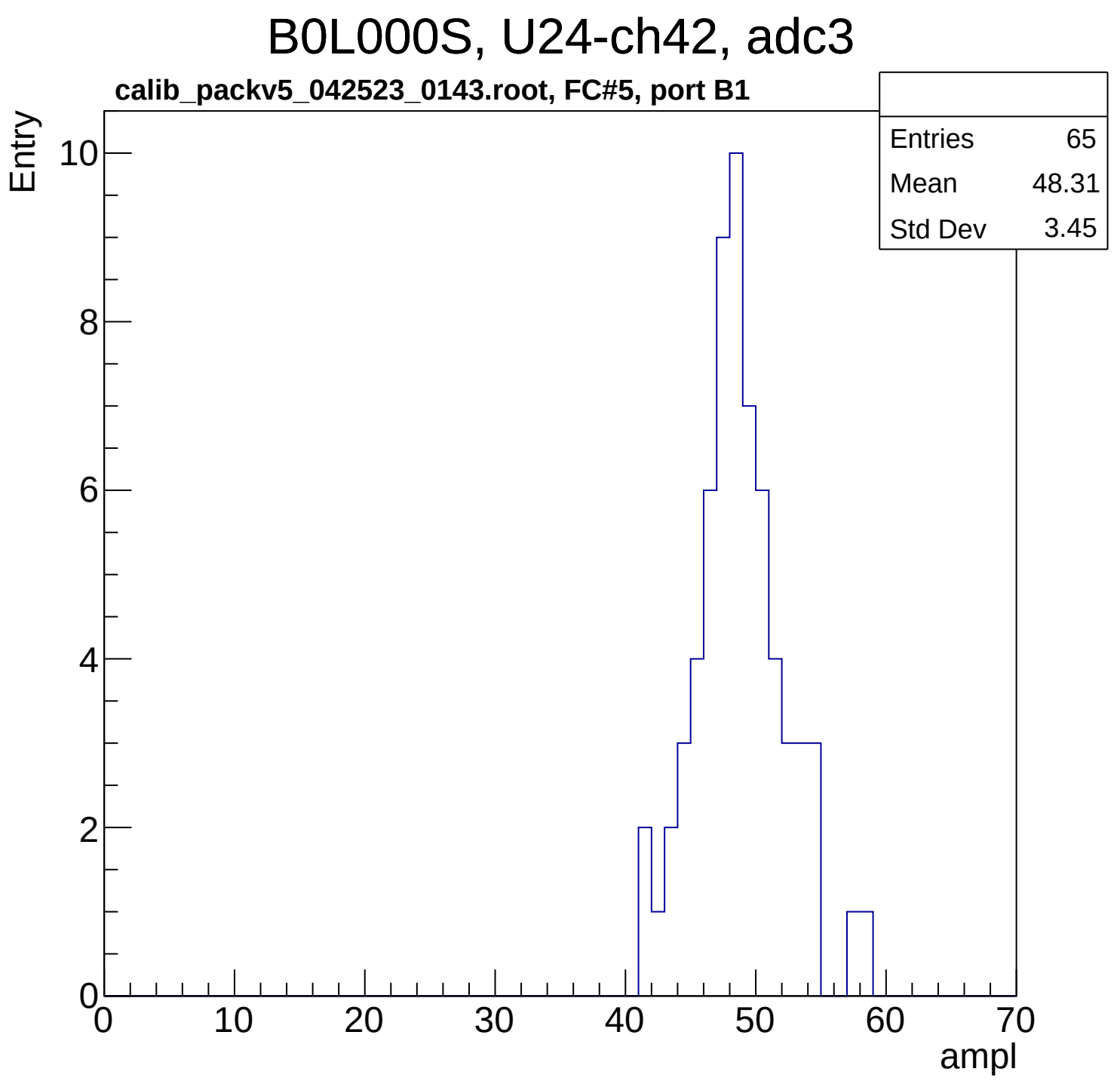

B0L000S, U24-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 42.33
Std Dev 3.021
Gaus mean : 42.8033
Gaus Width: 3.0933
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 48.31
Std Dev 3.45
Entry
0
2
4
6
8
10
0
10
20
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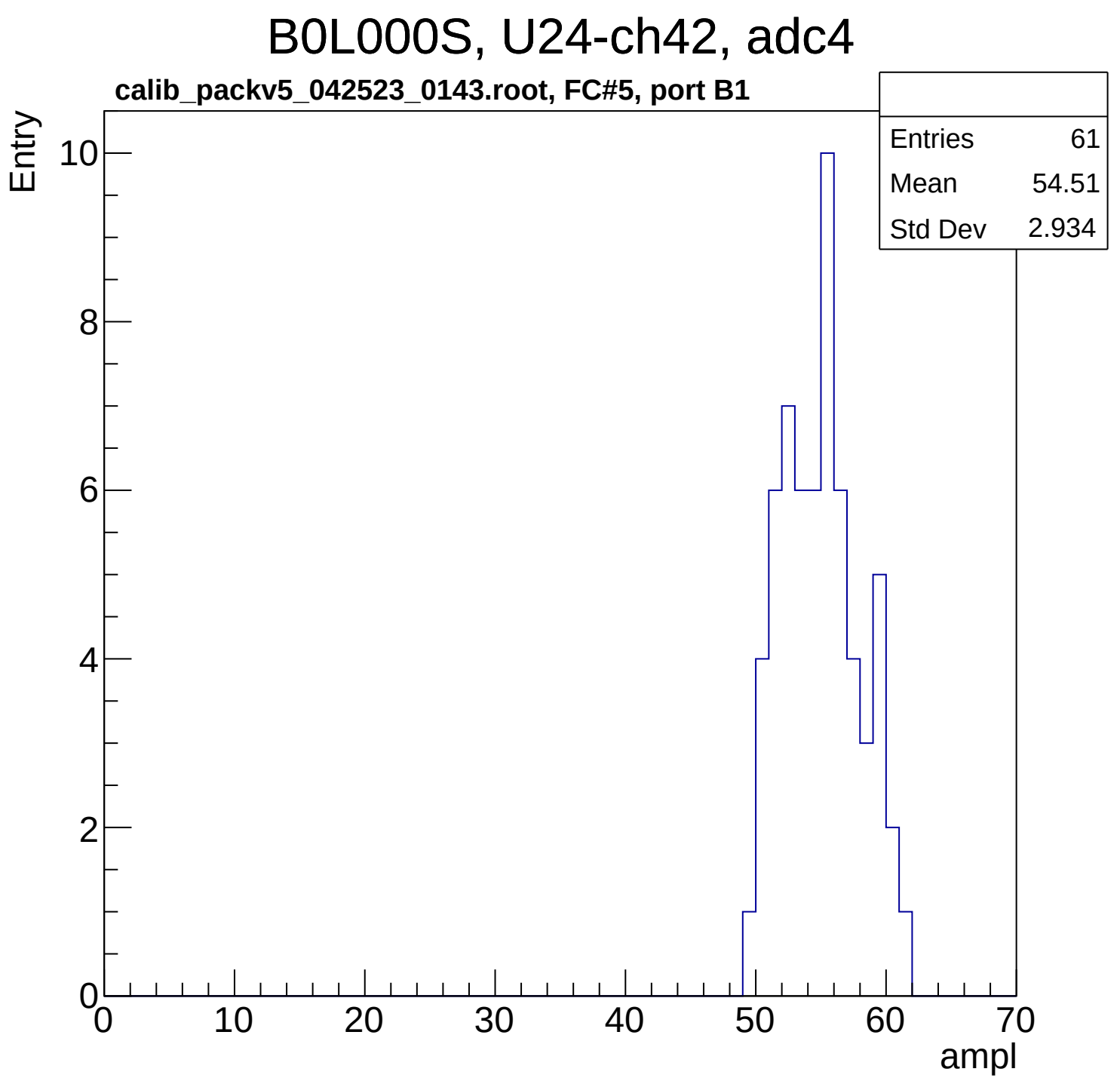
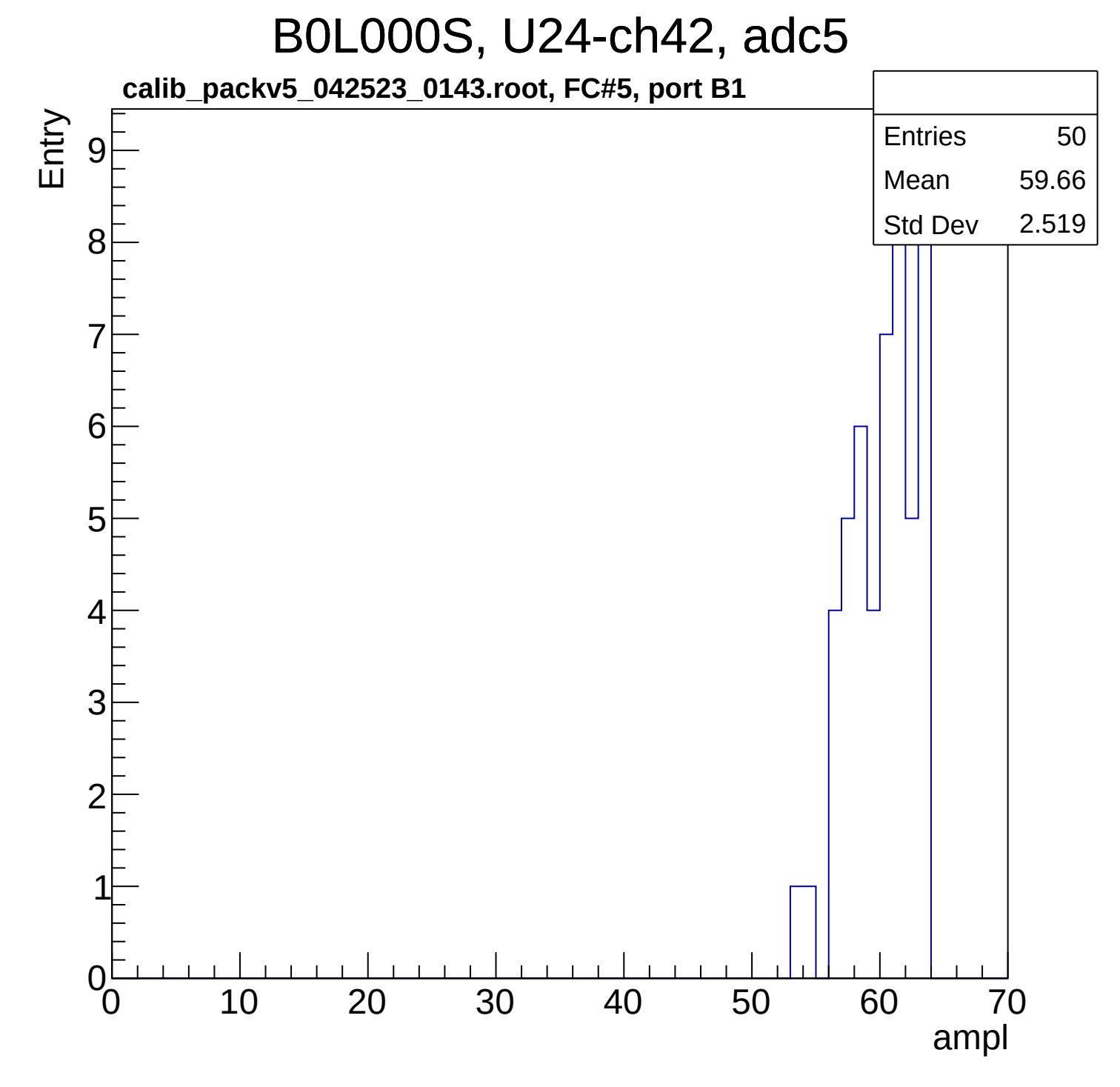
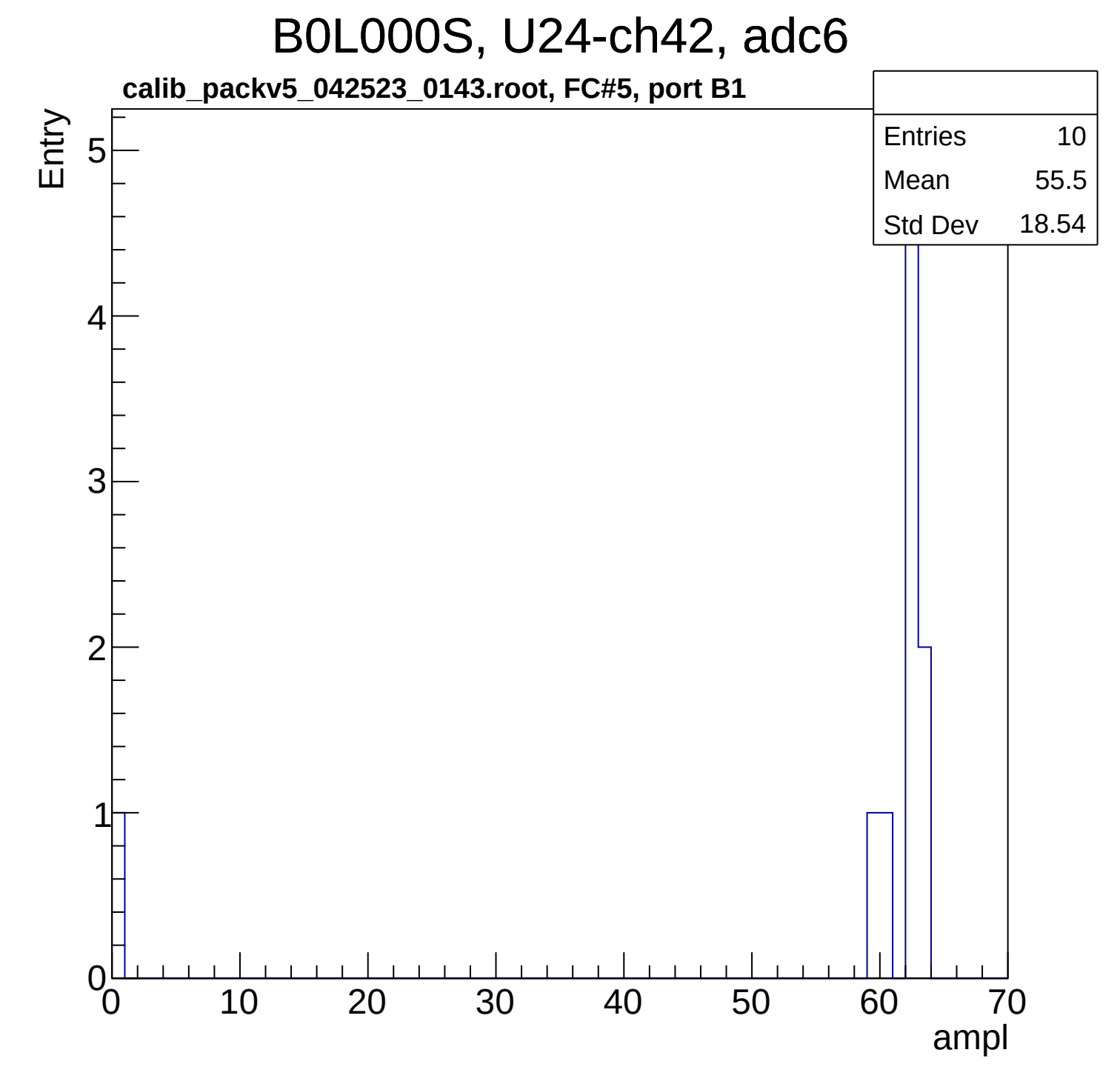
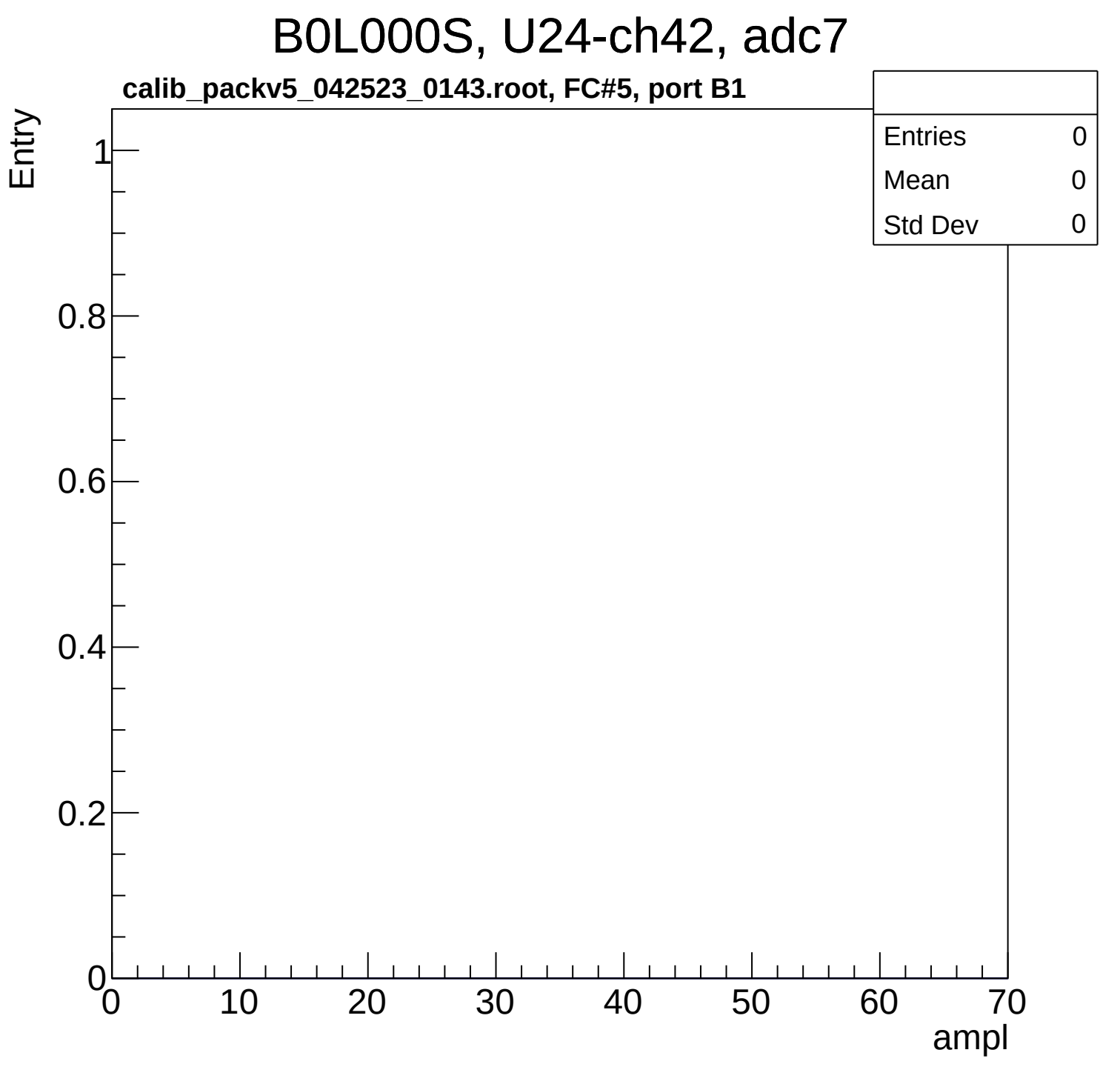
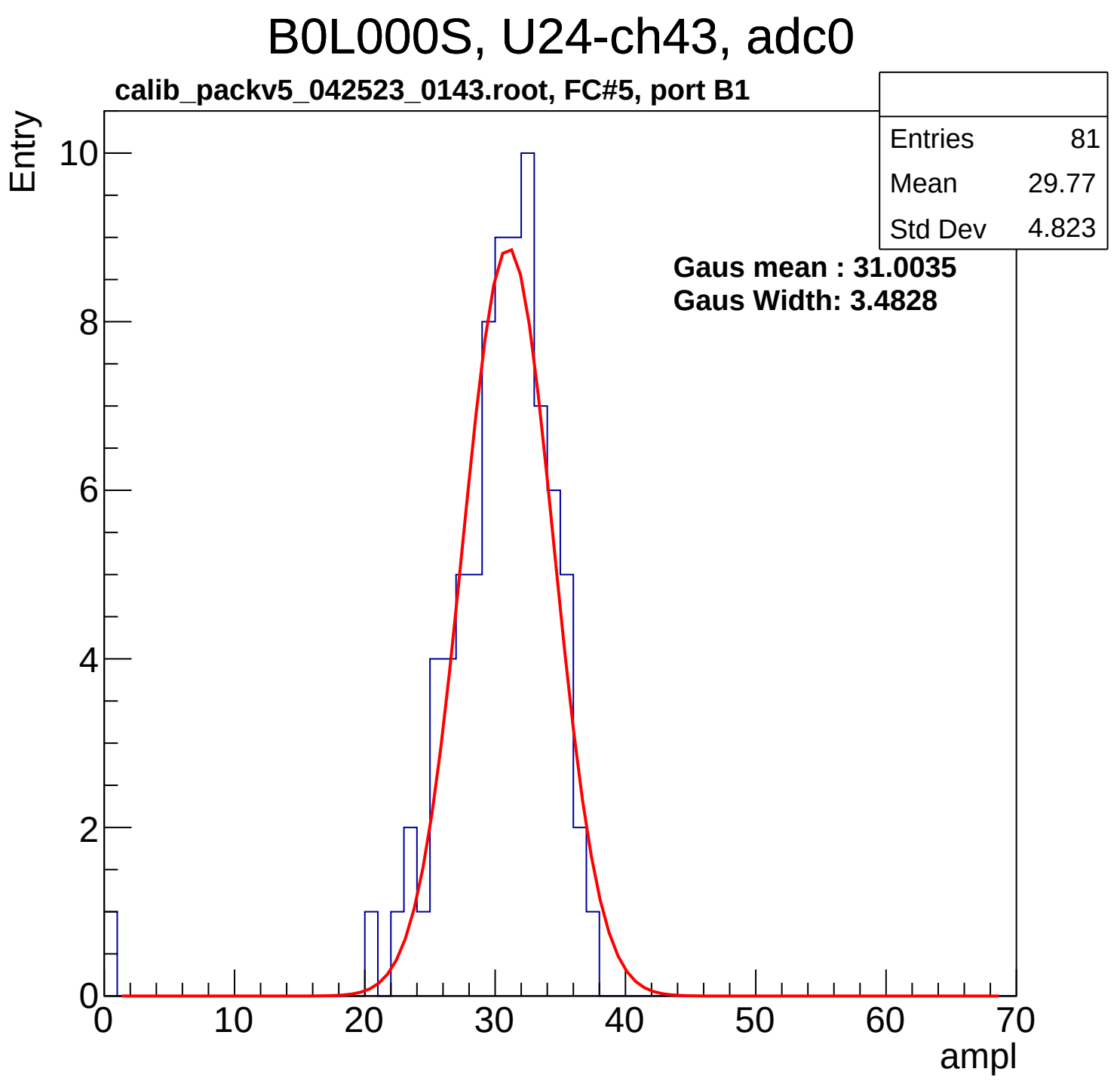
60
70
ampl

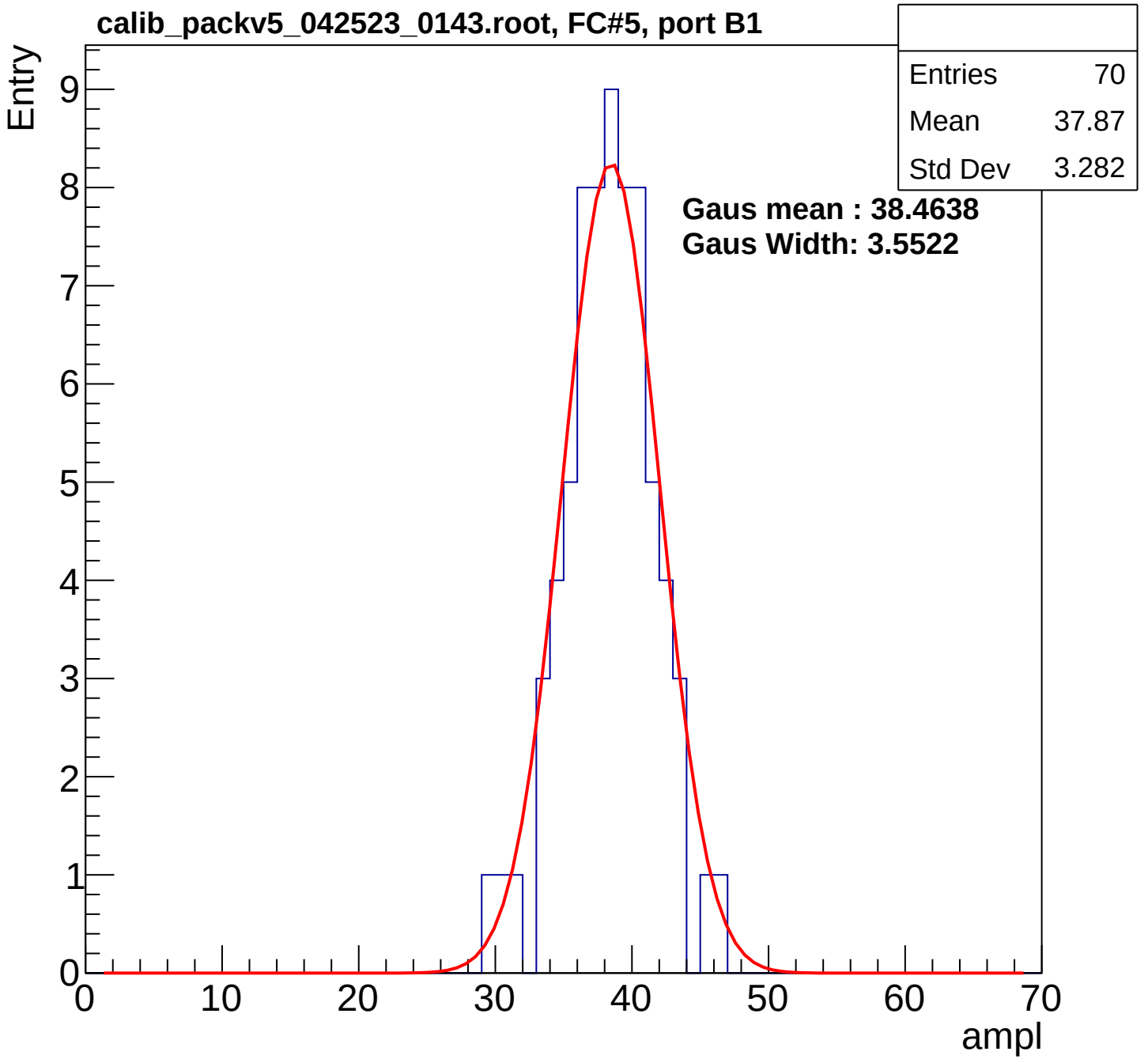

B0L000S, U24-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.51
Std Dev 2.934
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

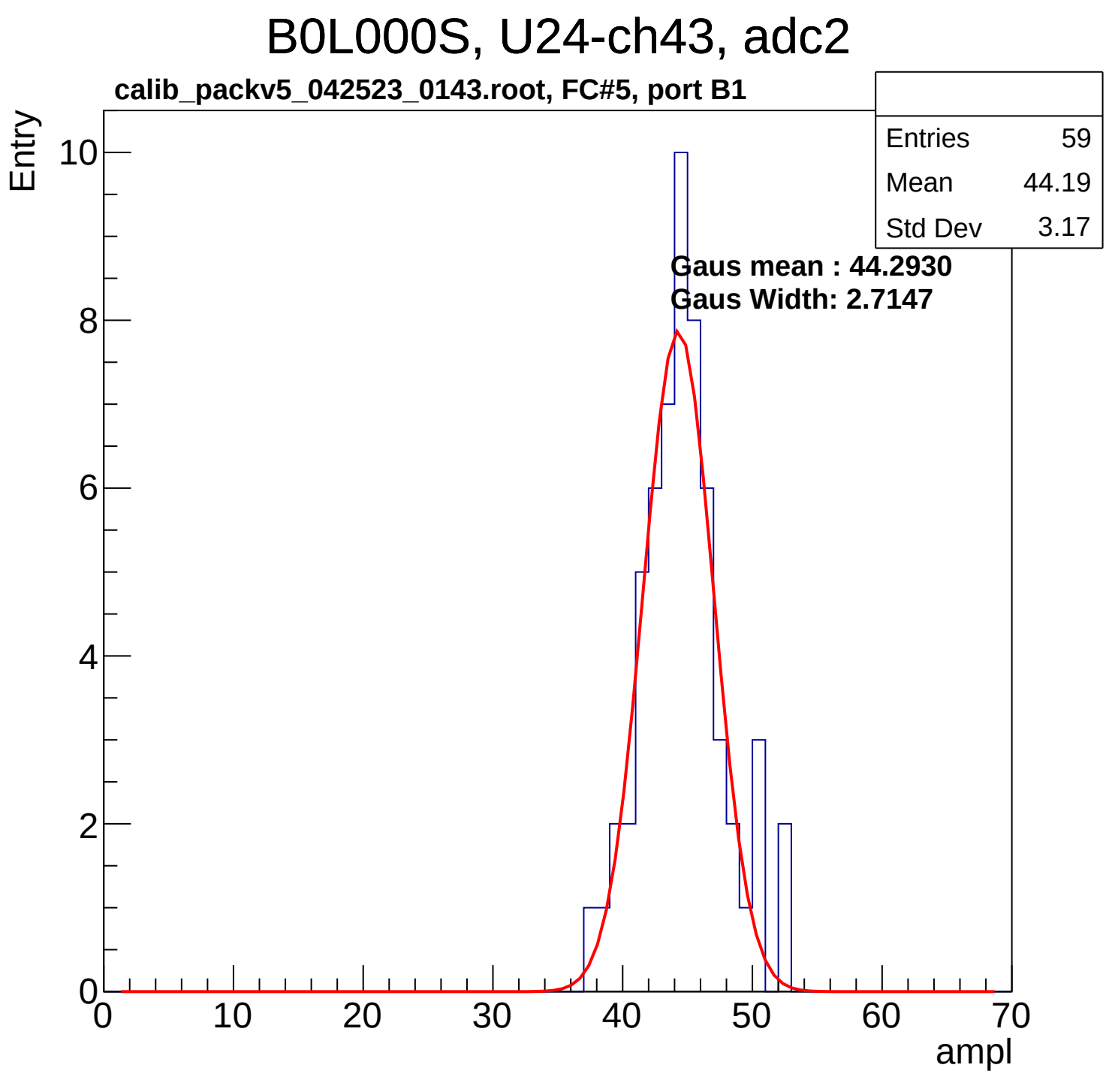

B0L000S, U24-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.66
Std Dev 2.519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

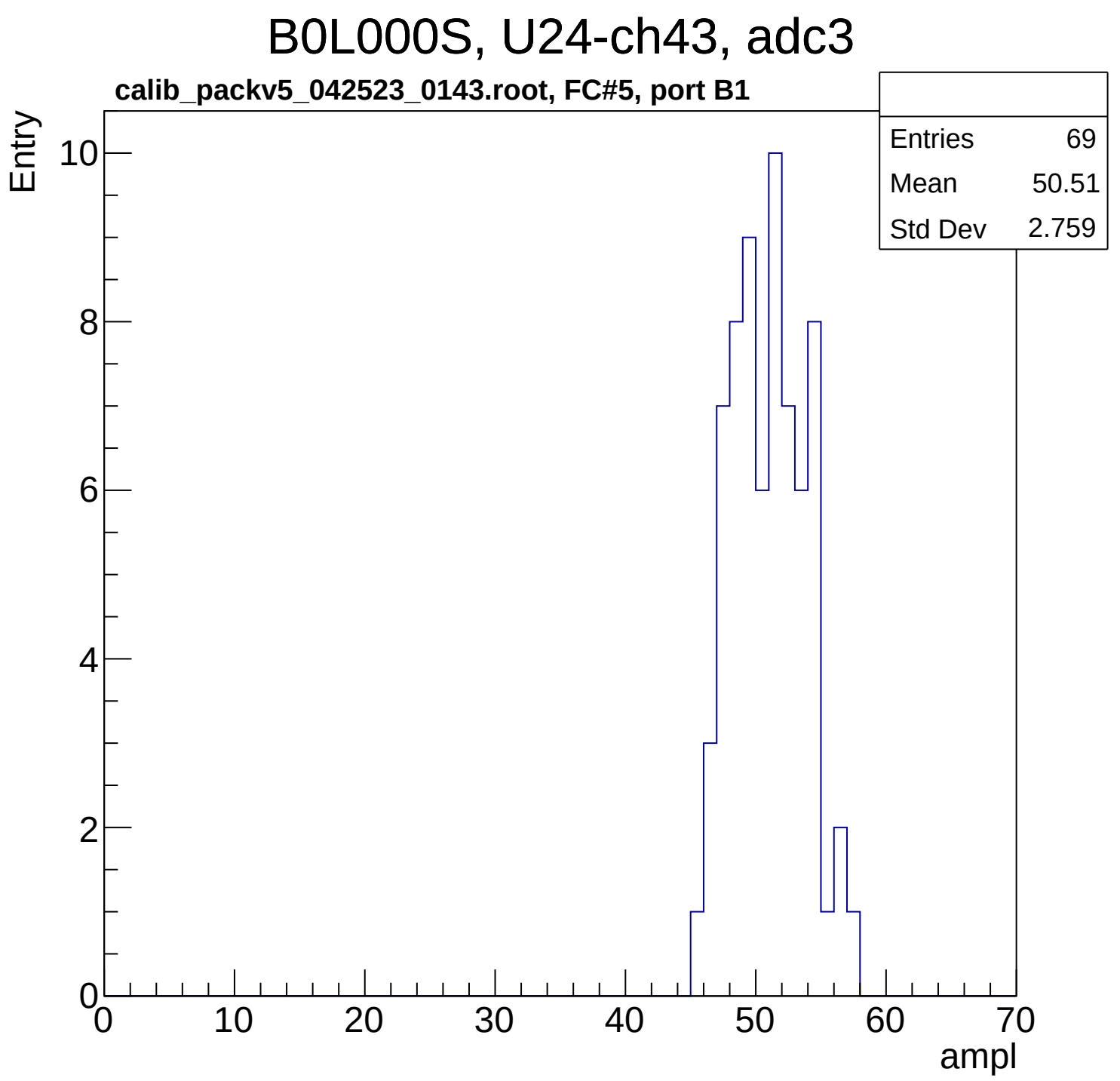

B0L000S, U24-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.5
Std Dev 18.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

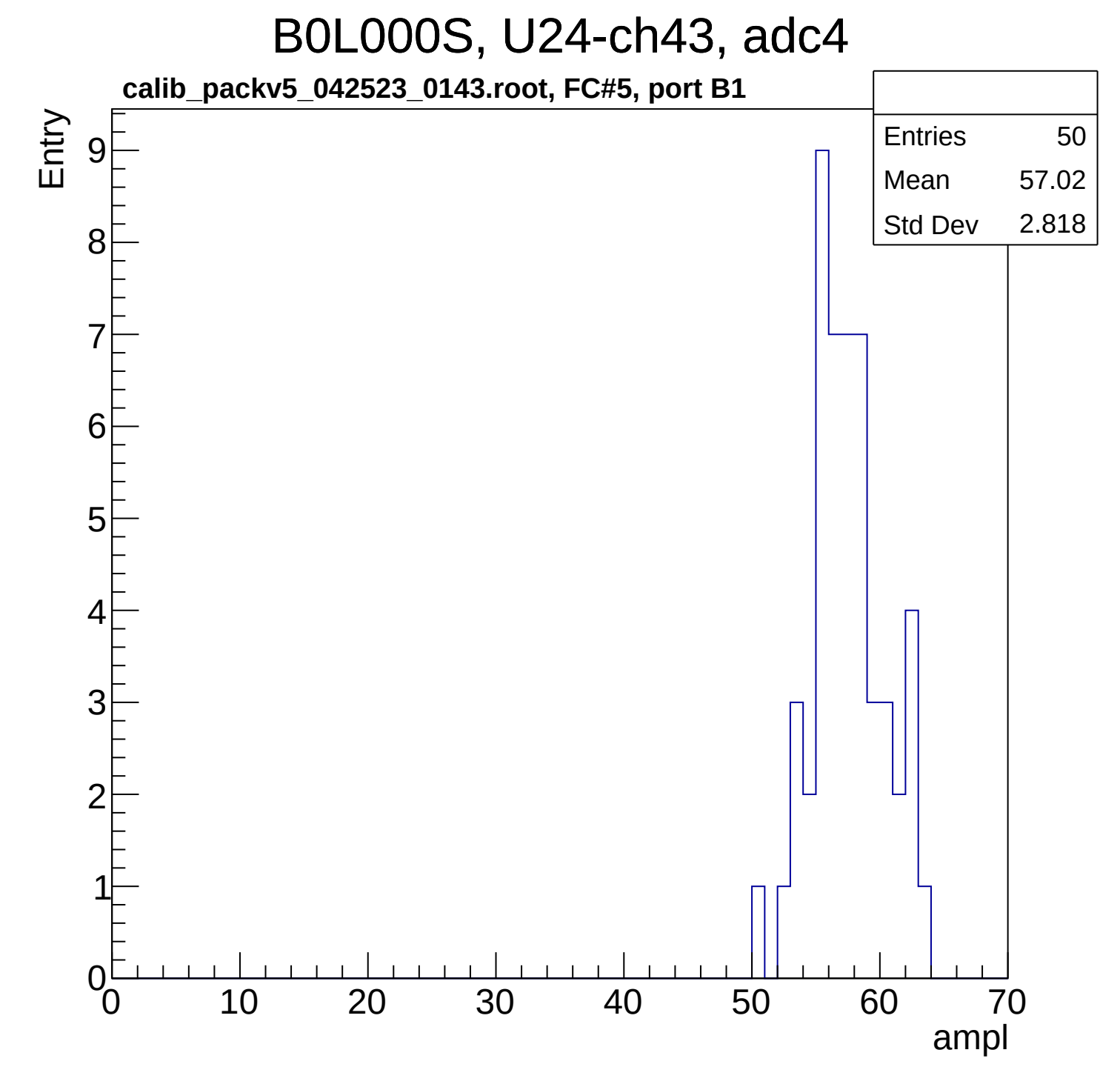

B0L000S, U24-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

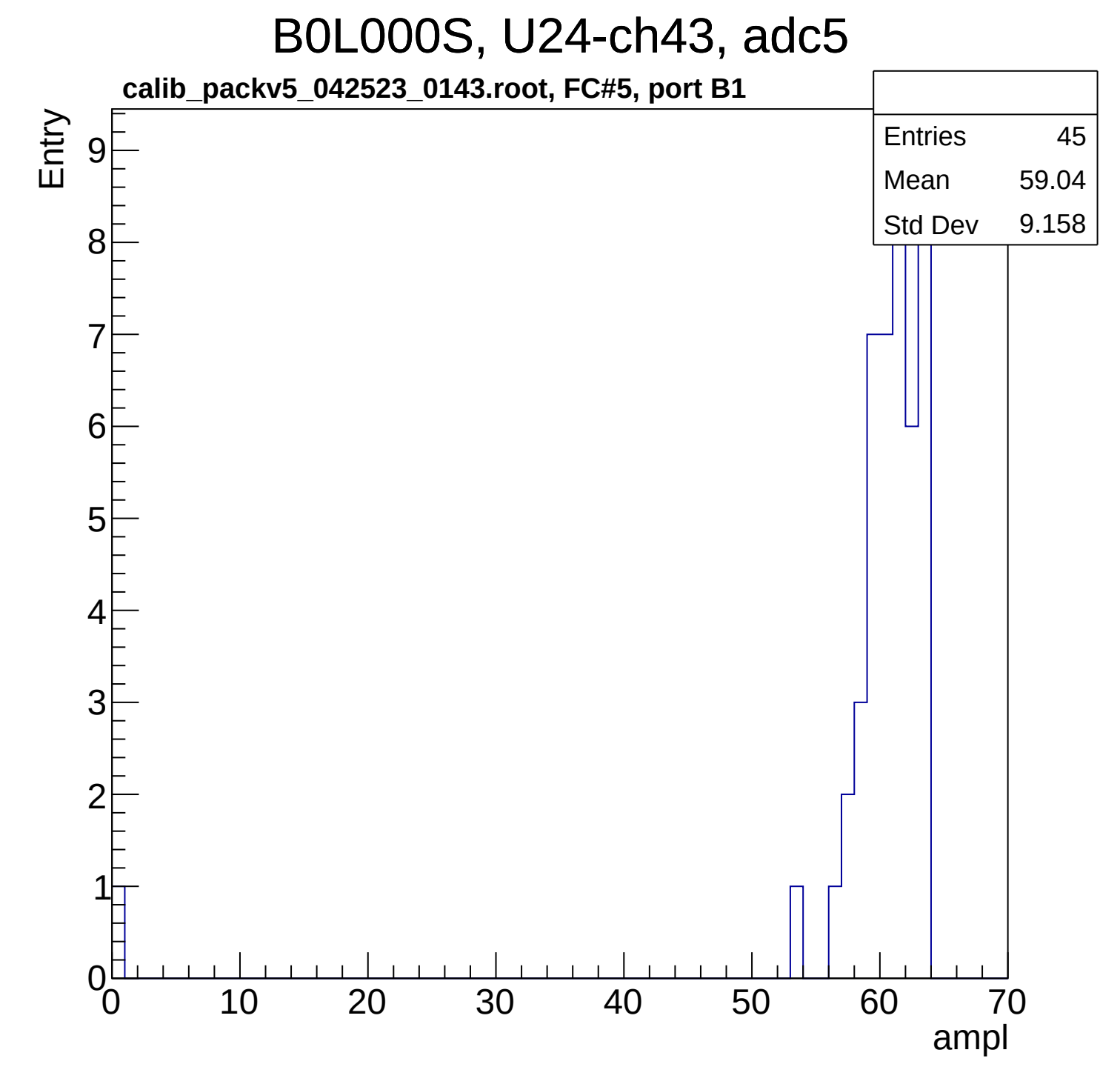

B0L000S, U24-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 29.77
Std Dev 4.823
Gaus mean : 31.0035
Gaus Width: 3.4828
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

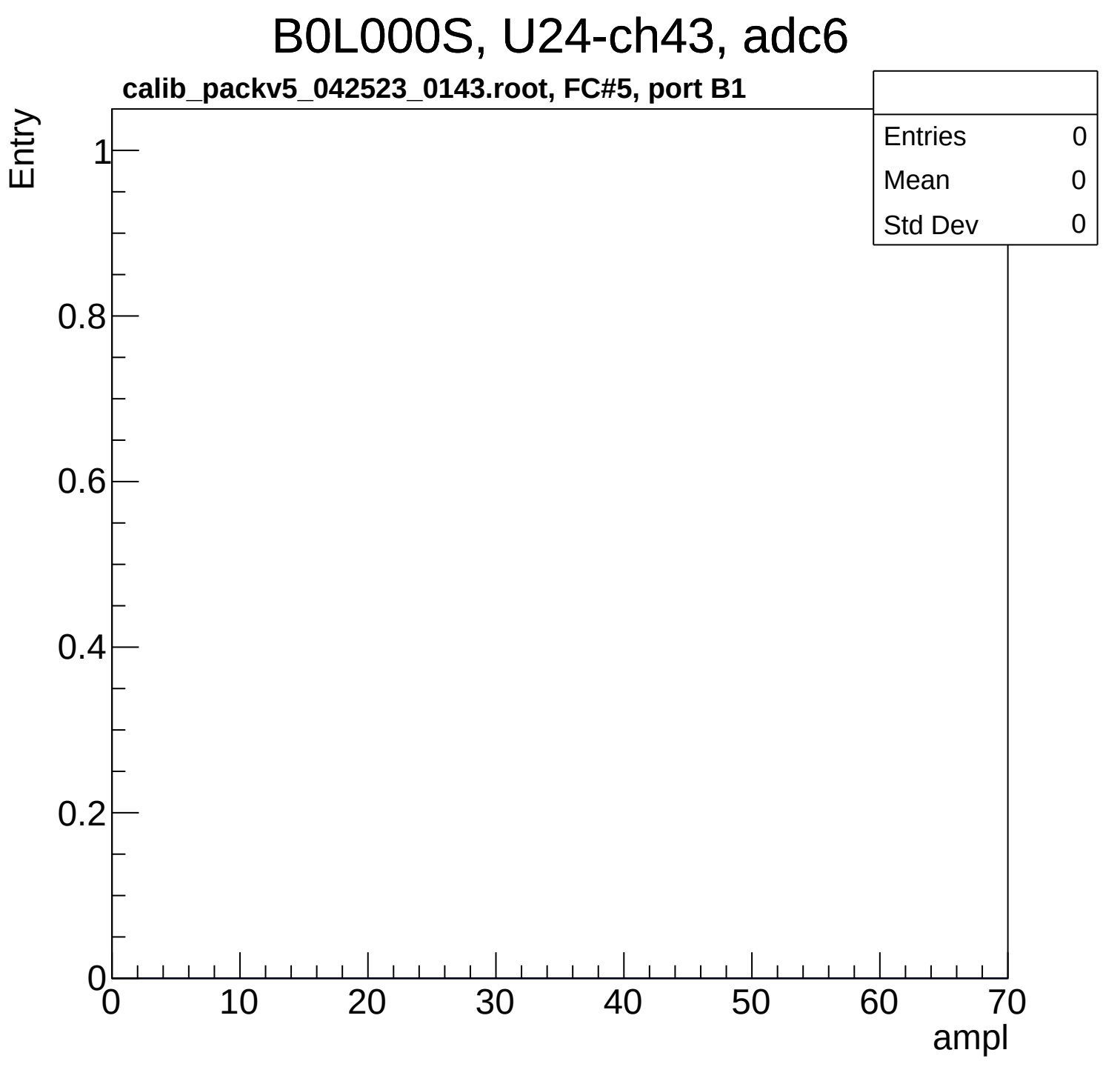

B0L000S, U24-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.87
Std Dev 3.282
Gaus mean : 38.4638
Gaus Width: 3.5522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 44.19
Std Dev 3.17
Gaus mean : 44.2930
Gaus Width: 2.7147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.51
Std Dev 2.759
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.02
Std Dev 2.818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.04
Std Dev 9.158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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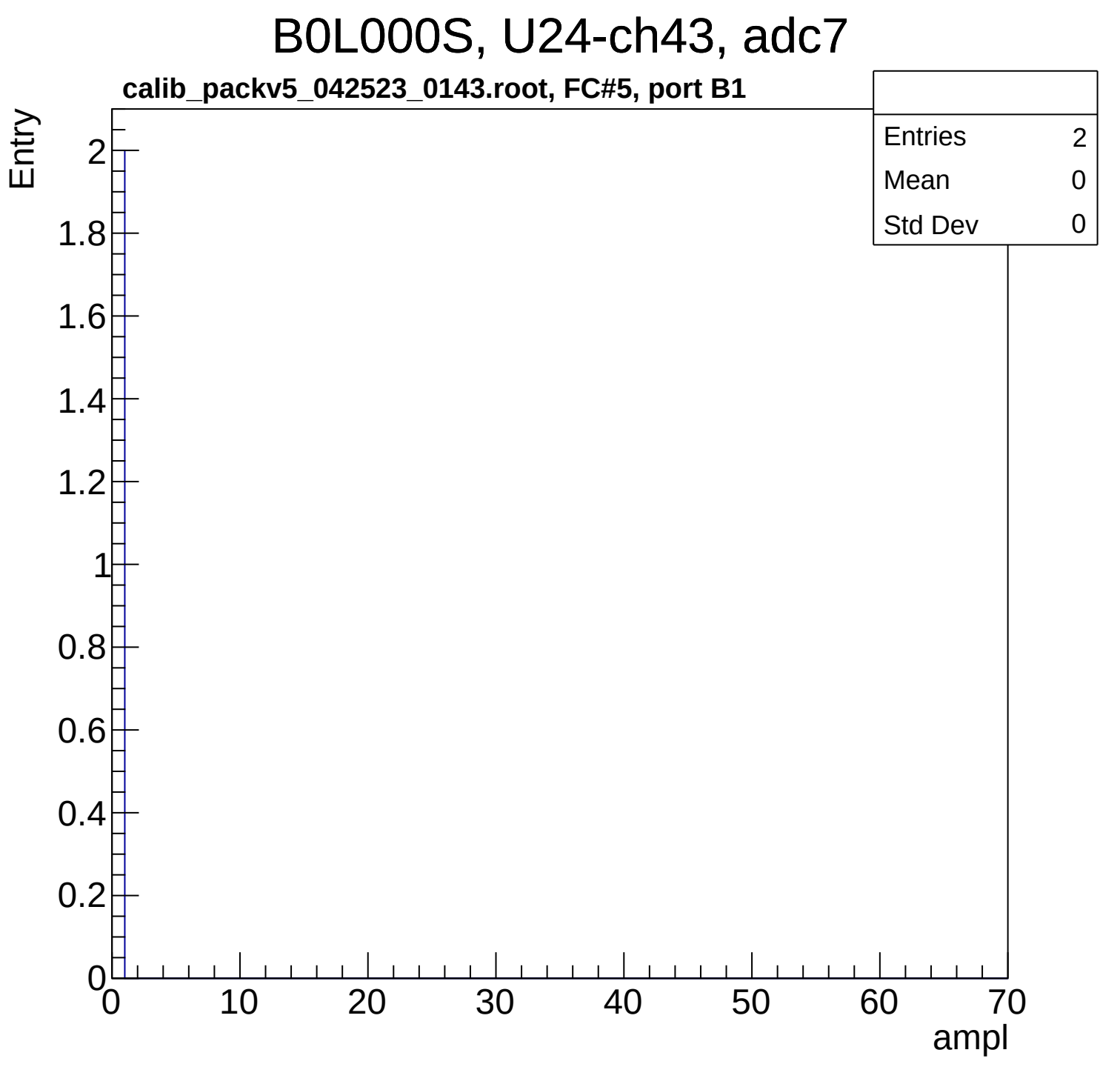
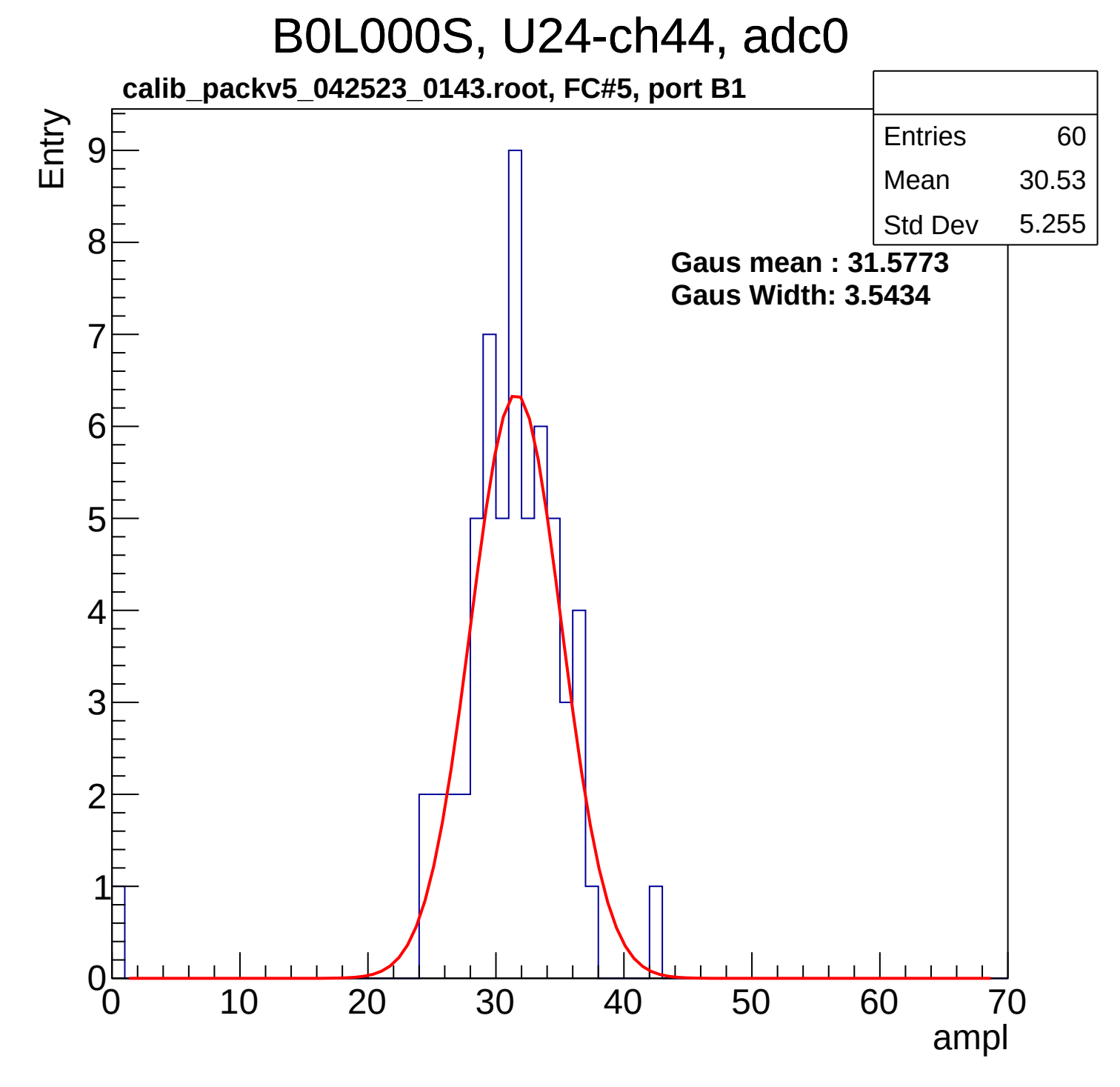
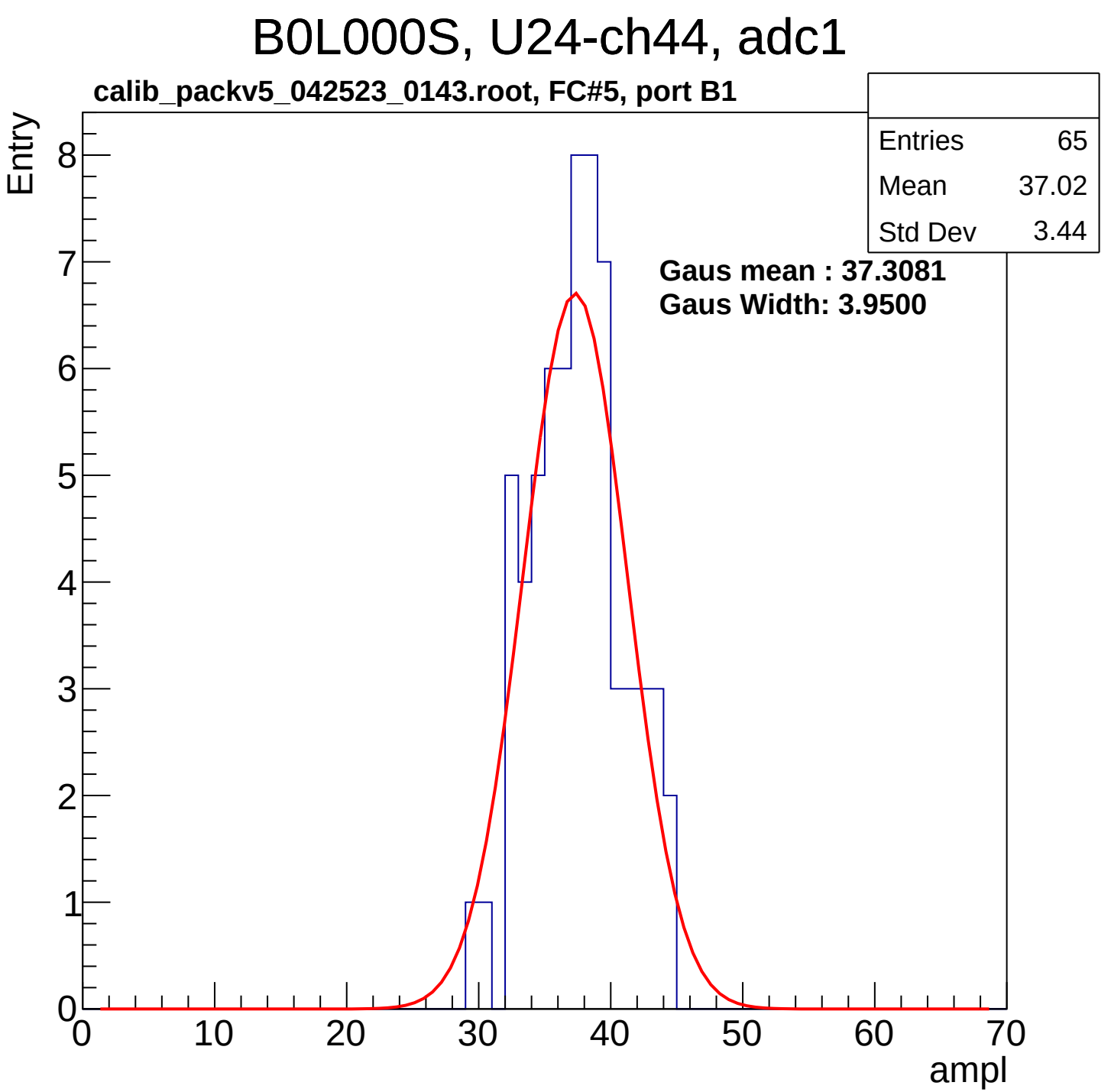
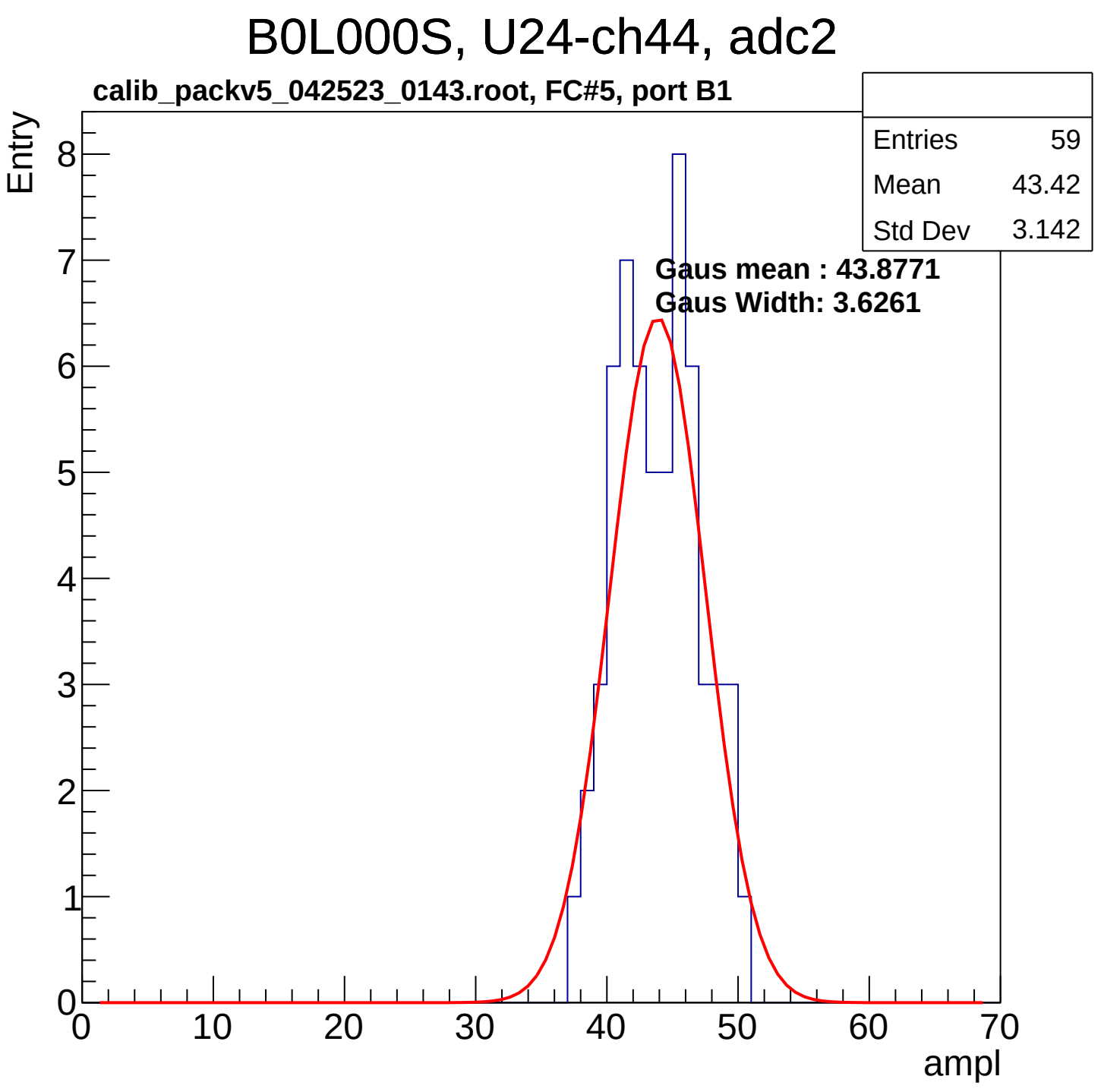
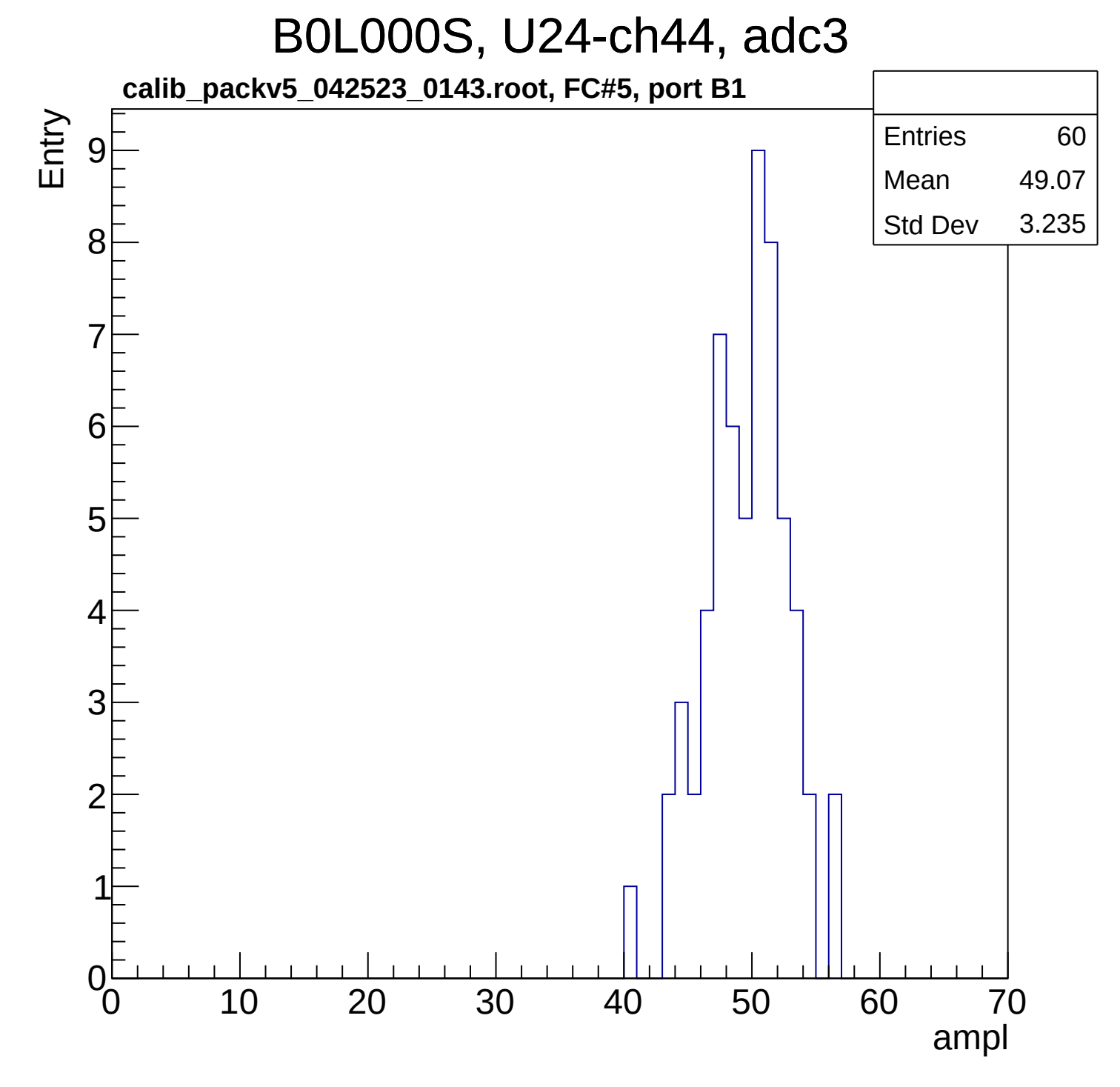
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

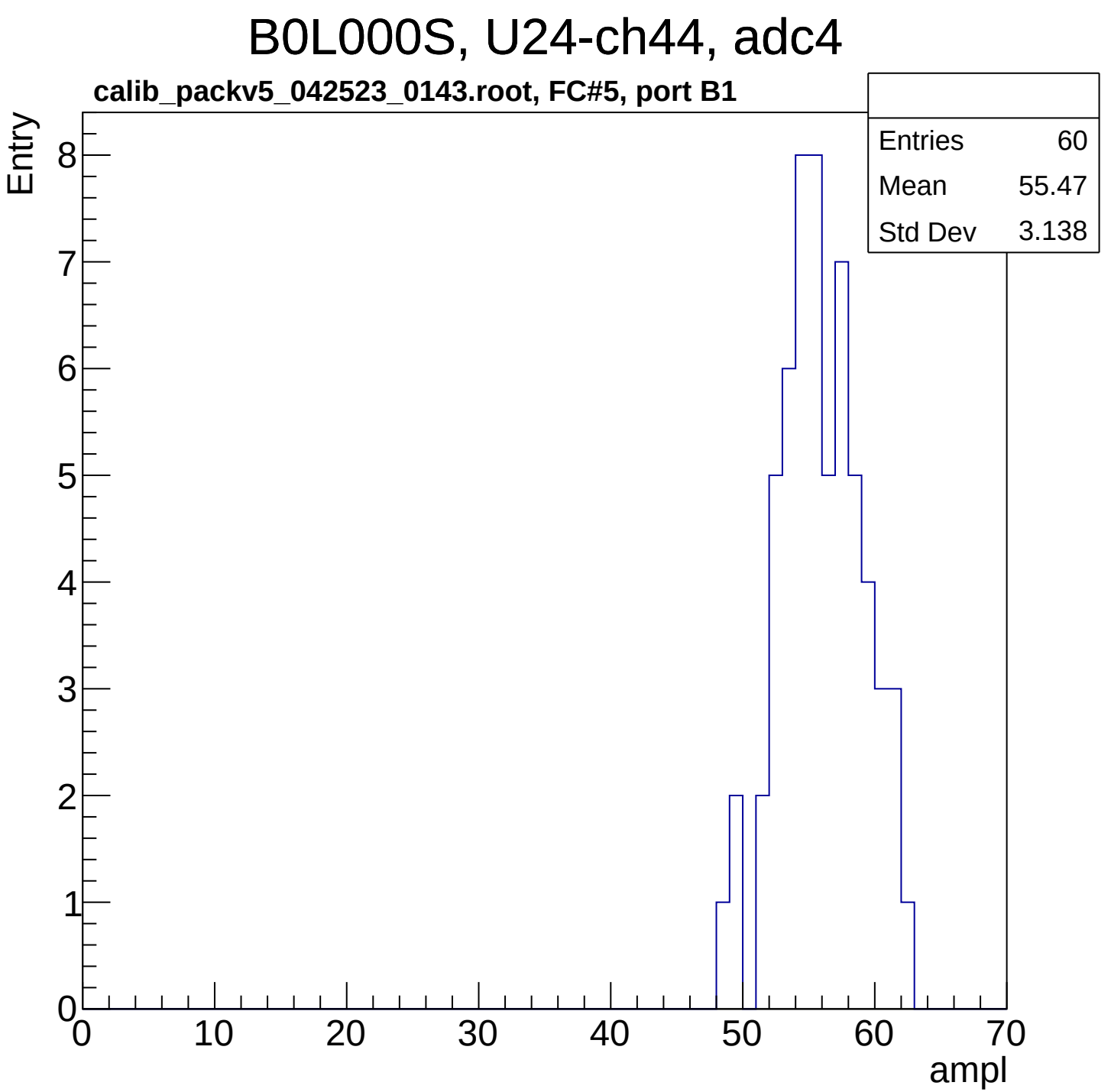

B0L000S, U24-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

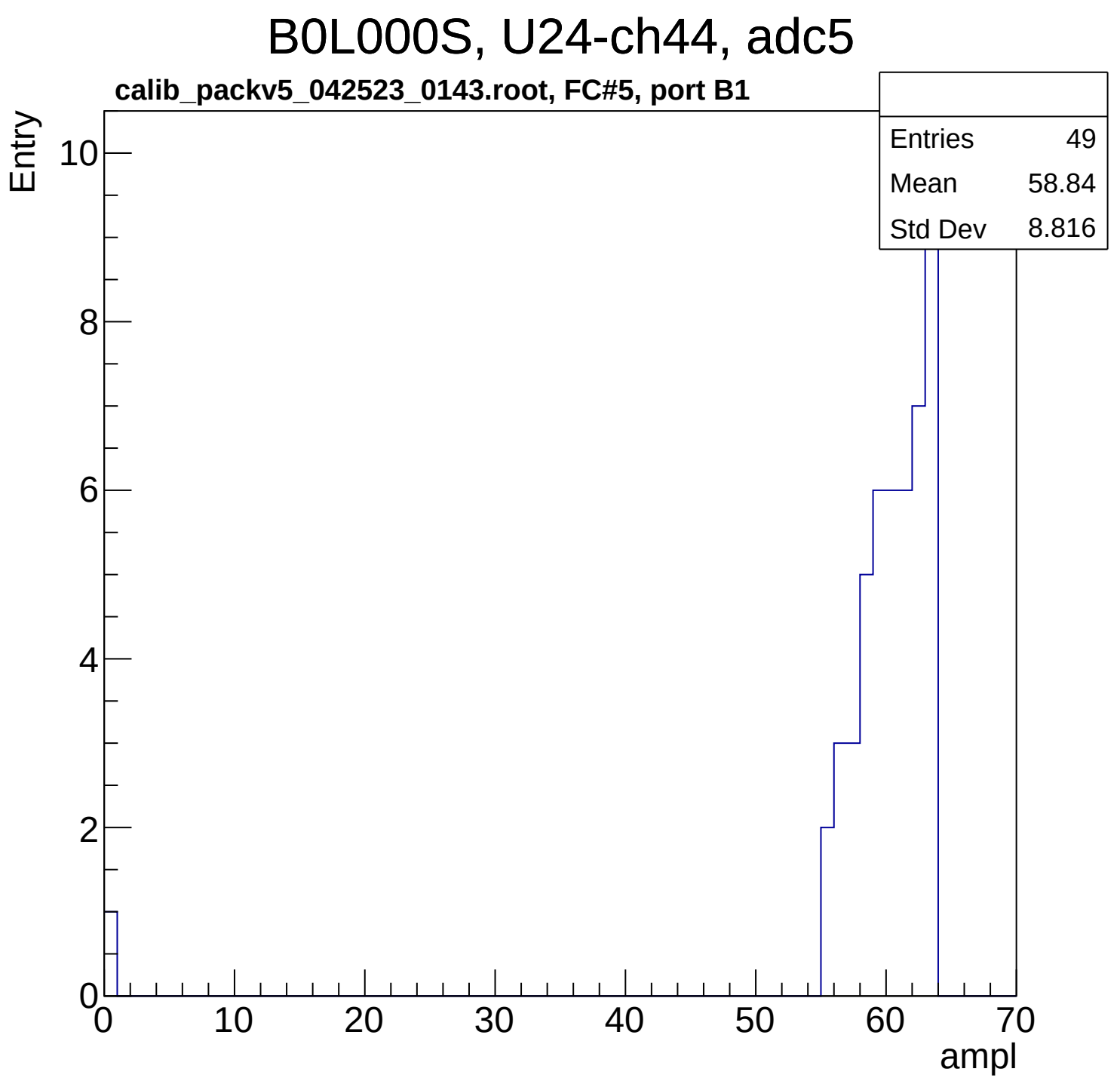

B0L000S, U24-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.53
Std Dev 5.255
Gaus mean : 31.5773
Gaus Width: 3.5434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.02
Std Dev 3.44
Gaus mean : 37.3081
Gaus Width: 3.9500
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.42
Std Dev 3.142
Gaus mean : 43.8771
Gaus Width: 3.6261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 49.07
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 55.47
Std Dev 3.138
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.84
Std Dev 8.816
Entry
0
2
4
6
8
10
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10
20
30
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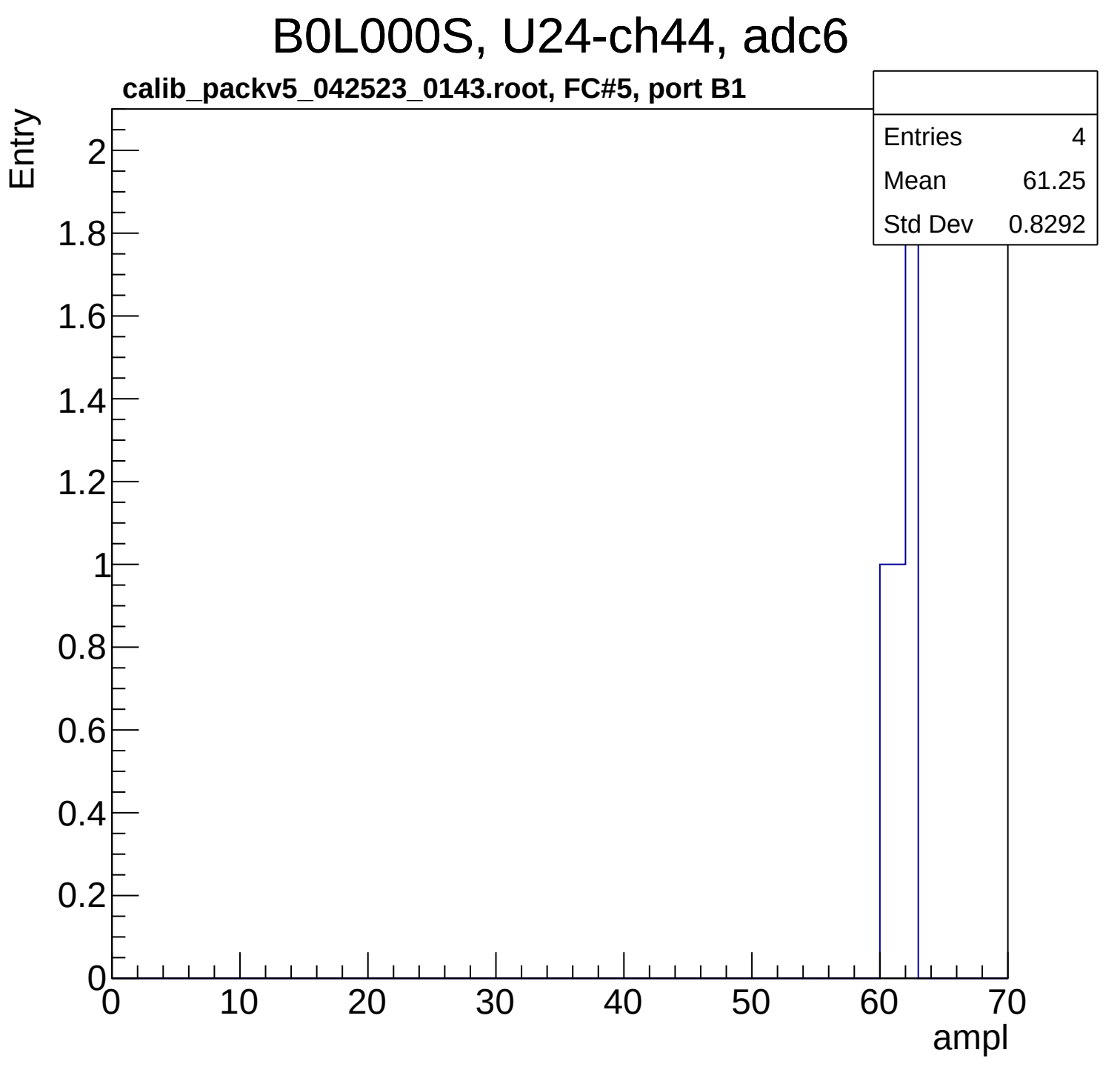
50
60
70
ampl


B0L000S, U24-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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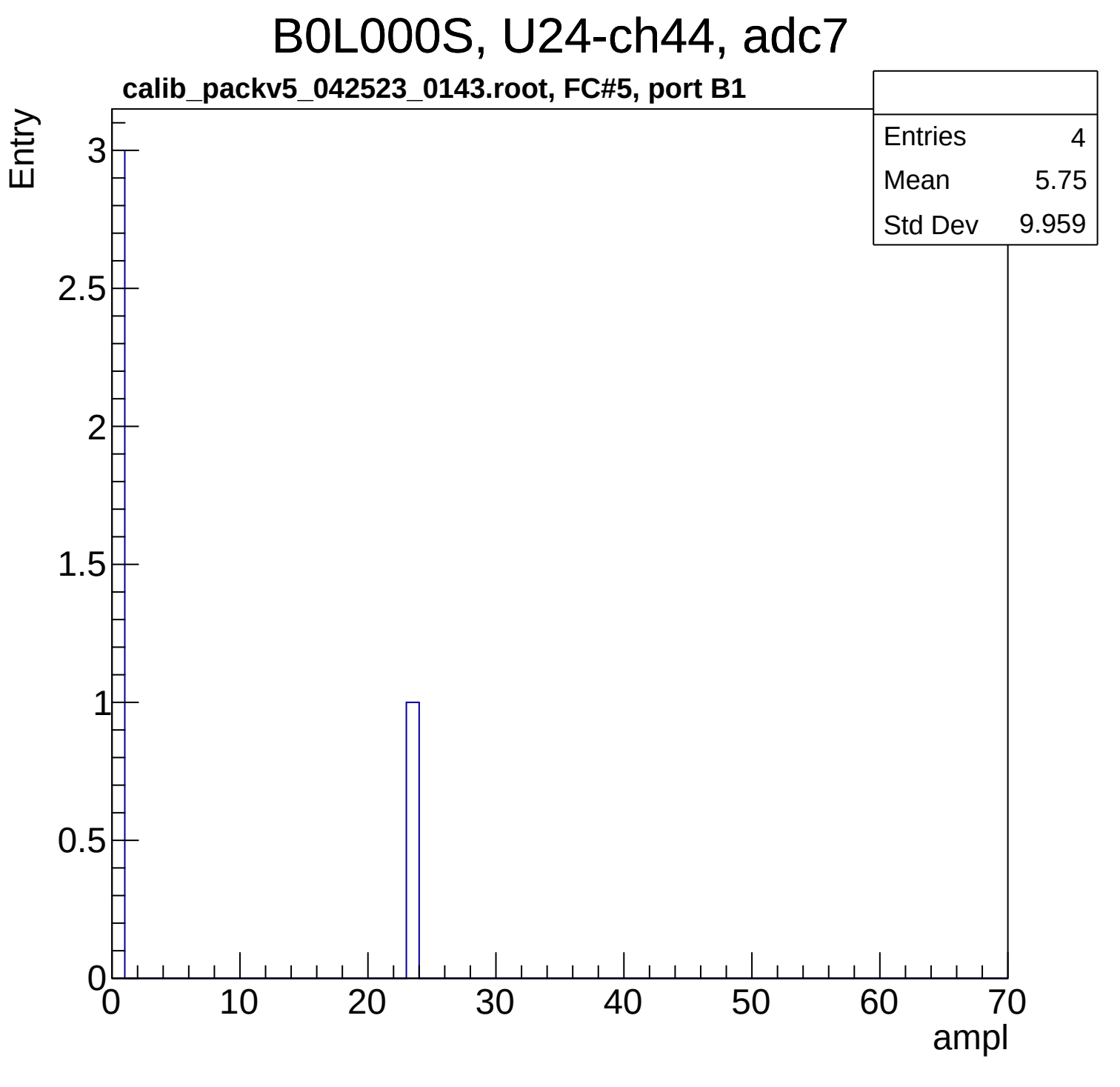
B0L000S, U24-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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40
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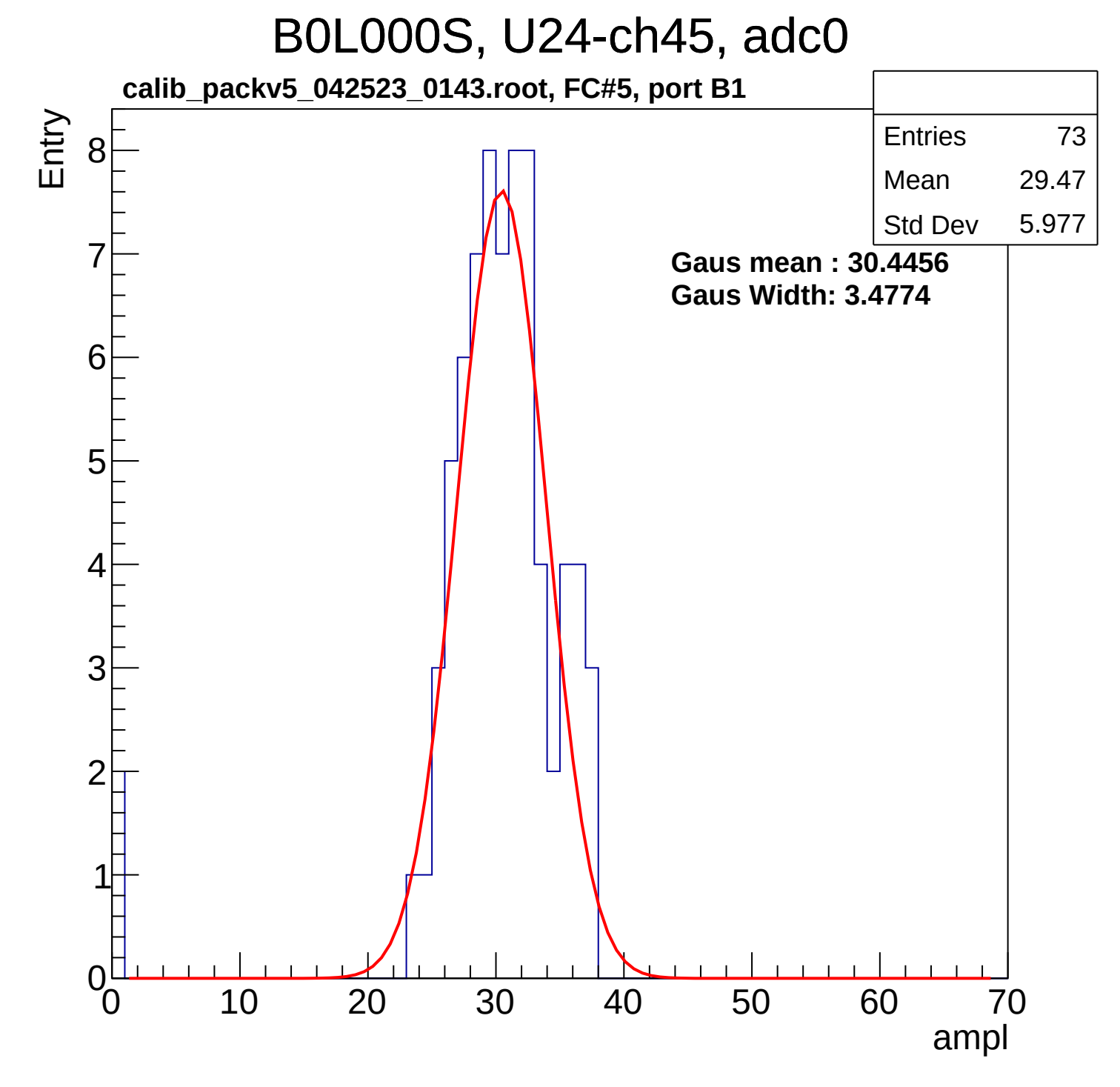
60
70


B0L000S, U24-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.47
Std Dev 5.977
Gaus mean : 30.4456
Gaus Width: 3.4774
Entry
ampl
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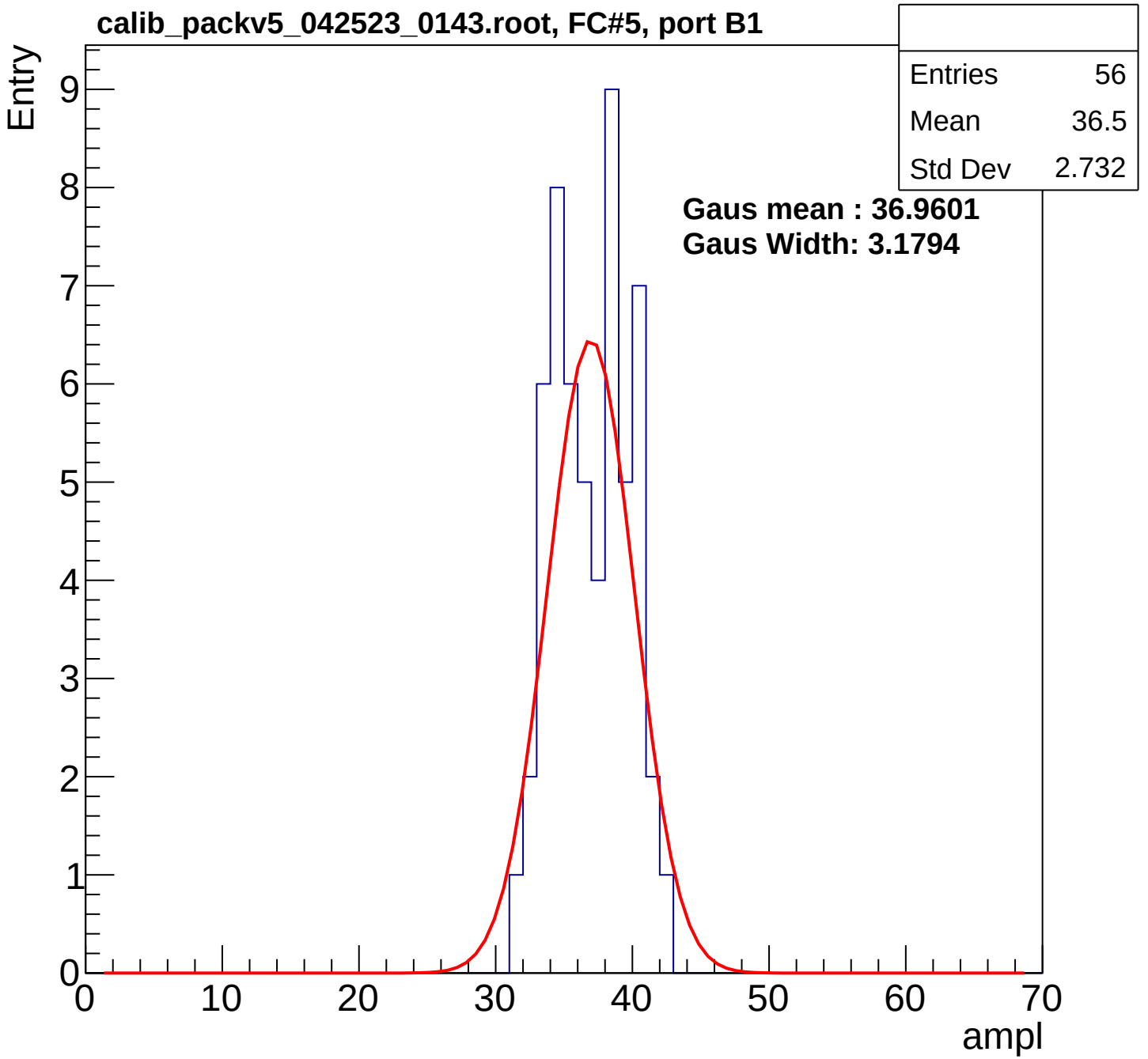
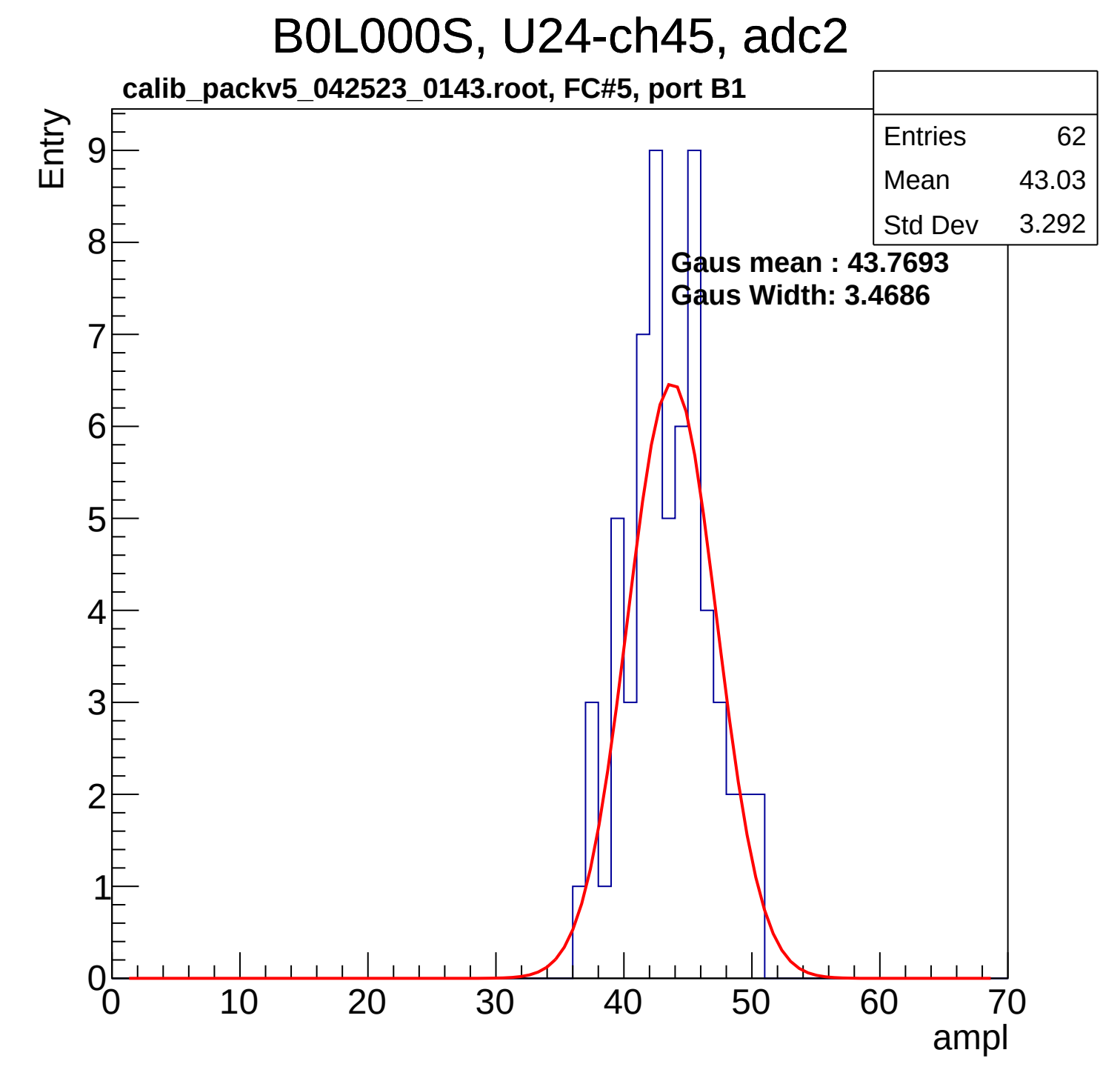
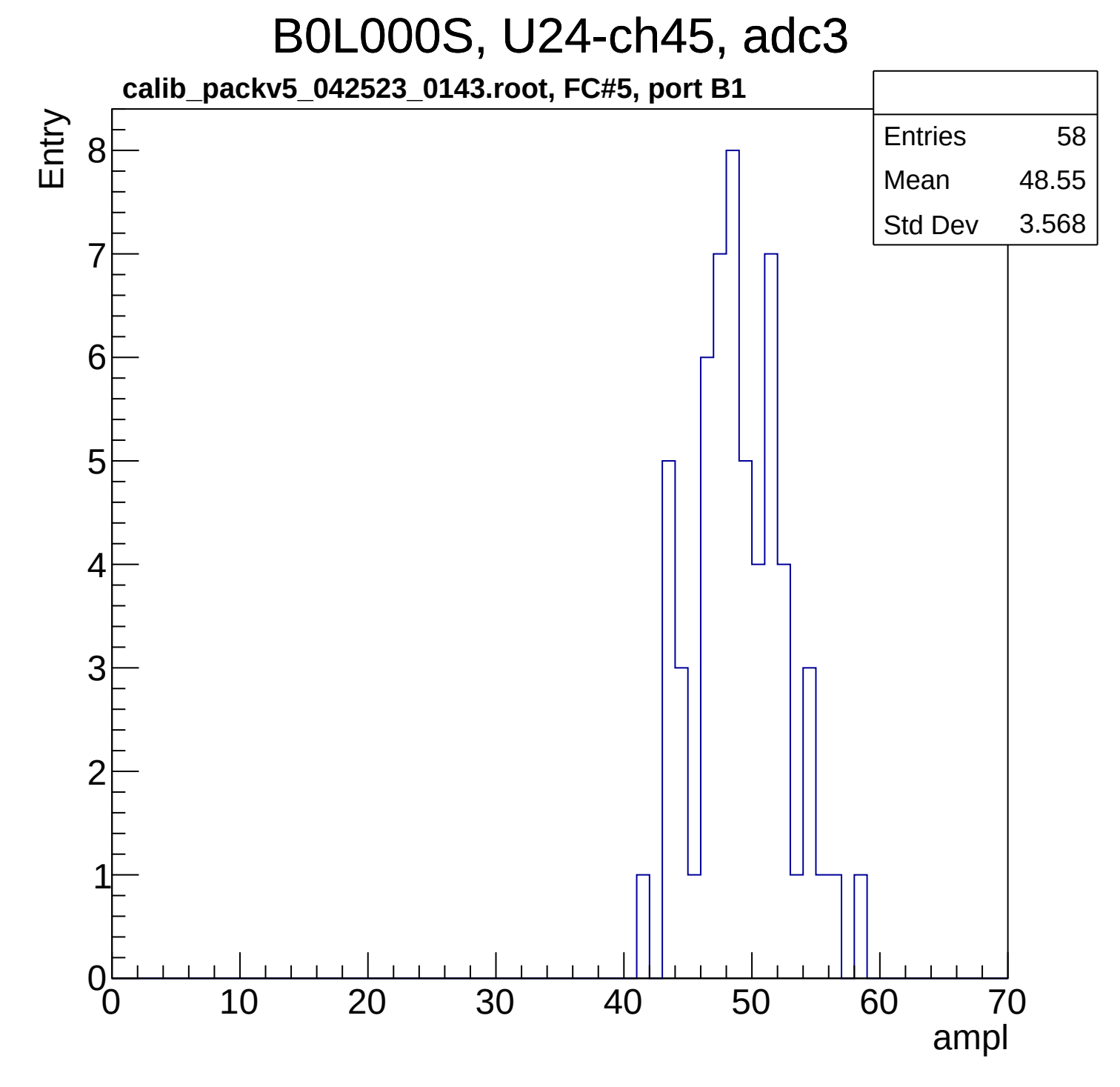
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.5
Std Dev 2.732
Gaus mean : 36.9601
Gaus Width: 3.1794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.03
Std Dev 3.292
Gaus mean : 43.7693
Gaus Width: 3.4686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.55
Std Dev 3.568
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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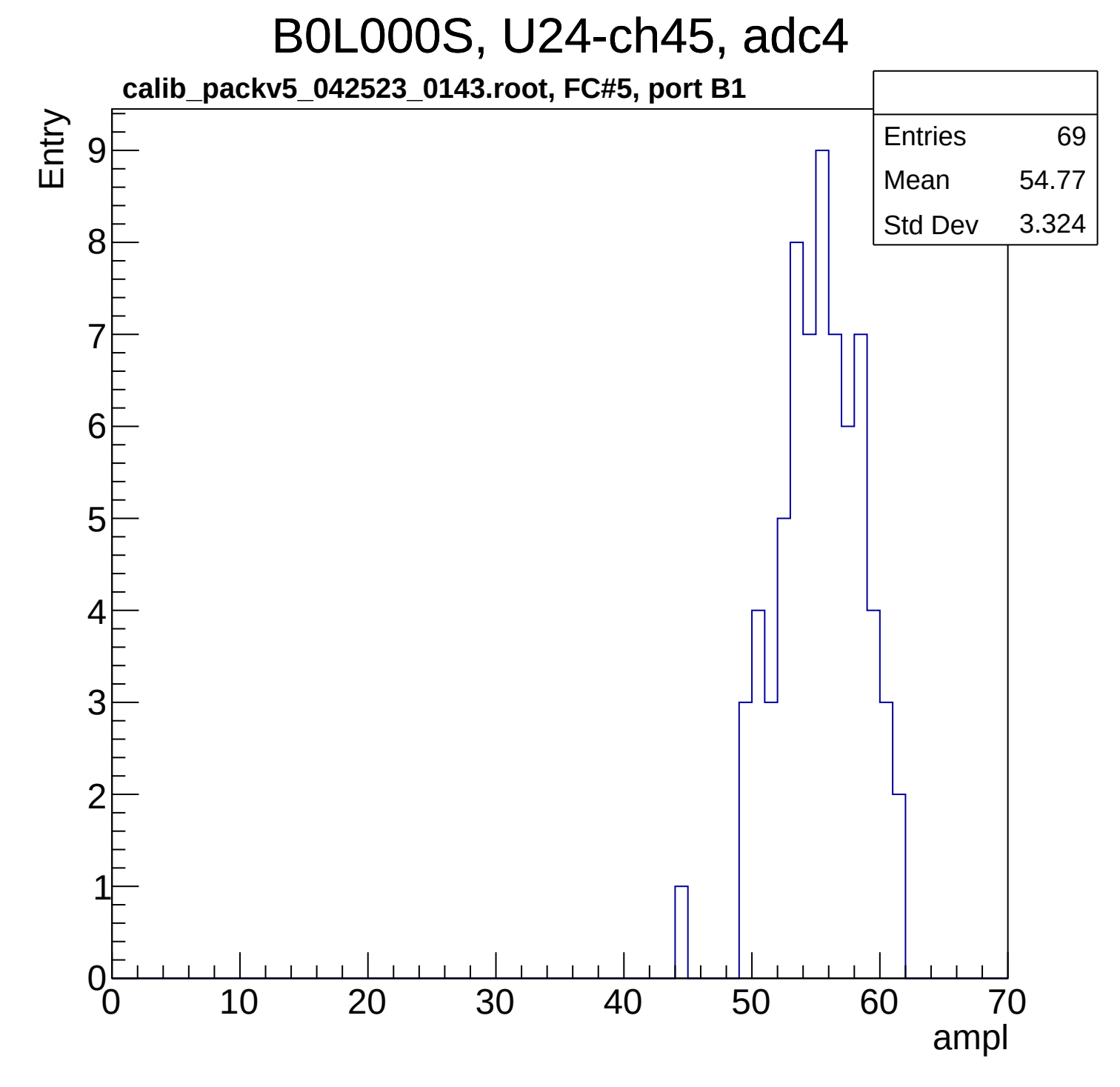
B0L000S, U24-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 54.77
Std Dev 3.324
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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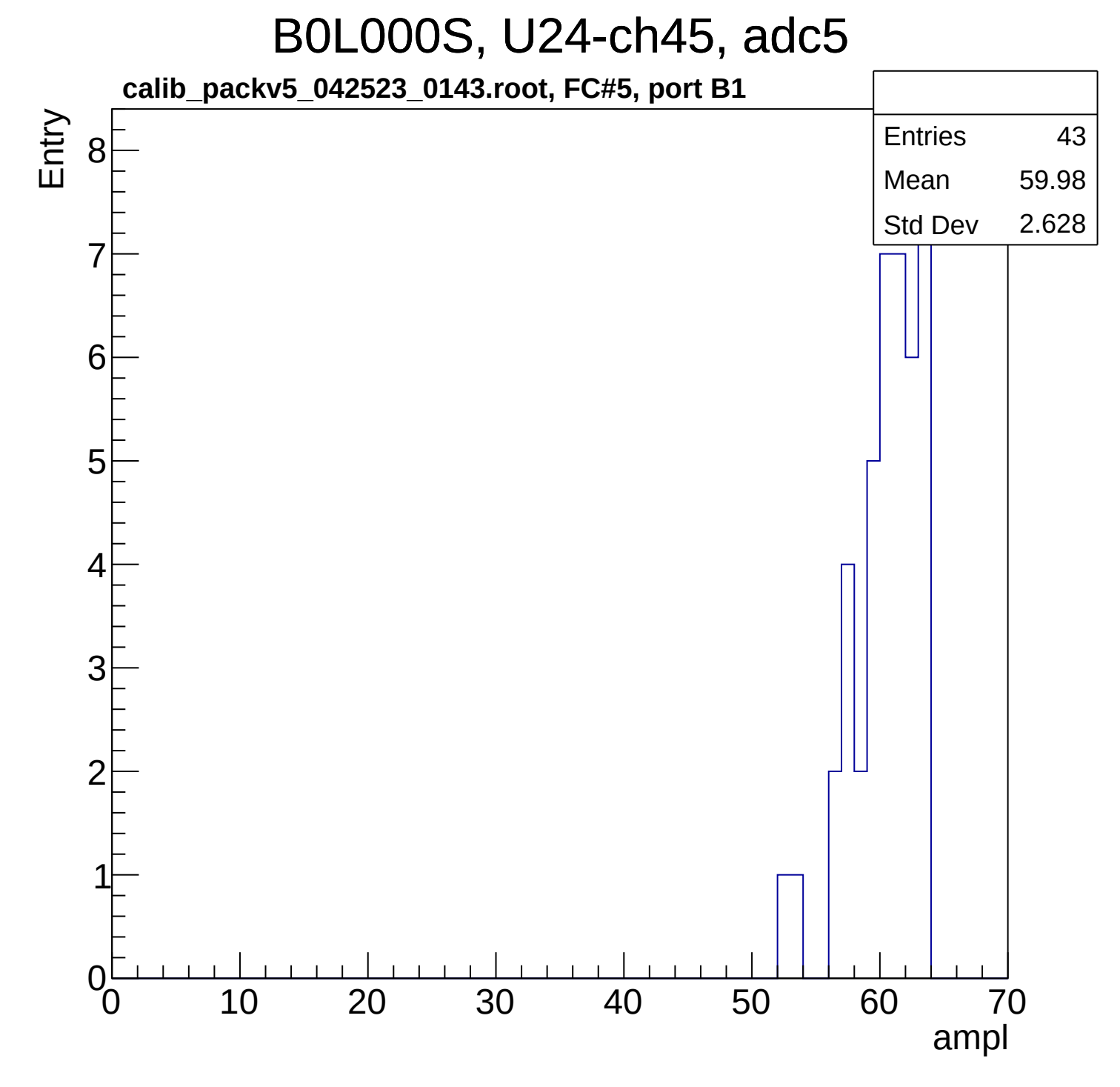
B0L000S, U24-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.98
Std Dev 2.628
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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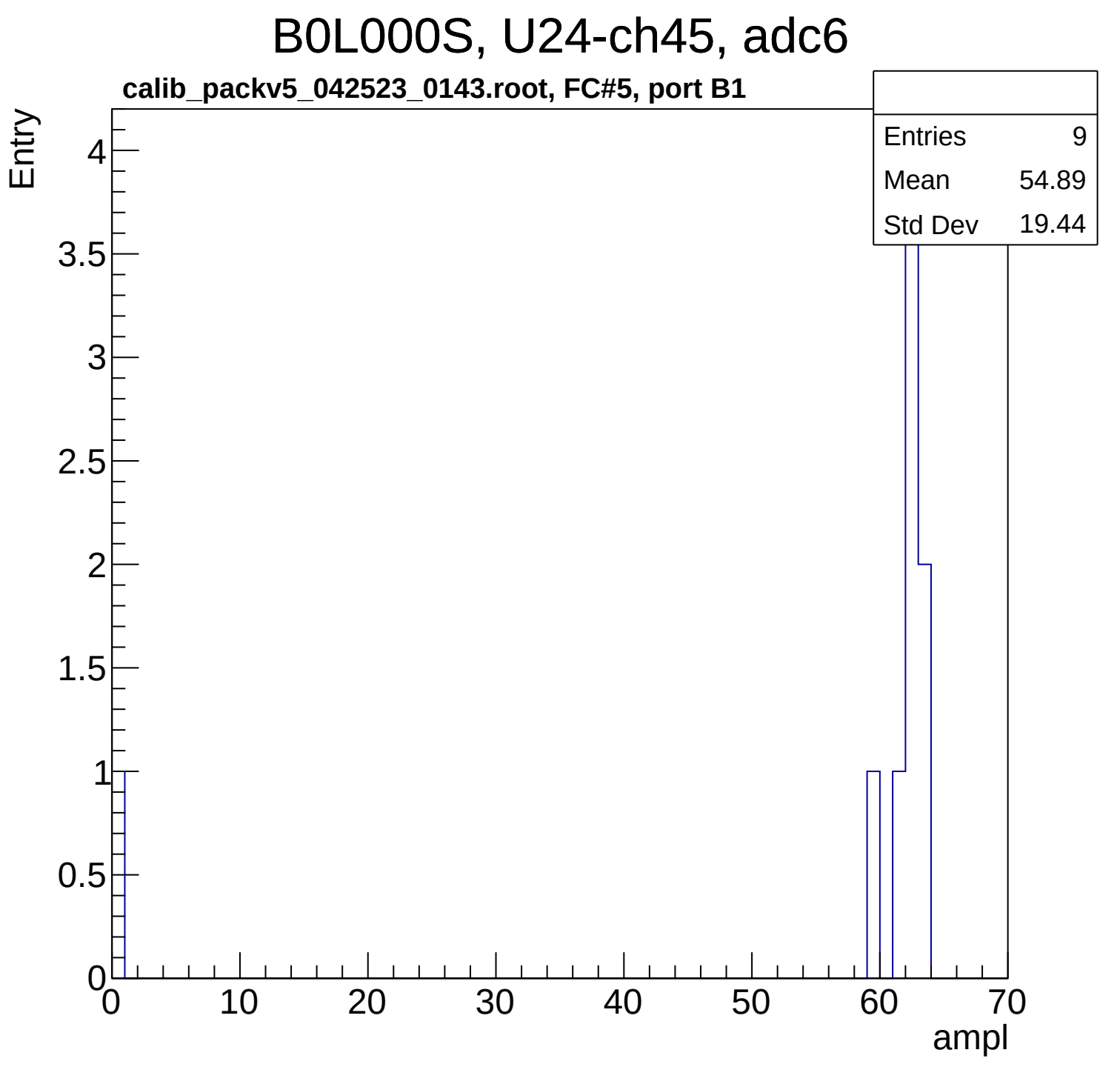
B0L000S, U24-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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60
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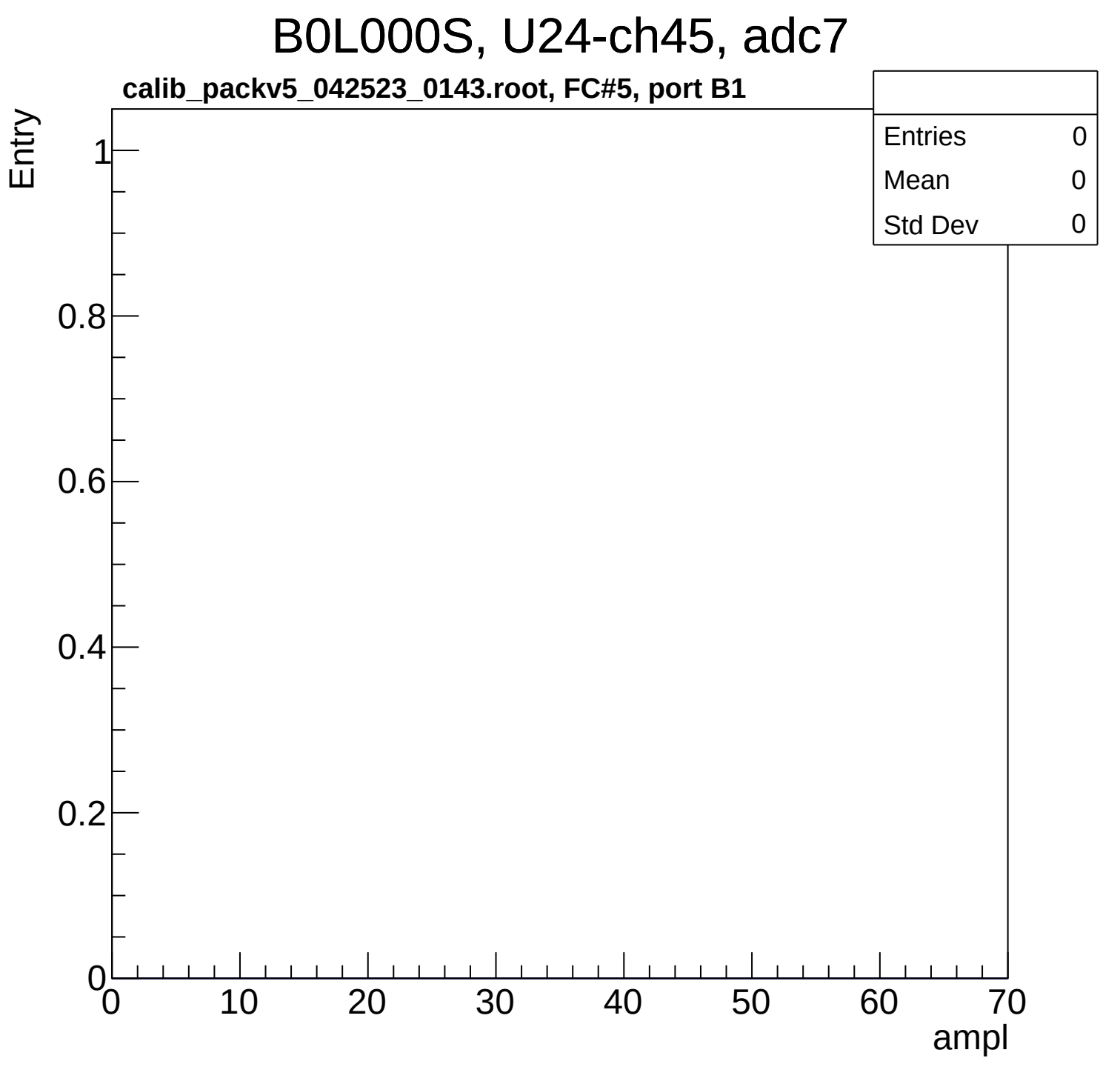
B0L000S, U24-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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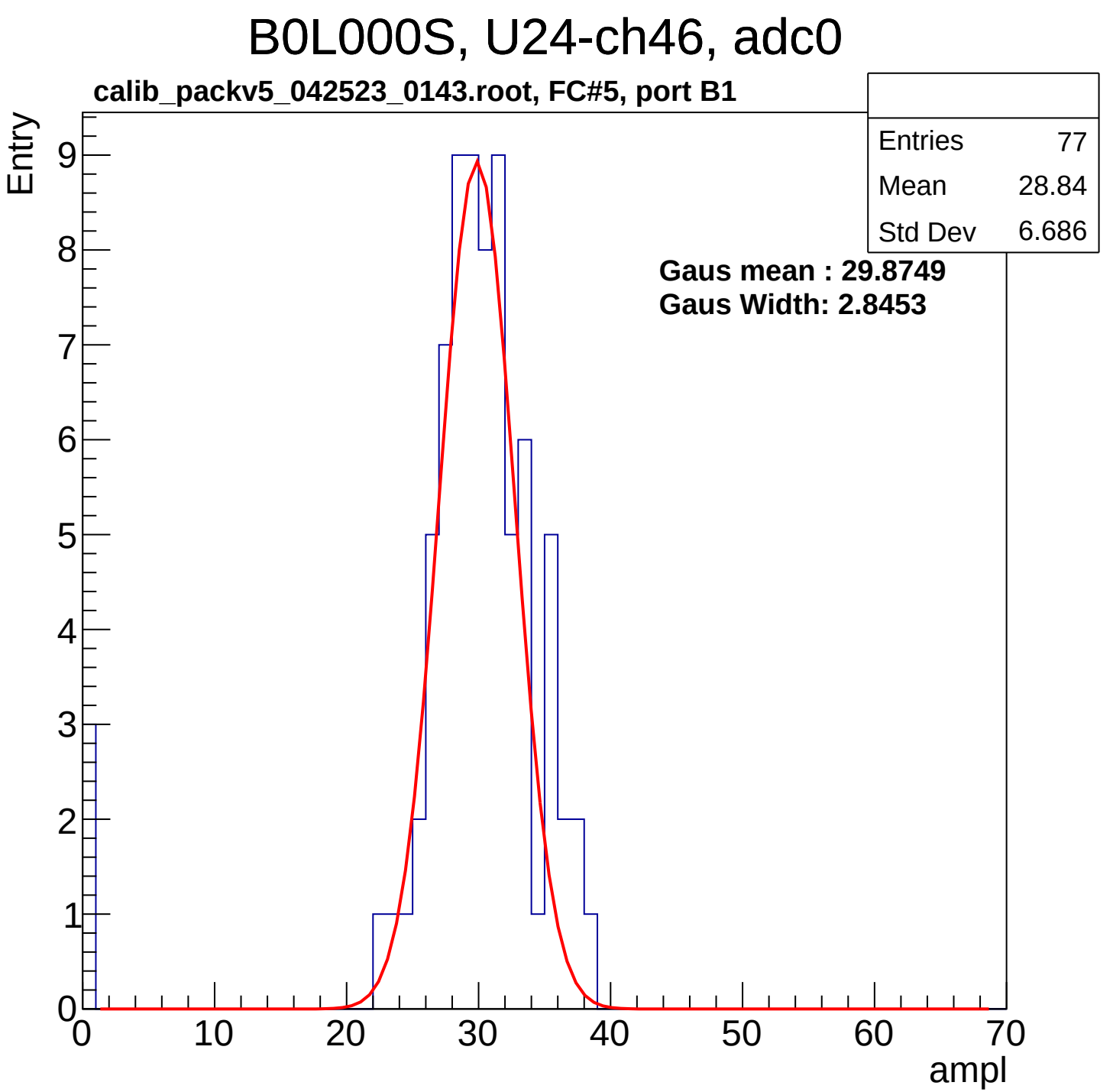
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.84
Std Dev 6.686
Gaus mean : 29.8749
Gaus Width: 2.8453
Entry
ampl
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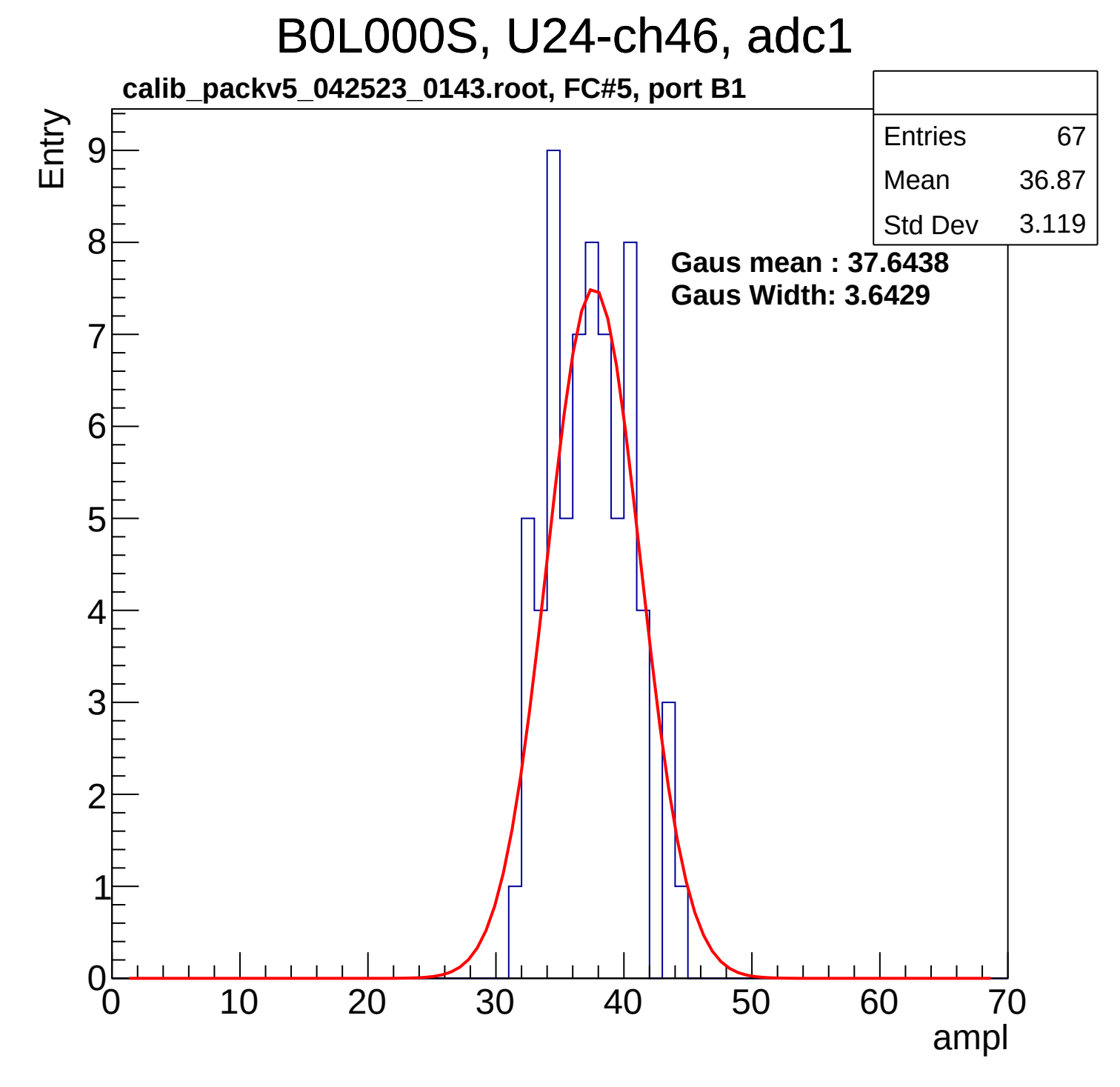
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.87
Std Dev 3.119
Gaus mean : 37.6438
Gaus Width: 3.6429
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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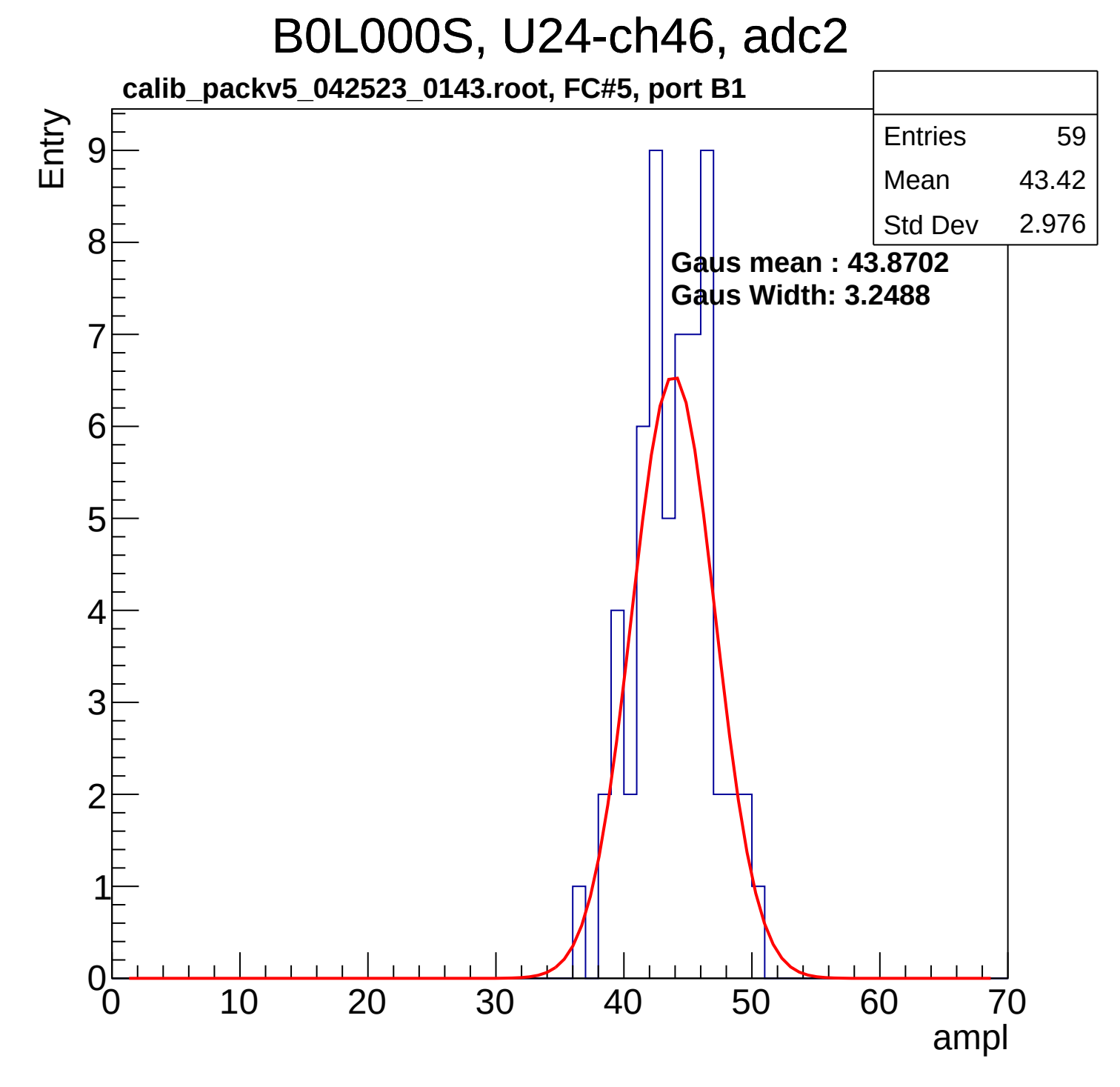
B0L000S, U24-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.42
Std Dev 2.976
Gaus mean : 43.8702
Gaus Width: 3.2488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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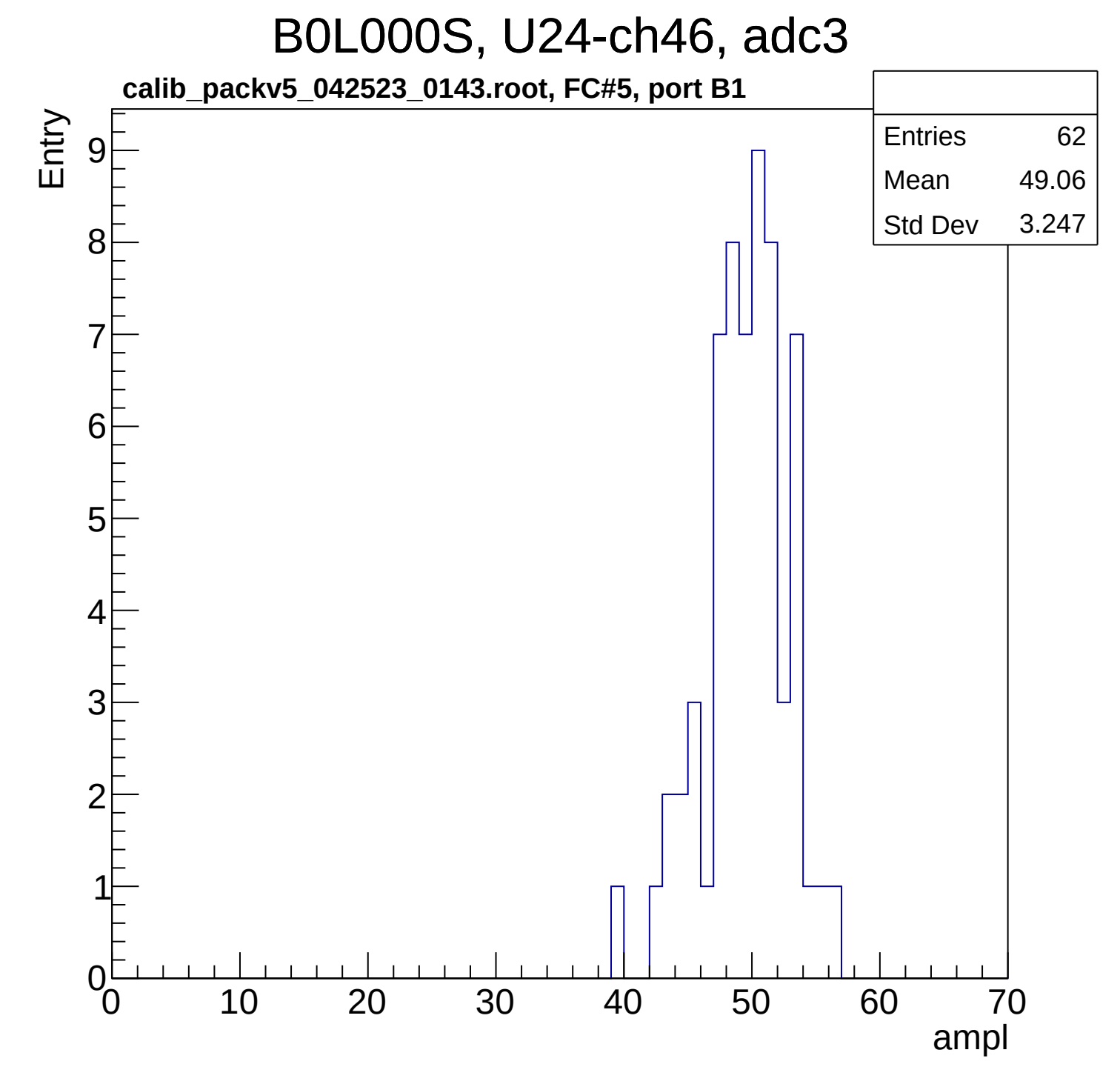
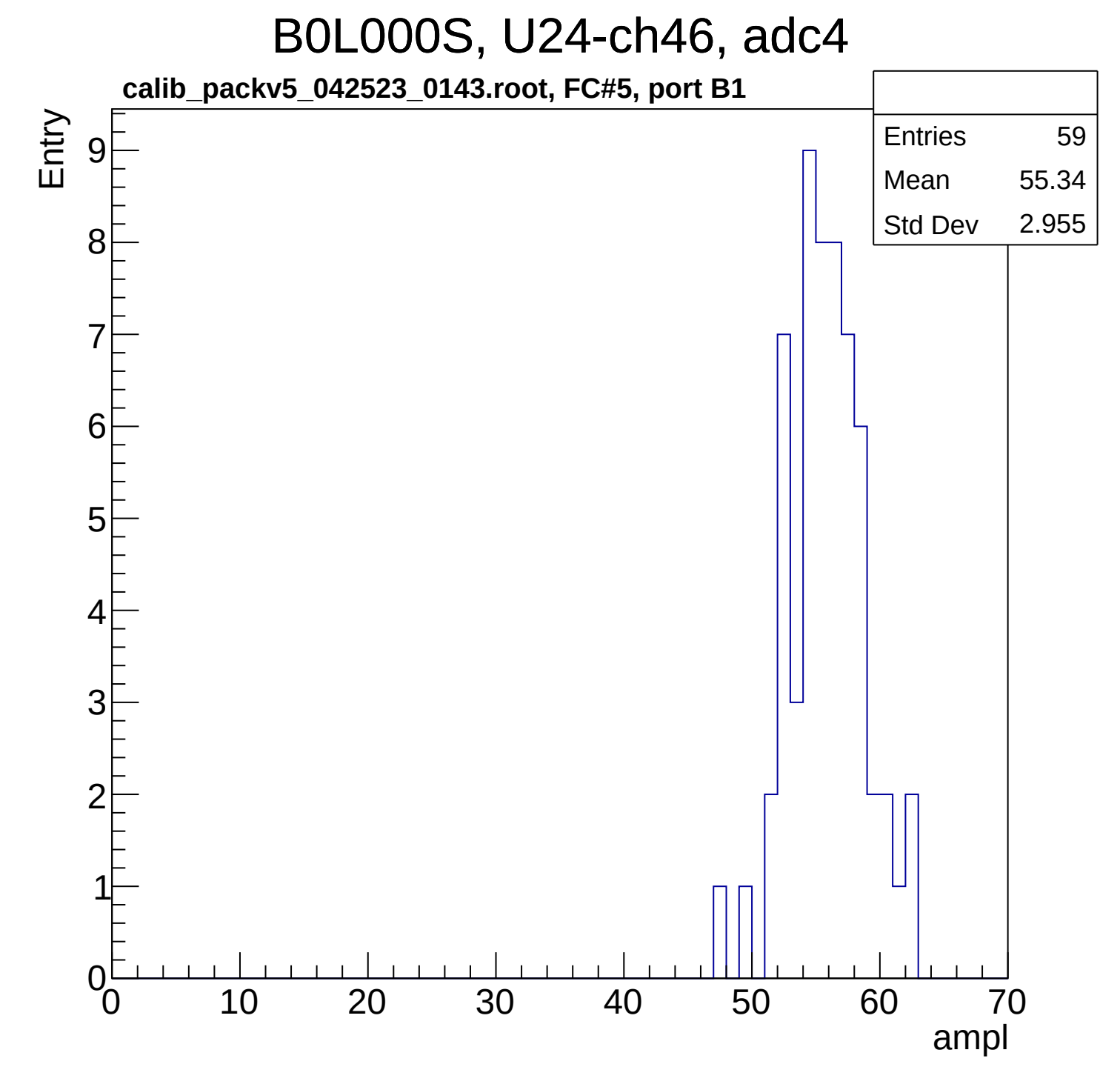
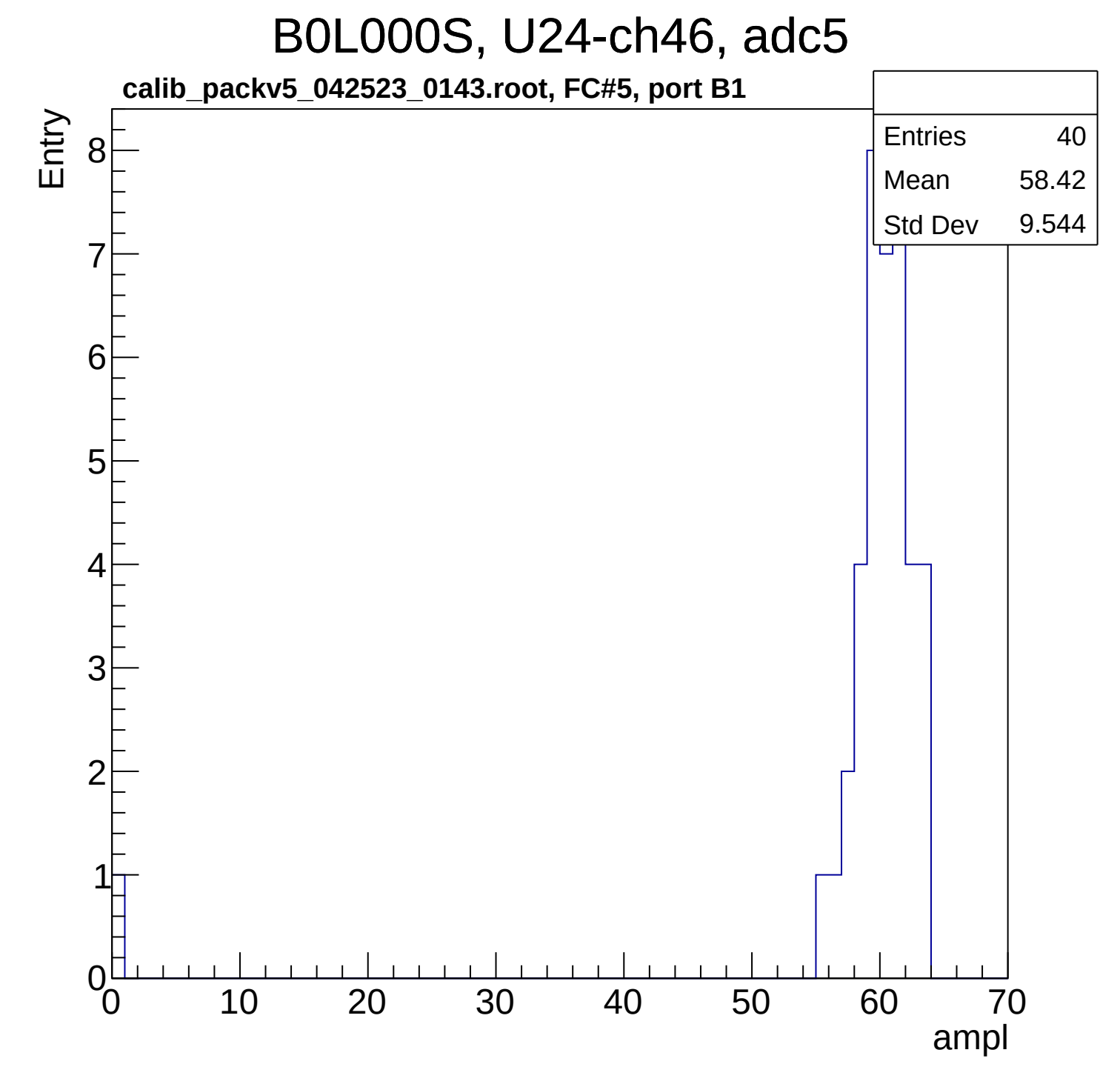

B0L000S, U24-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.06
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.34
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.42
Std Dev 9.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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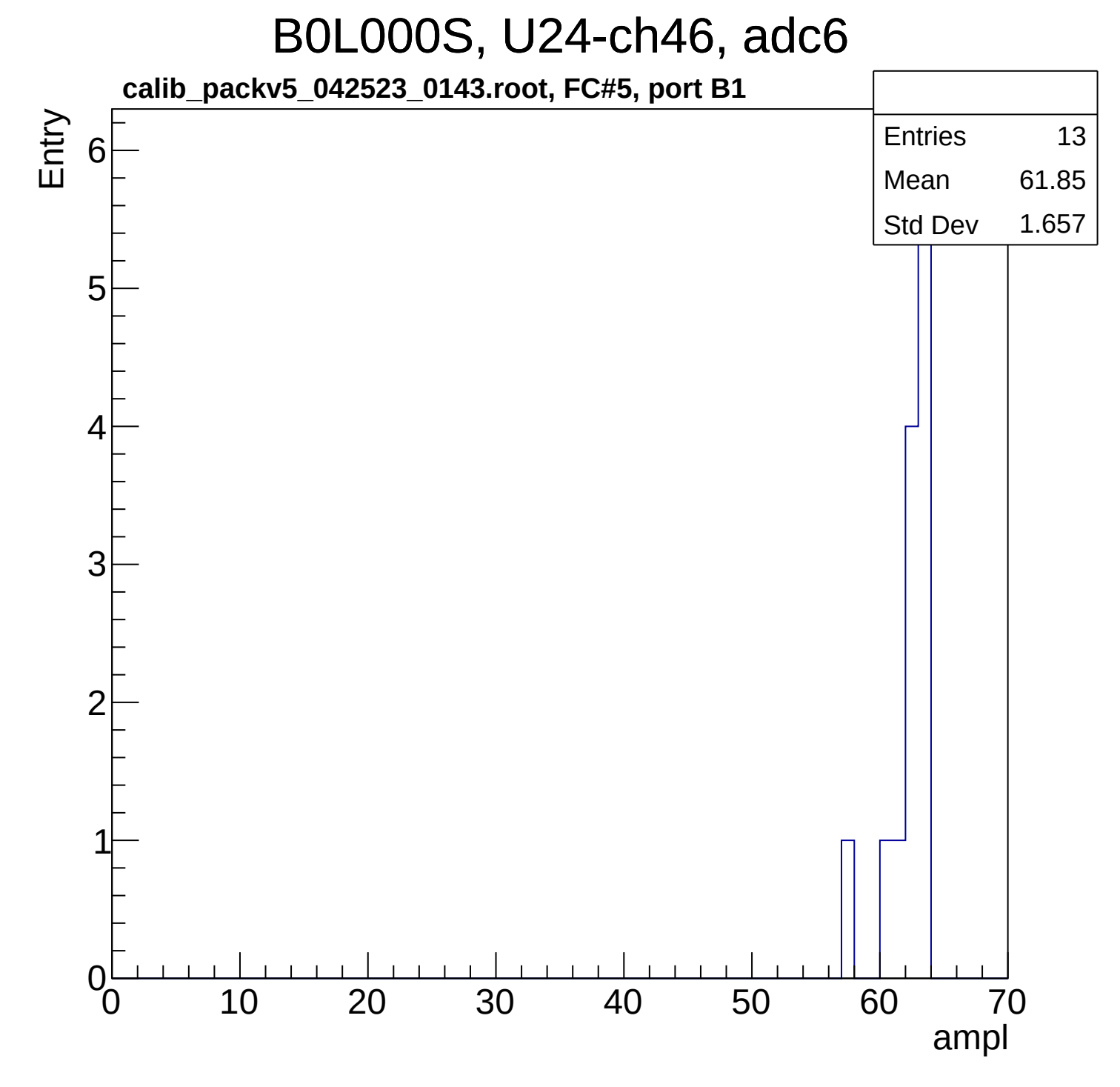
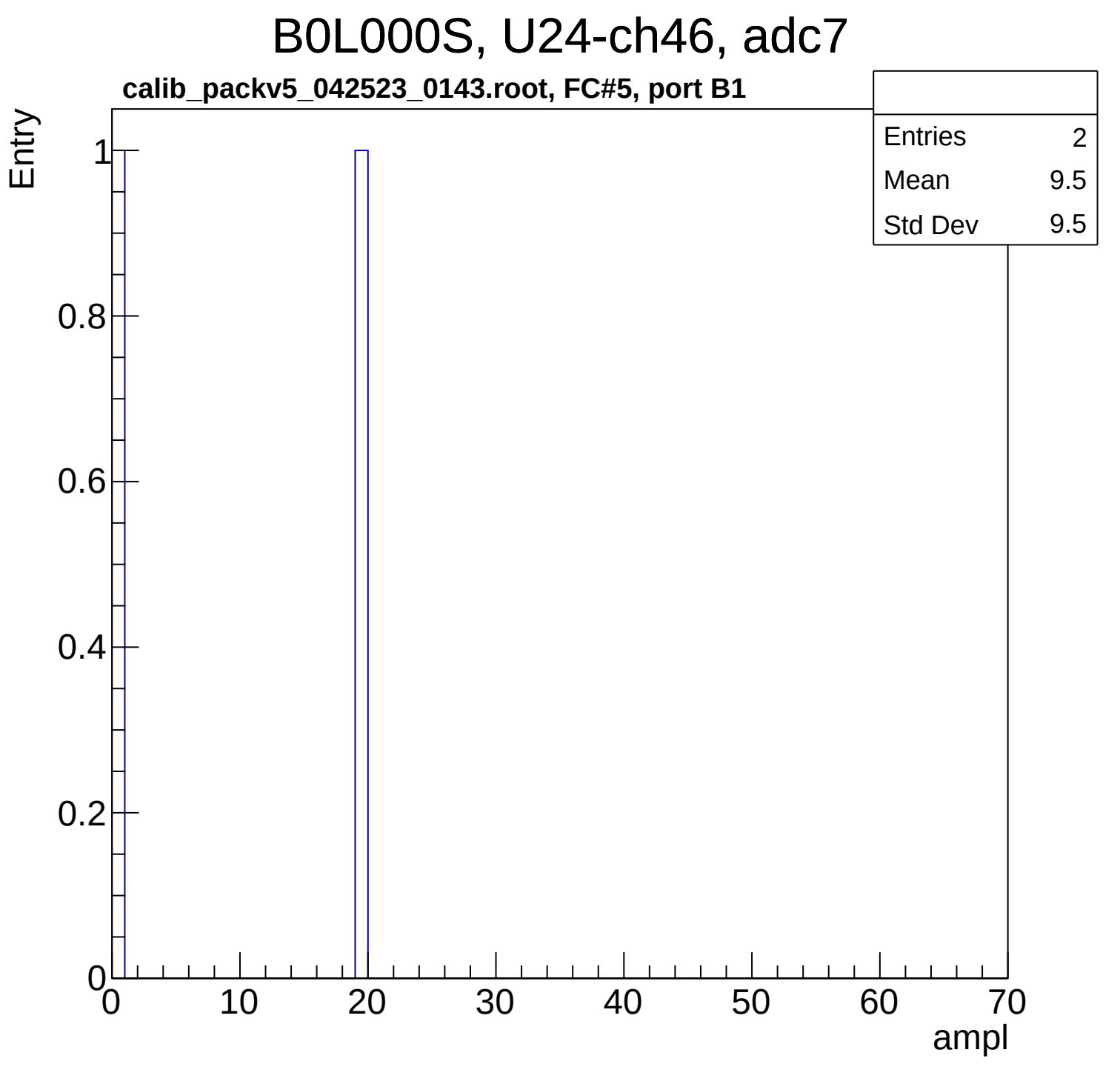
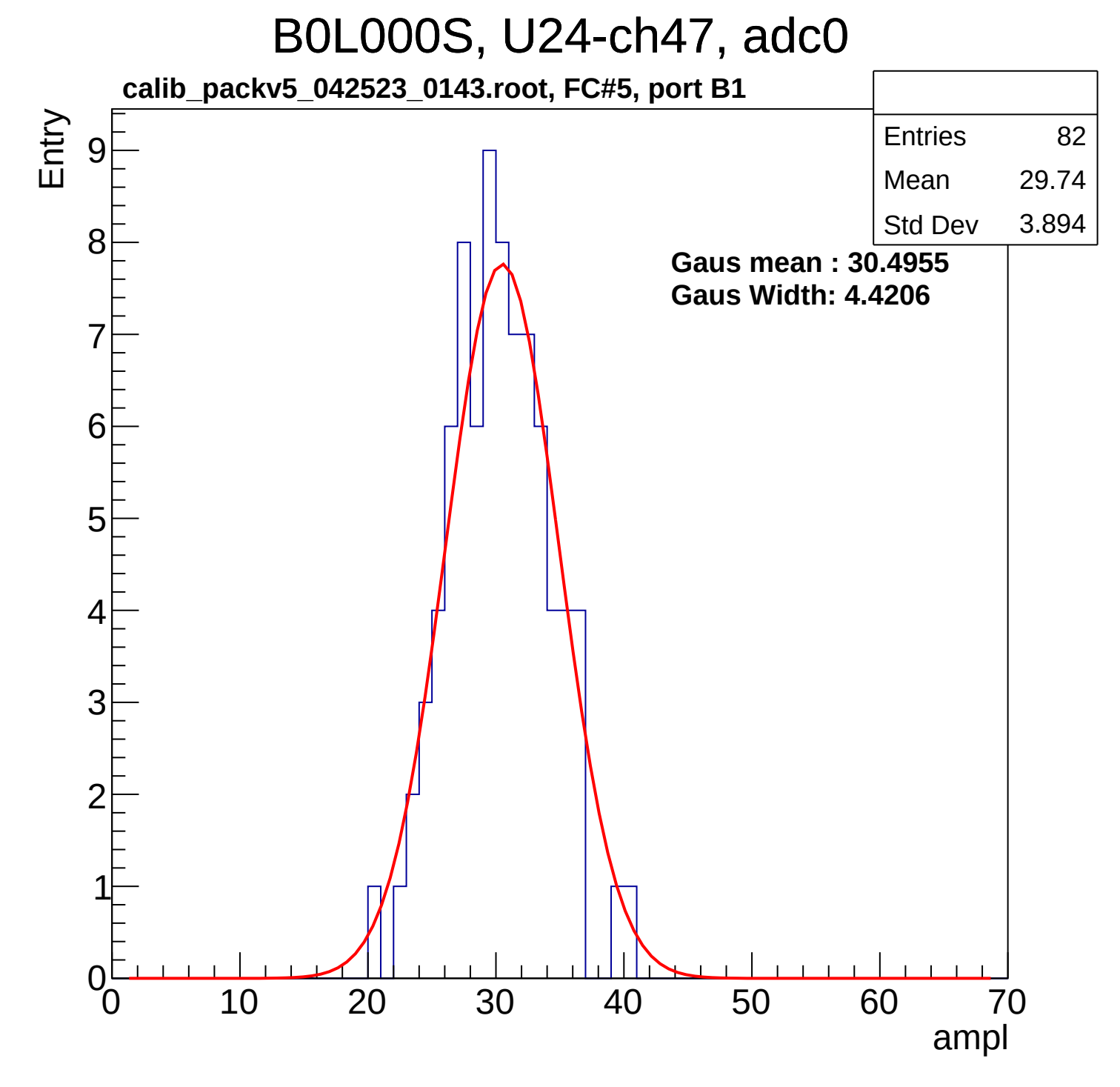

B0L000S, U24-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.85
Std Dev 1.657
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 29.74
Std Dev 3.894
Gaus mean : 30.4955
Gaus Width: 4.4206
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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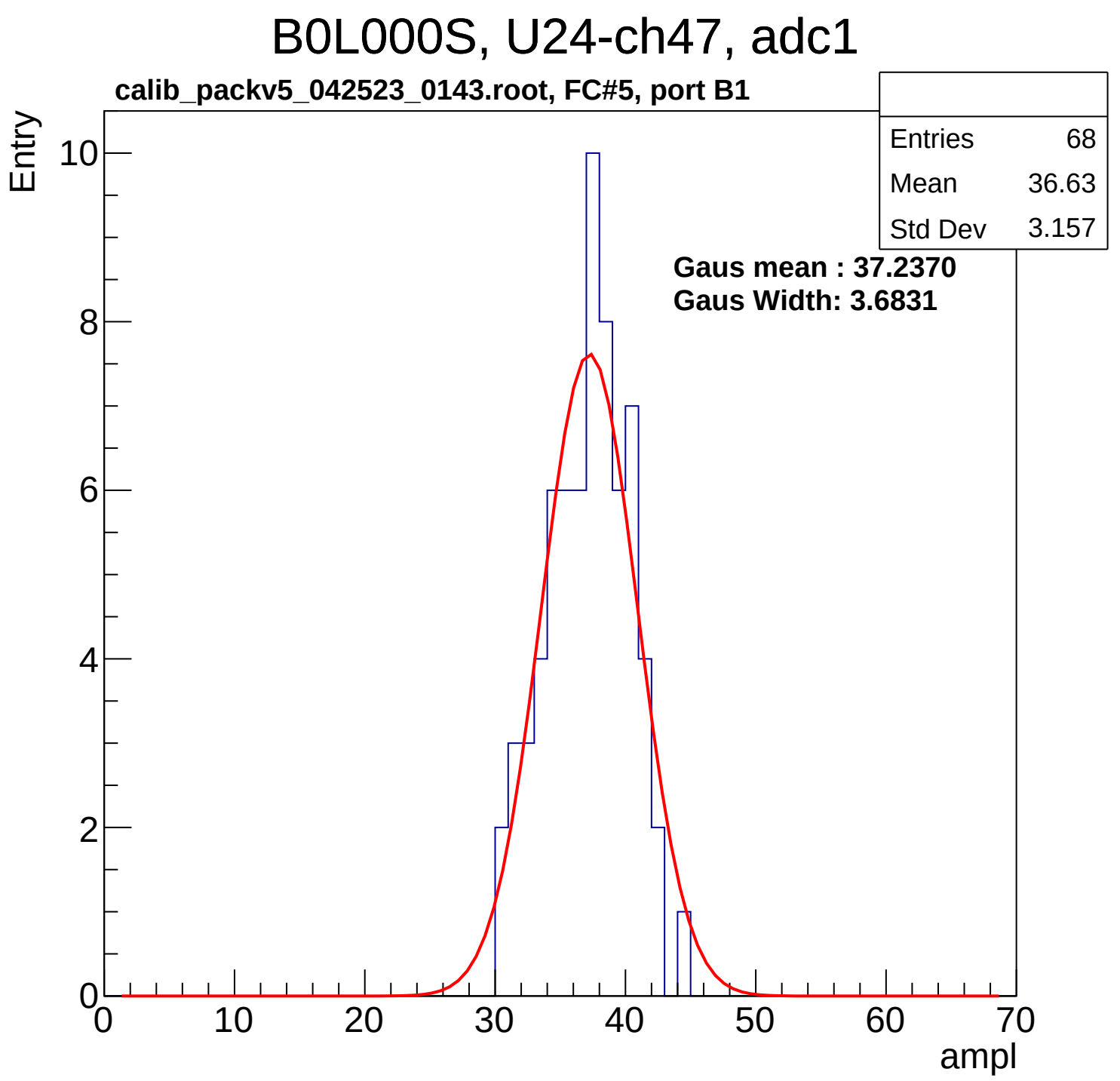
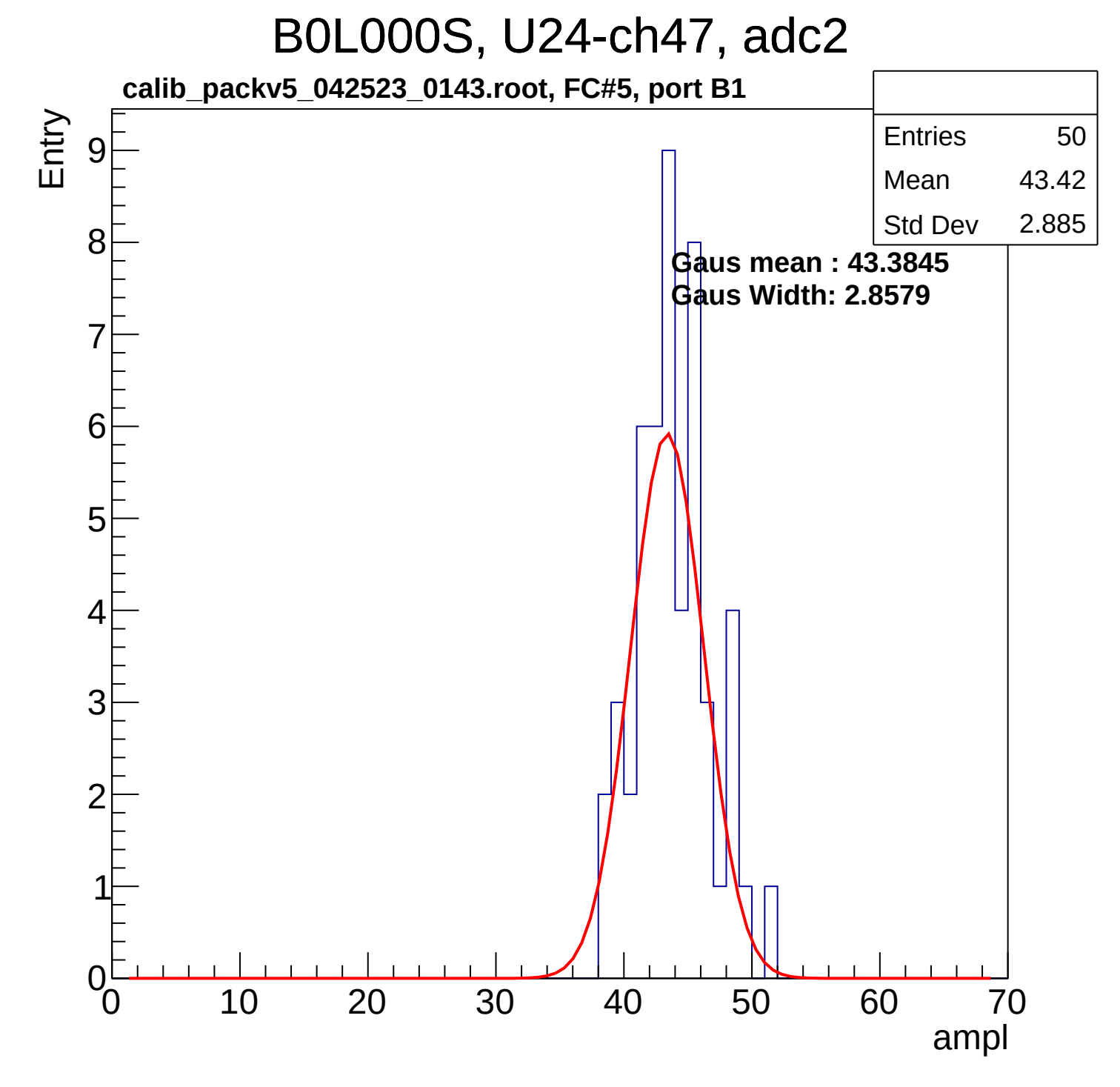
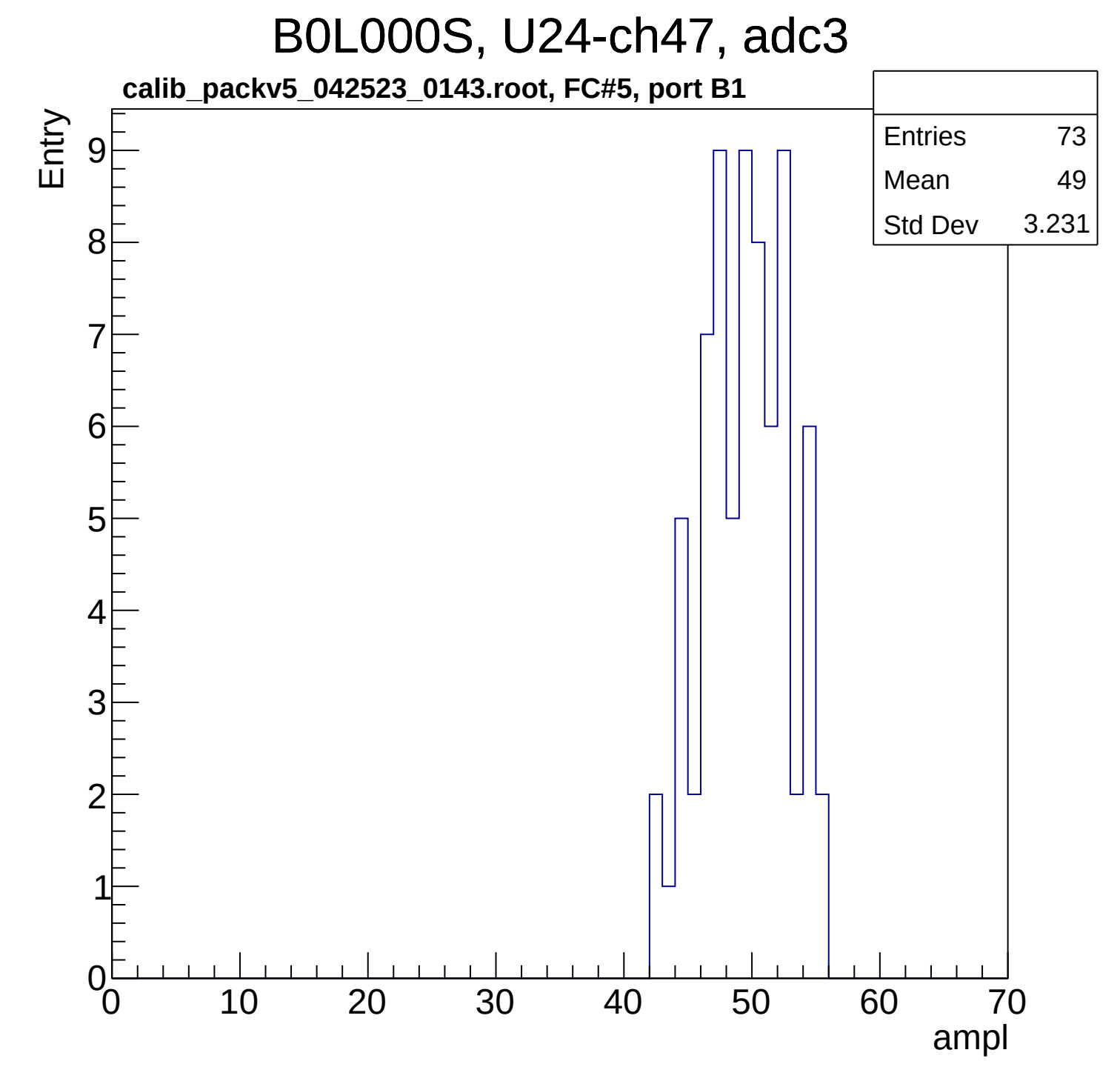
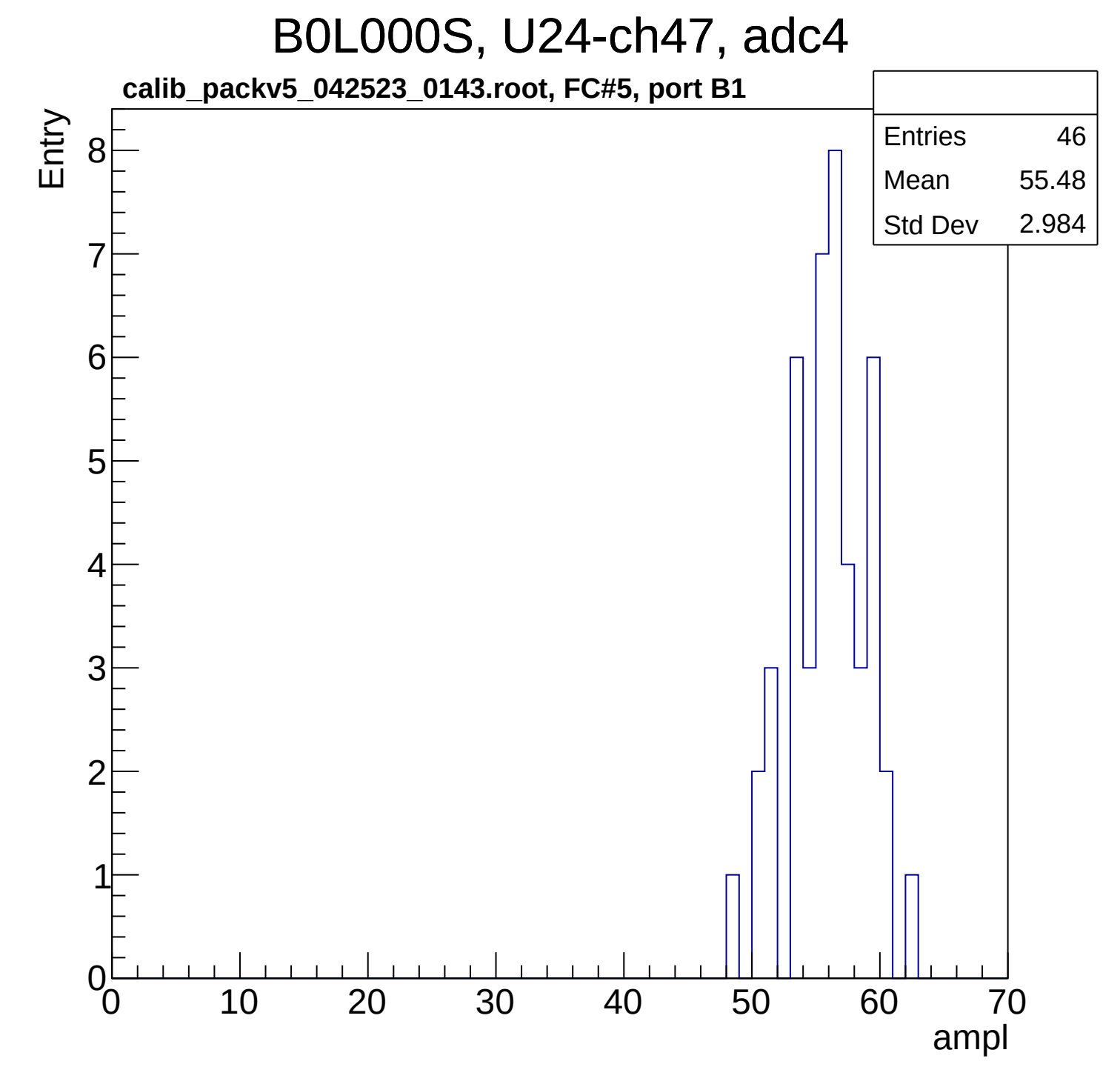

B0L000S, U24-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.63
Std Dev 3.157
Gaus mean : 37.2370
Gaus Width: 3.6831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 43.42
Std Dev 2.885
Gaus mean : 43.3845
Gaus Width: 2.8579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 55.48
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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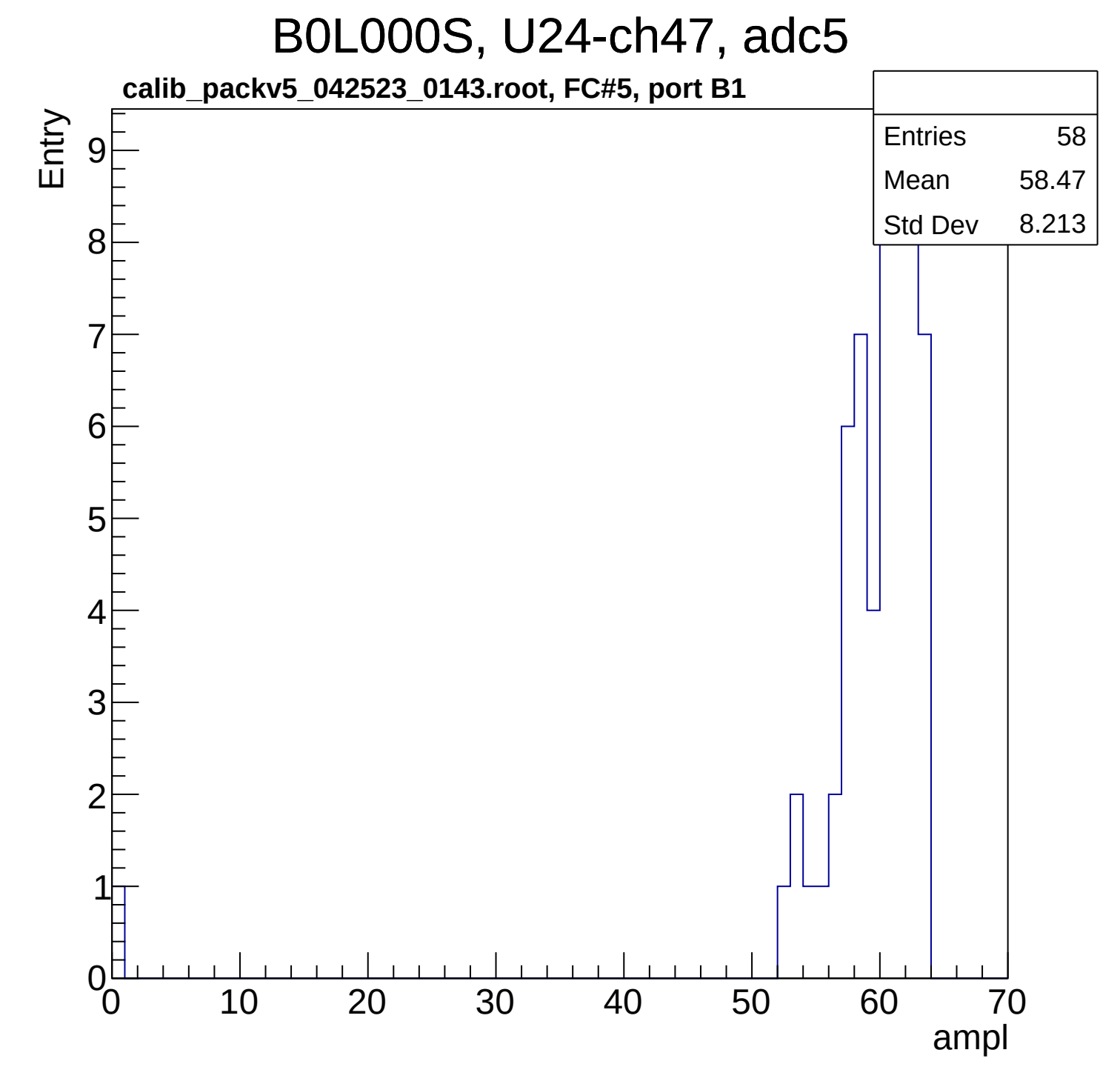
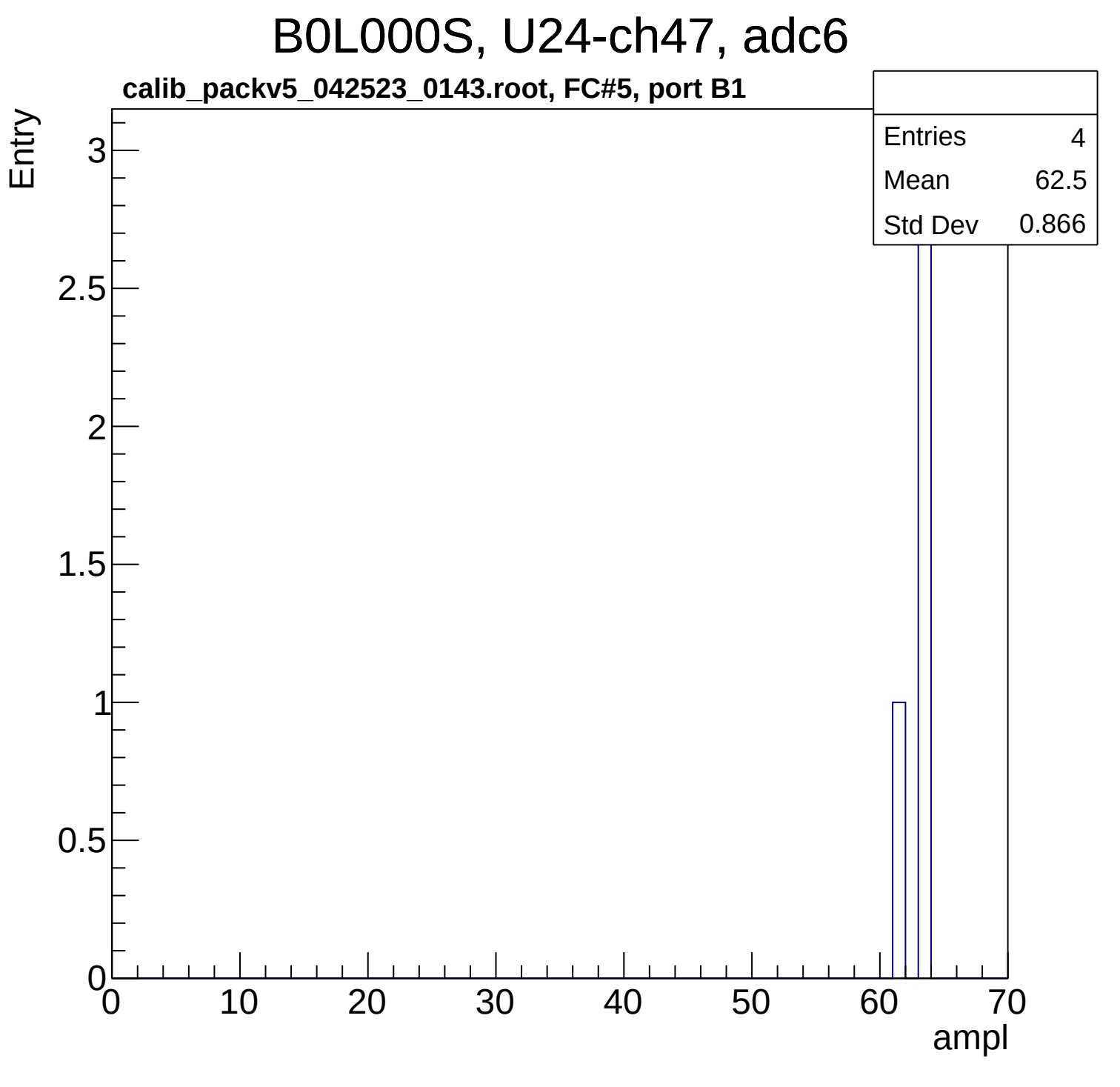
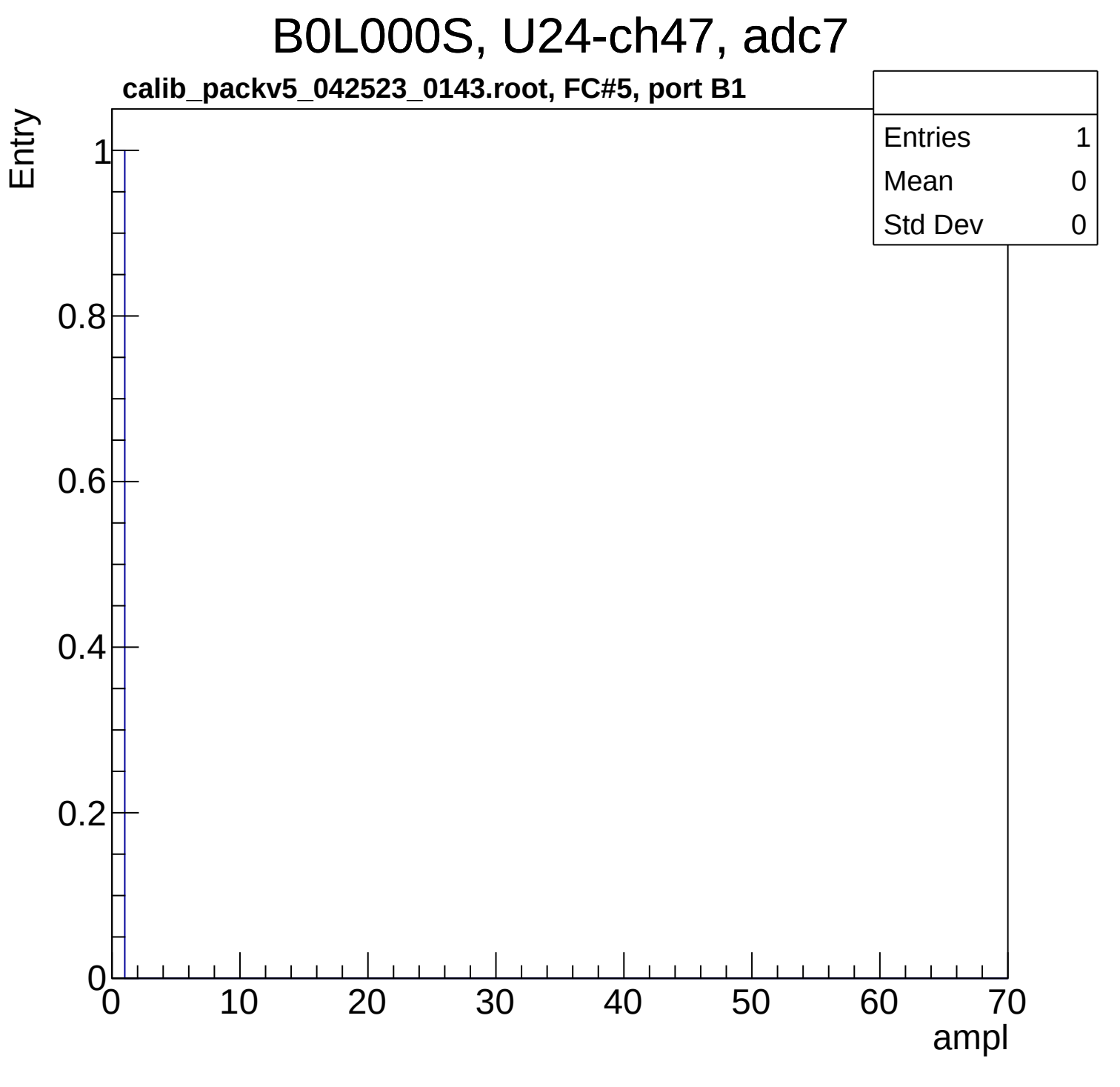
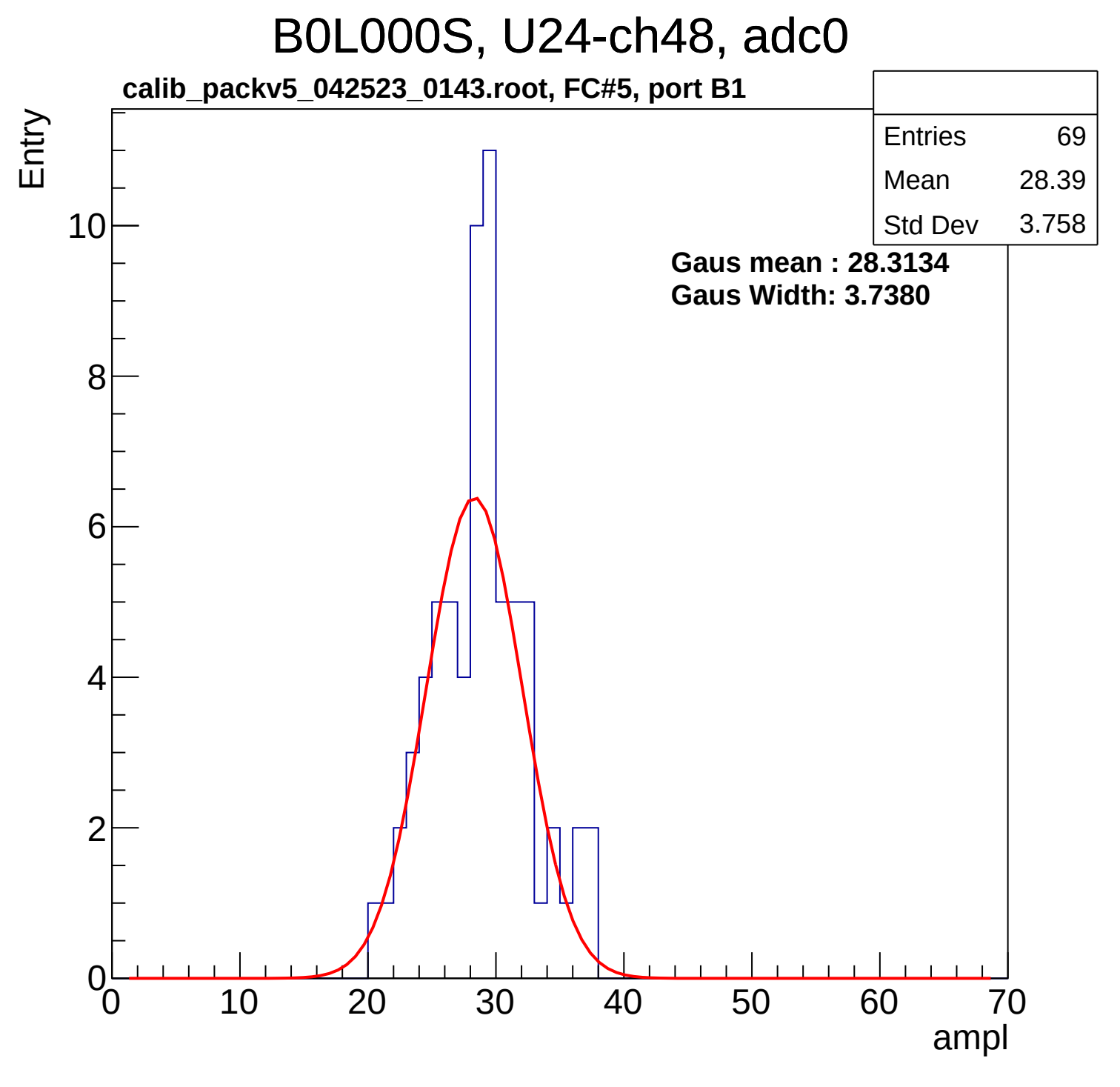
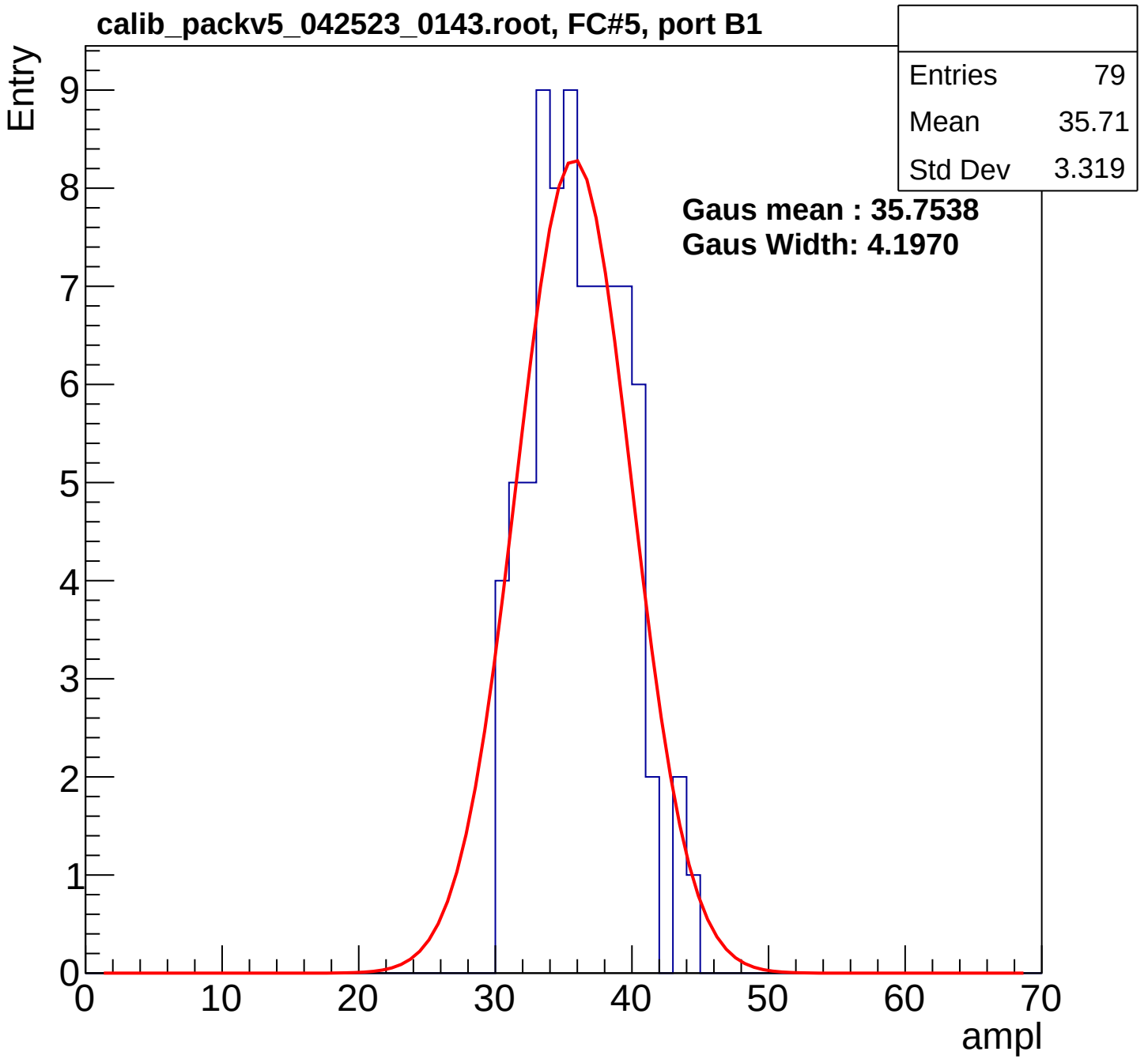
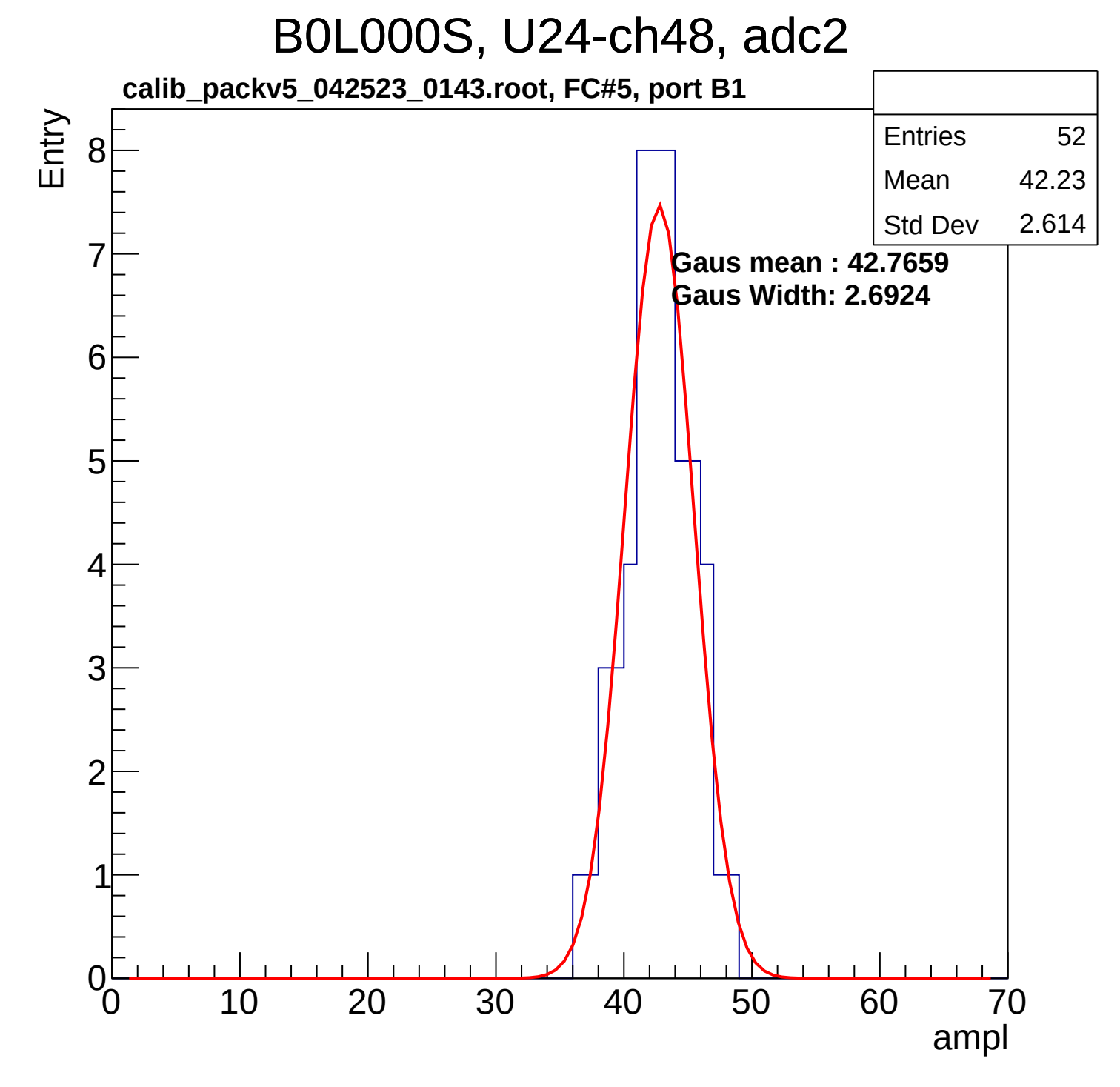

B0L000S, U24-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.47
Std Dev 8.213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

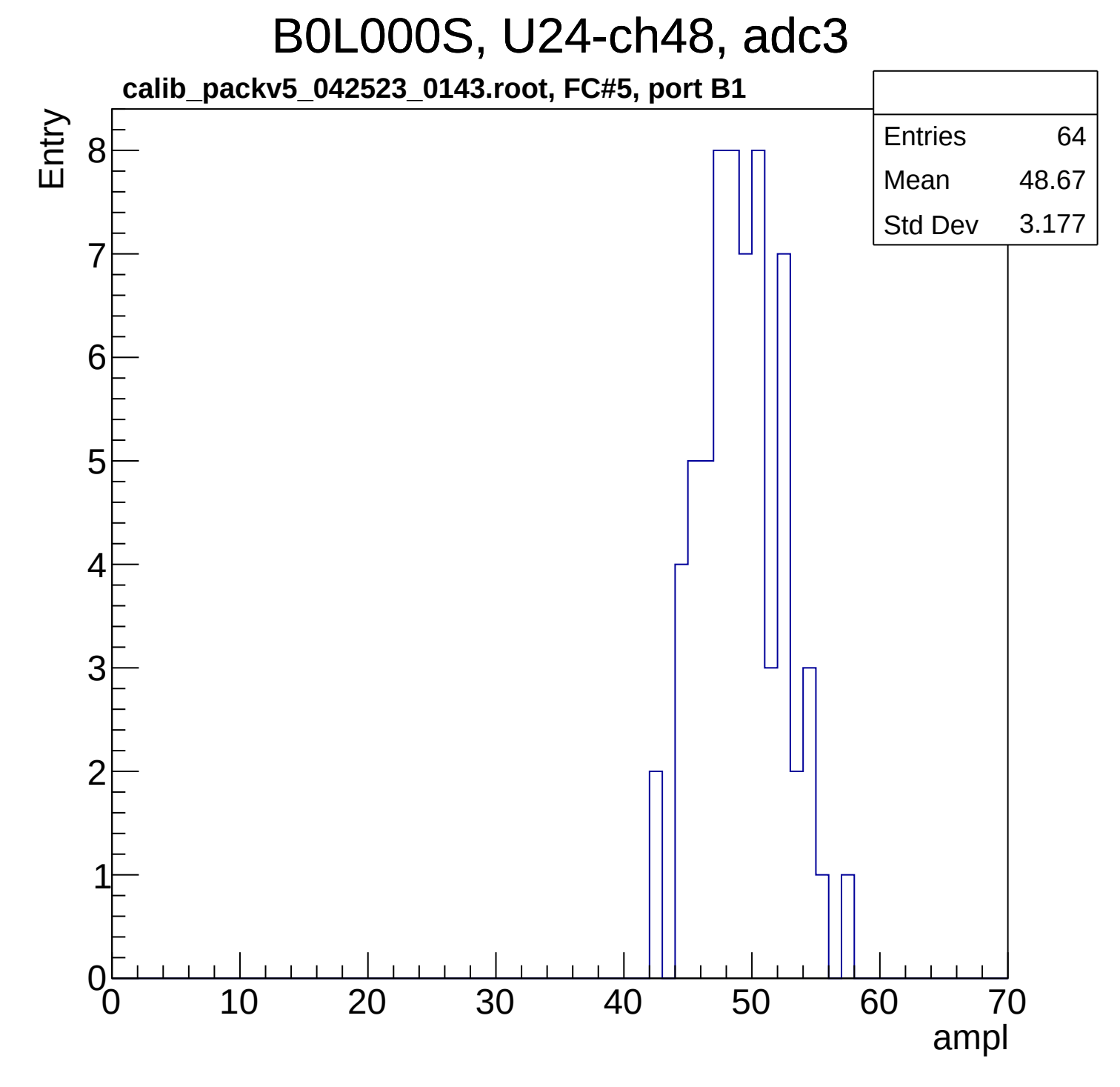

B0L000S, U24-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 28.39
Std Dev 3.758
Gaus mean : 28.3134
Gaus Width: 3.7380
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 35.71
Std Dev 3.319
Gaus mean : 35.7538
Gaus Width: 4.1970
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.23
Std Dev 2.614
Gaus mean : 42.7659
Gaus Width: 2.6924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 48.67
Std Dev 3.177
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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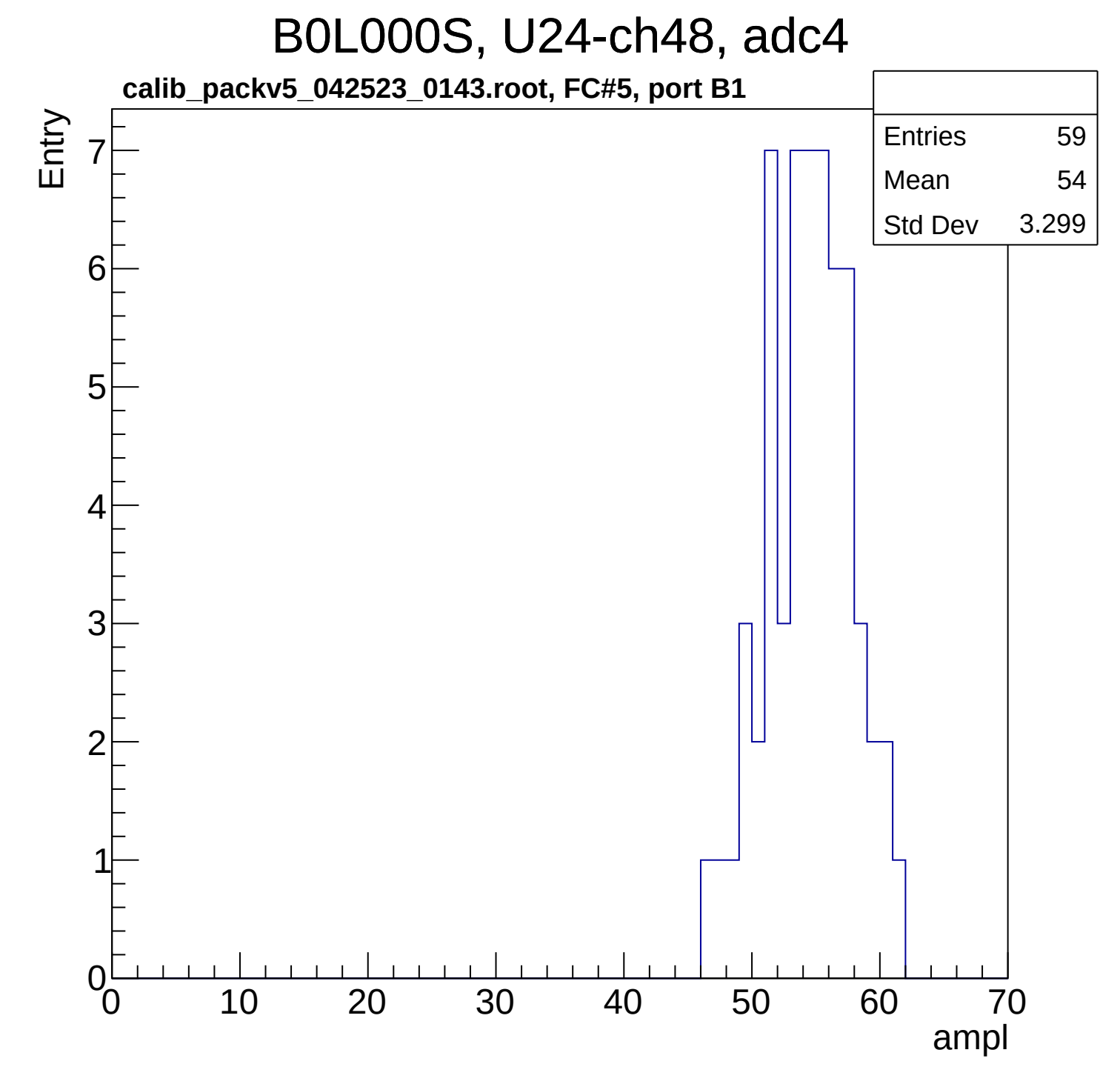
B0L000S, U24-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54
Std Dev 3.299
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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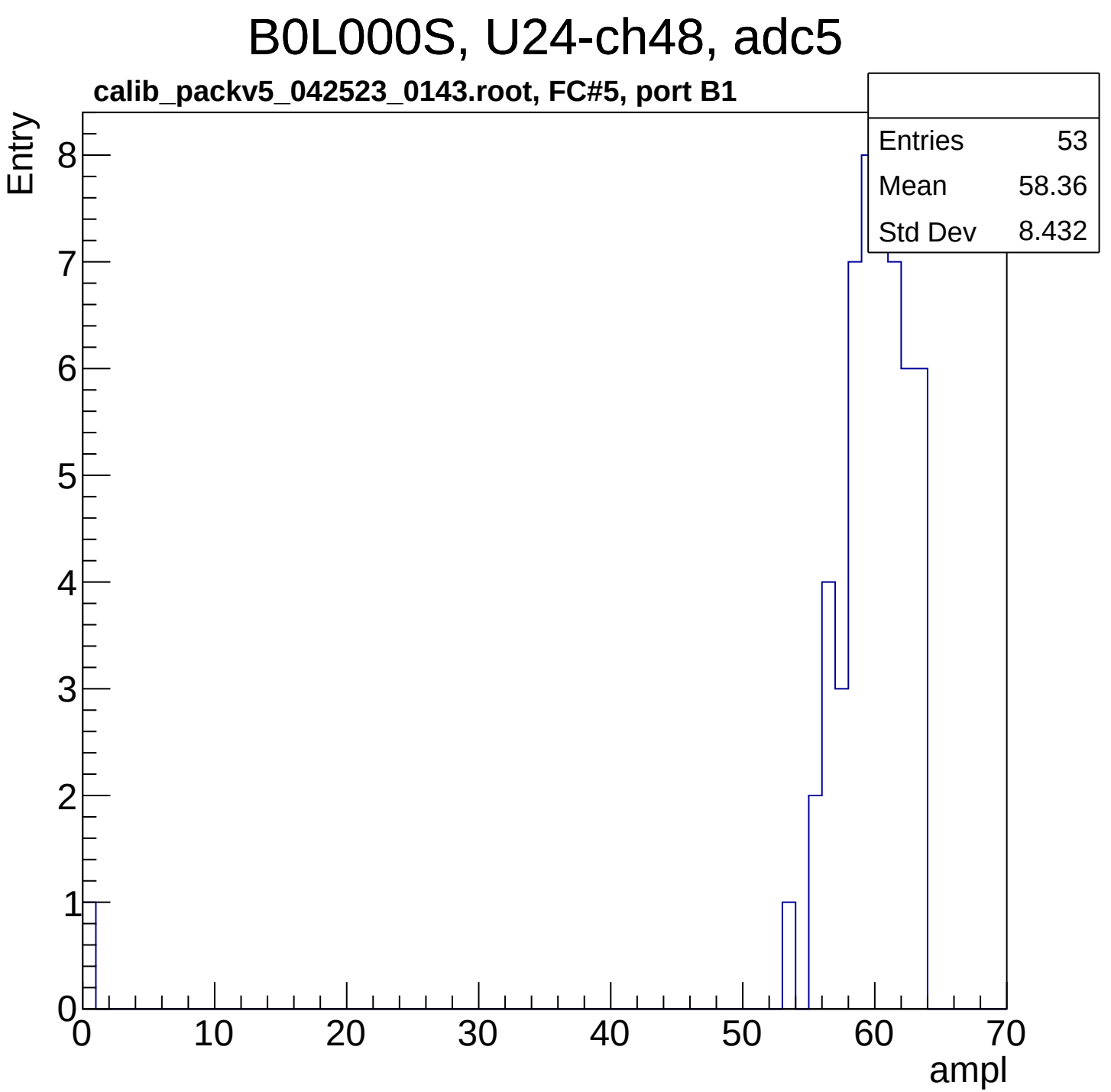
B0L000S, U24-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.36
Std Dev 8.432
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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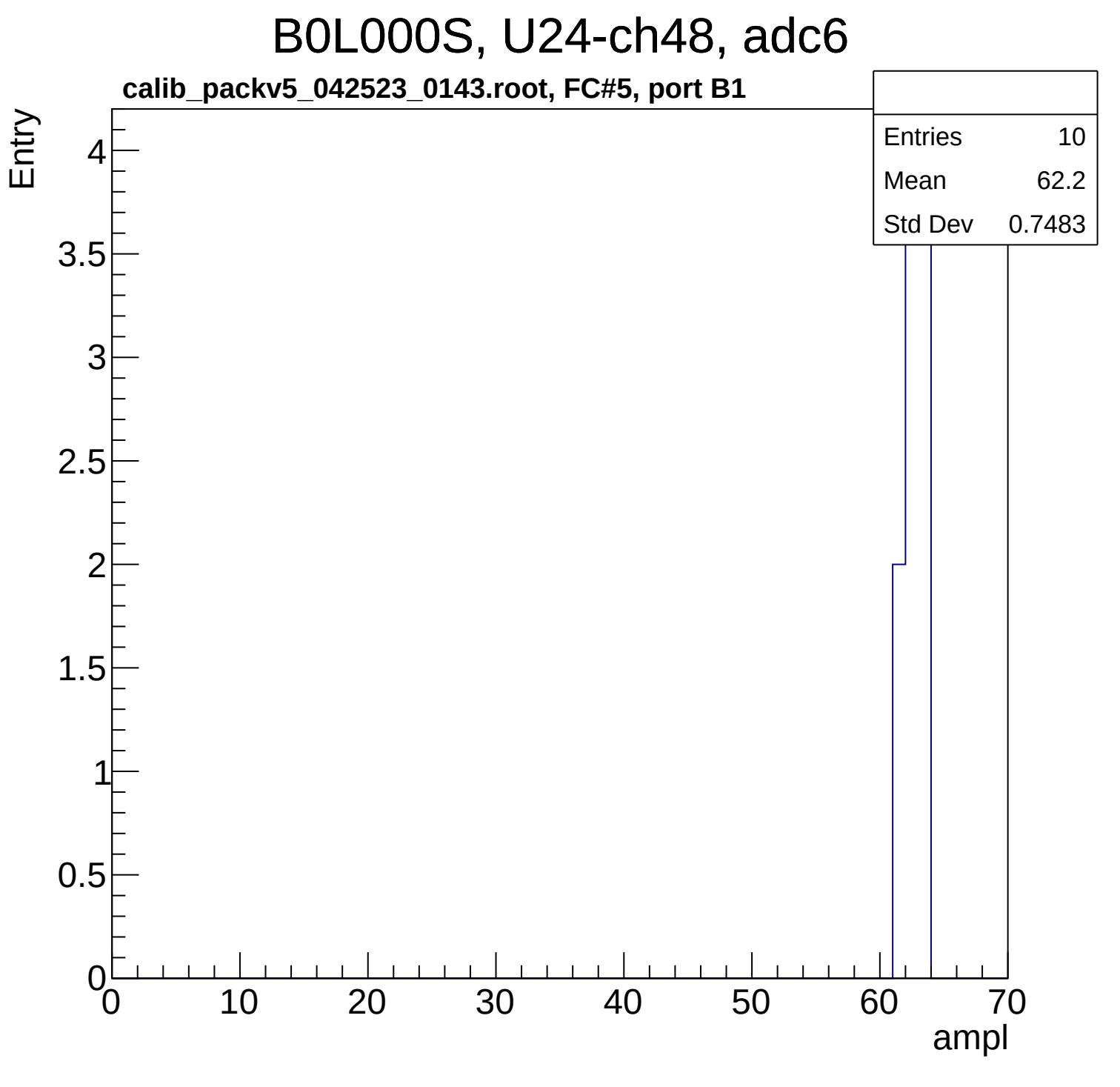
B0L000S, U24-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.2
Std Dev 0.7483
Entry
ampl
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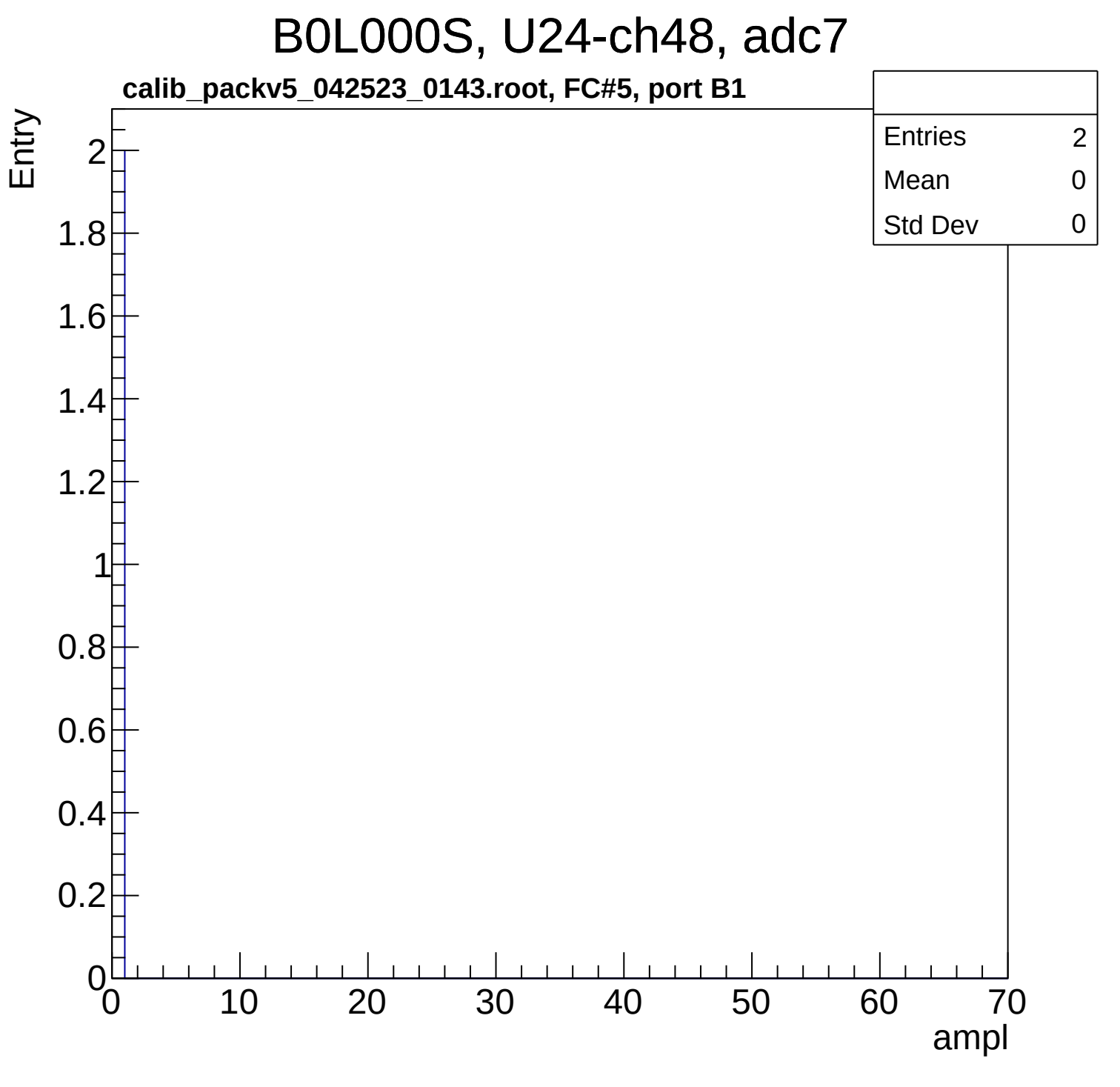
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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30
40
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60
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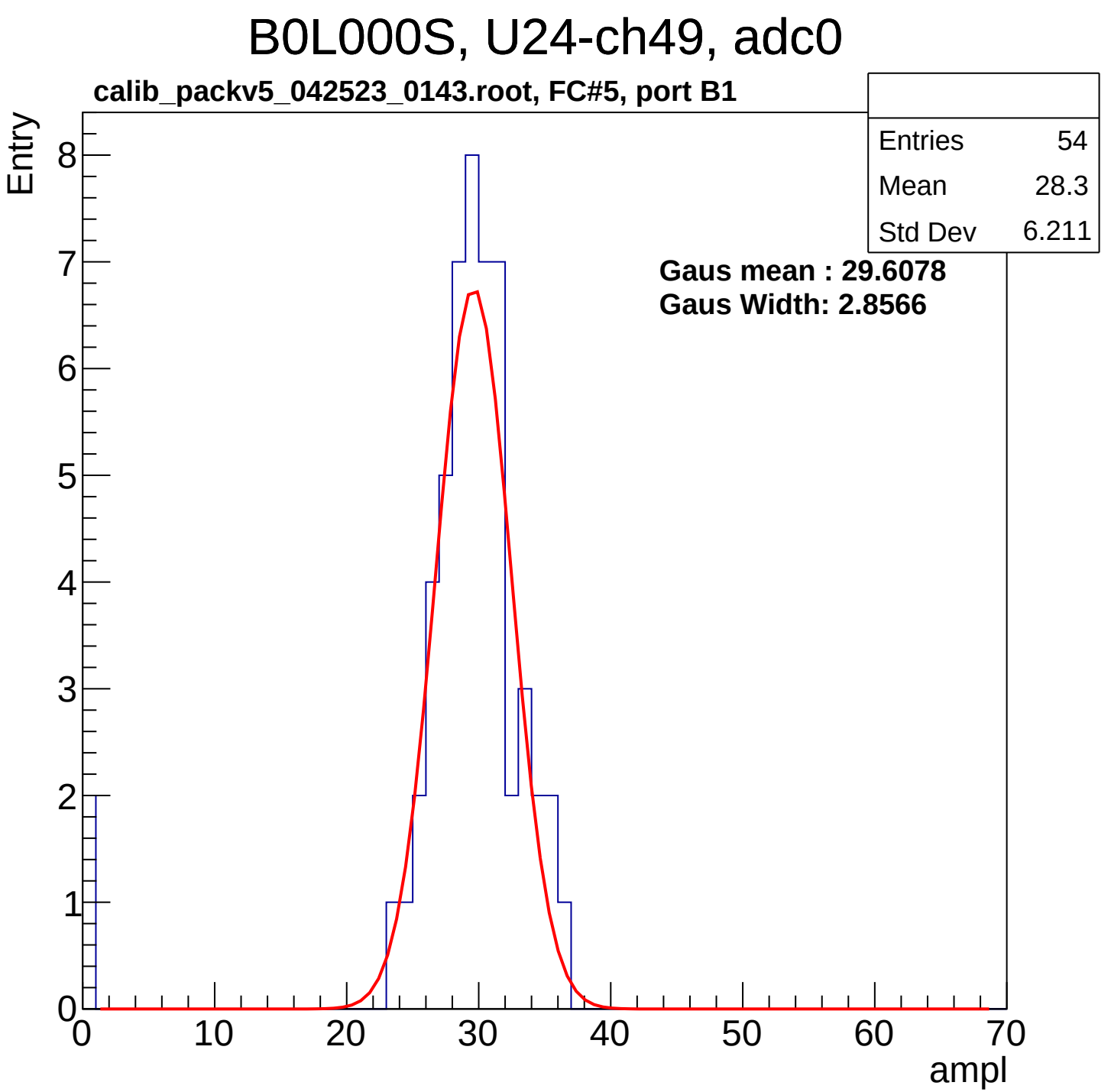
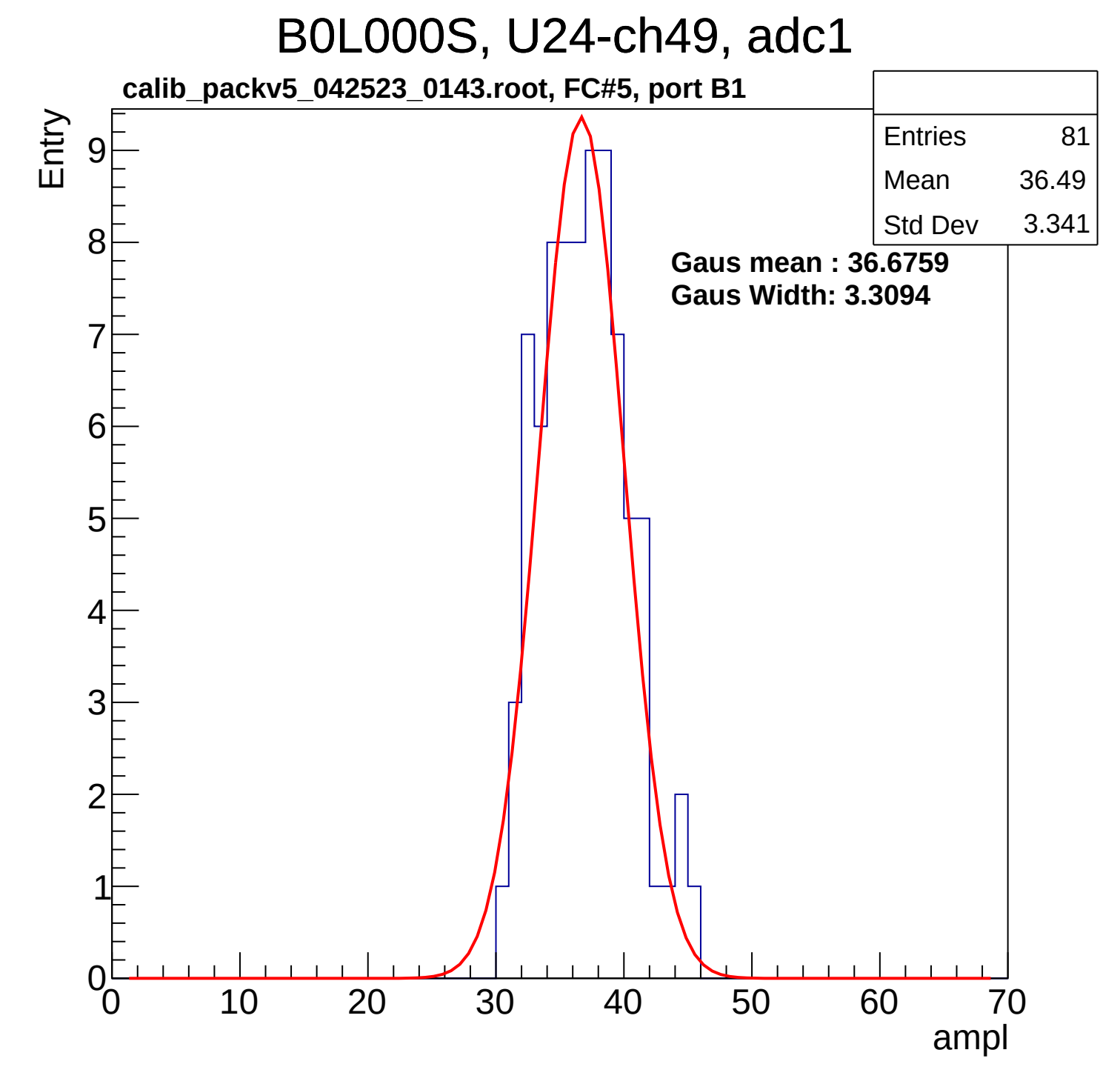
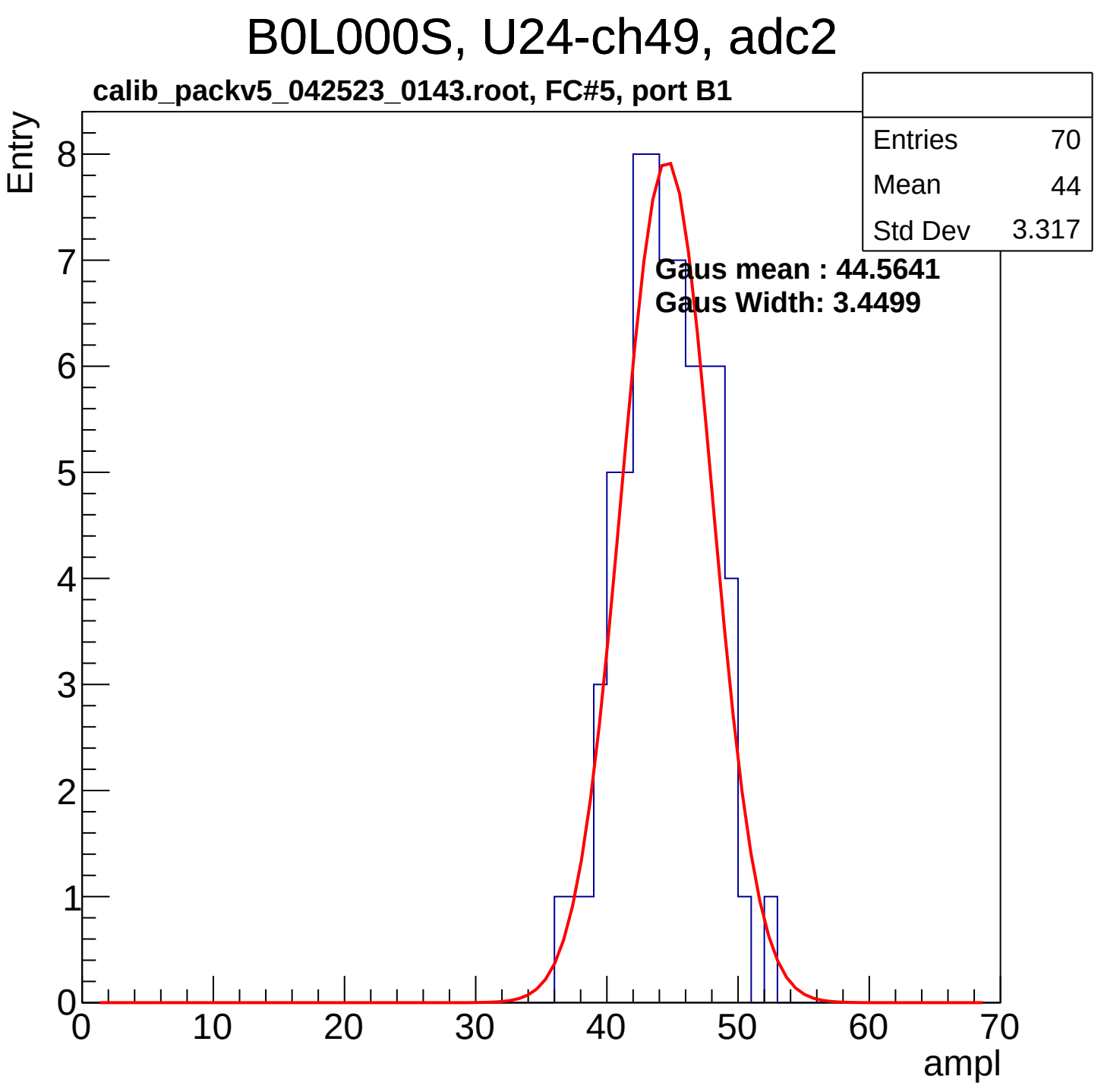
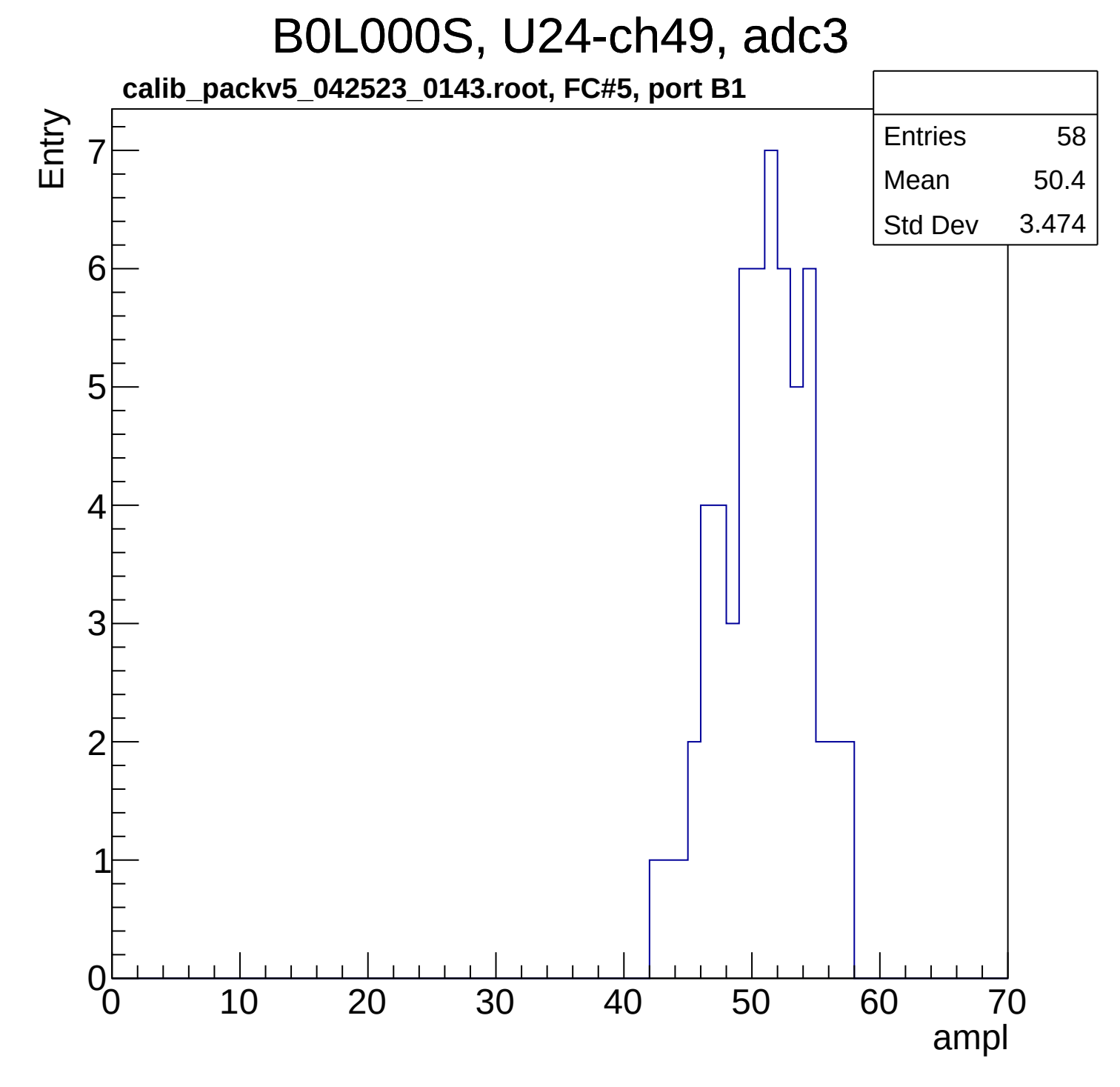

B0L000S, U24-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 28.3
Std Dev 6.211
Gaus mean : 29.6078
Gaus Width: 2.8566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 36.49
Std Dev 3.341
Gaus mean : 36.6759
Gaus Width: 3.3094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44
Std Dev 3.317
Gaus mean : 44.5641
Gaus Width: 3.4499
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 50.4
Std Dev 3.474
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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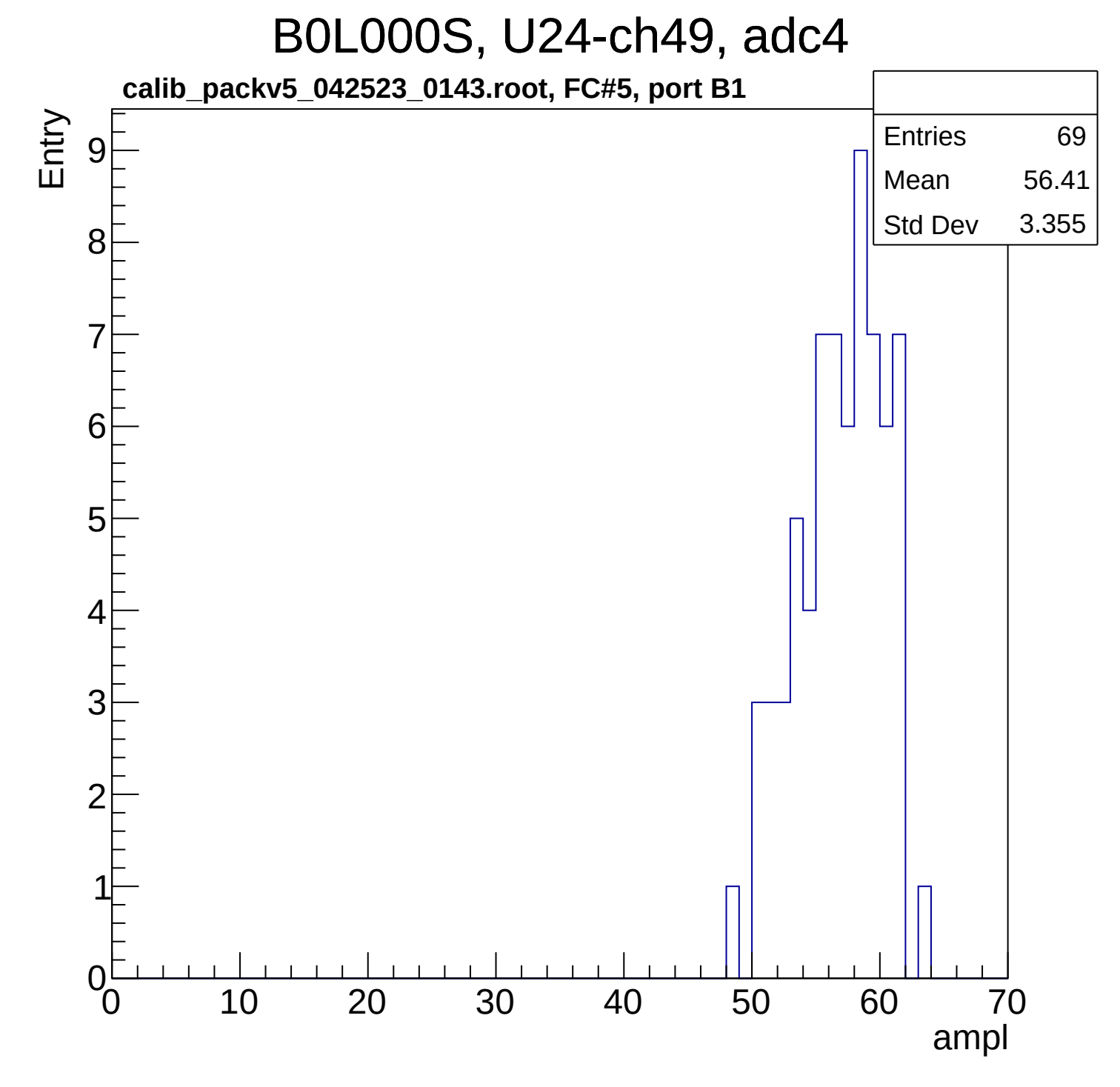
B0L000S, U24-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 56.41
Std Dev 3.355
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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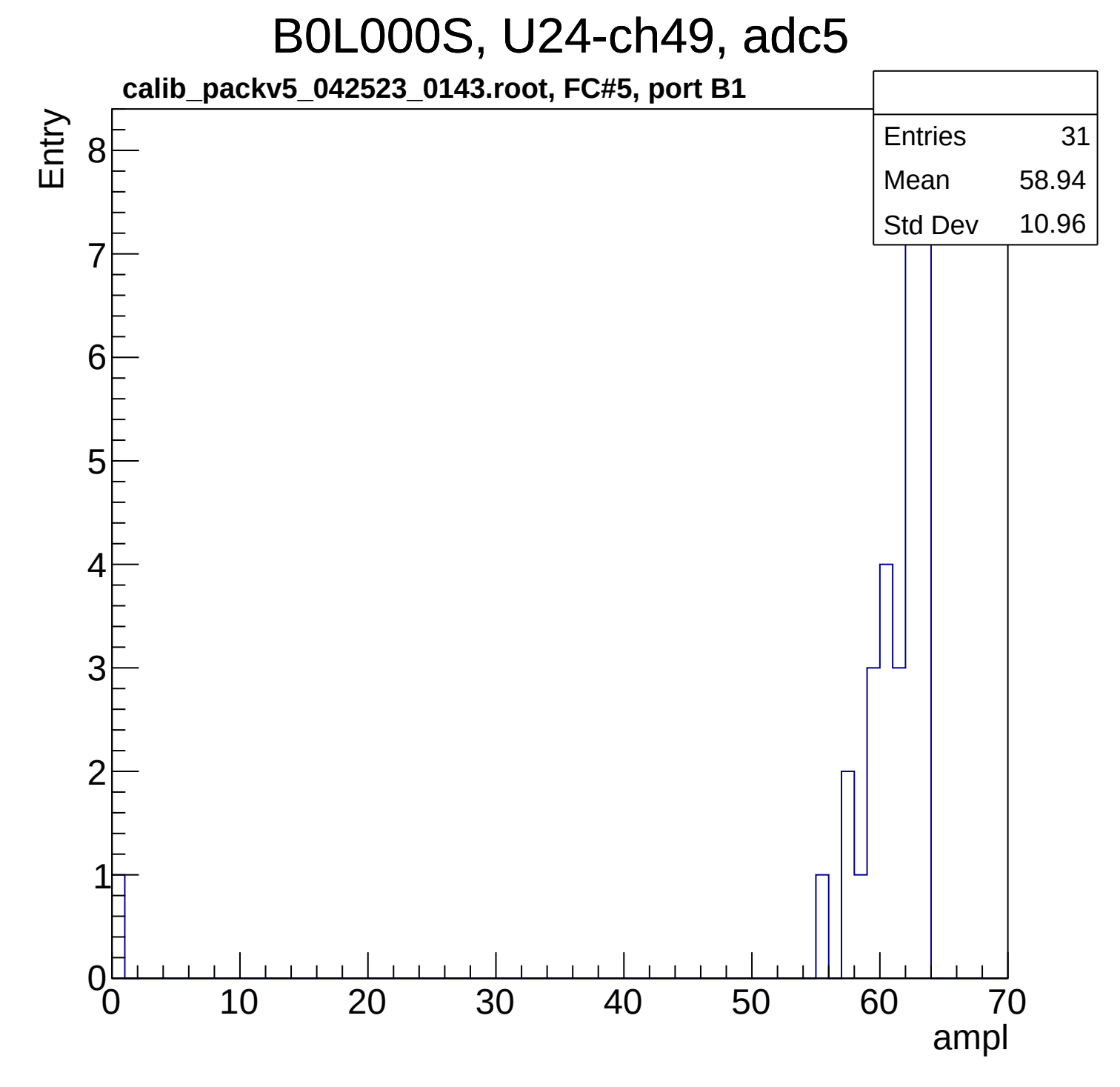
B0L000S, U24-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.94
Std Dev 10.96
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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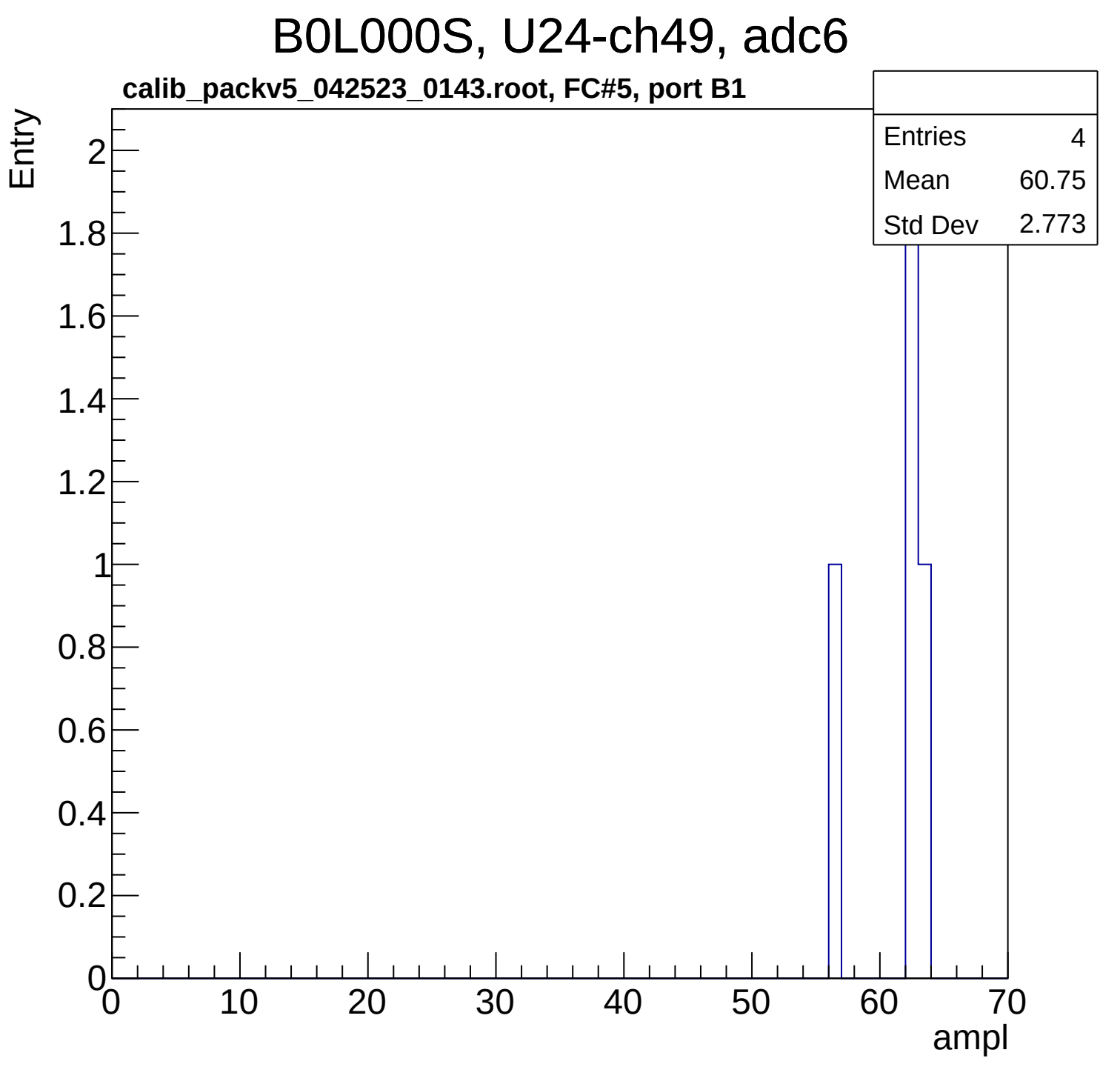
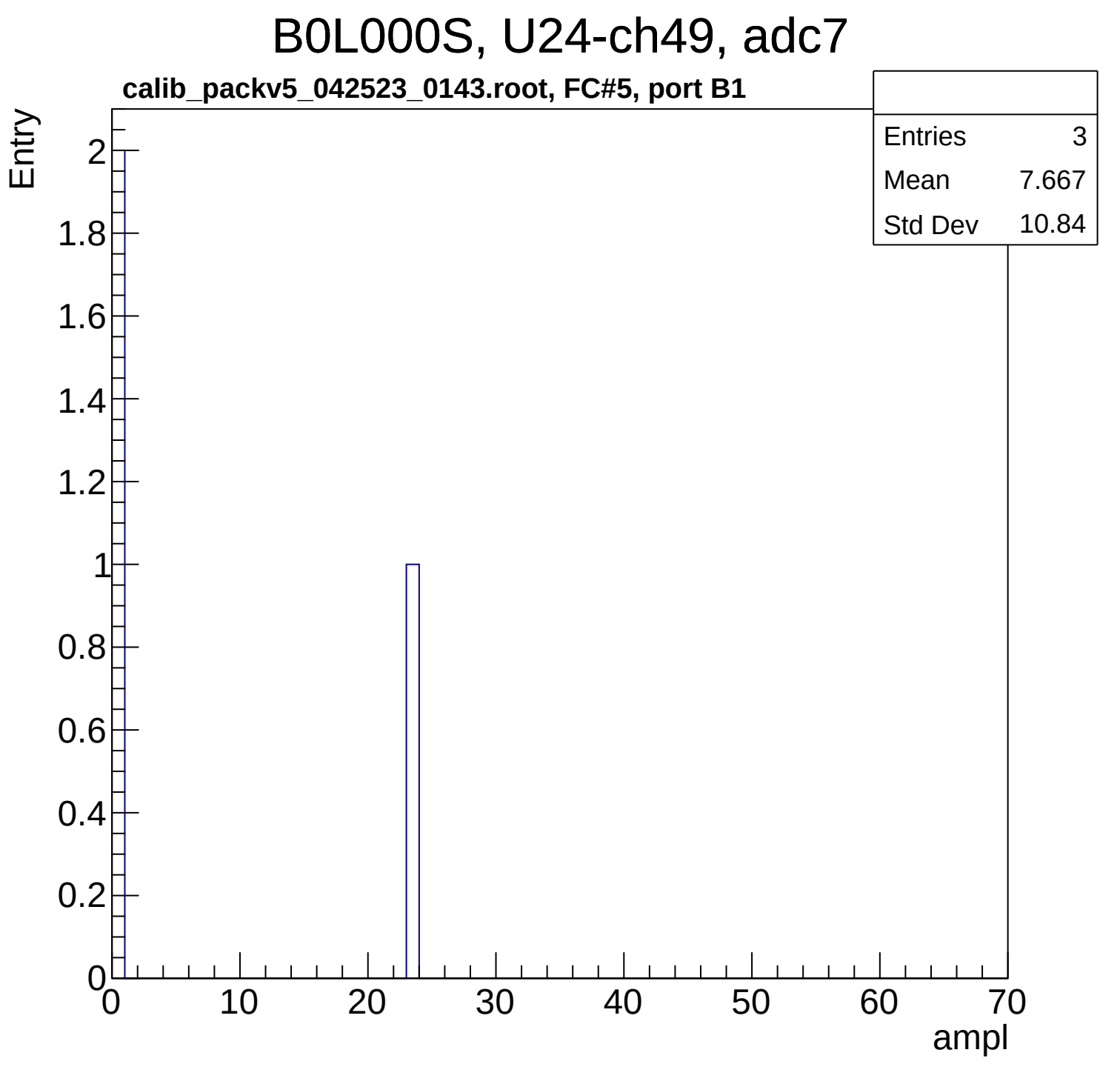

B0L000S, U24-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.75
Std Dev 2.773
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
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1.4
1.6
1.8
2
0
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40
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60
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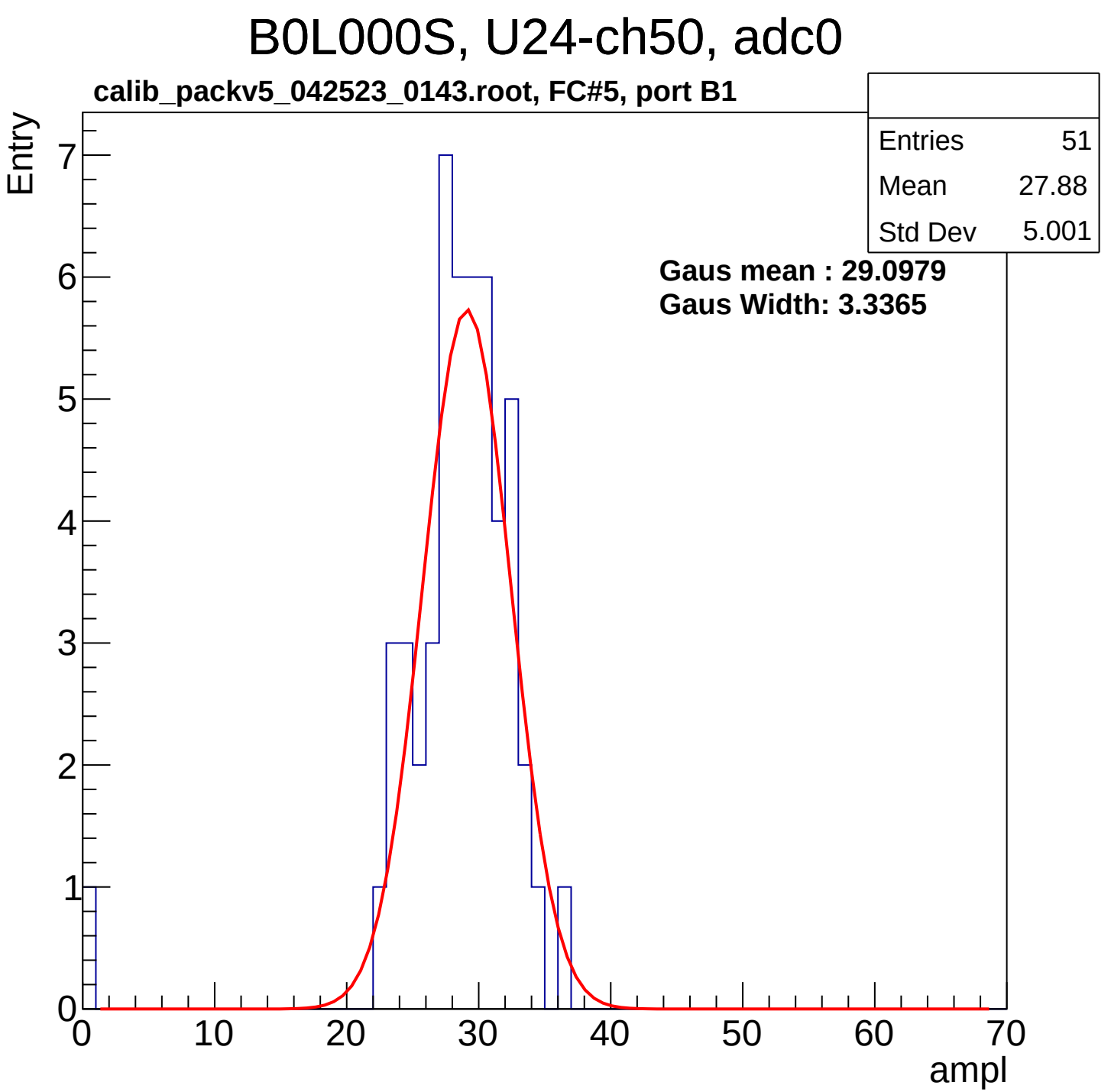
B0L000S, U24-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 27.88
Std Dev 5.001
Gaus mean : 29.0979
Gaus Width: 3.3365
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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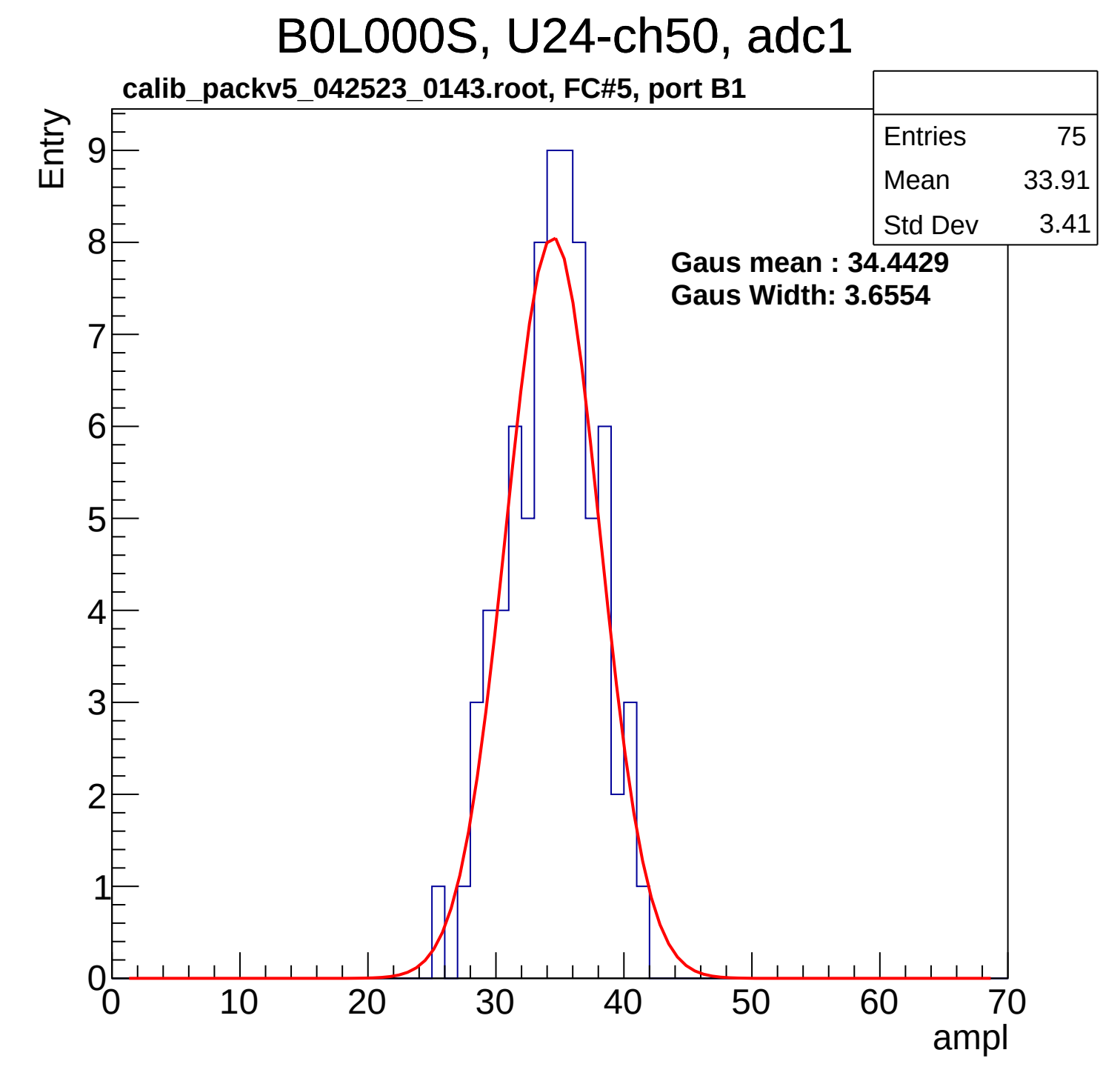
B0L000S, U24-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 33.91
Std Dev 3.41
Gaus mean : 34.4429
Gaus Width: 3.6554
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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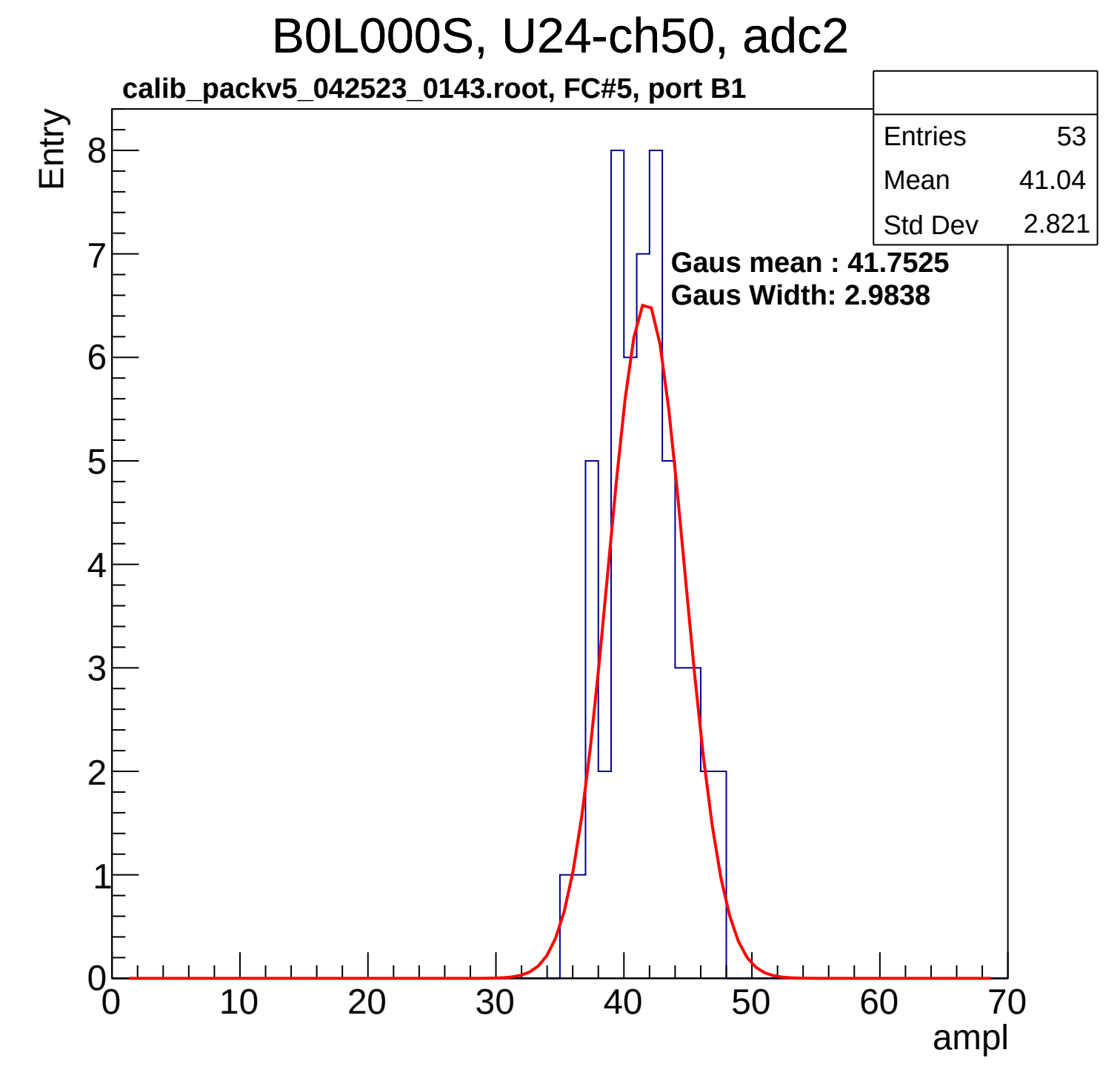
B0L000S, U24-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 41.04
Std Dev 2.821
Gaus mean : 41.7525
Gaus Width: 2.9838
Entry
ampl
0
1
2
3
4
5
6
7
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0
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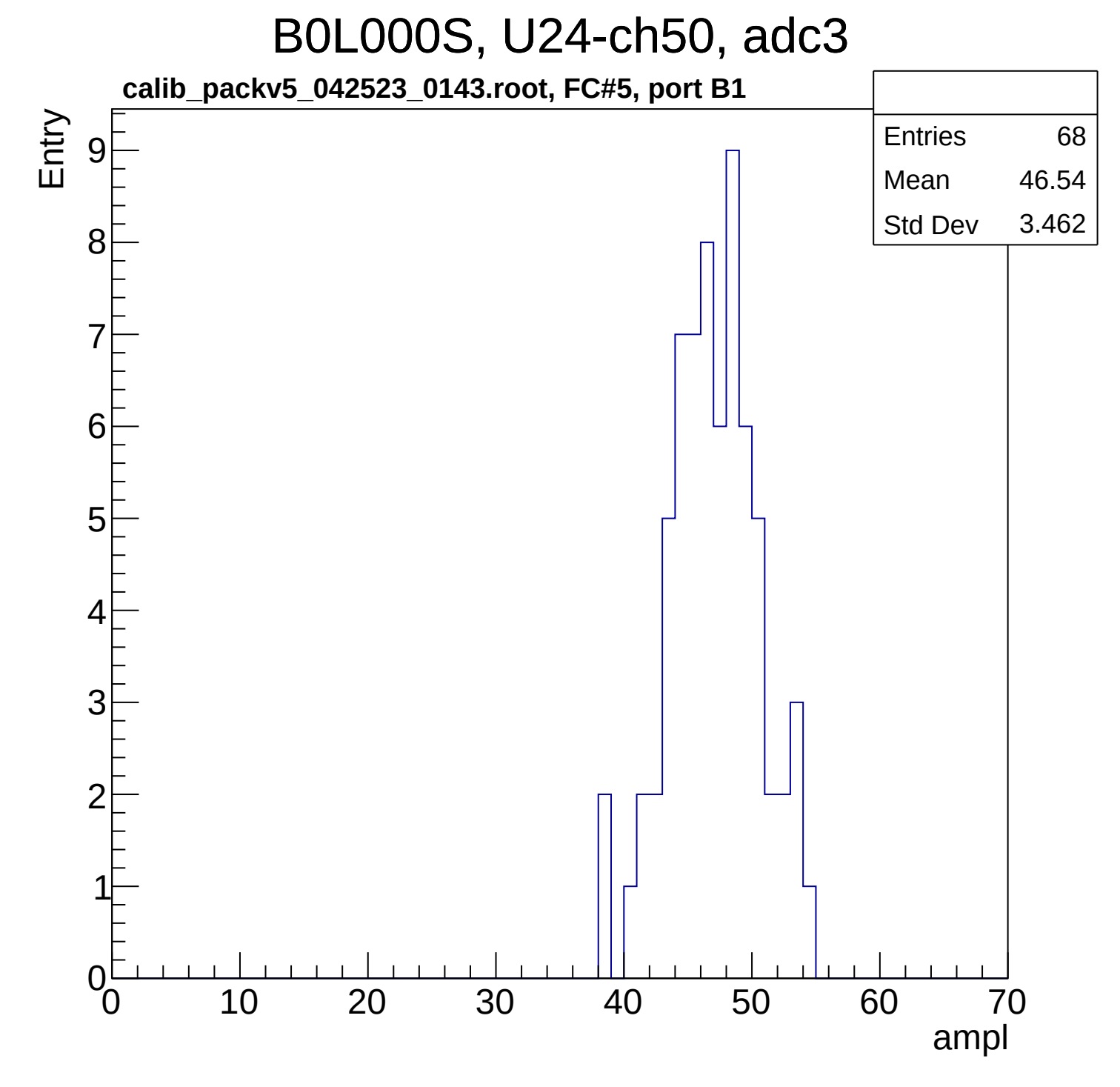
B0L000S, U24-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 46.54
Std Dev 3.462
Entry
ampl
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3
4
5
6
7
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0
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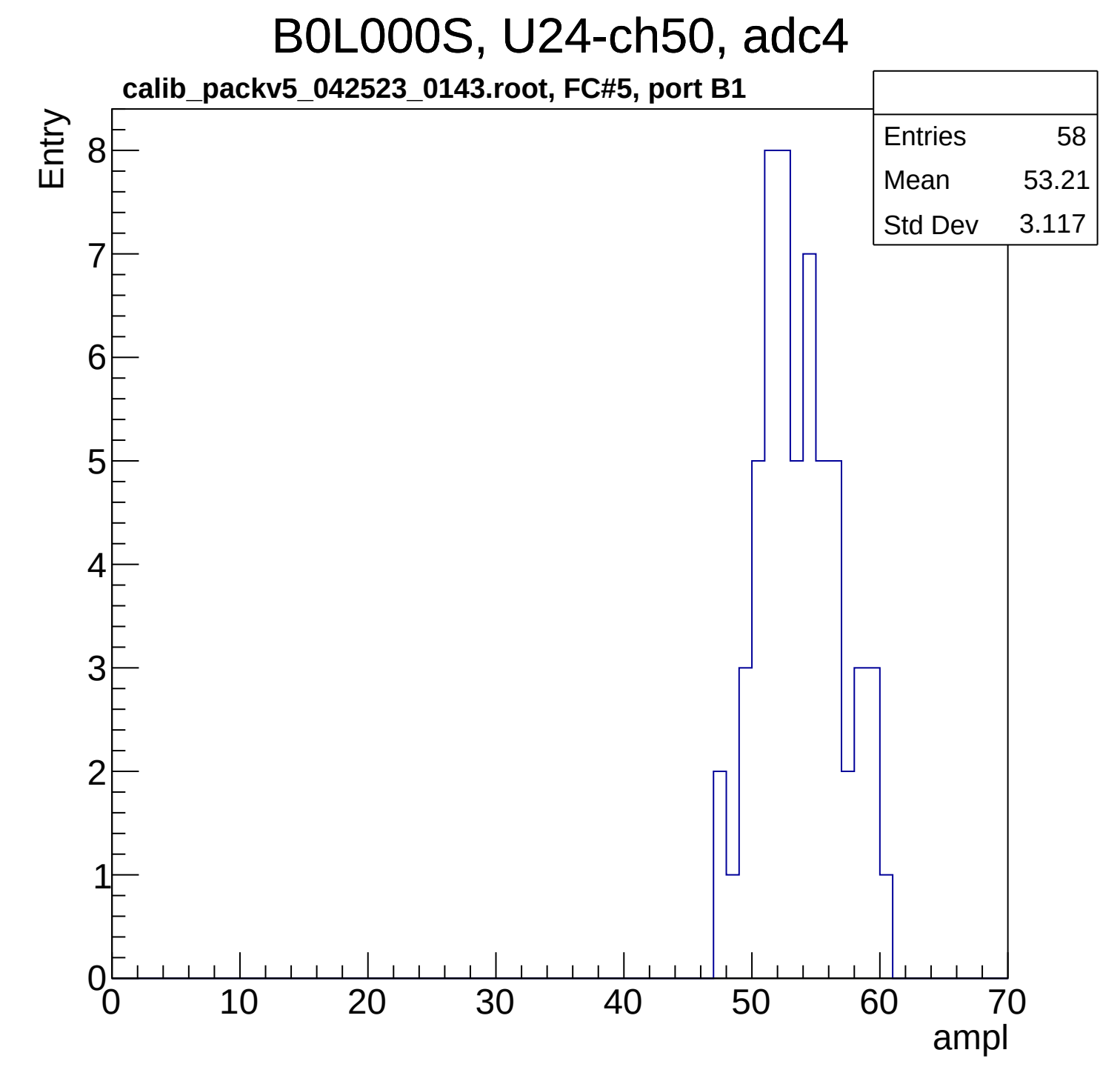
B0L000S, U24-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.21
Std Dev 3.117
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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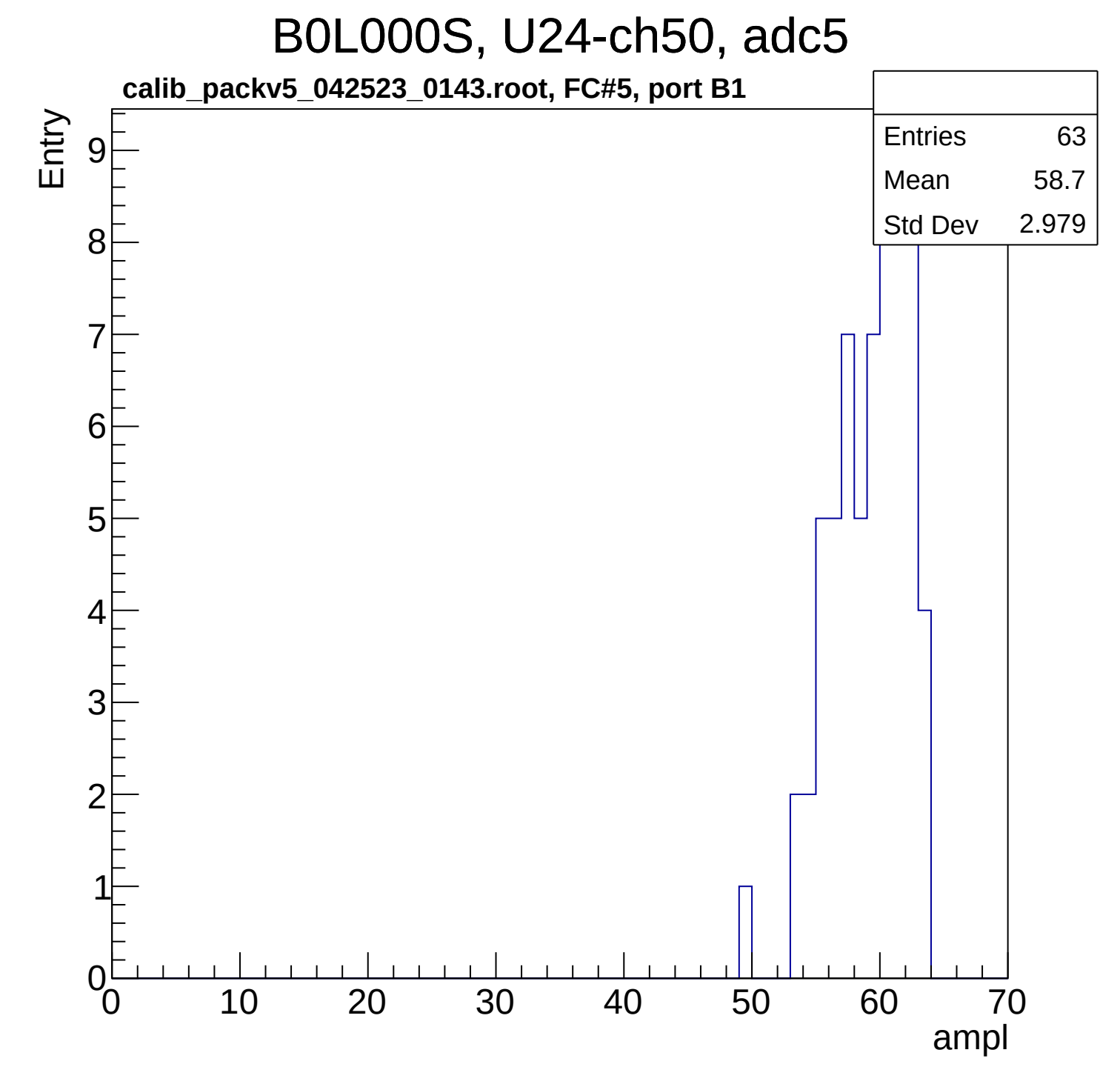
B0L000S, U24-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 58.7
Std Dev 2.979
Entry
ampl
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2
3
4
5
6
7
8
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0
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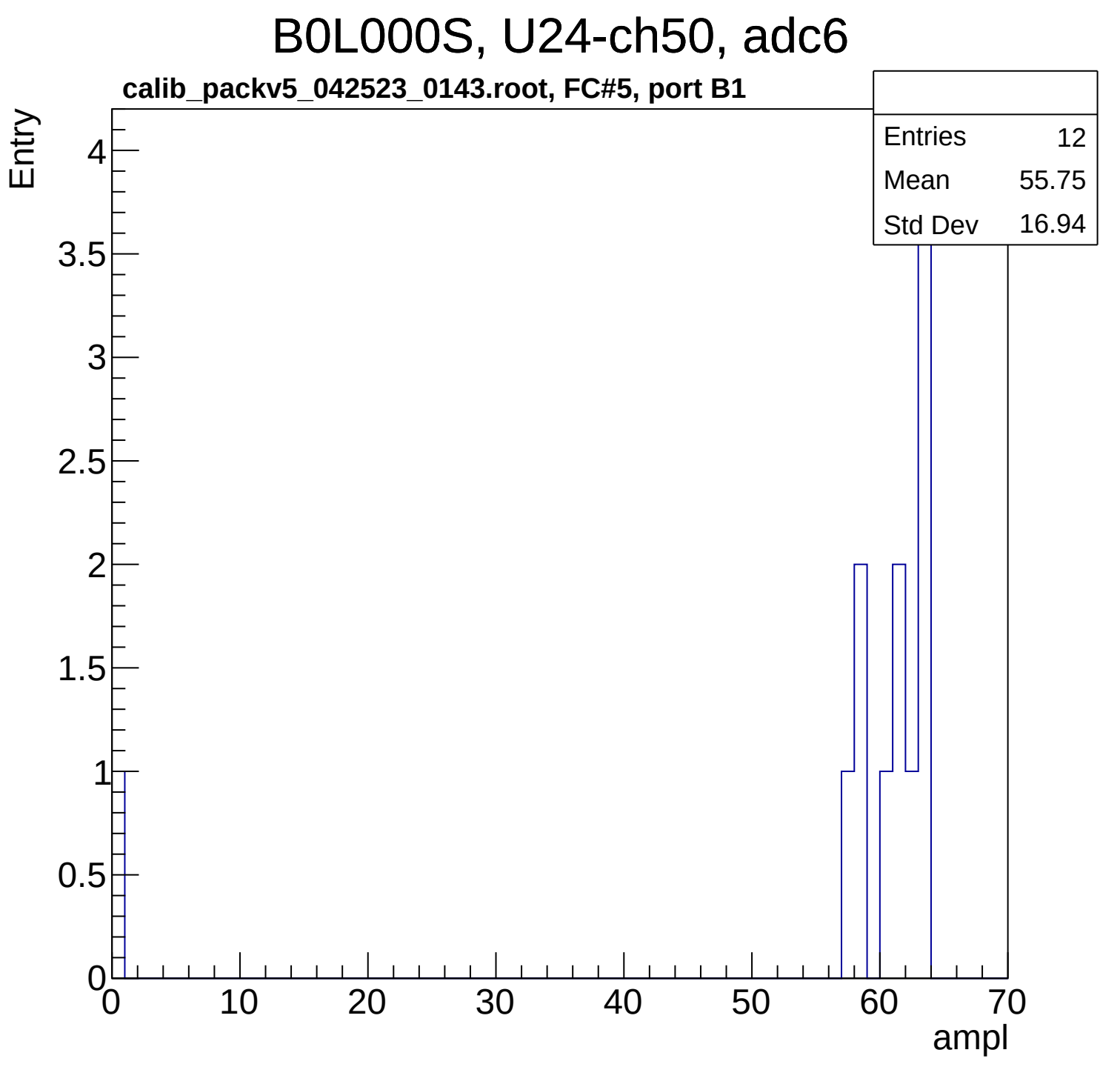
B0L000S, U24-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 55.75
Std Dev 16.94
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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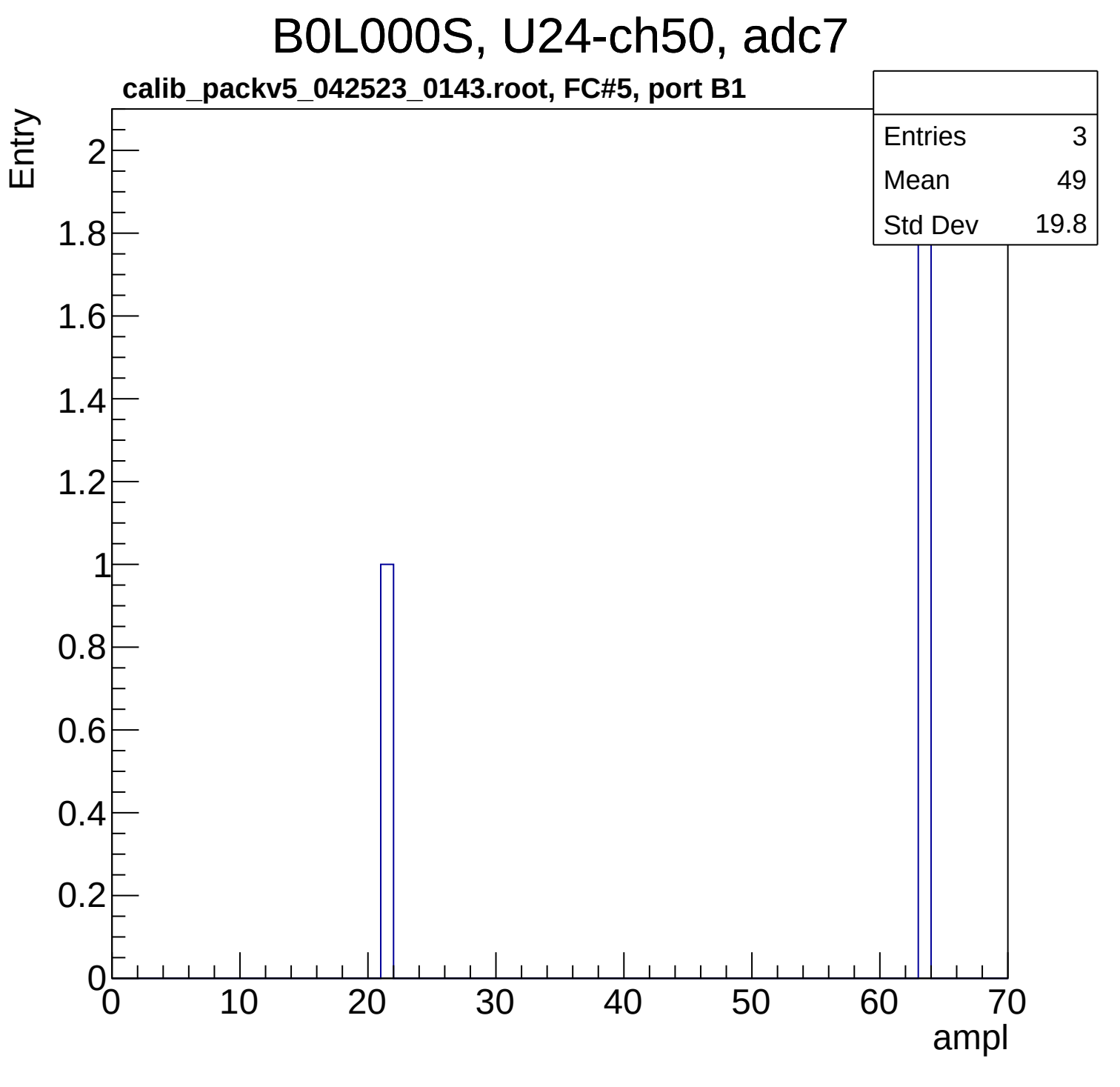
B0L000S, U24-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 49
Std Dev 19.8
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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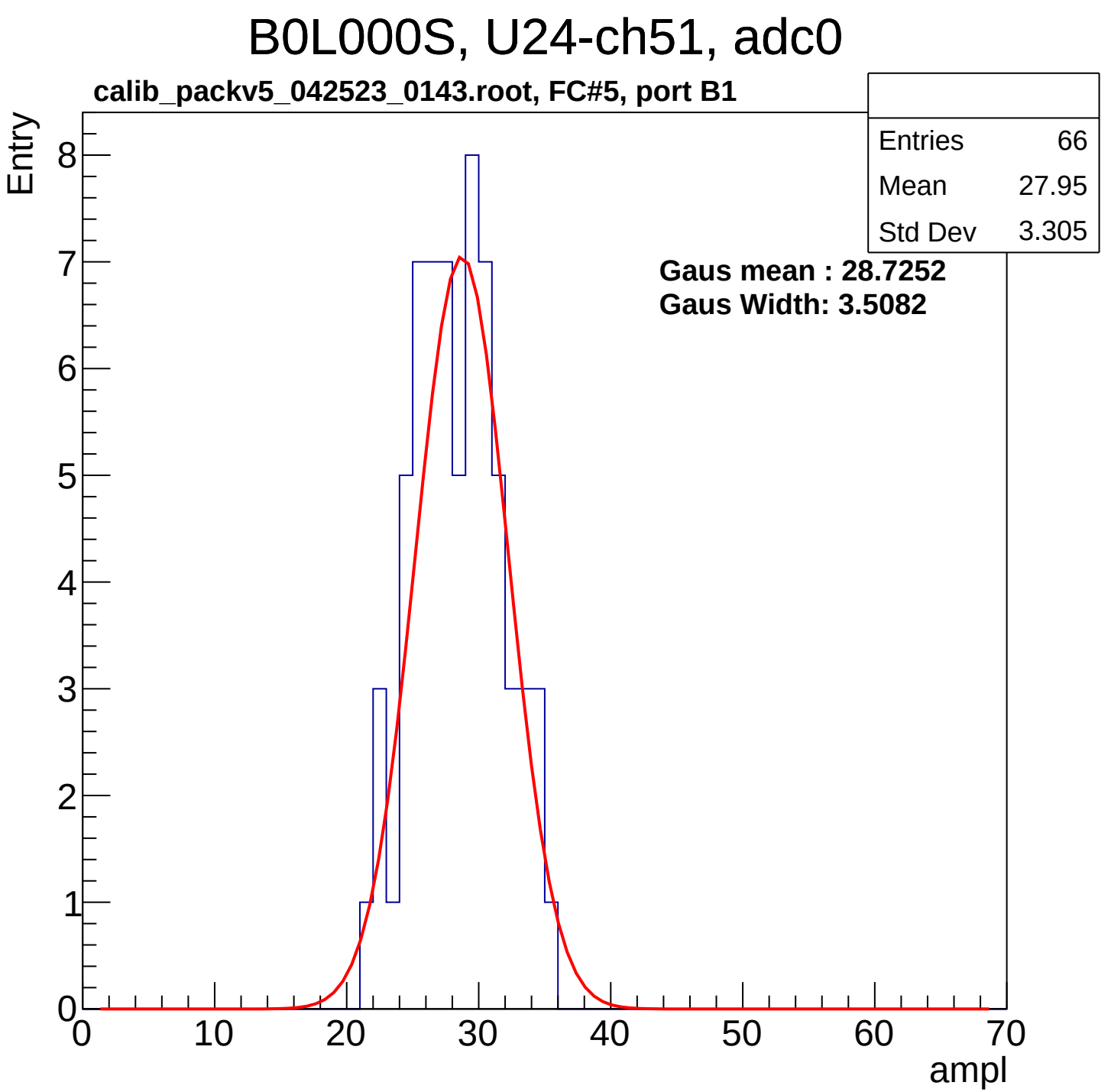
B0L000S, U24-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 27.95
Std Dev 3.305
Gaus mean : 28.7252
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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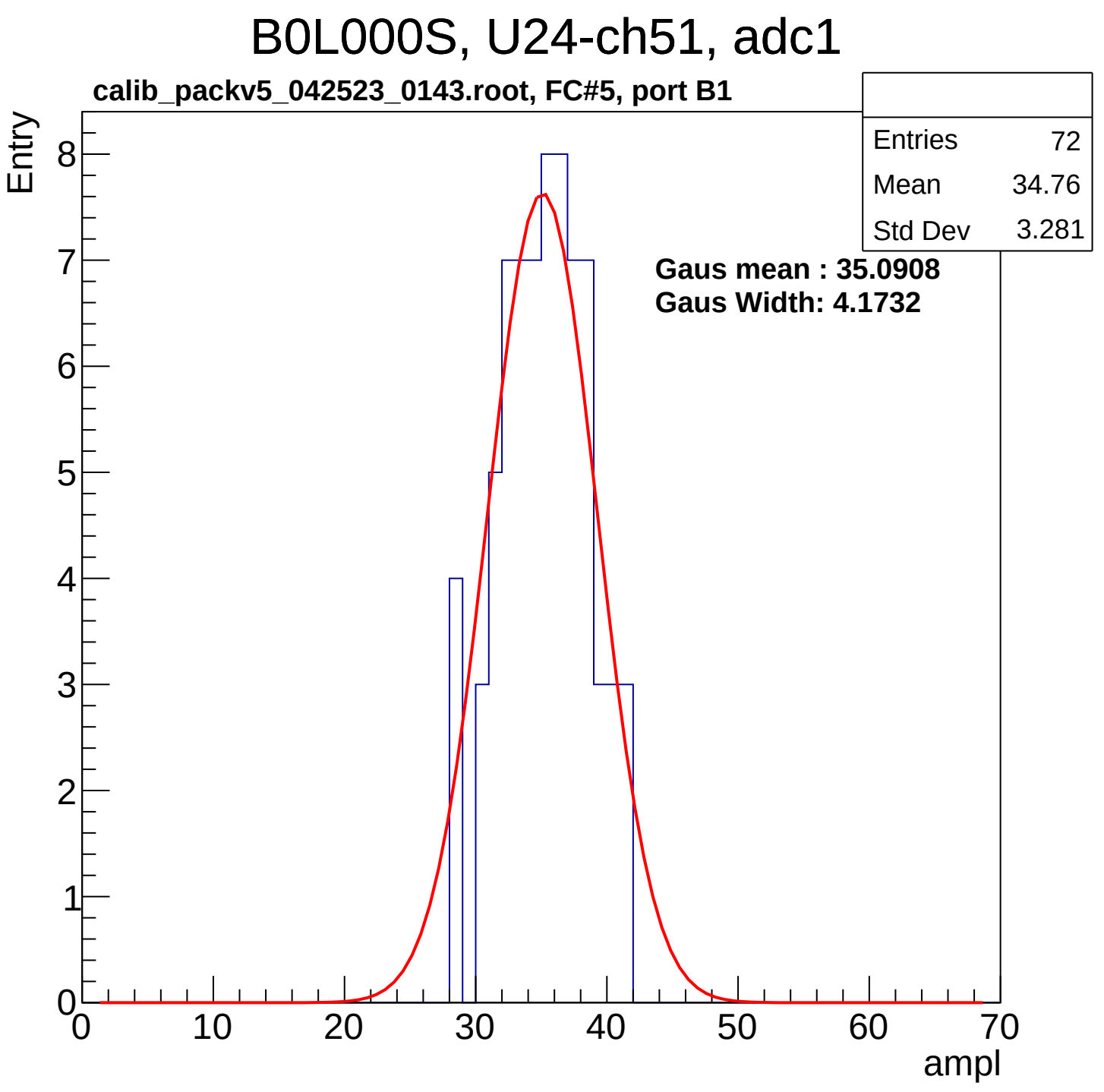
B0L000S, U24-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 34.76
Std Dev 3.281
Gaus mean : 35.0908
Gaus Width: 4.1732
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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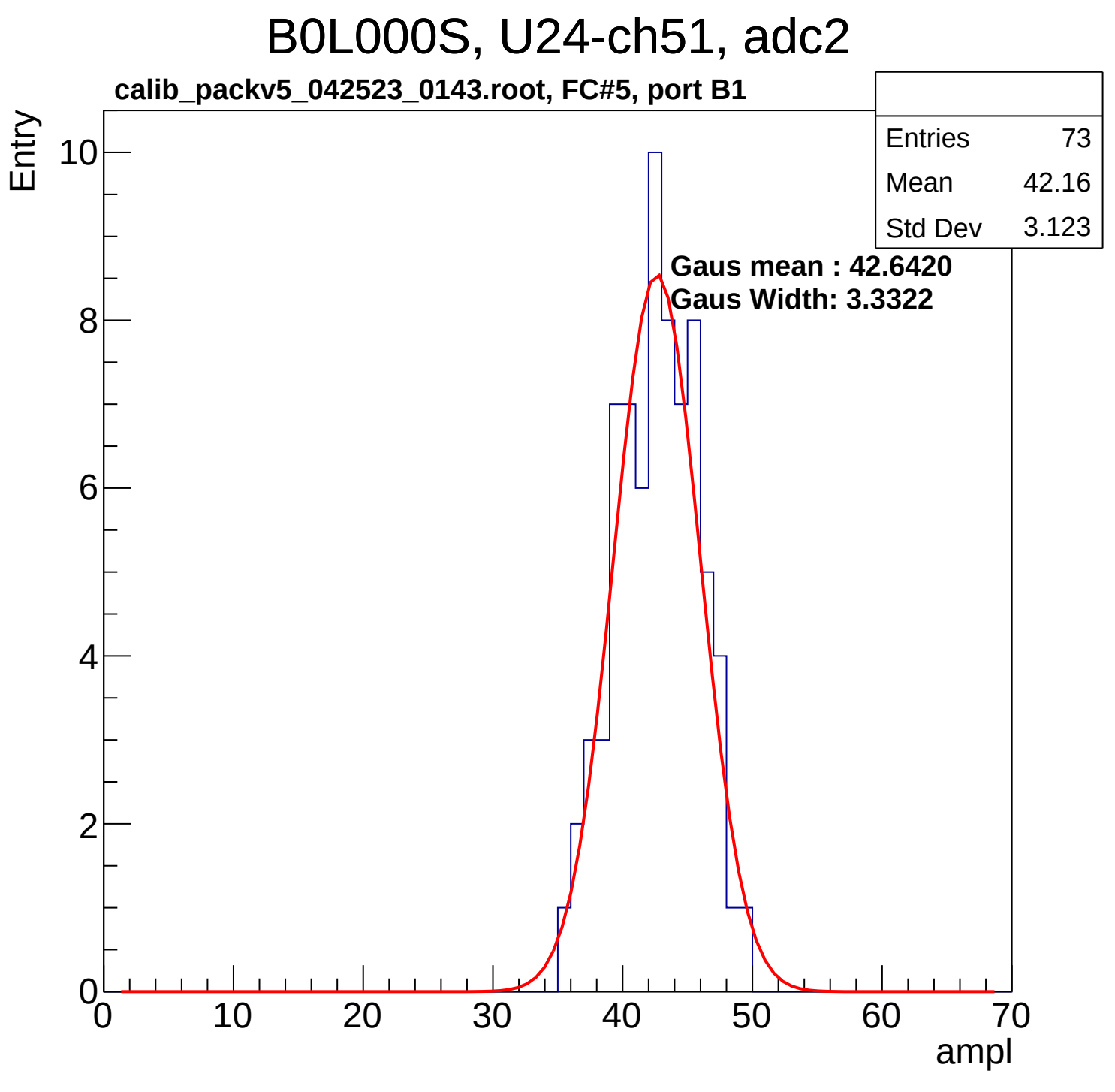
B0L000S, U24-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.16
Std Dev 3.123
Gaus mean : 42.6420
Gaus Width: 3.3322
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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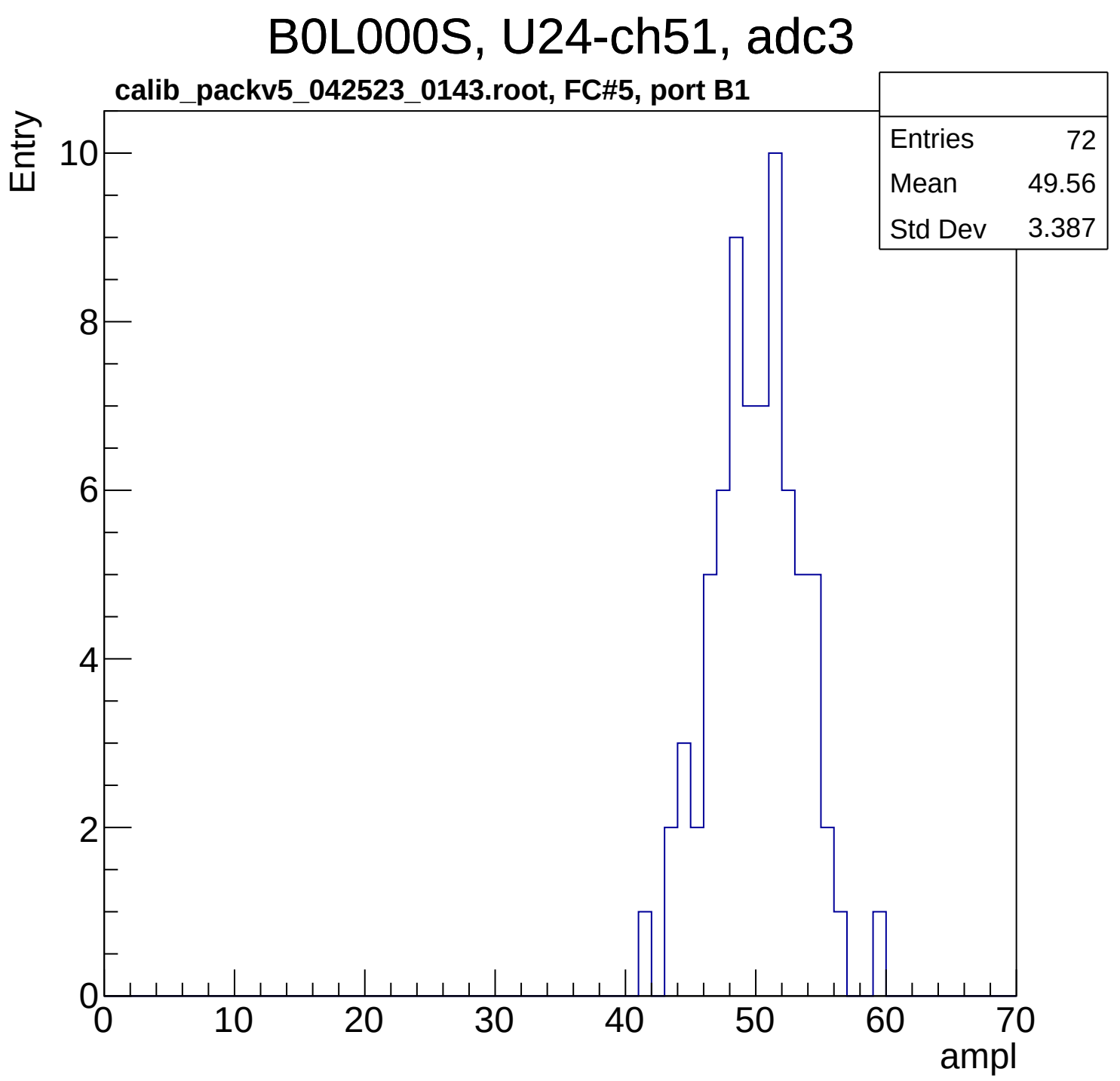
B0L000S, U24-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.56
Std Dev 3.387
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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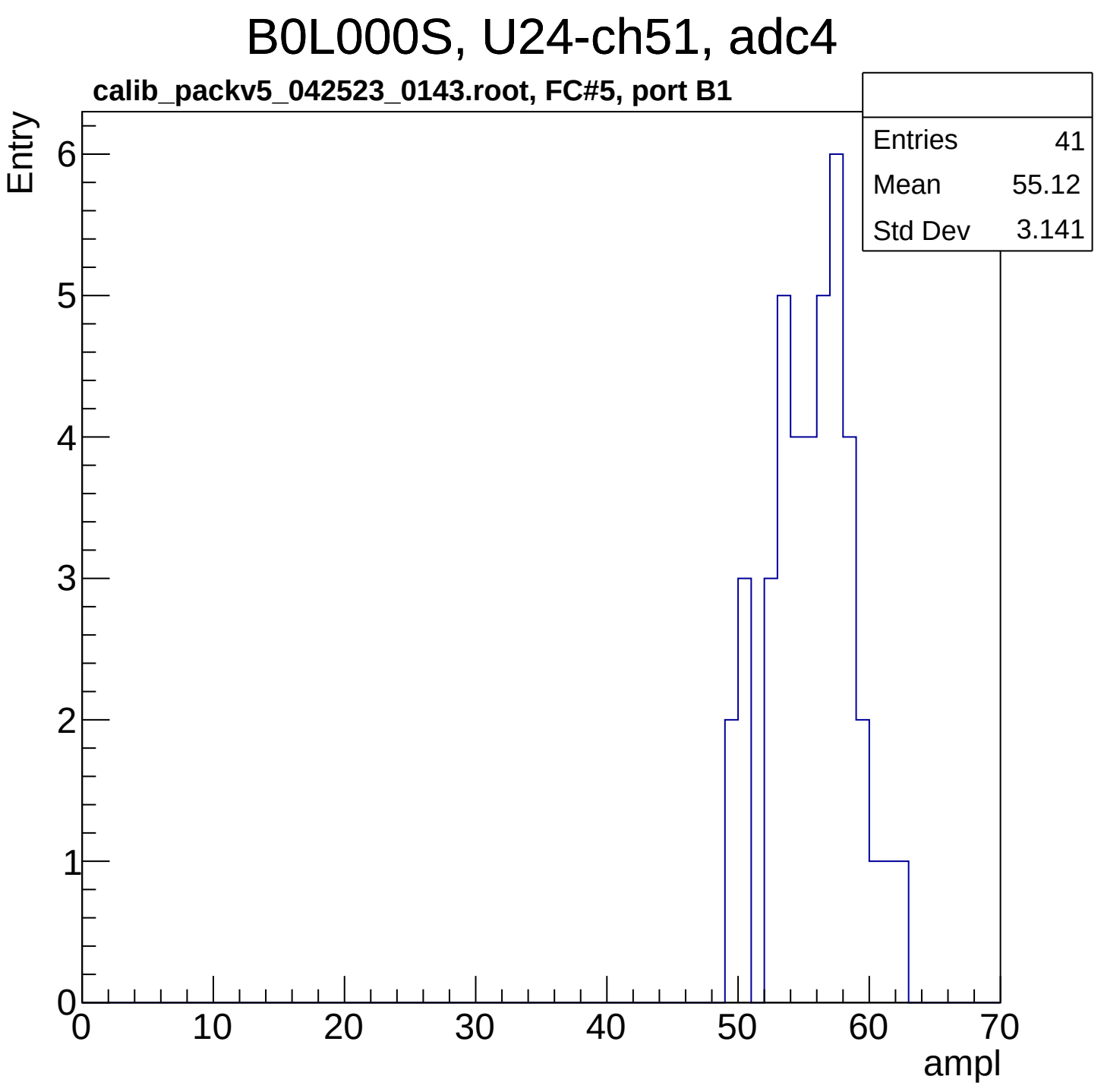
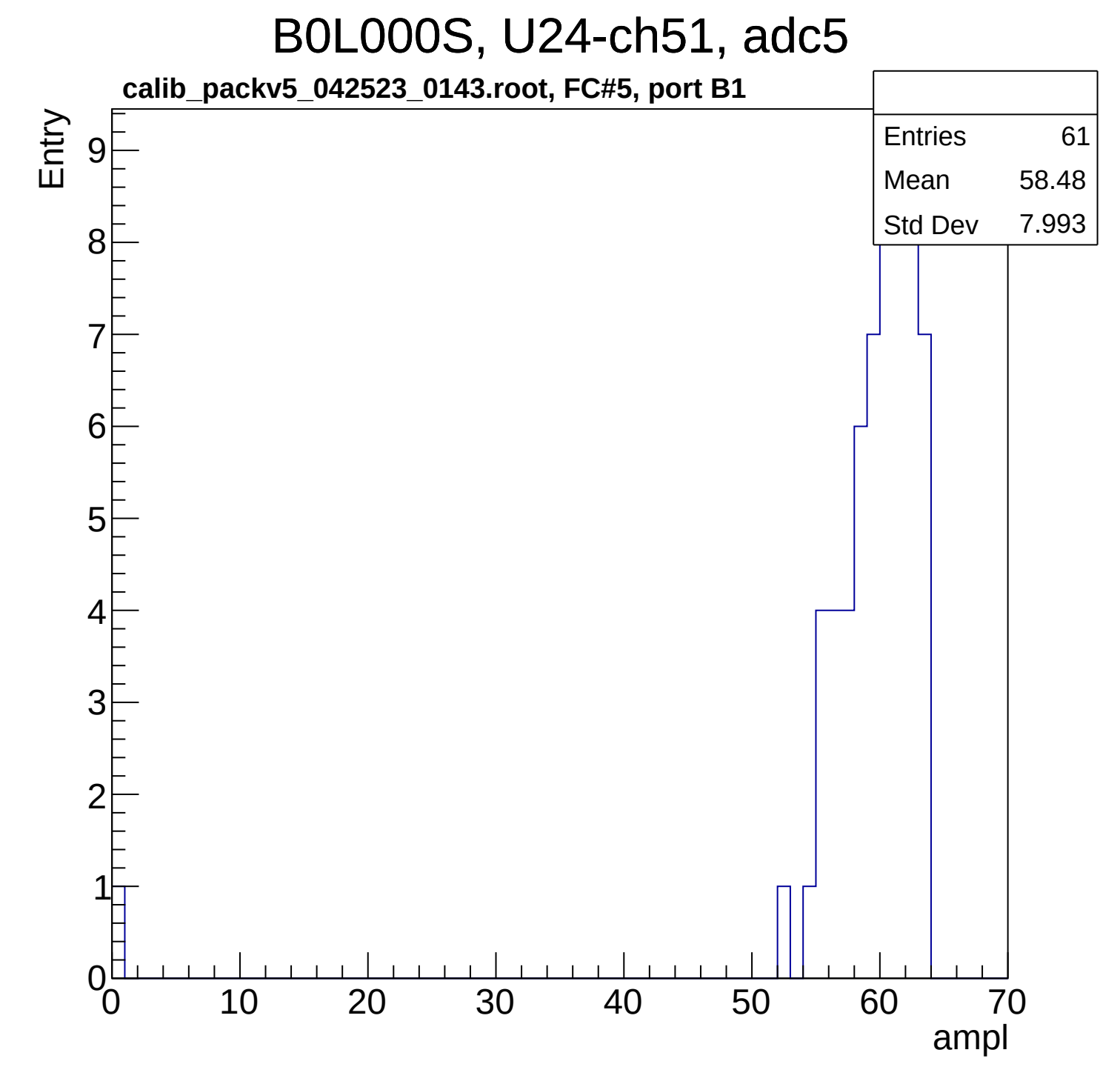

B0L000S, U24-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 55.12
Std Dev 3.141
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 58.48
Std Dev 7.993
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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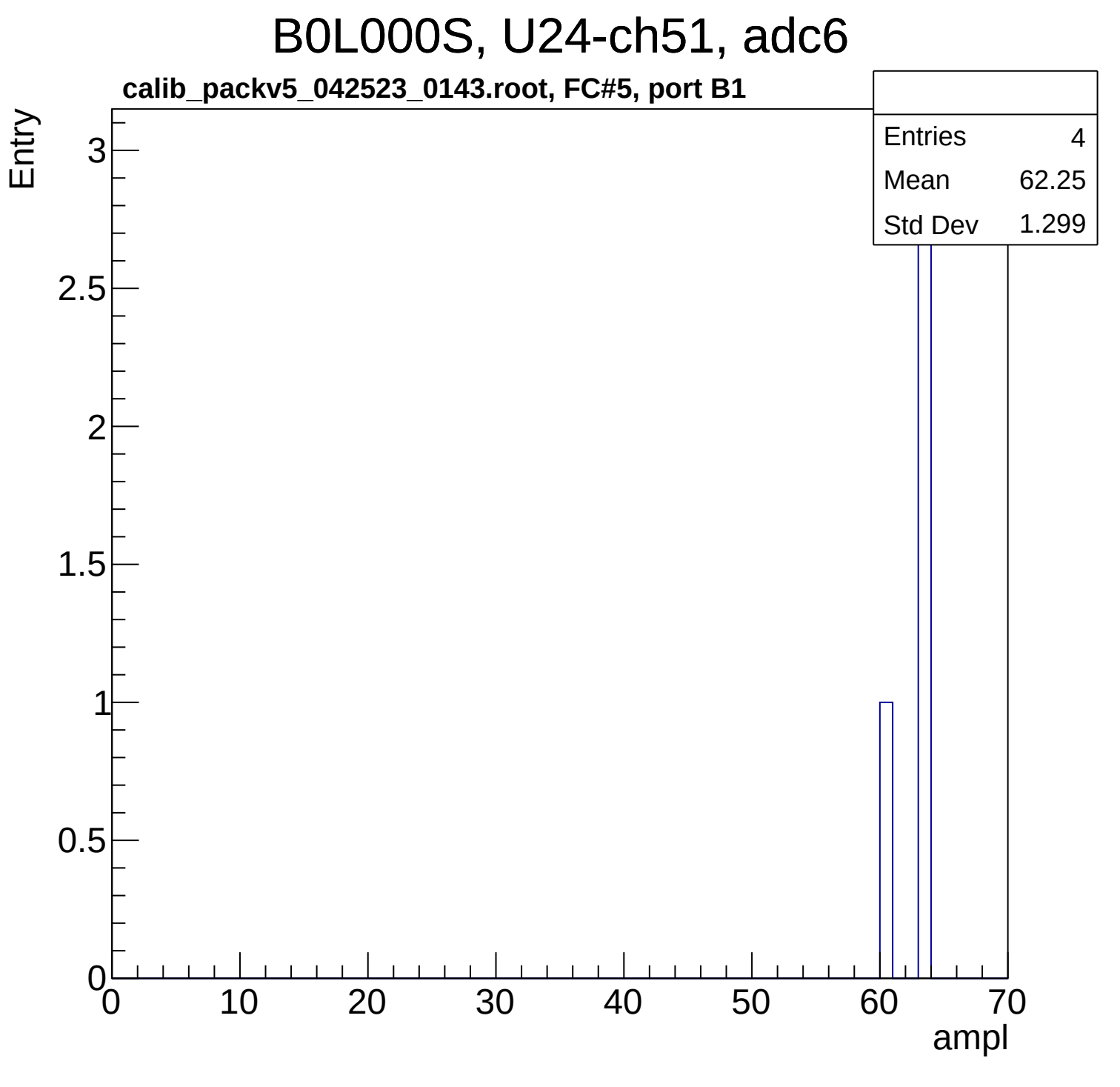
B0L000S, U24-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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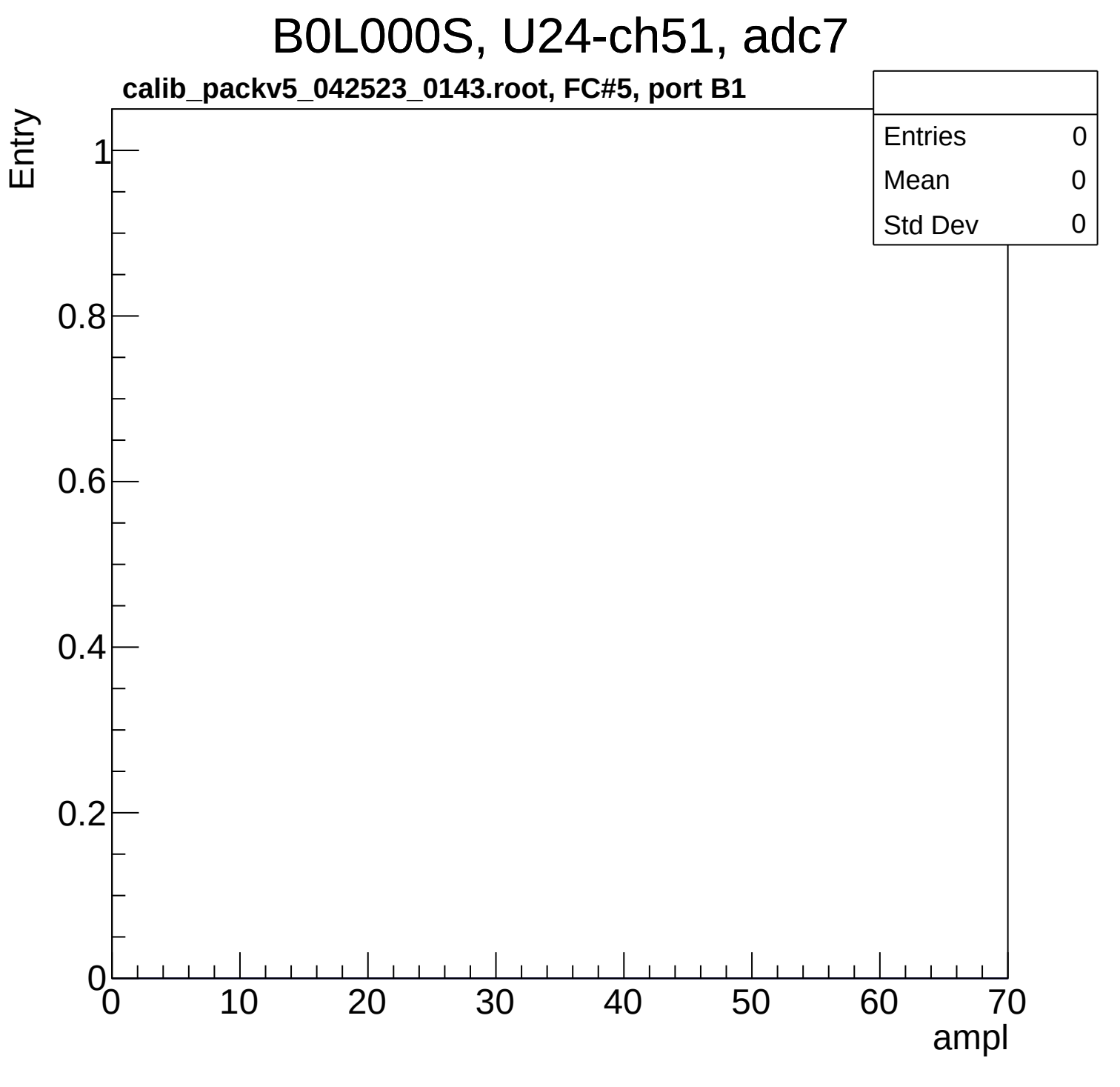
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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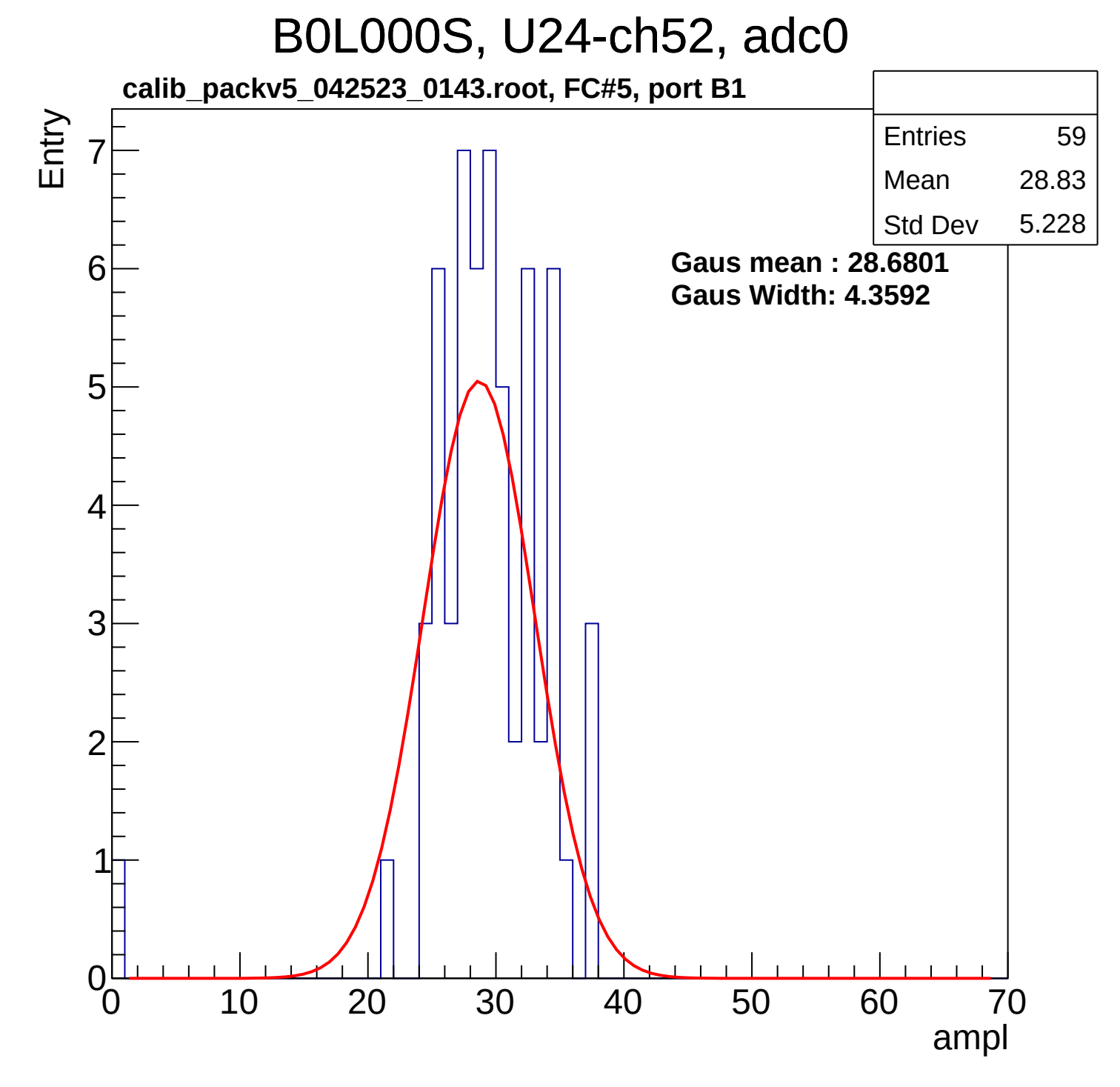
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 28.83
Std Dev 5.228
Gaus mean : 28.6801
Gaus Width: 4.3592
Entry
ampl
0
1
2
3
4
5
6
7
0
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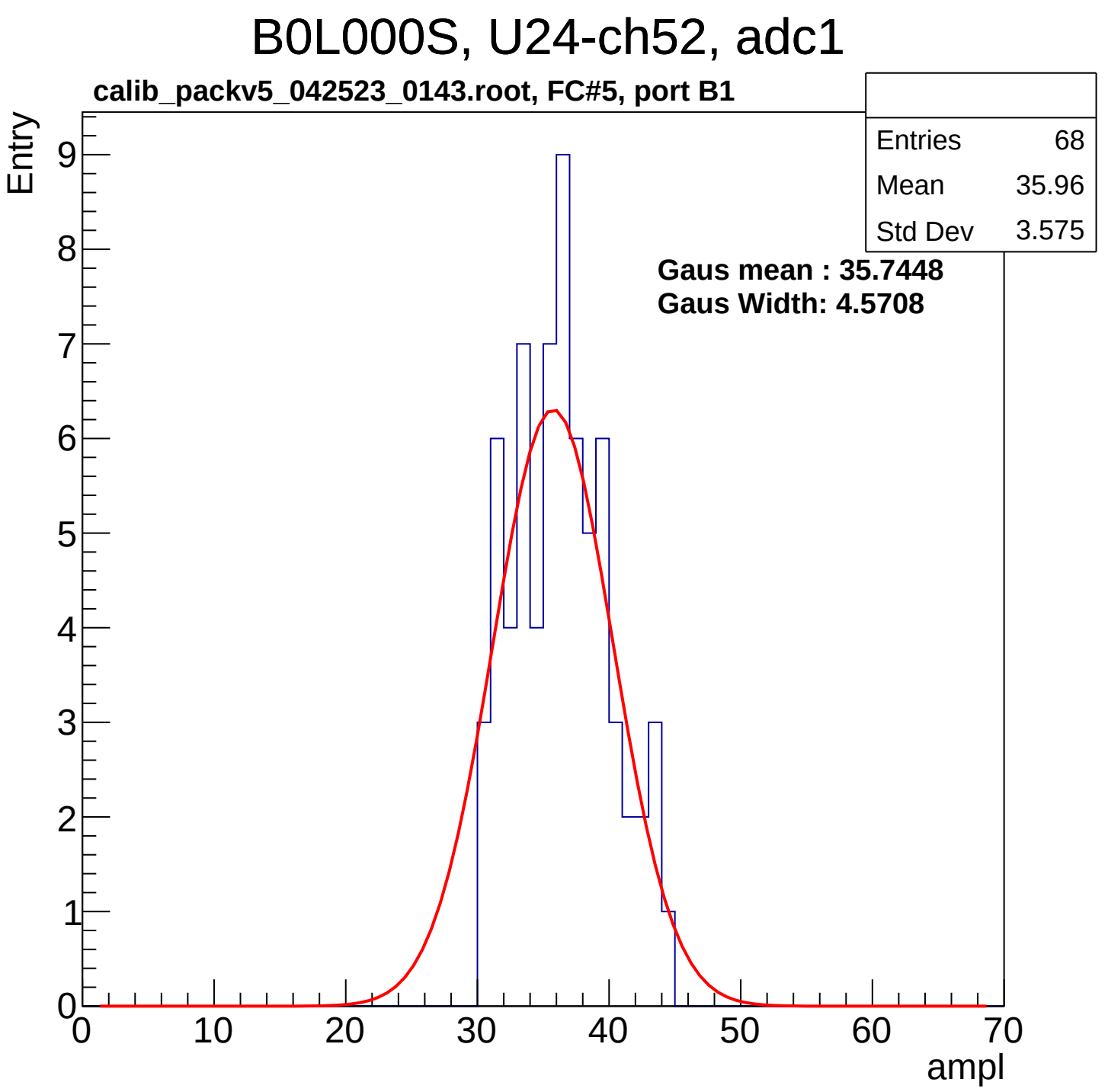
20
30
40
50
60
70


B0L000S, U24-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.96
Std Dev 3.575
Gaus mean : 35.7448
Gaus Width: 4.5708
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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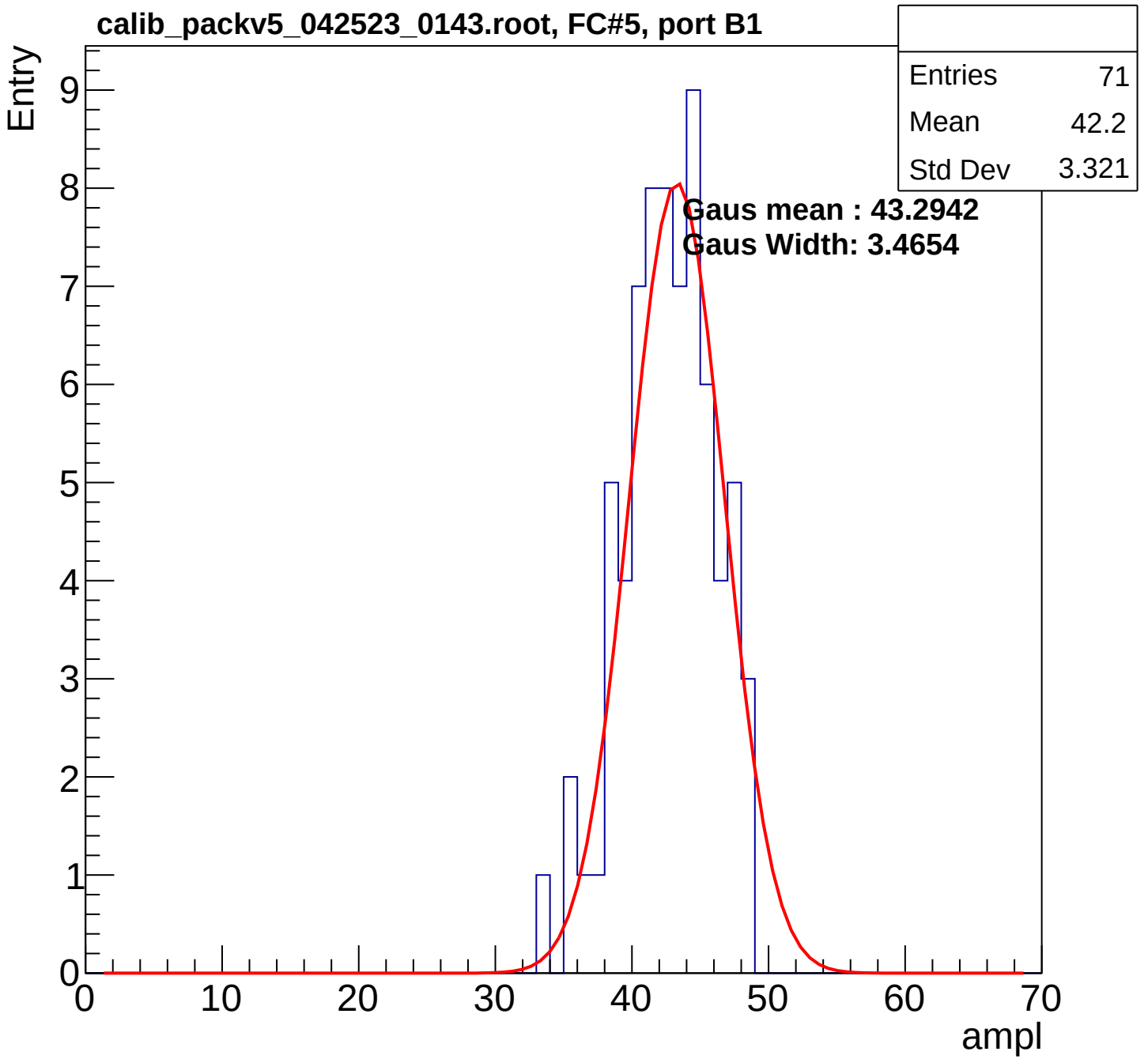
B0L000S, U24-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.2
Std Dev 3.321
Gaus mean : 43.2942
Gaus Width: 3.4654
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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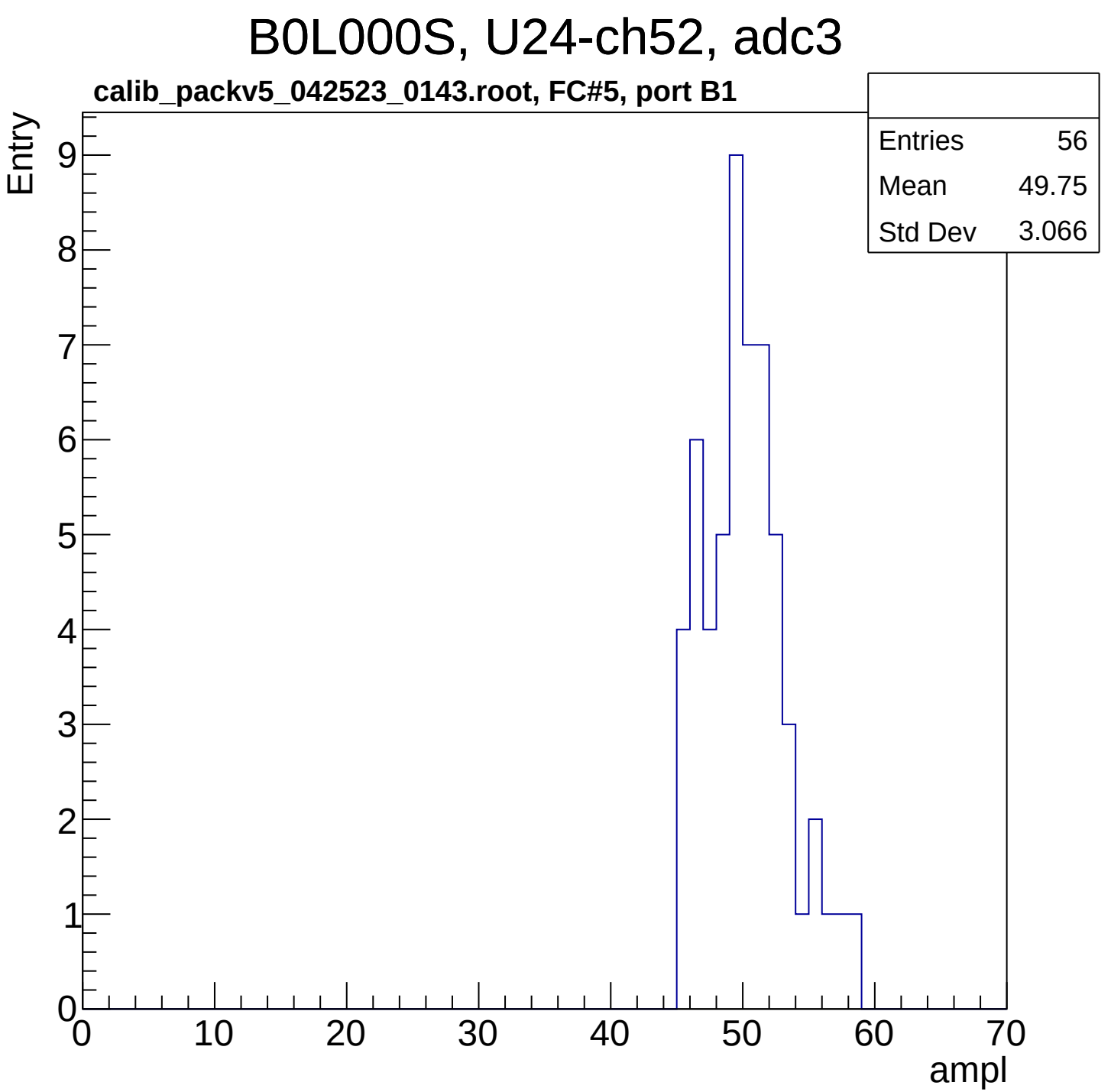
B0L000S, U24-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 49.75
Std Dev 3.066
Entry
ampl
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3
4
5
6
7
8
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0
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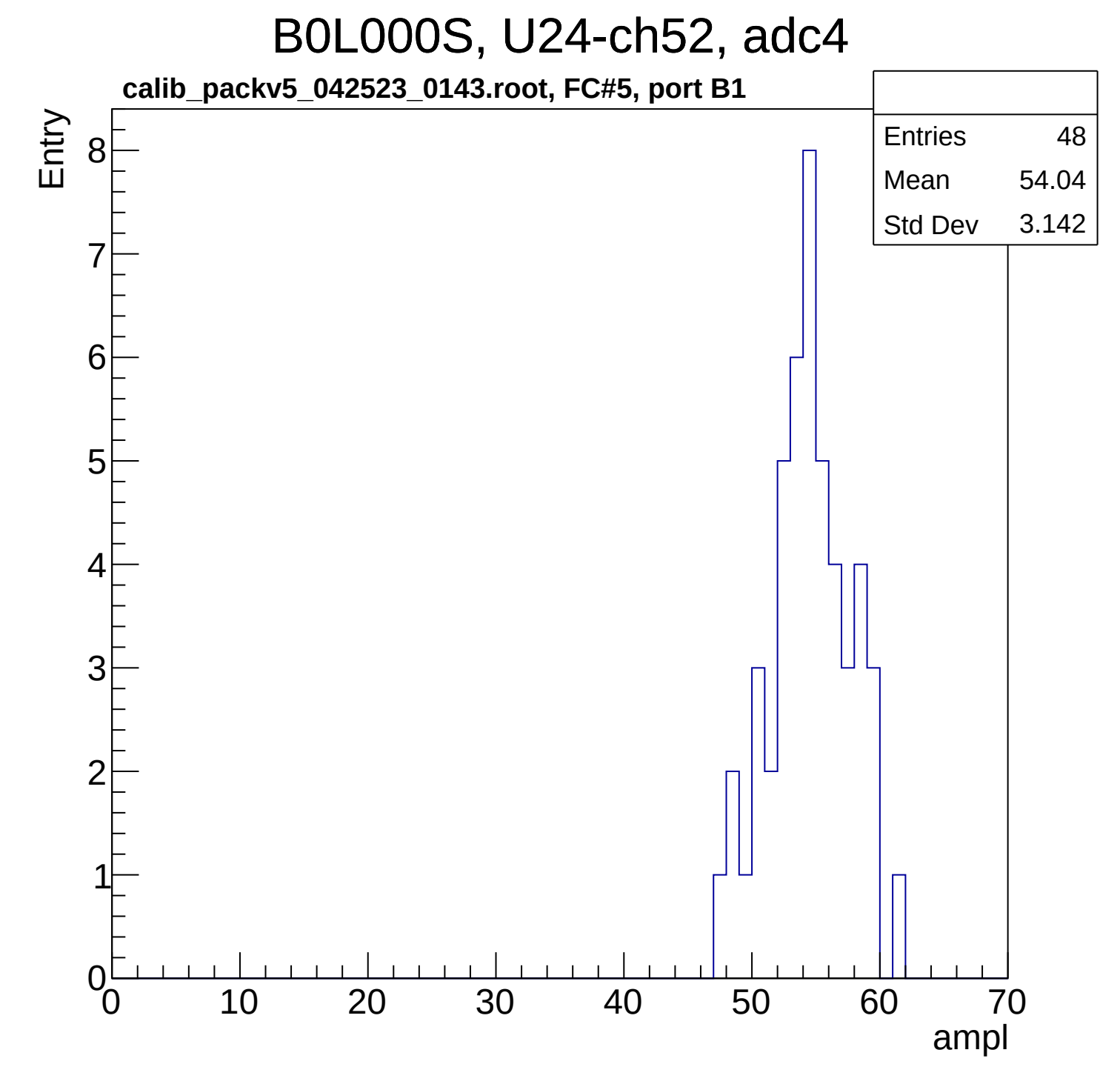
B0L000S, U24-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.04
Std Dev 3.142
Entry
ampl
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2
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4
5
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7
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0
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60
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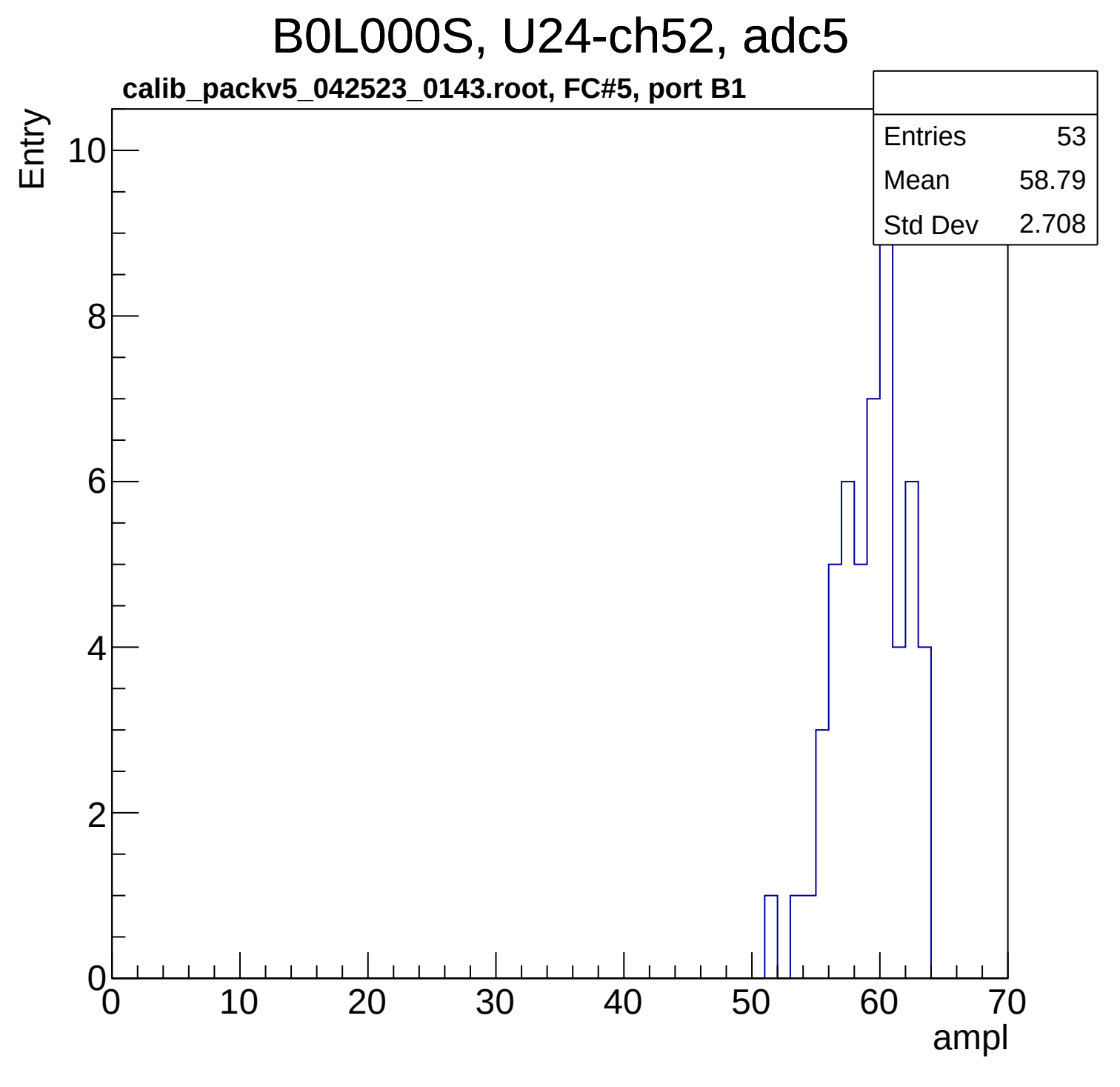
B0L000S, U24-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.79
Std Dev 2.708
Entry
0
2
4
6
8
10
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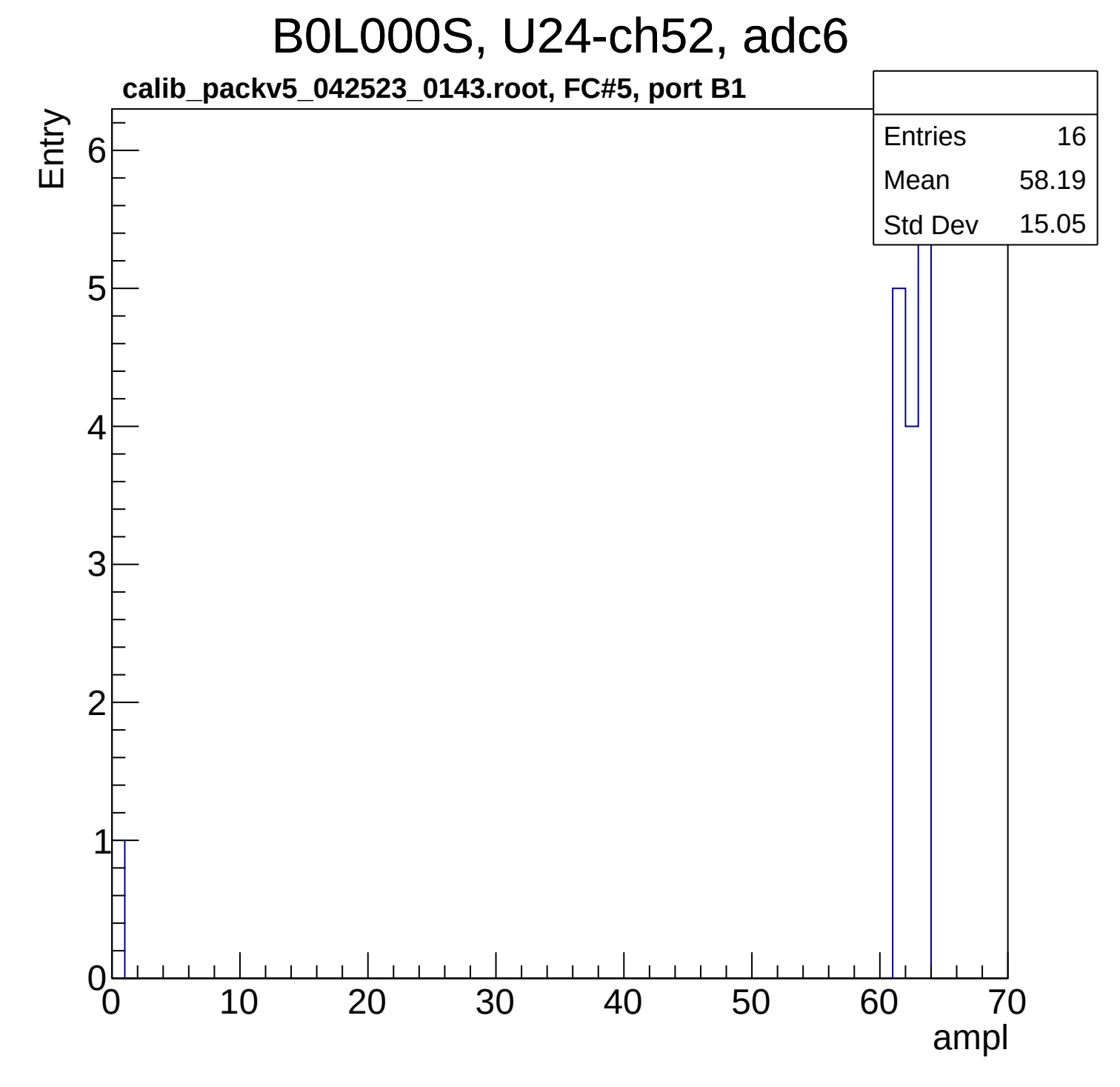
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 58.19
Std Dev 15.05
Entry
0
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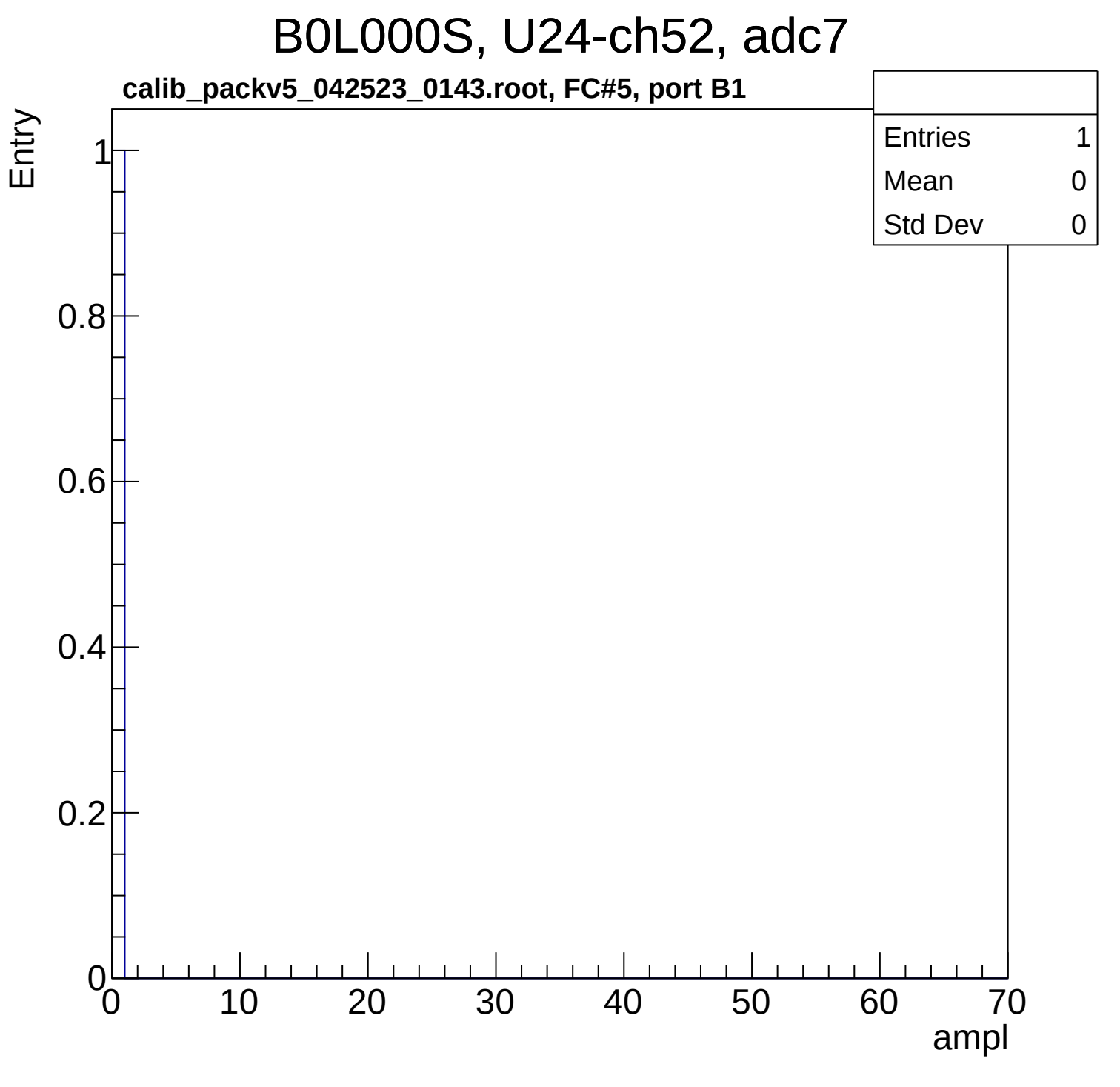
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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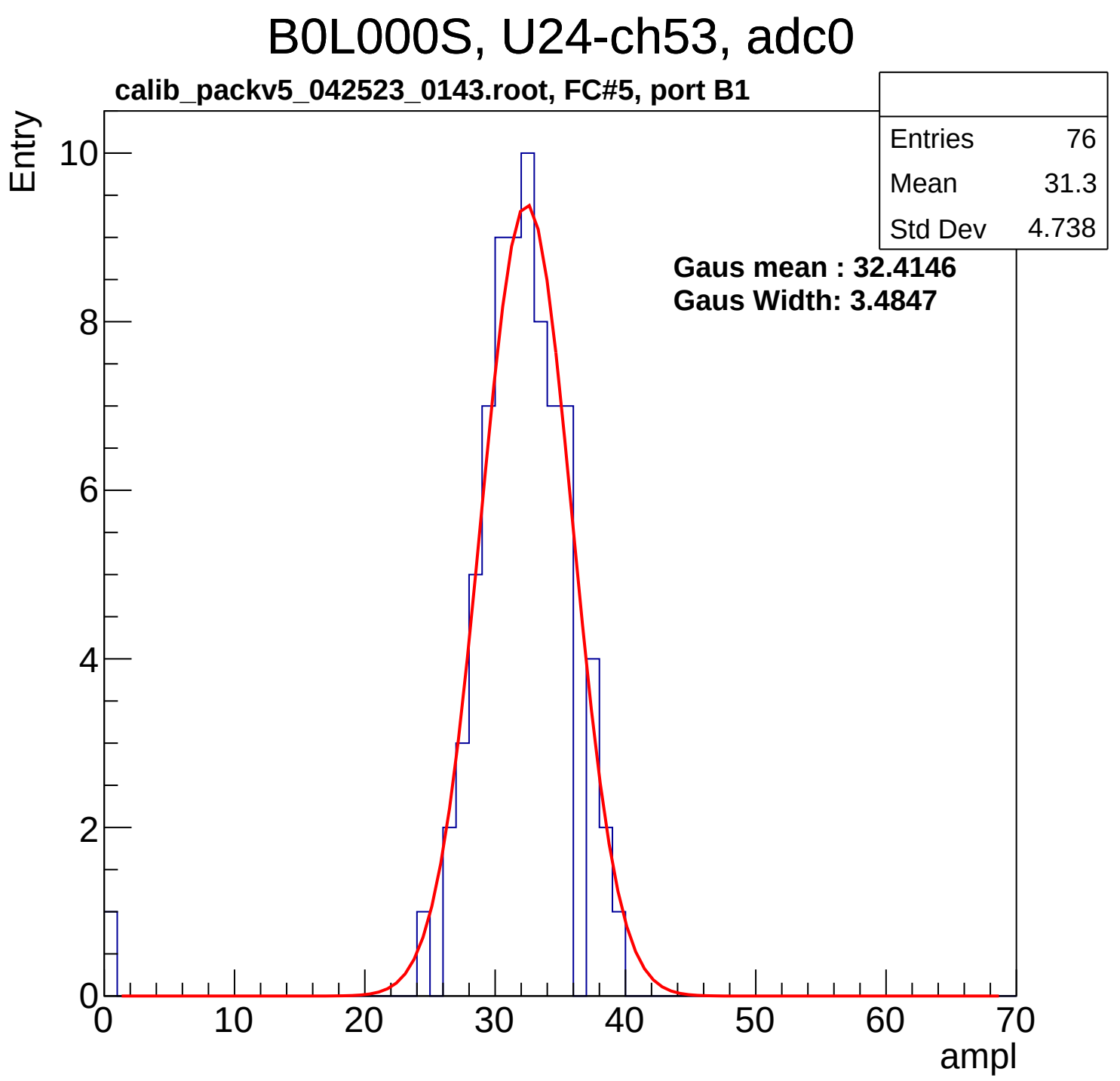
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 31.3
Std Dev 4.738
Gaus mean : 32.4146
Gaus Width: 3.4847
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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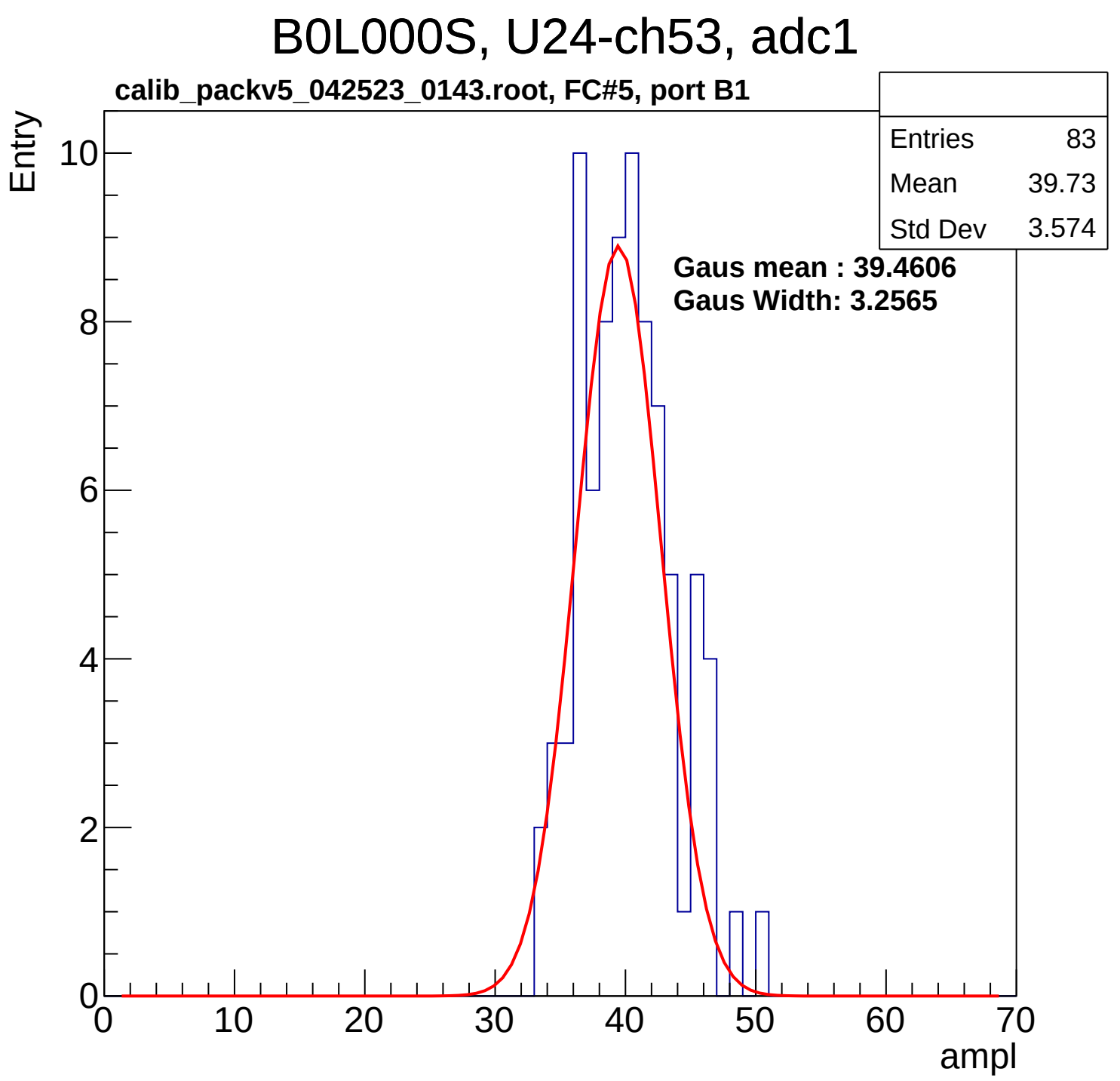
B0L000S, U24-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 39.73
Std Dev 3.574
Gaus mean : 39.4606
Gaus Width: 3.2565
Entry
ampl
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6
8
10
0
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20
30
40
50
60
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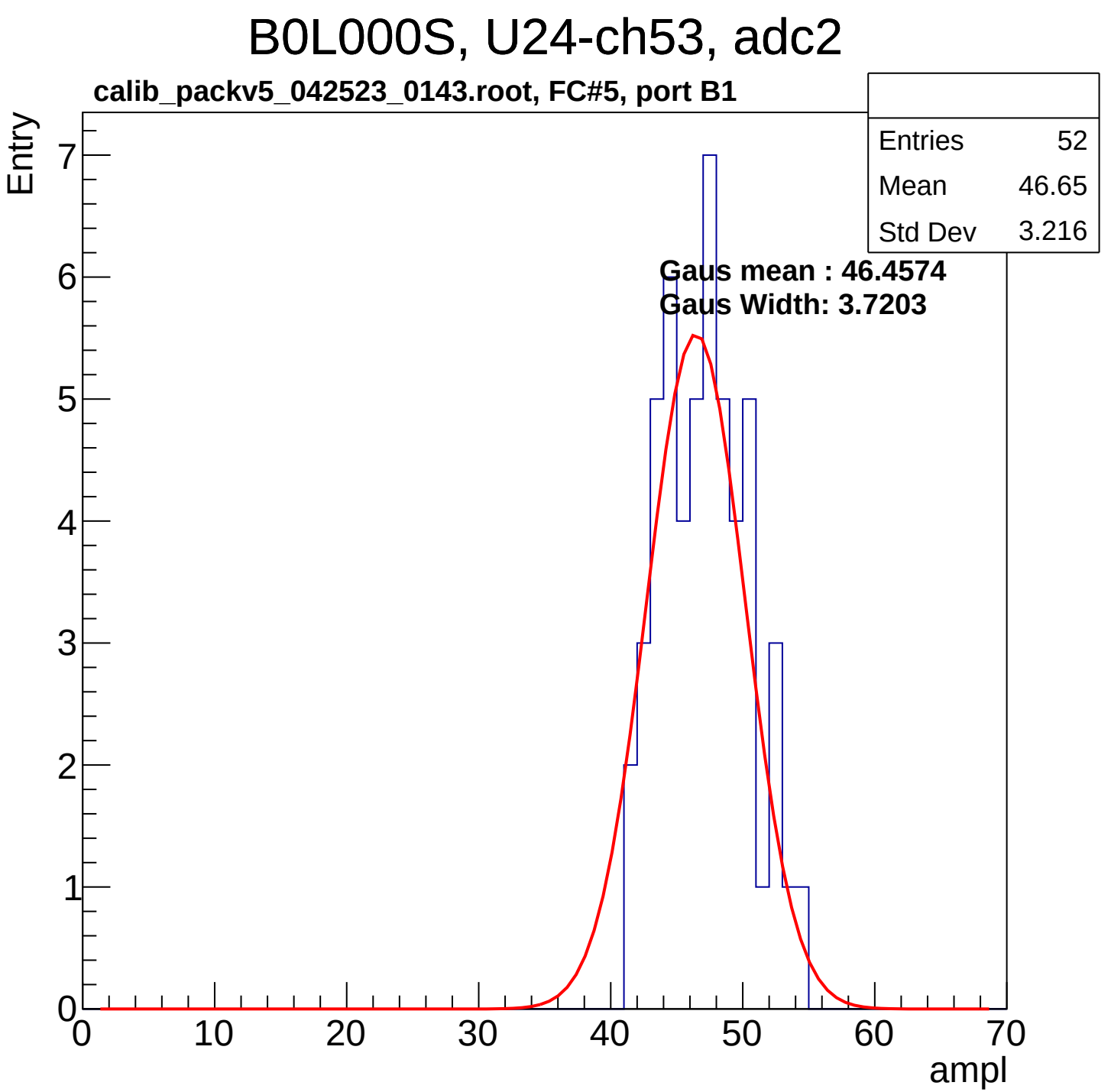
B0L000S, U24-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 46.65
Std Dev 3.216
Gaus mean : 46.4574
Gaus Width: 3.7203
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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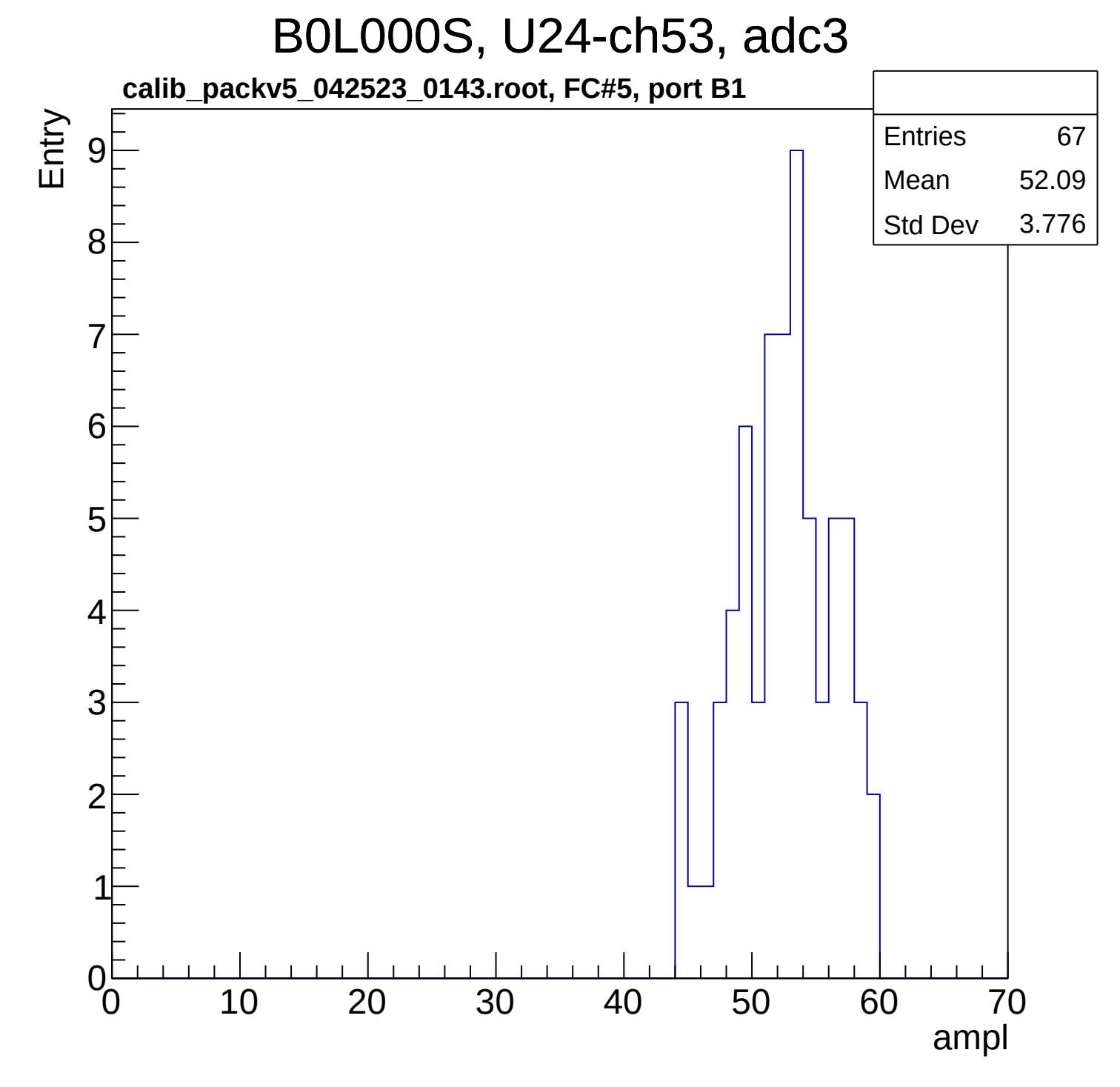
B0L000S, U24-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 52.09
Std Dev 3.776
Entry
ampl
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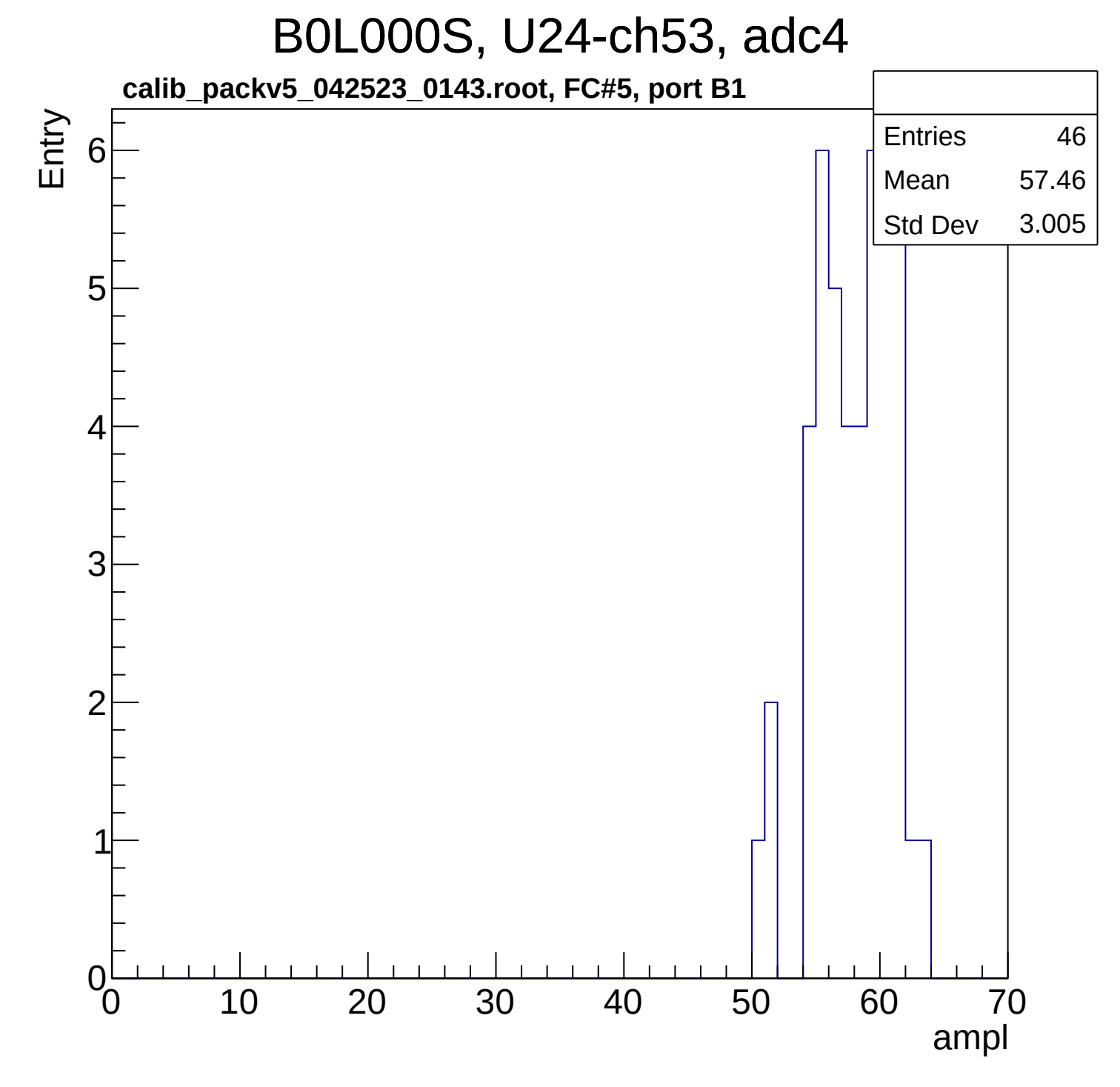
B0L000S, U24-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.46
Std Dev 3.005
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50
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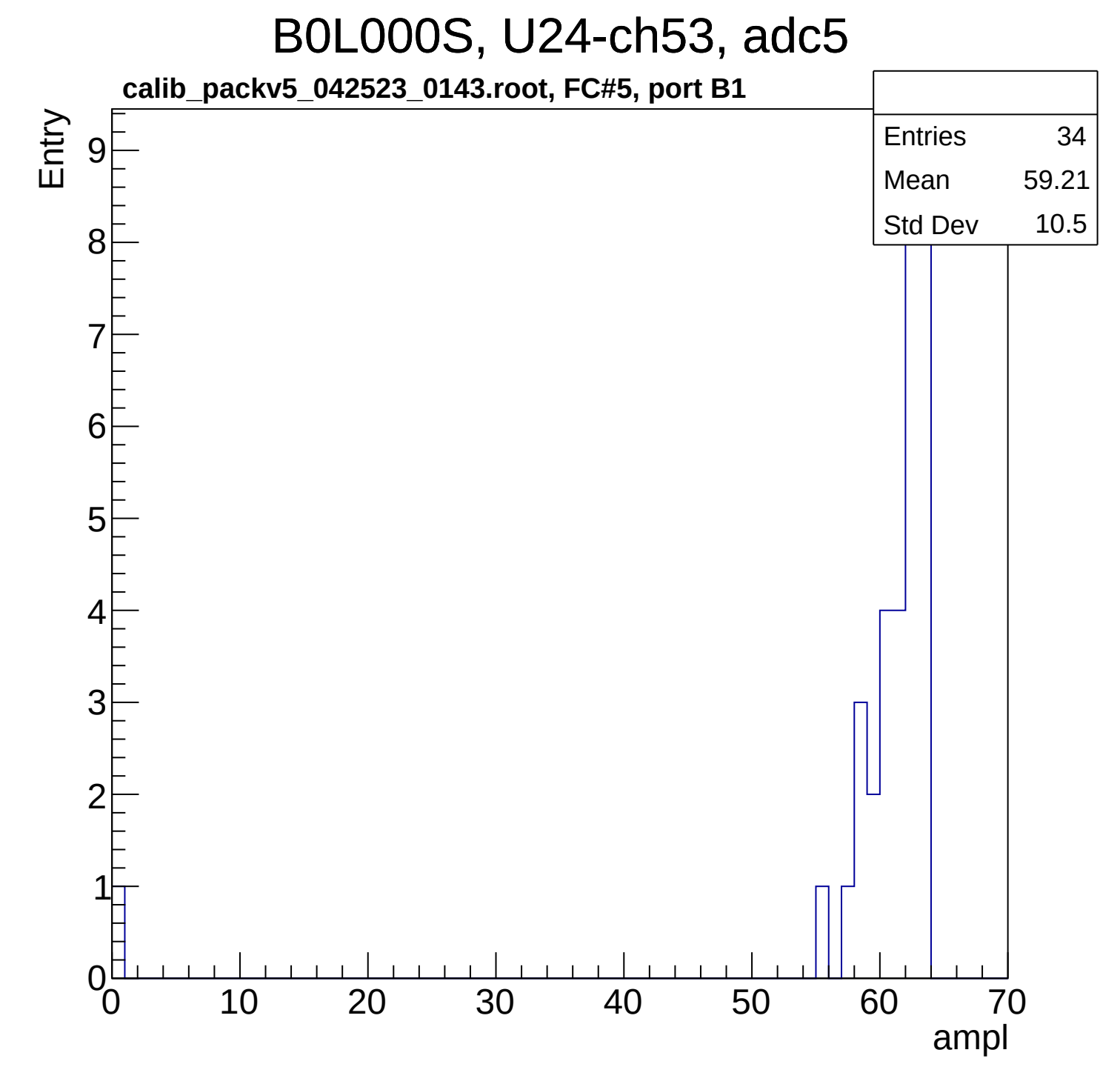
70
ampl


B0L000S, U24-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.21
Std Dev 10.5
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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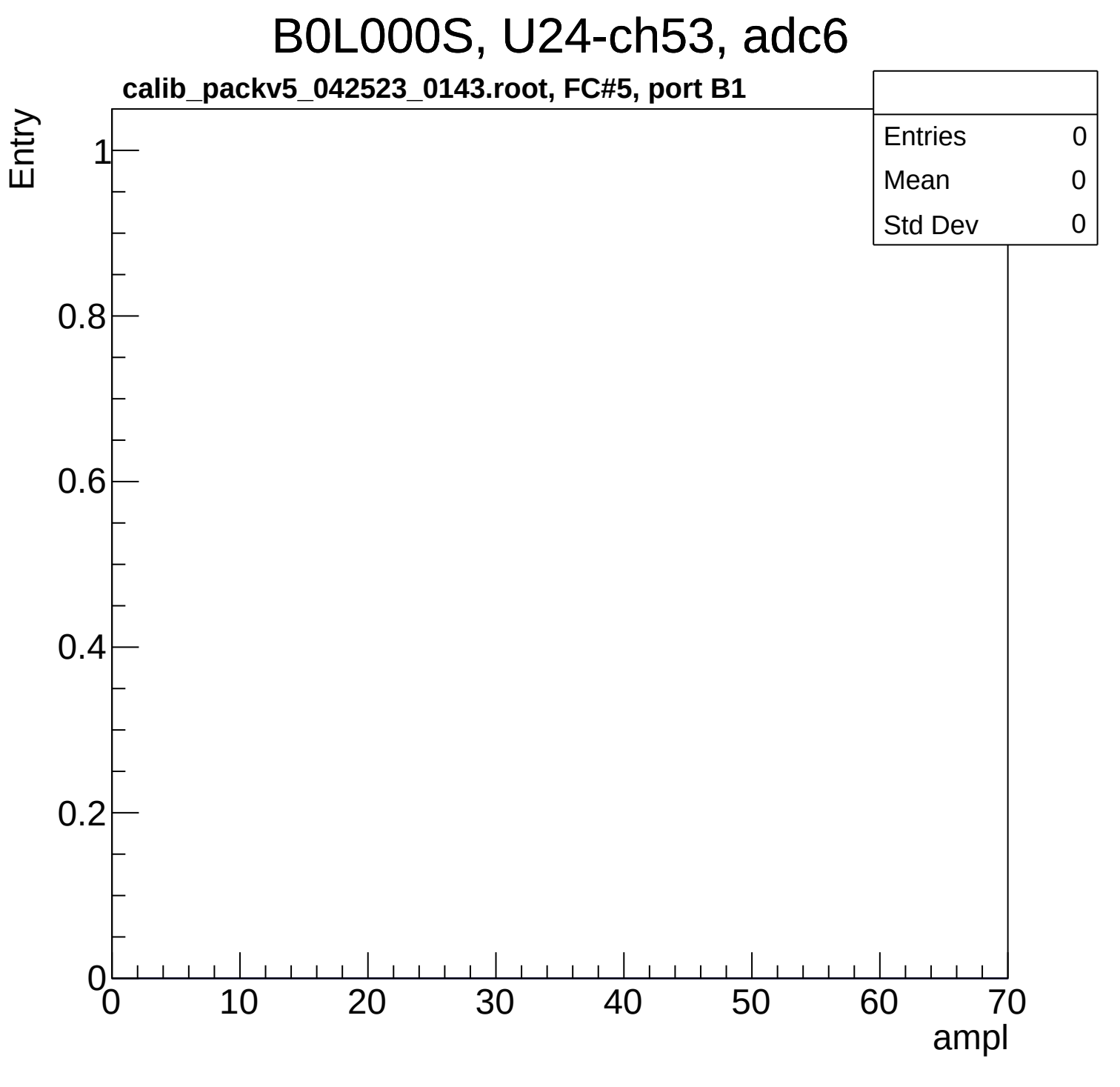
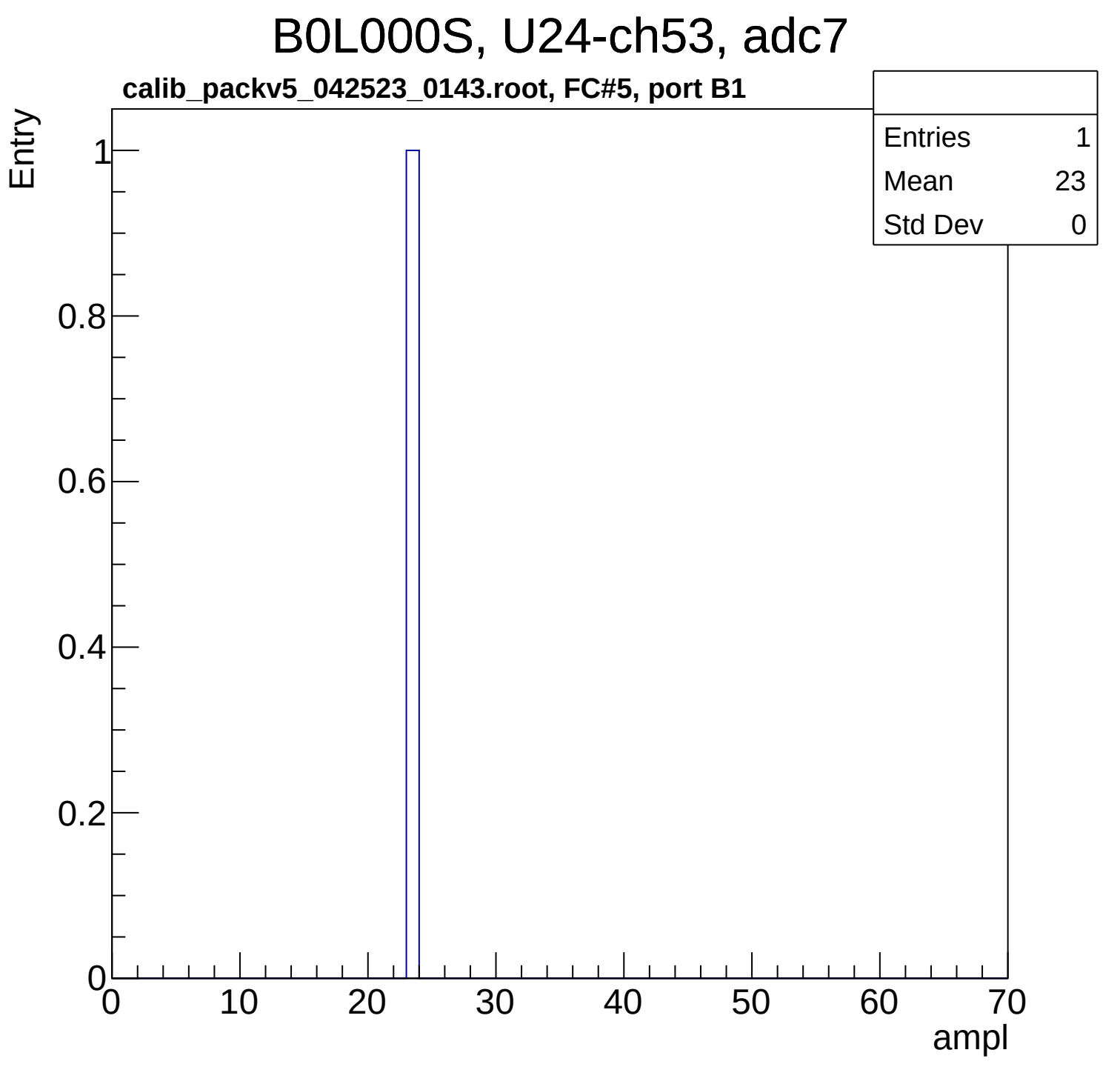

B0L000S, U24-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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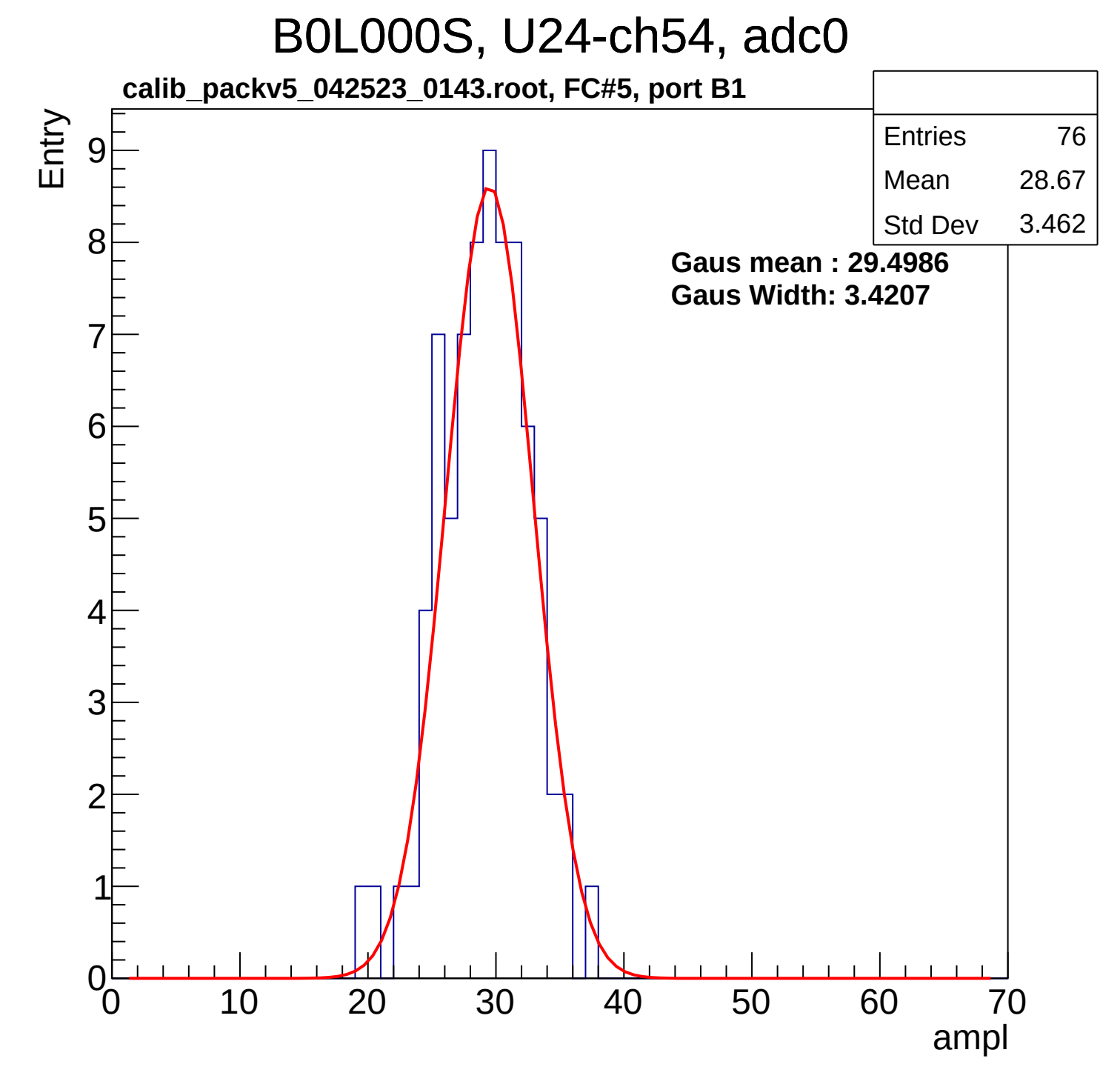
50
60
70
ampl


B0L000S, U24-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.67
Std Dev 3.462
Gaus mean : 29.4986
Gaus Width: 3.4207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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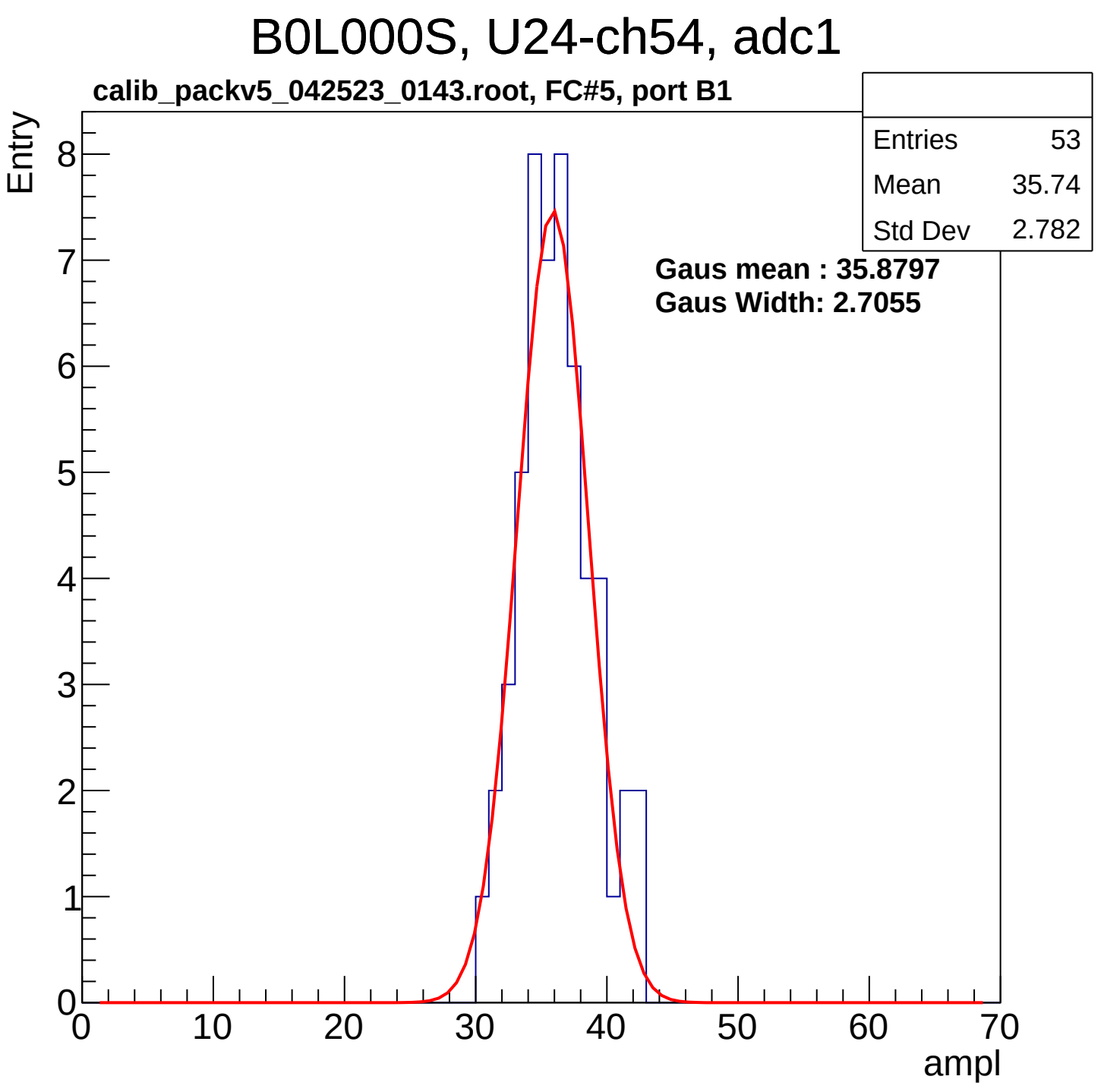
B0L000S, U24-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 35.74
Std Dev 2.782
Gaus mean : 35.8797
Gaus Width: 2.7055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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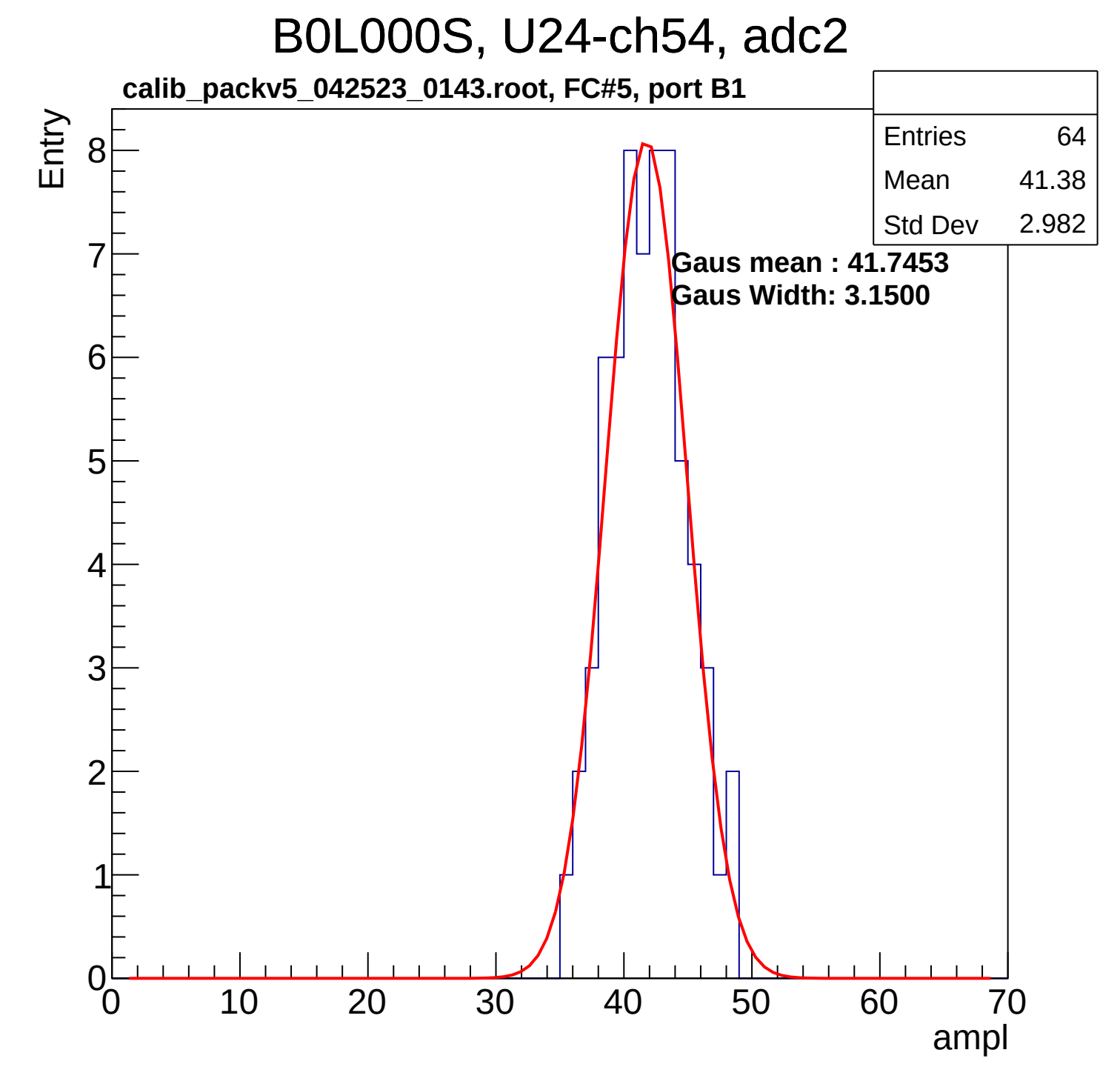
B0L000S, U24-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 41.38
Std Dev 2.982
Gaus mean : 41.7453
Gaus Width: 3.1500
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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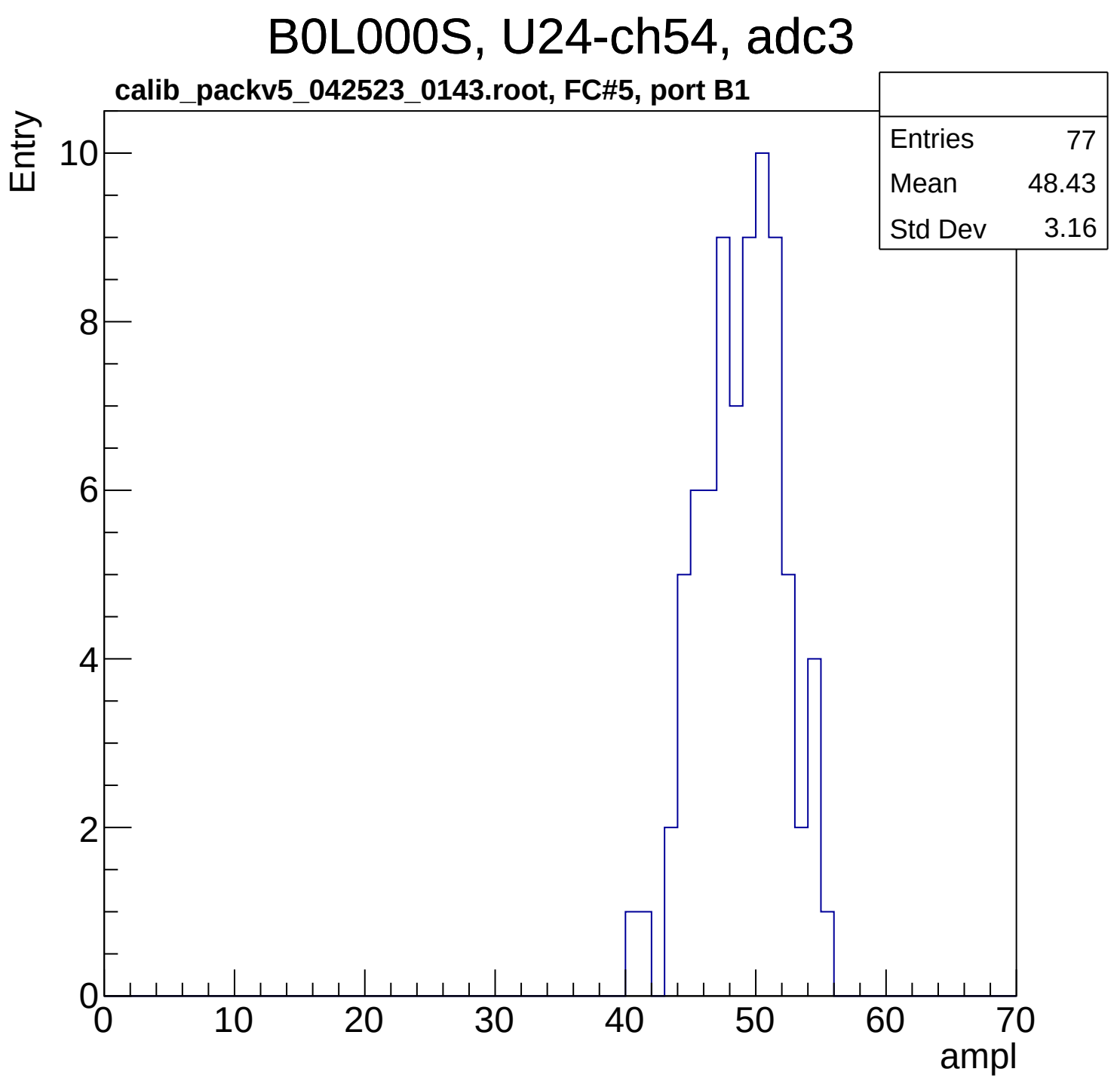
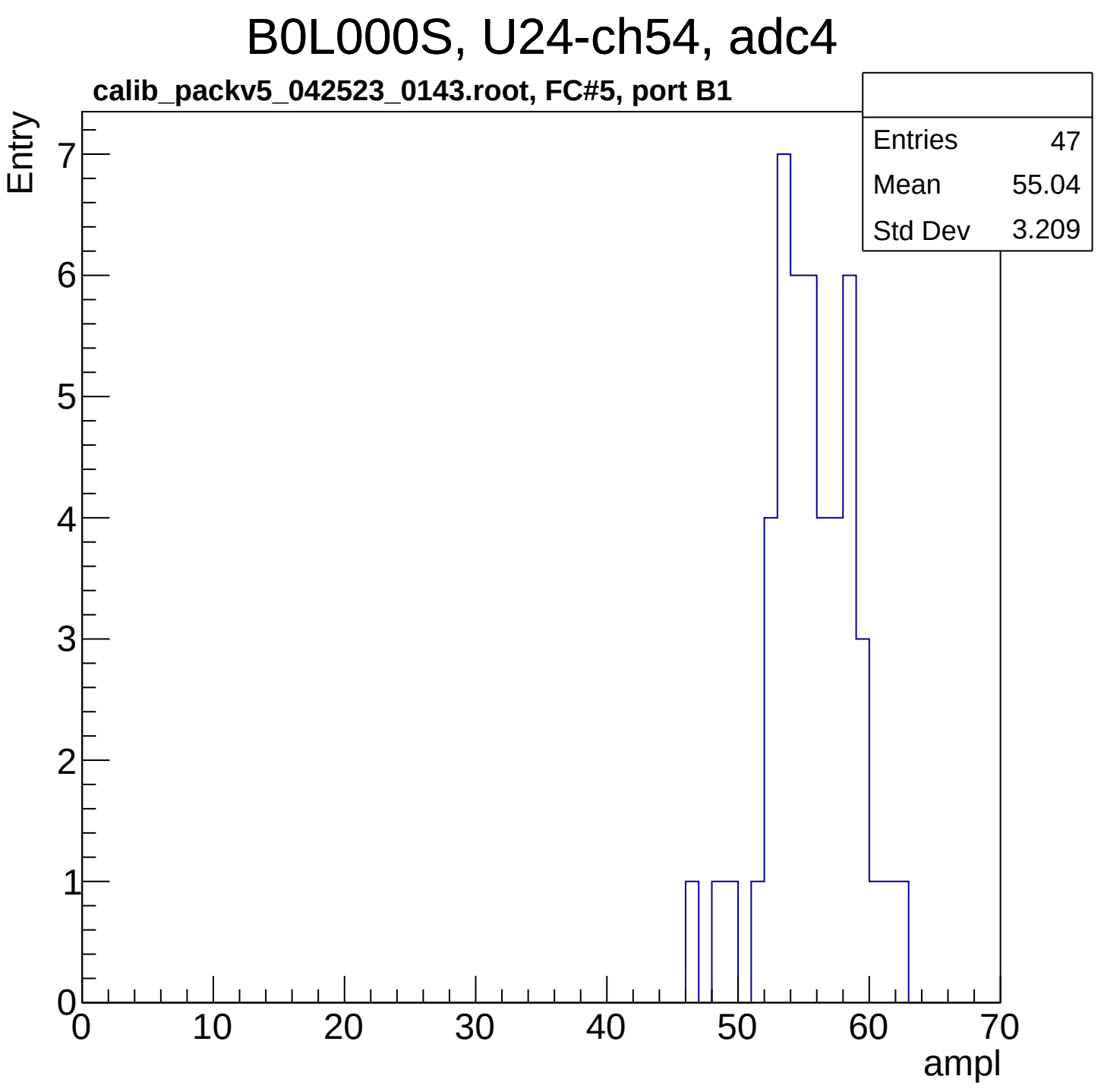

B0L000S, U24-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 48.43
Std Dev 3.16
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 55.04
Std Dev 3.209
Entry
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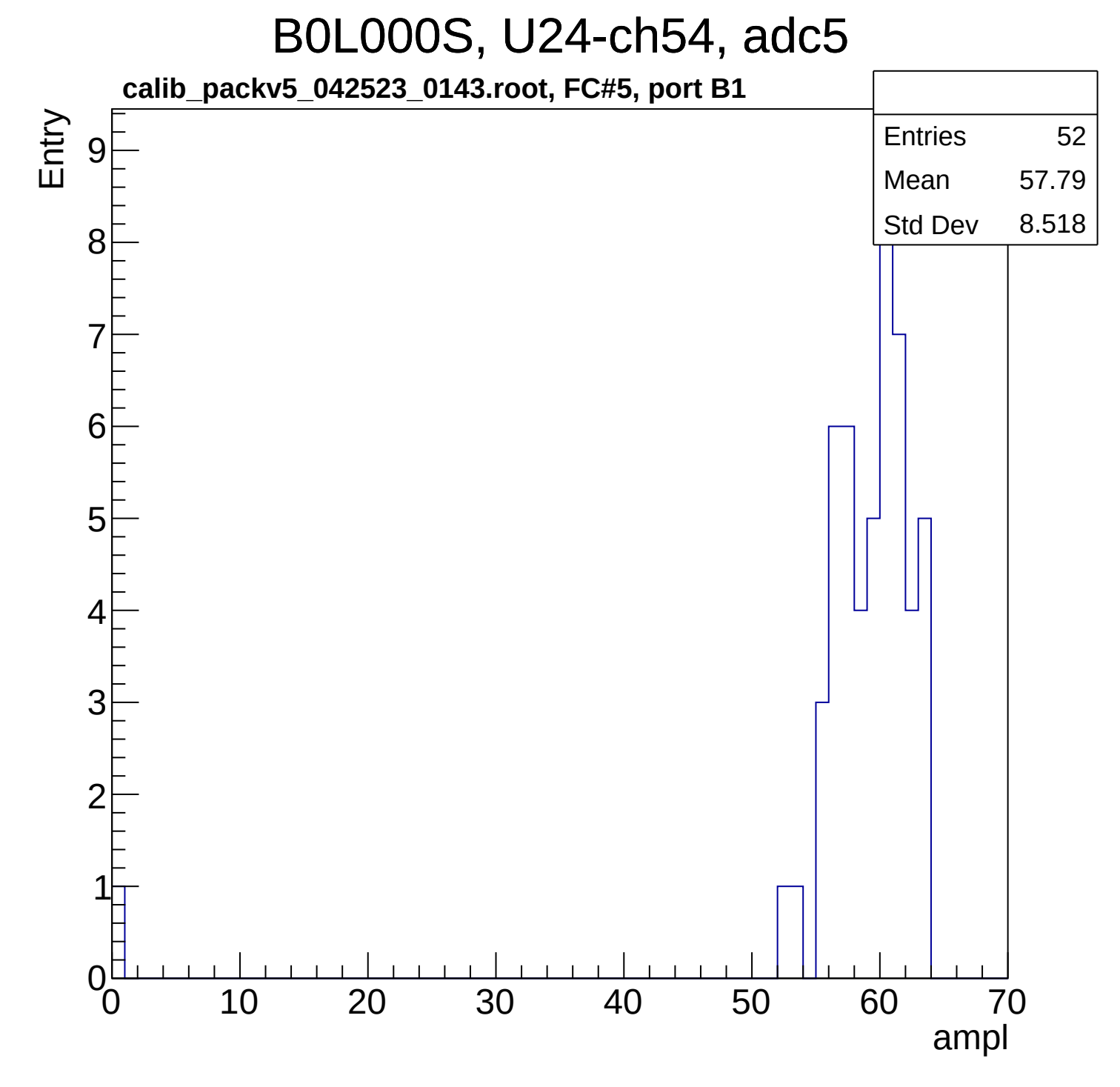
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.79
Std Dev 8.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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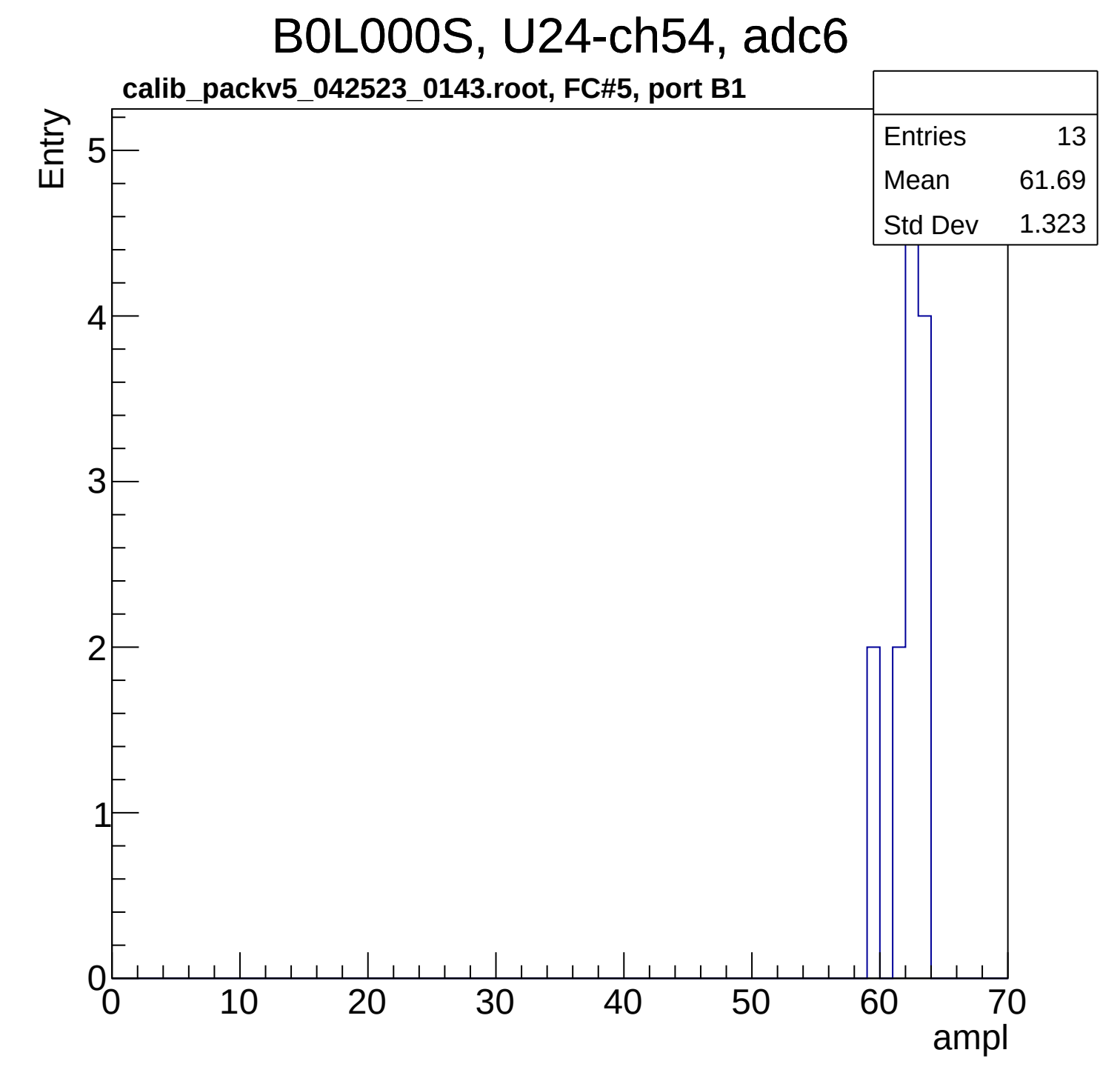
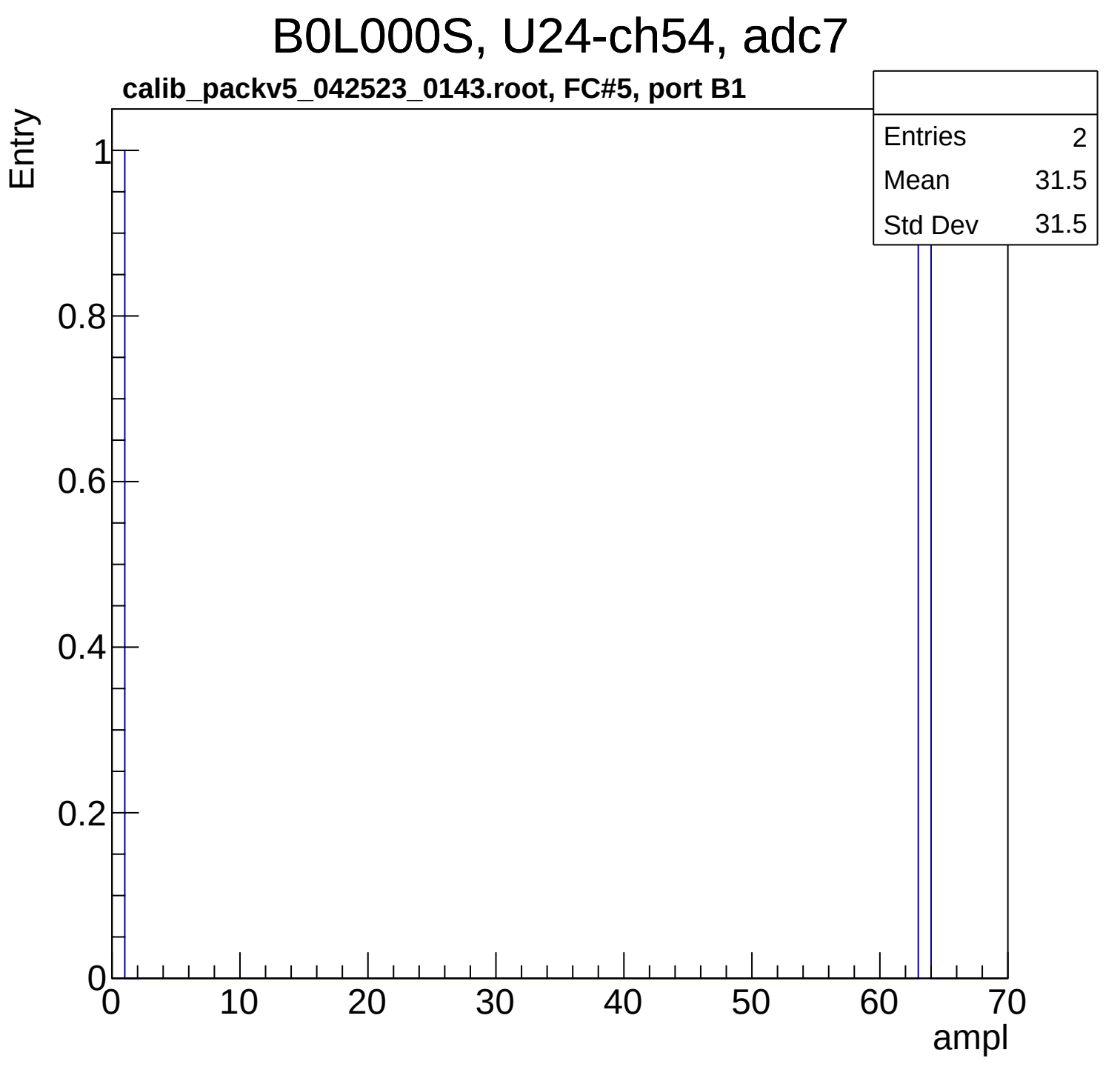
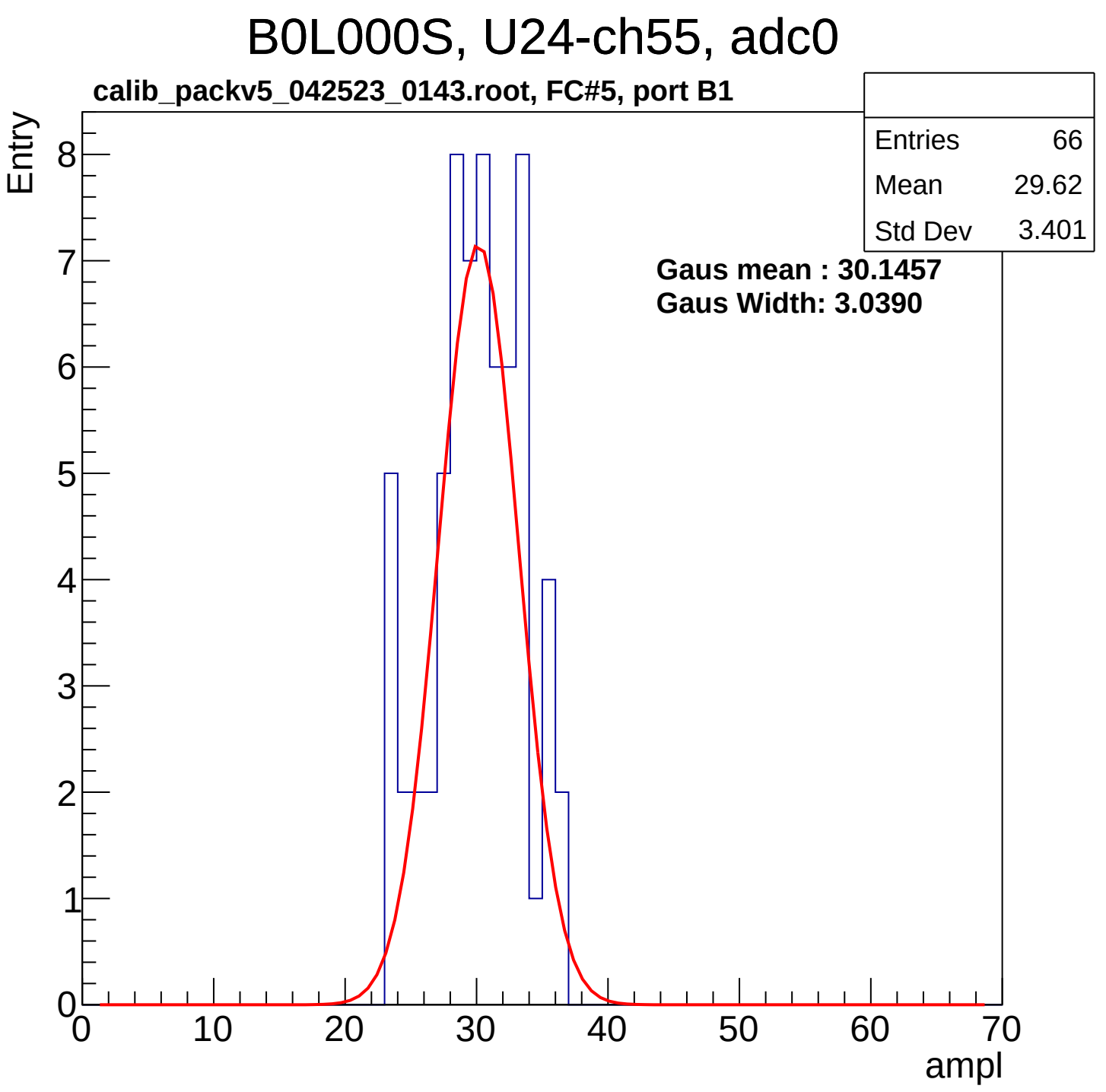
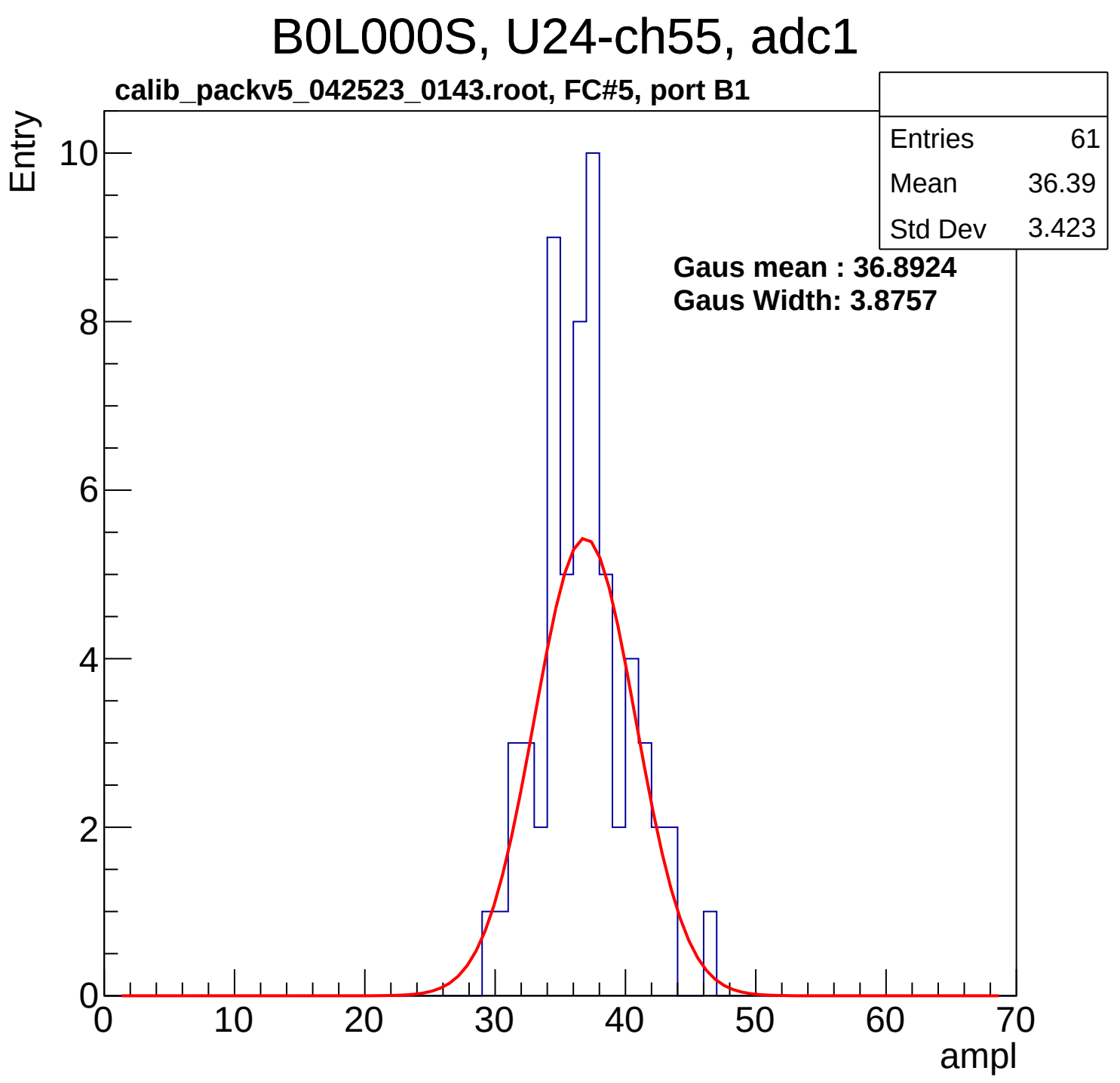
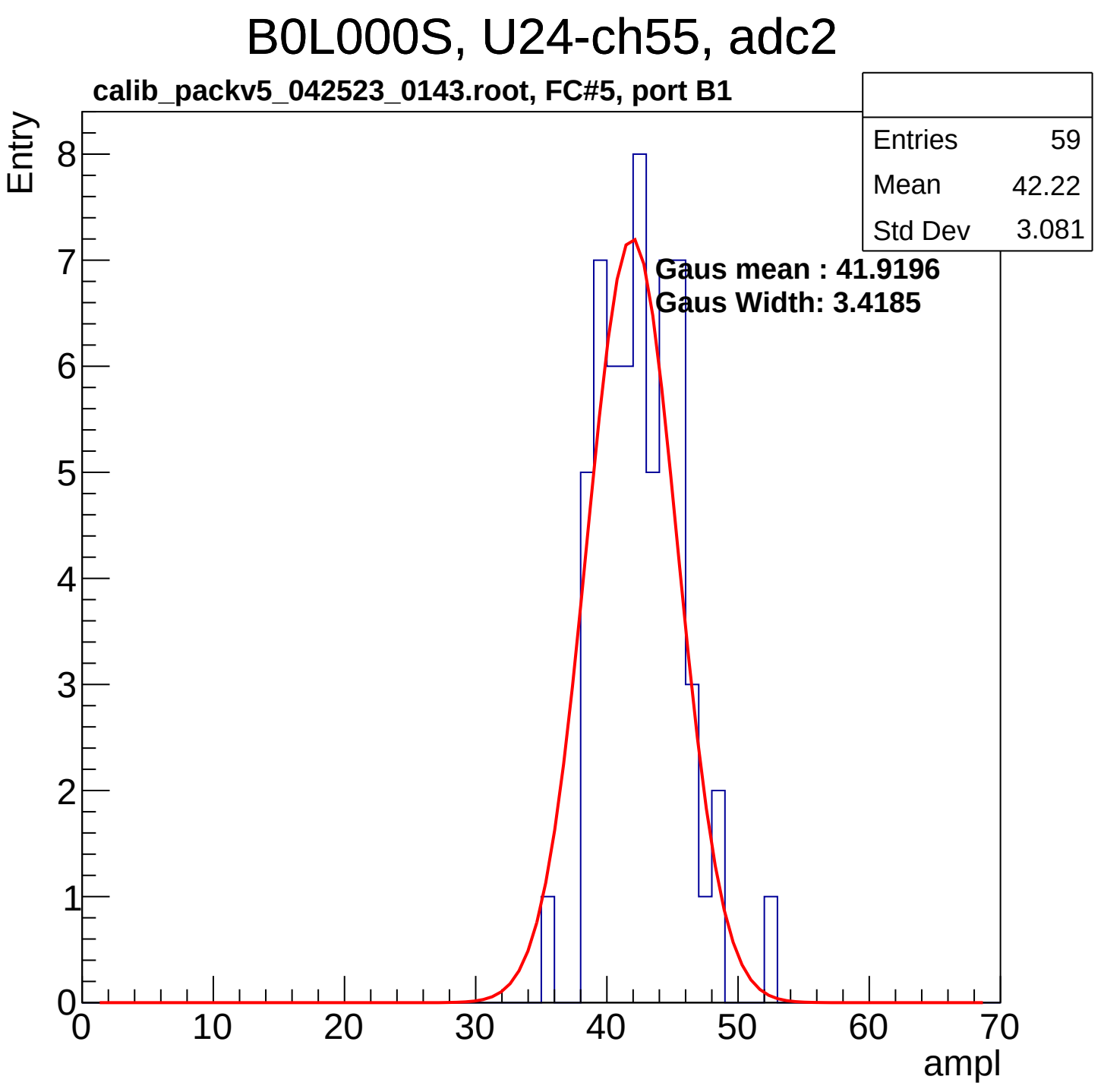
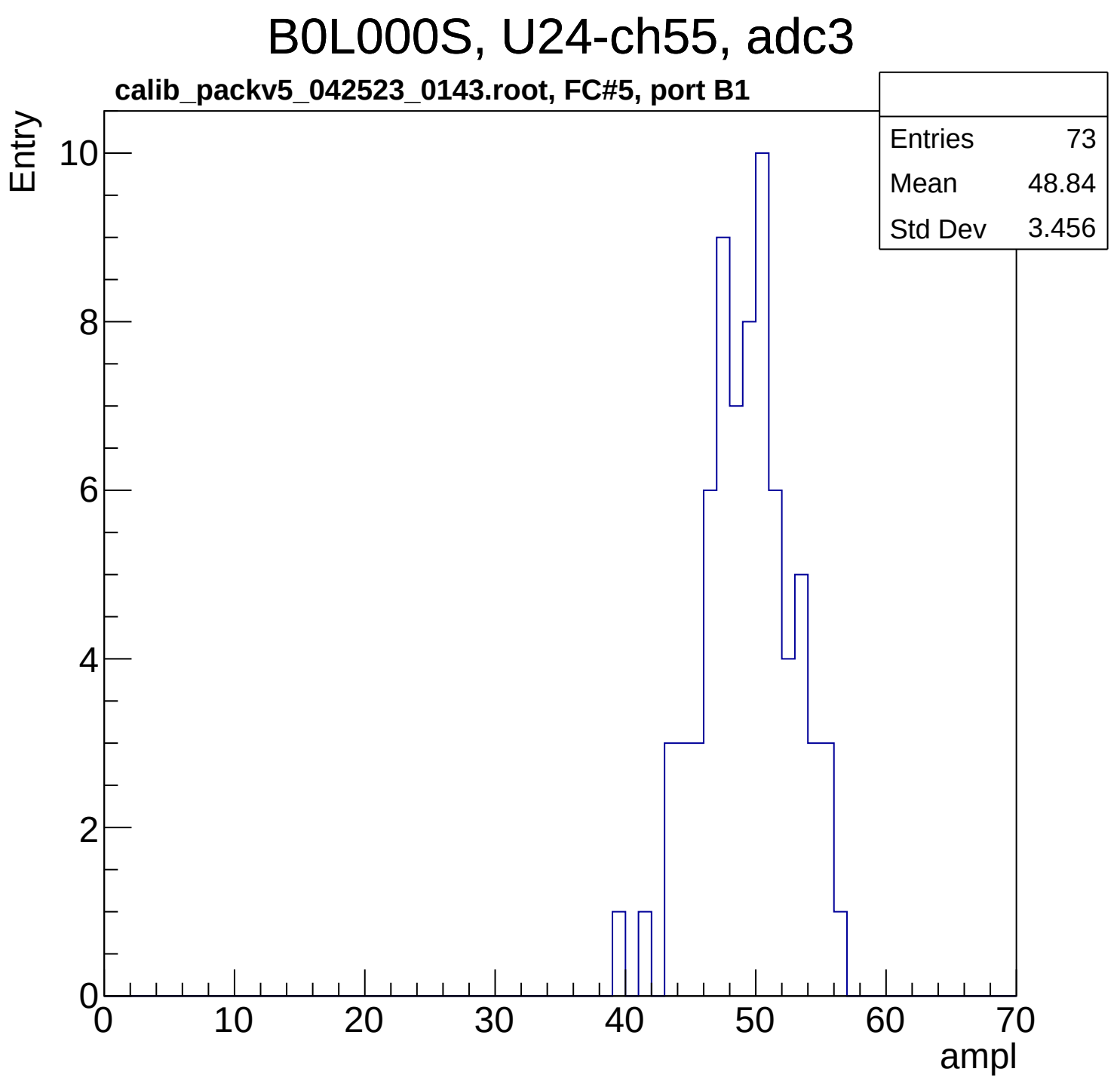

B0L000S, U24-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.69
Std Dev 1.323
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

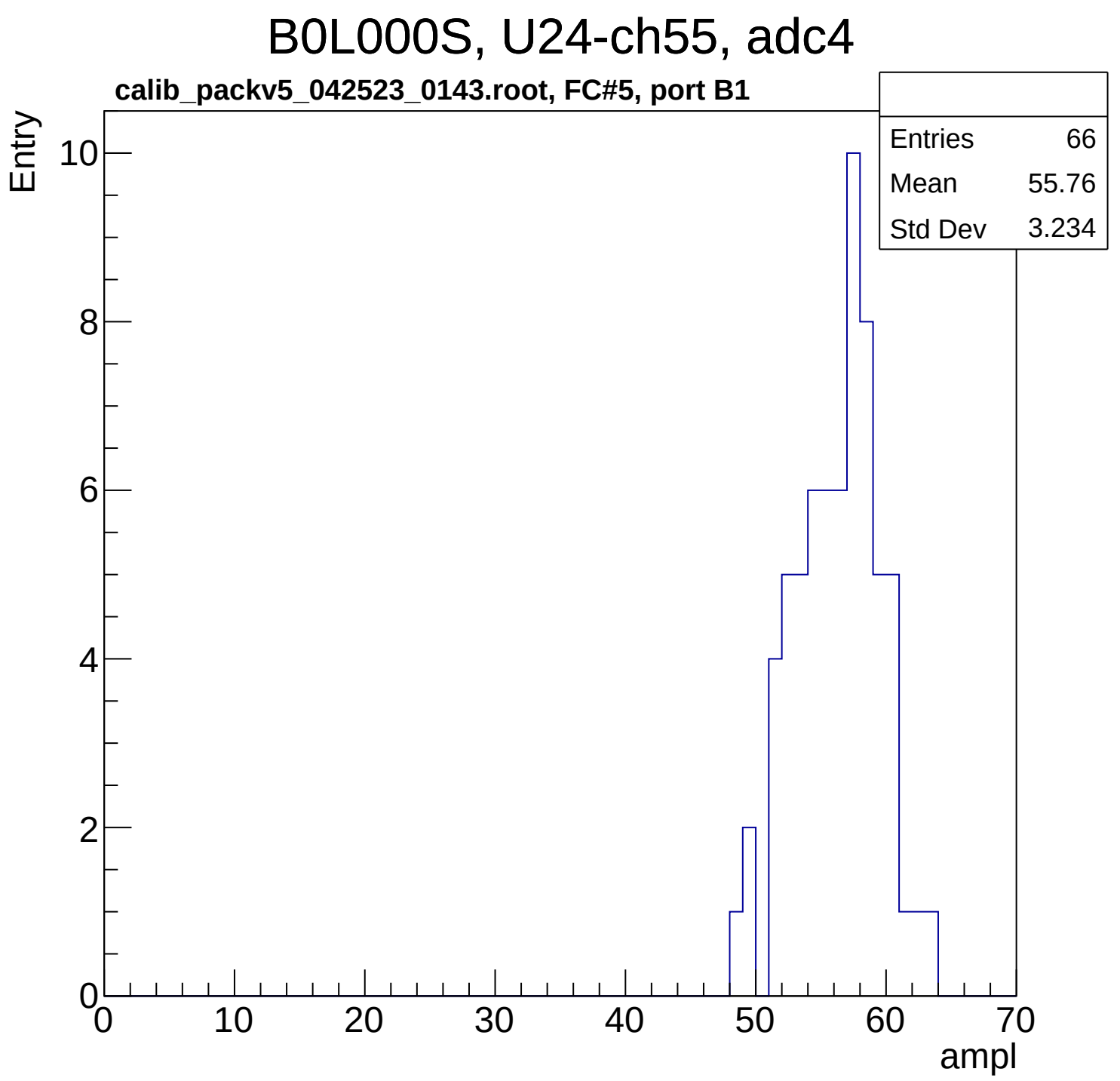

B0L000S, U24-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

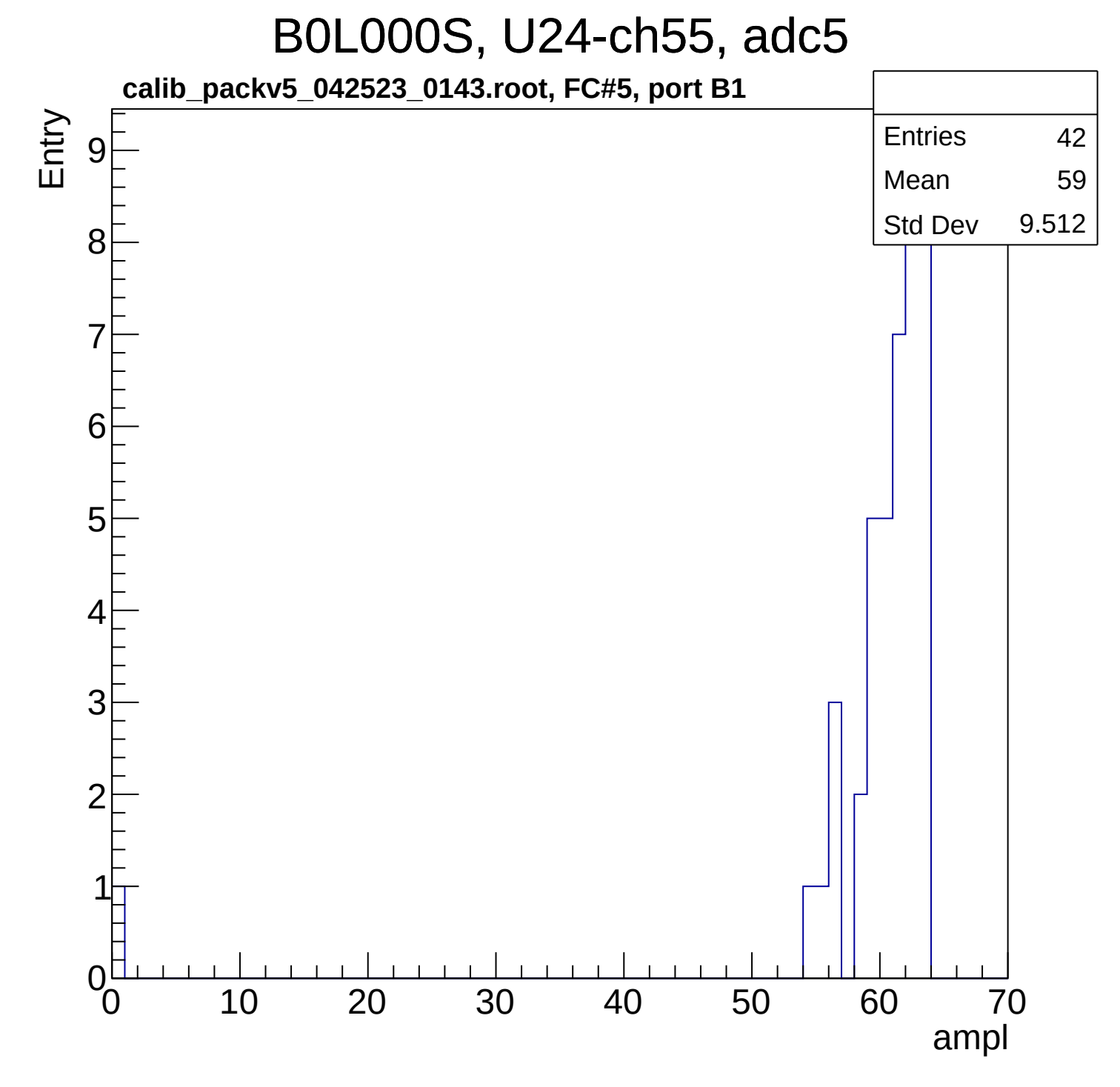

B0L000S, U24-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.62
Std Dev 3.401
Gaus mean : 30.1457
Gaus Width: 3.0390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.39
Std Dev 3.423
Gaus mean : 36.8924
Gaus Width: 3.8757
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.22
Std Dev 3.081
Gaus mean : 41.9196
Gaus Width: 3.4185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.84
Std Dev 3.456
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 55.76
Std Dev 3.234
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59
Std Dev 9.512
Entry
ampl
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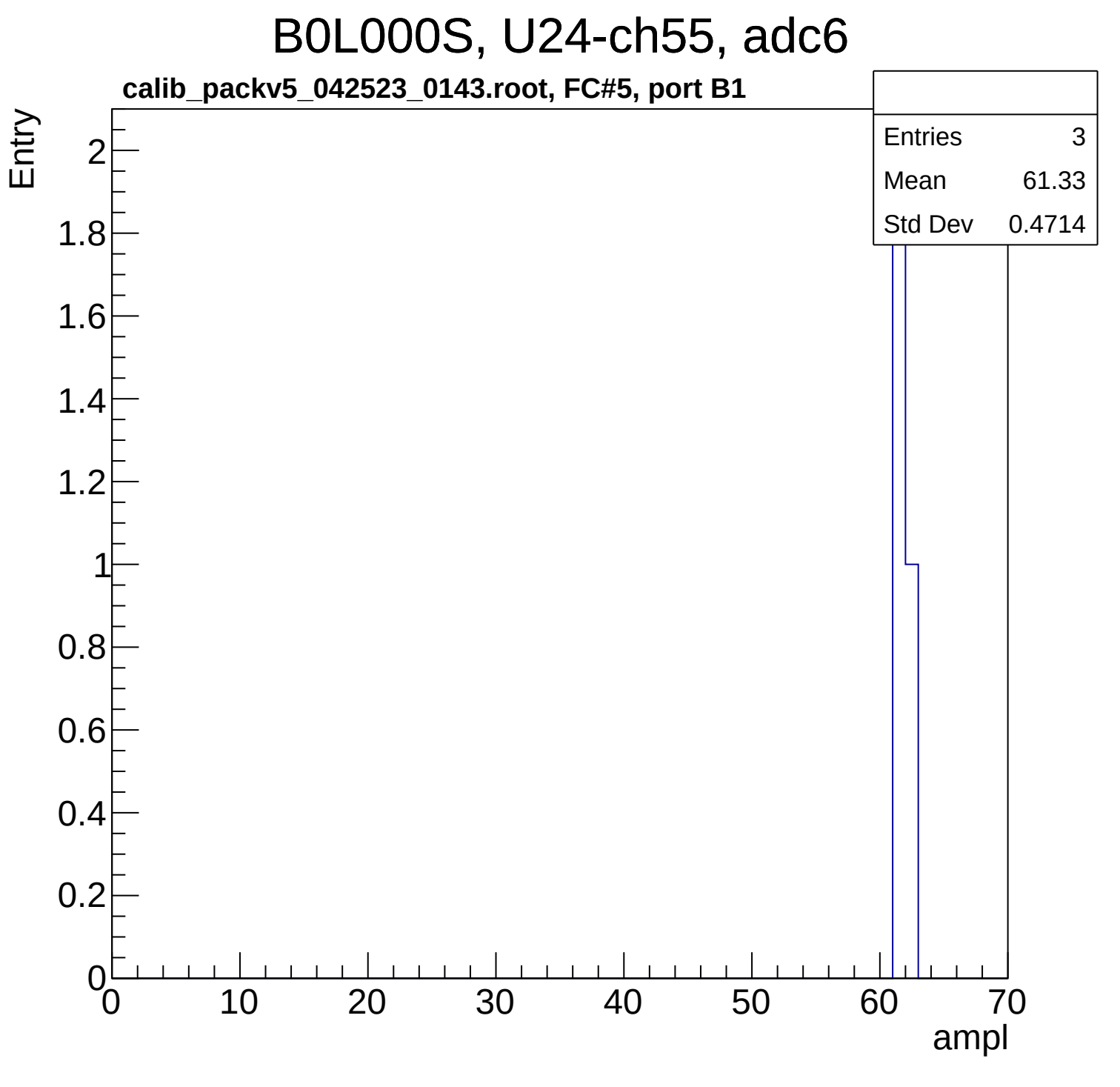
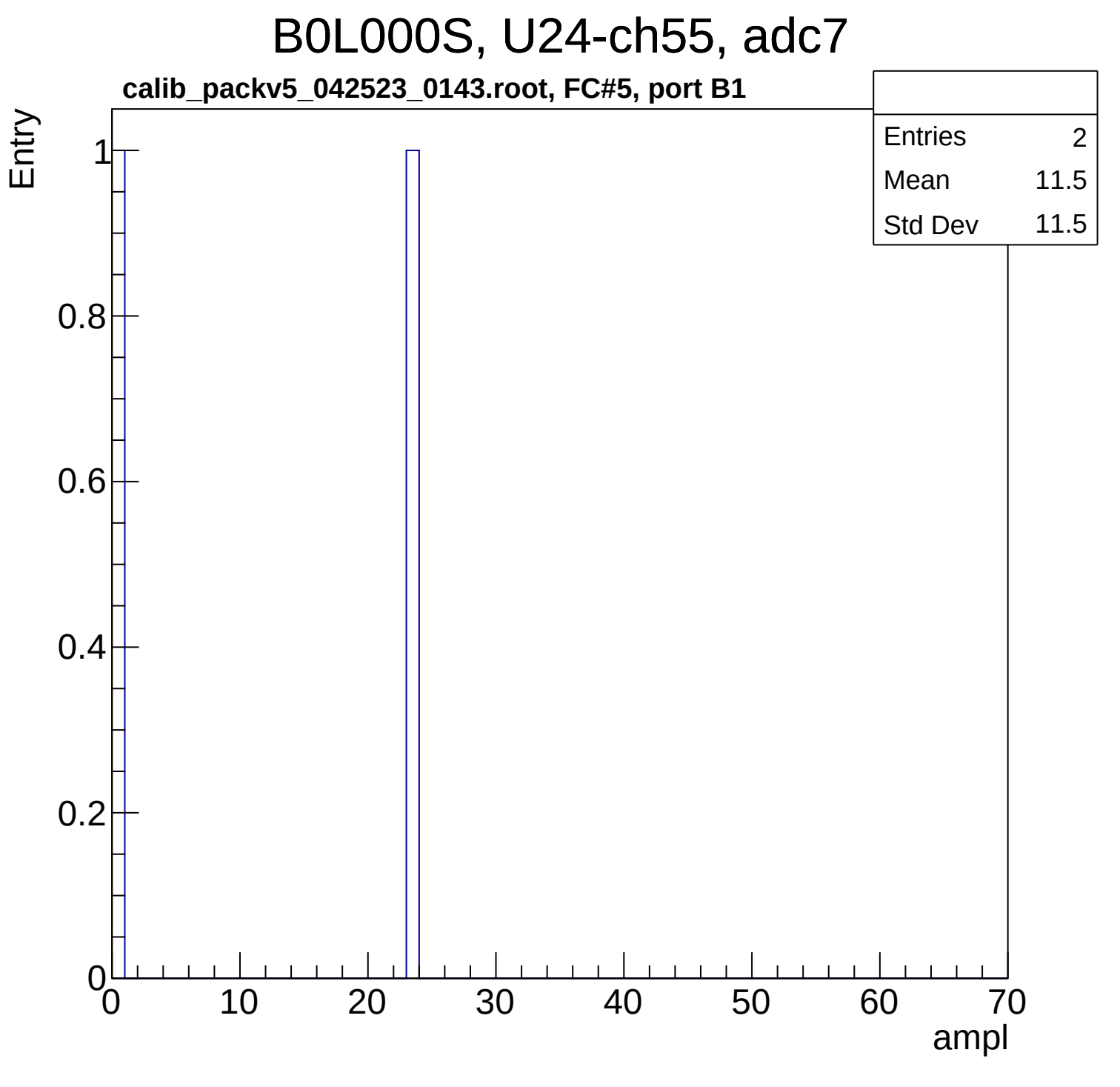
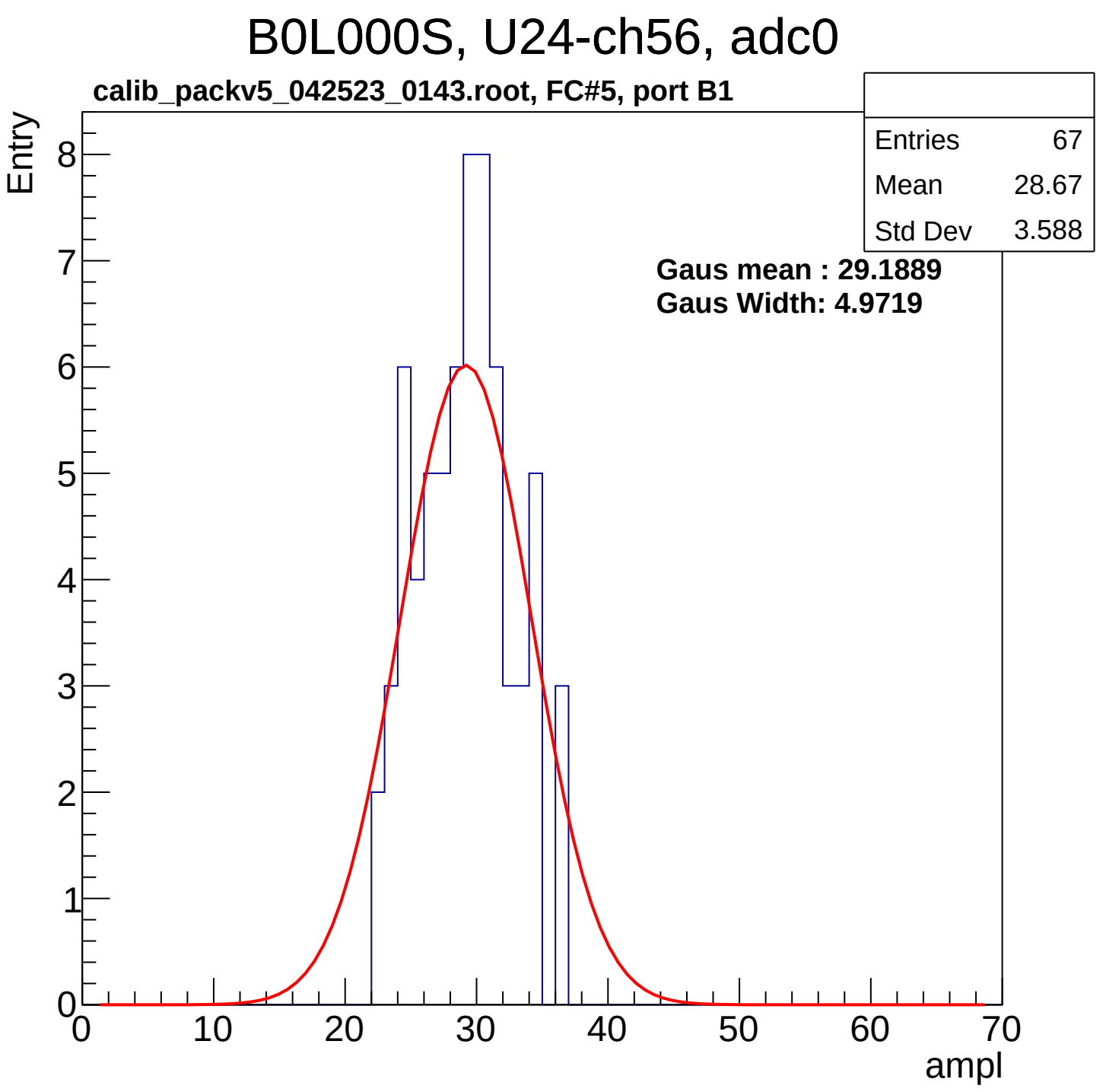
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.67
Std Dev 3.588
Gaus mean : 29.1889
Gaus Width: 4.9719
Entry
ampl
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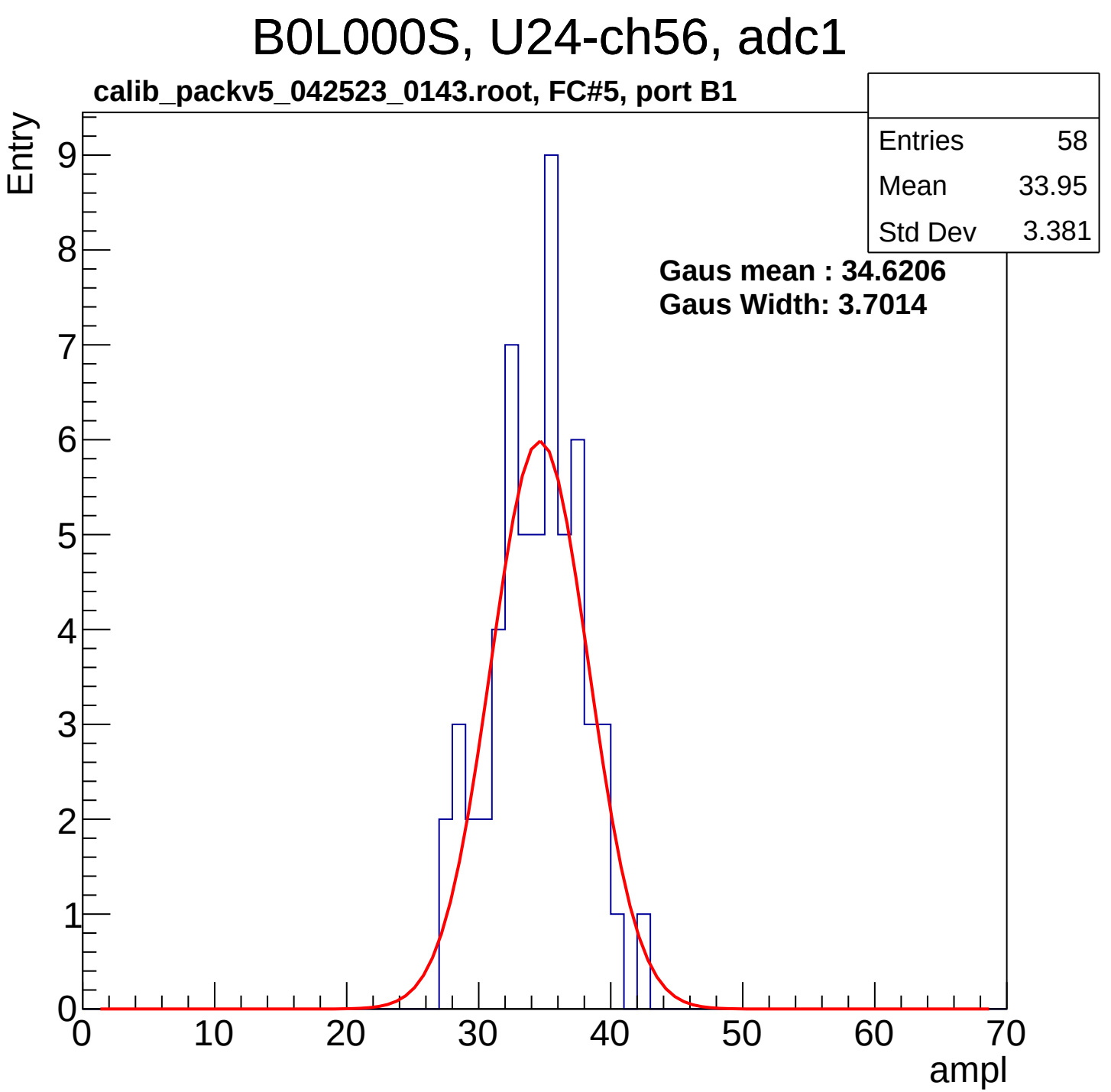
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 33.95
Std Dev 3.381
Gaus mean : 34.6206
Gaus Width: 3.7014
Entry
ampl
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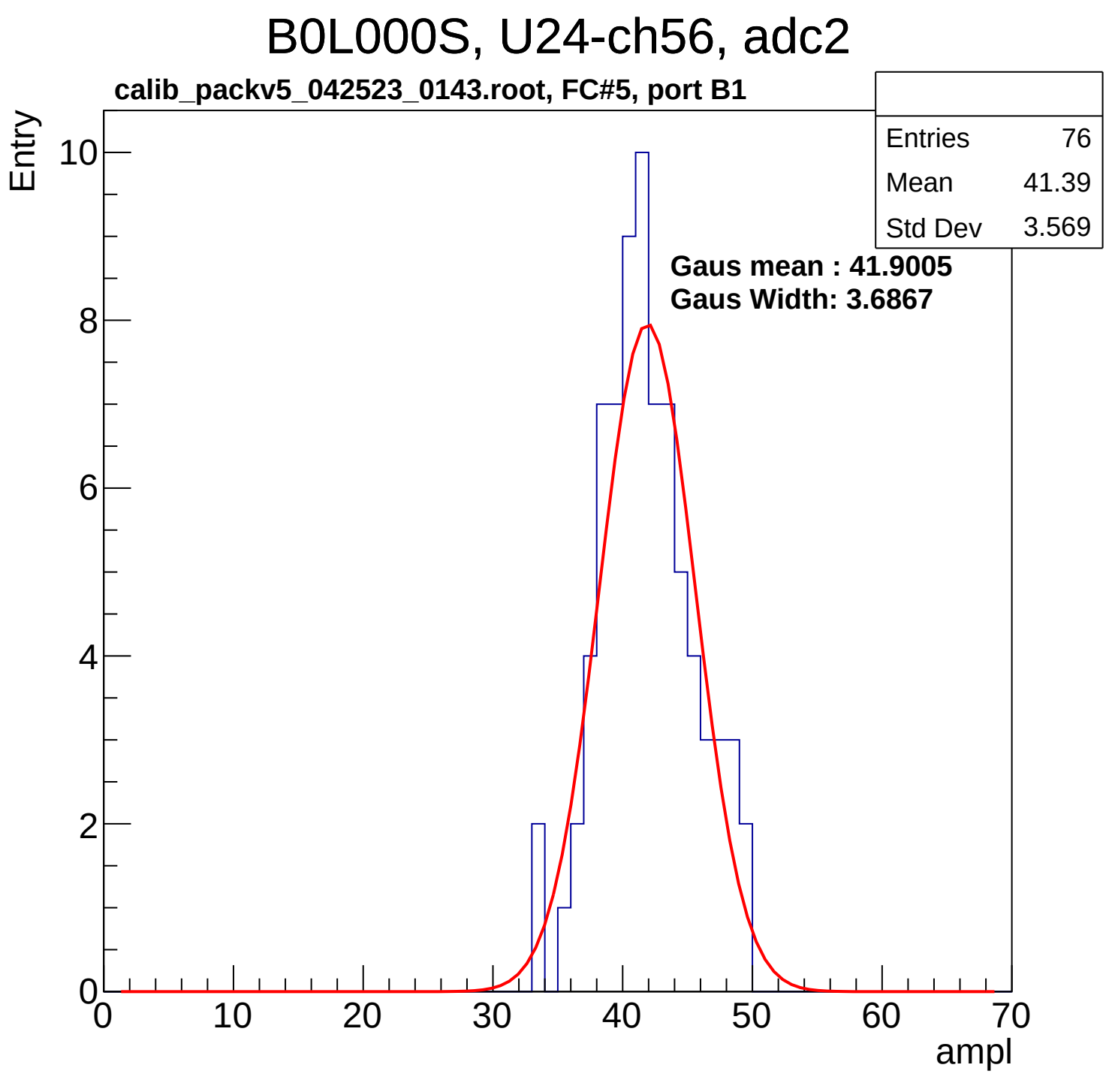
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 41.39
Std Dev 3.569
Gaus mean : 41.9005
Gaus Width: 3.6867
Entry
ampl
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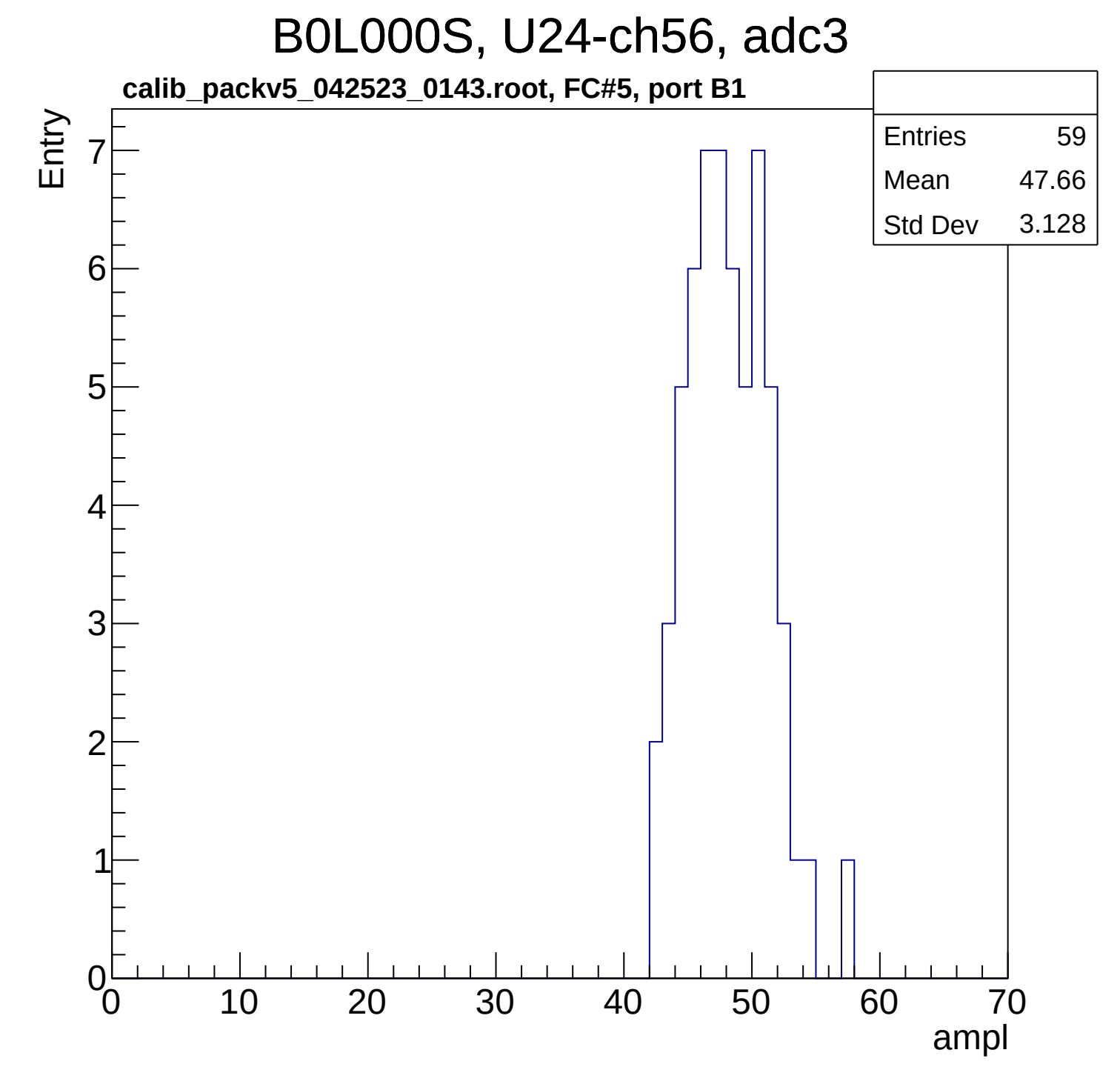
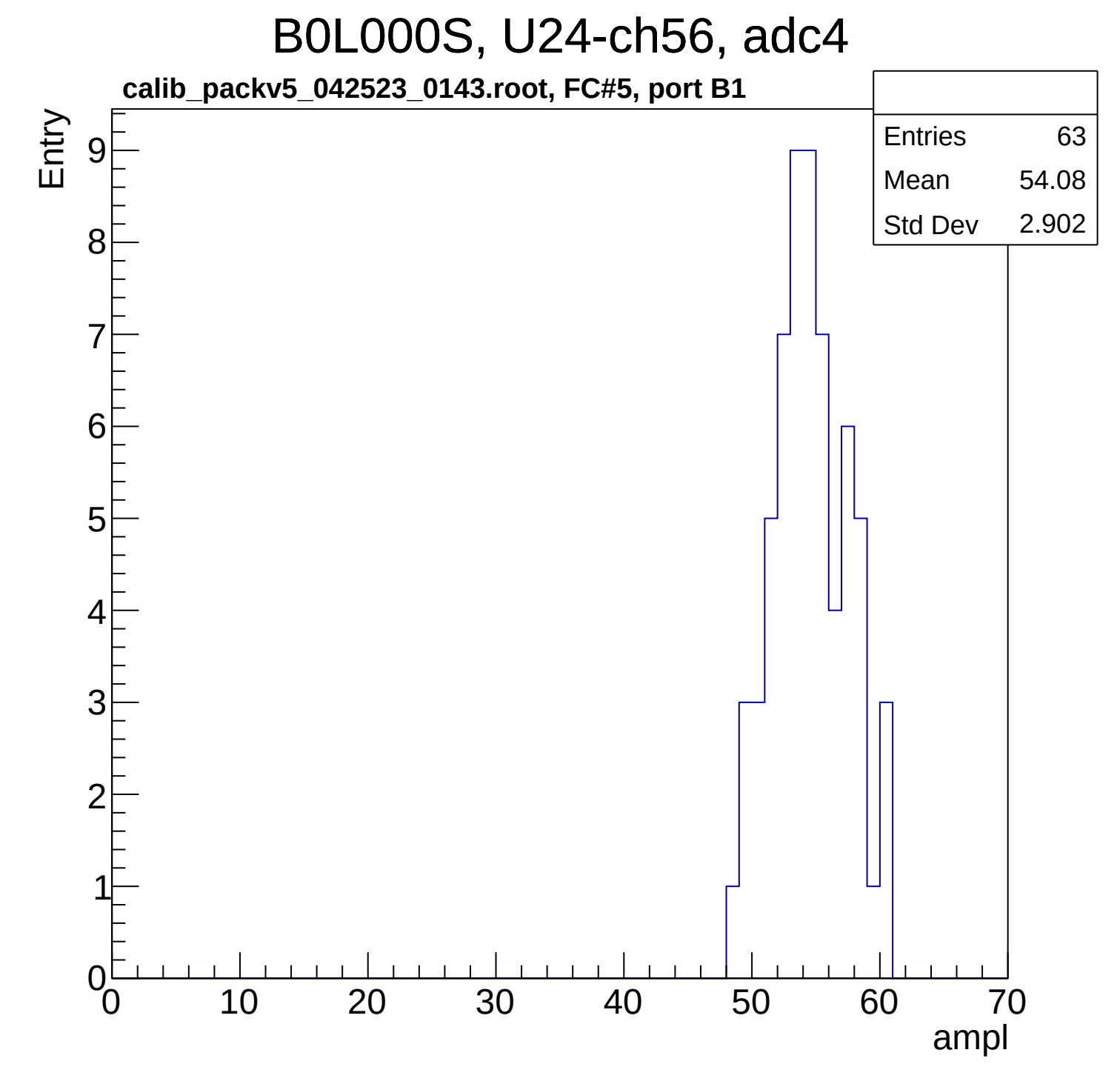
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 47.66
Std Dev 3.128
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.08
Std Dev 2.902
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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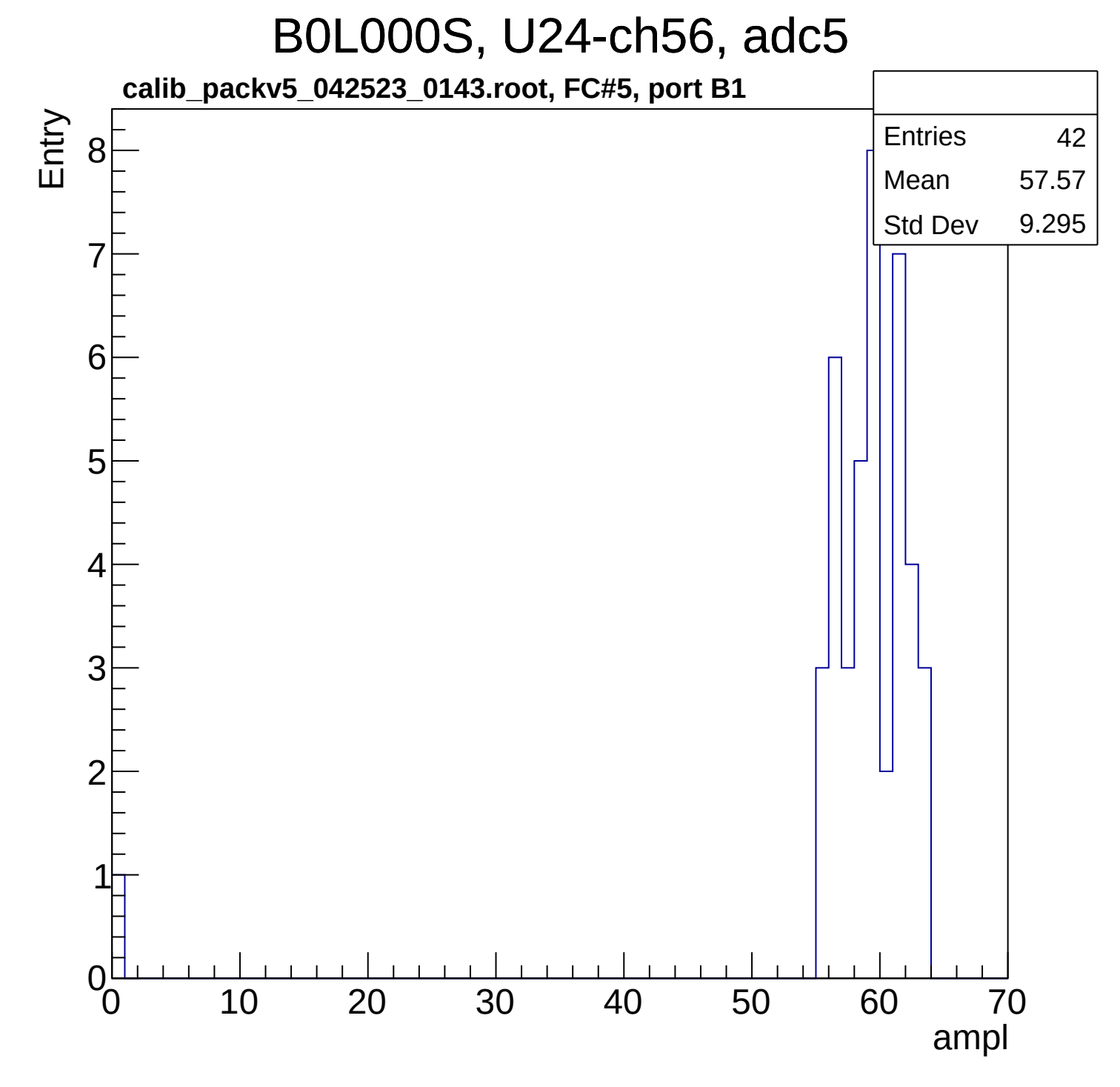
B0L000S, U24-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 57.57
Std Dev 9.295
Entry
ampl
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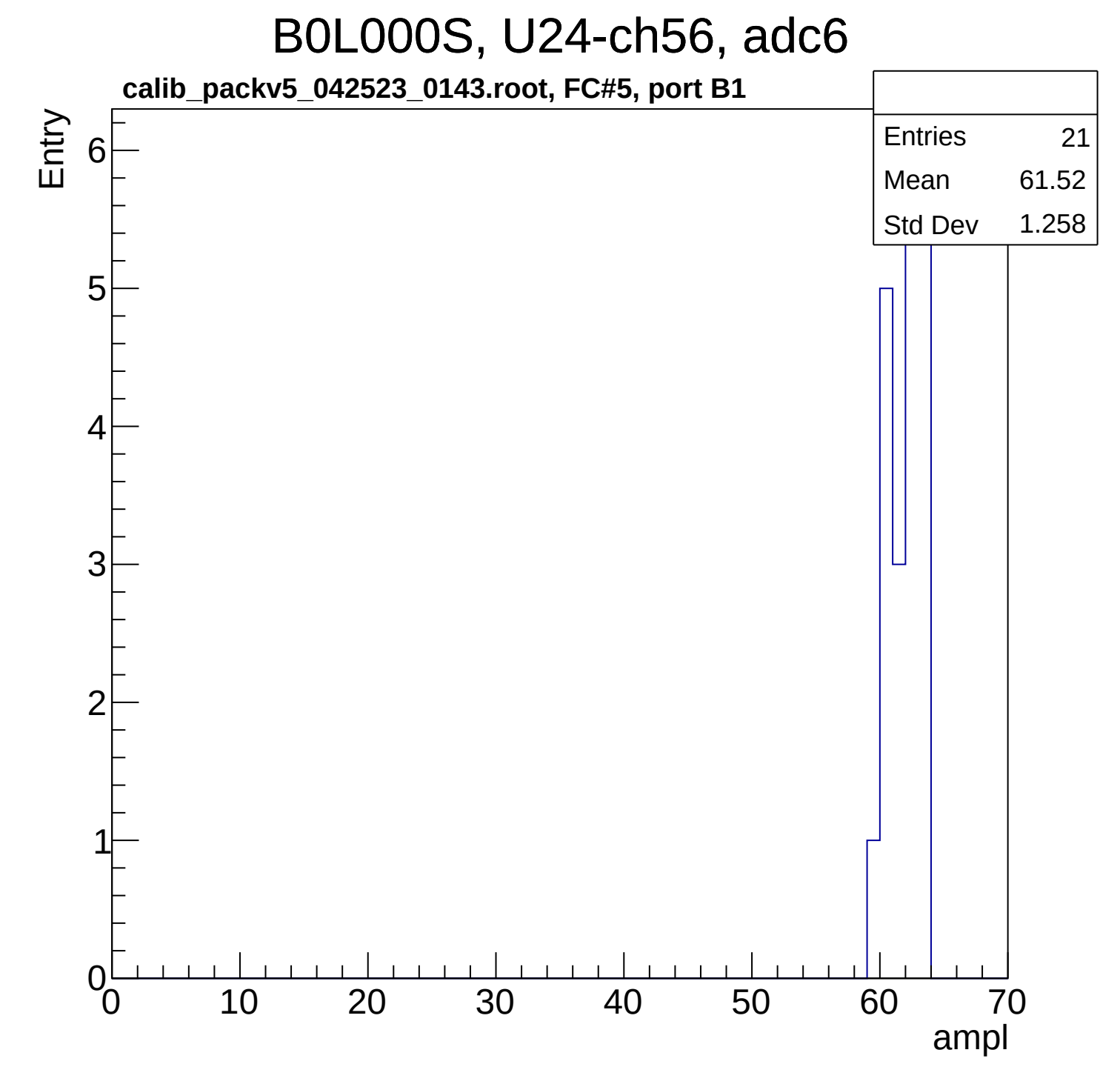
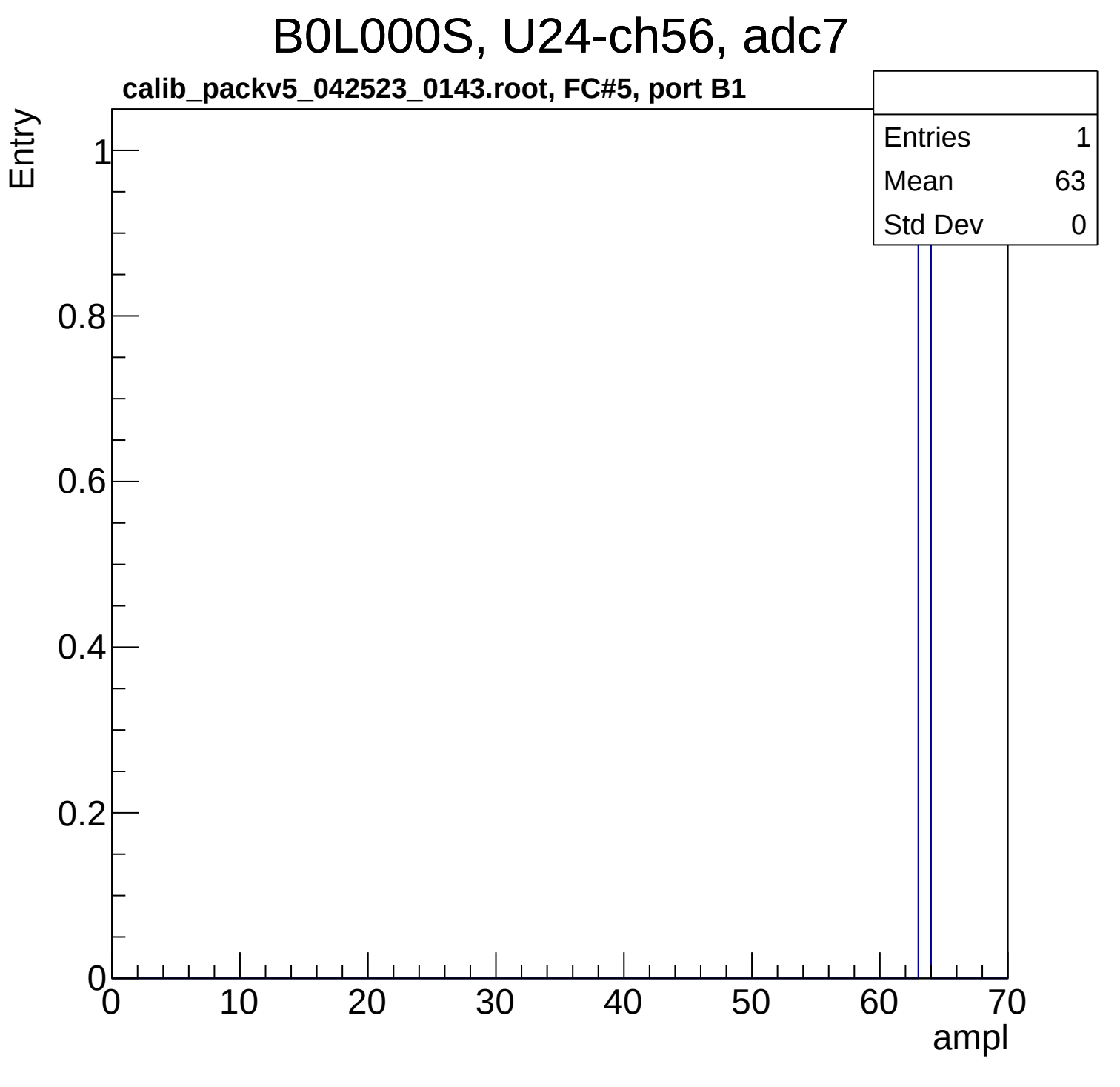
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.52
Std Dev 1.258
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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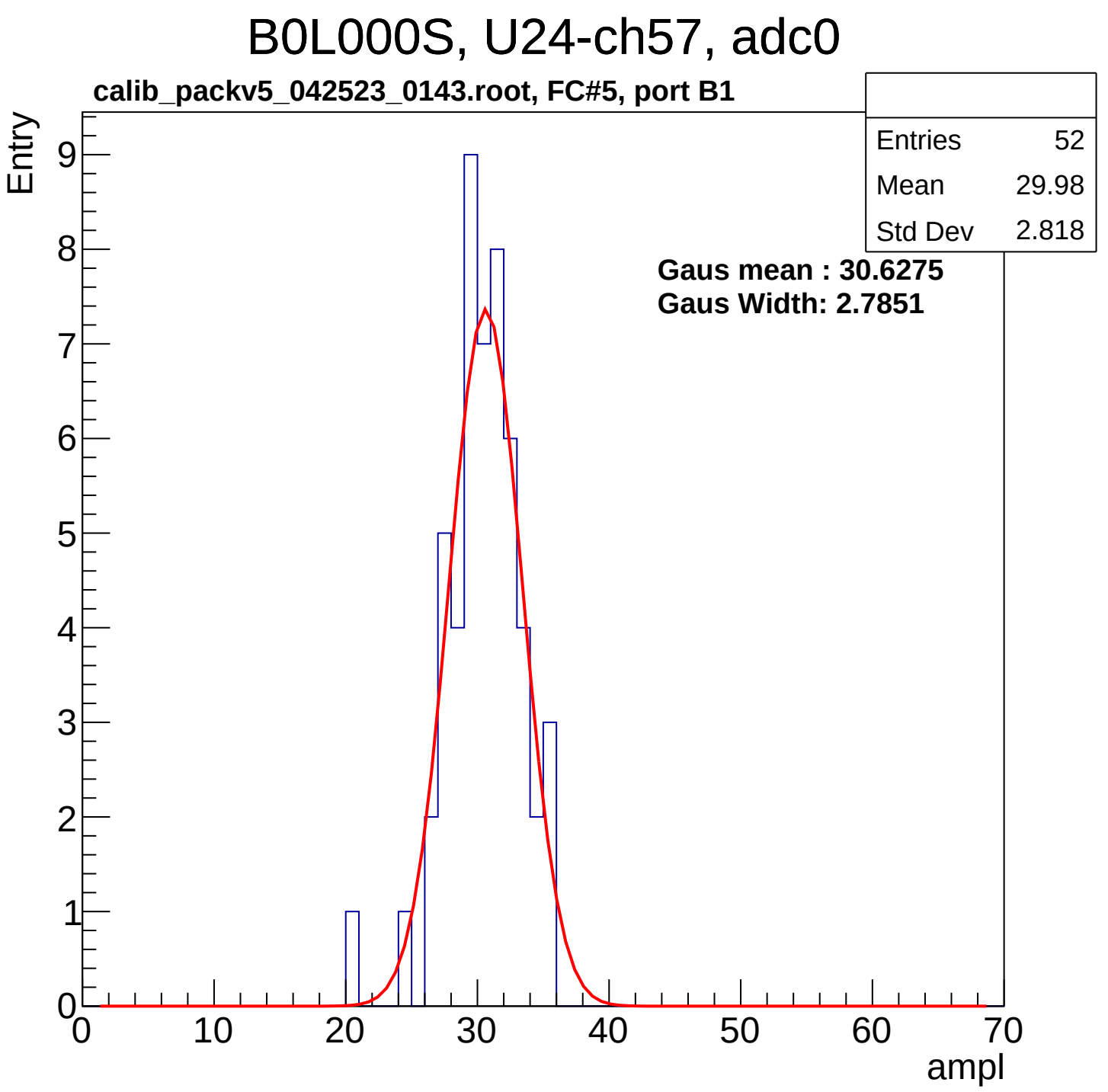
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 29.98
Std Dev 2.818
Gaus mean : 30.6275
Gaus Width: 2.7851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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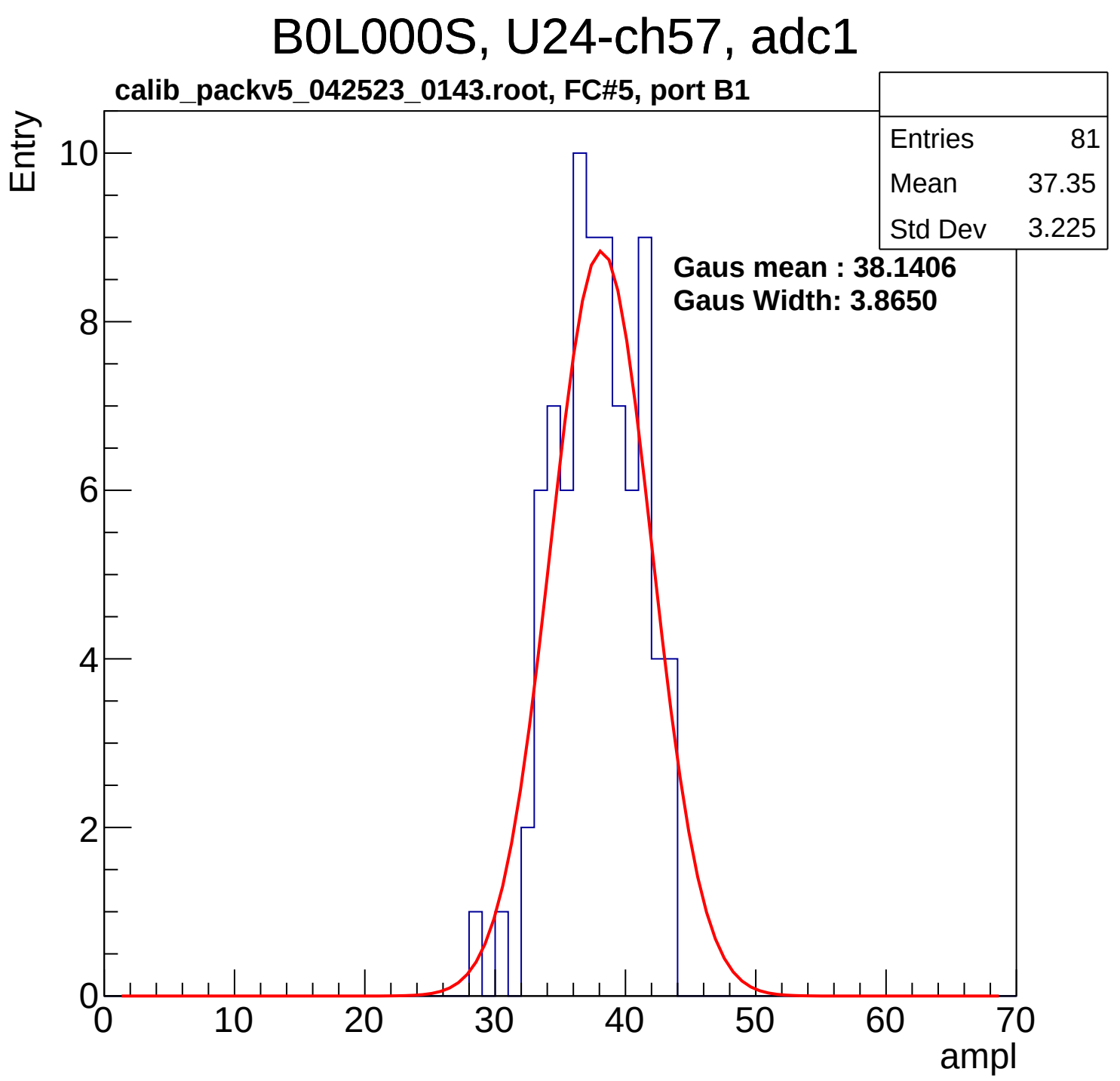
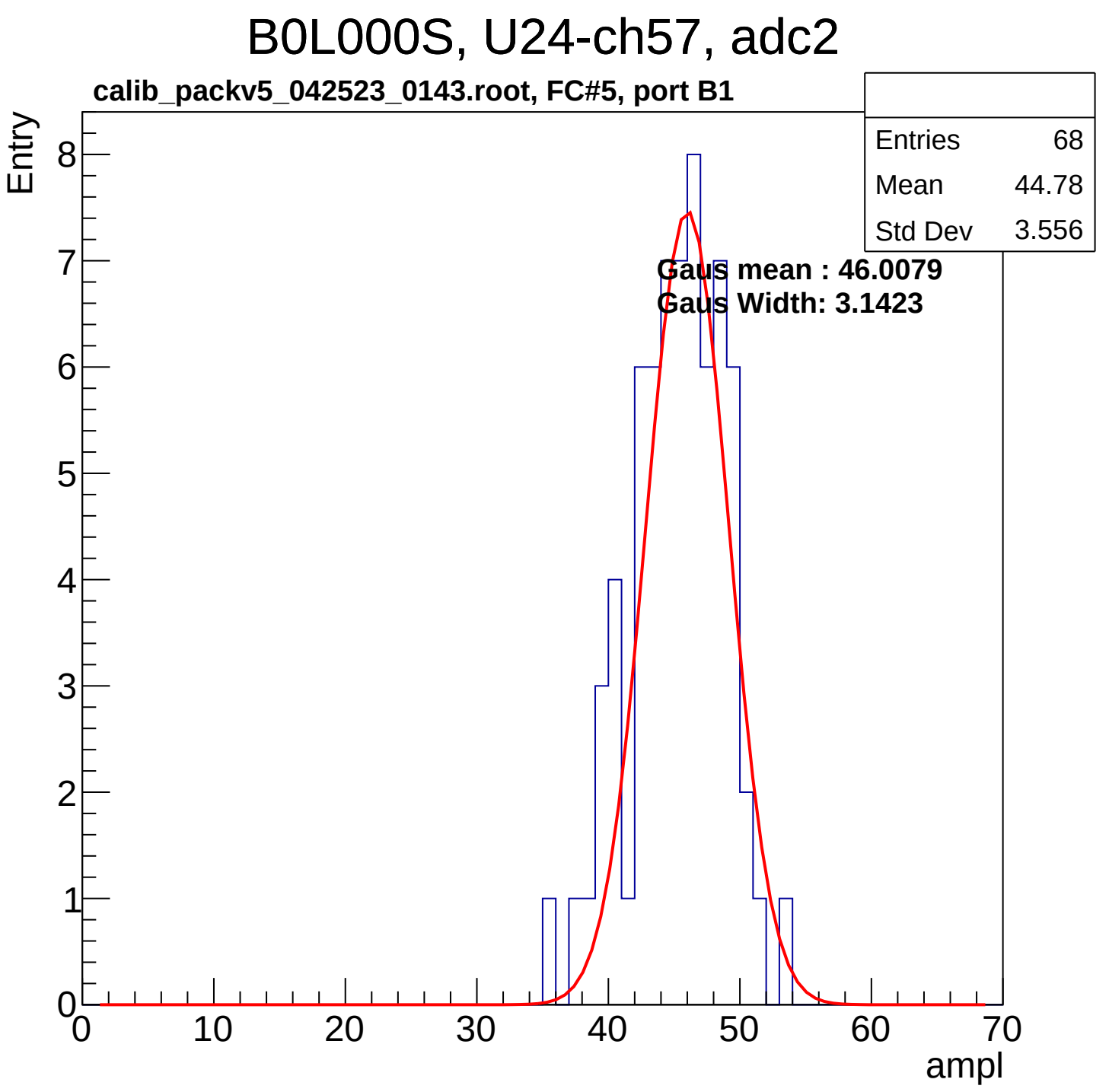

B0L000S, U24-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.35
Std Dev 3.225
Gaus mean : 38.1406
Gaus Width: 3.8650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.78
Std Dev 3.556
Gaus mean : 46.0079
Gaus Width: 3.1423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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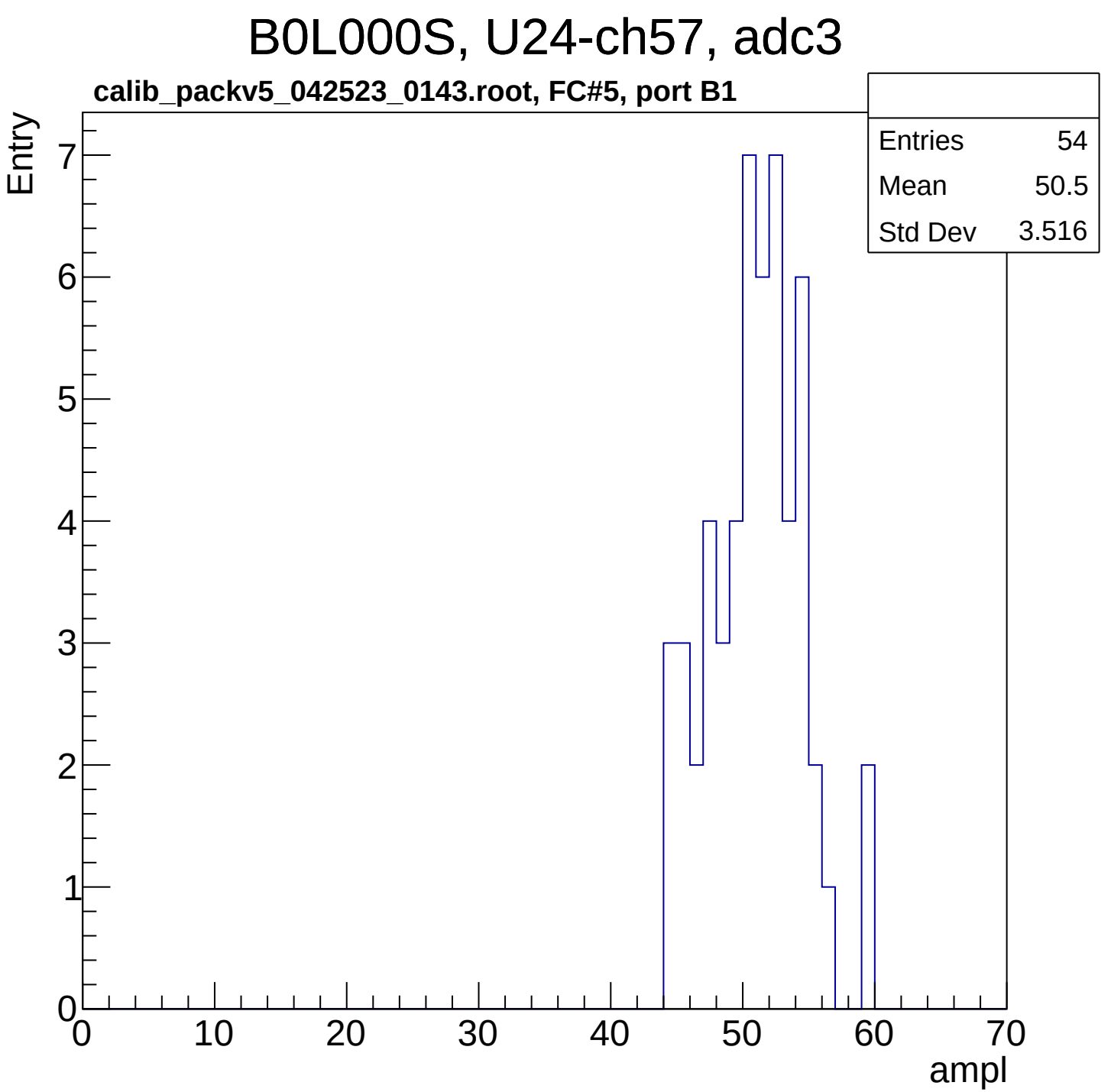
B0L000S, U24-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.5
Std Dev 3.516
Entry
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1
2
3
4
5
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7
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10
20
30
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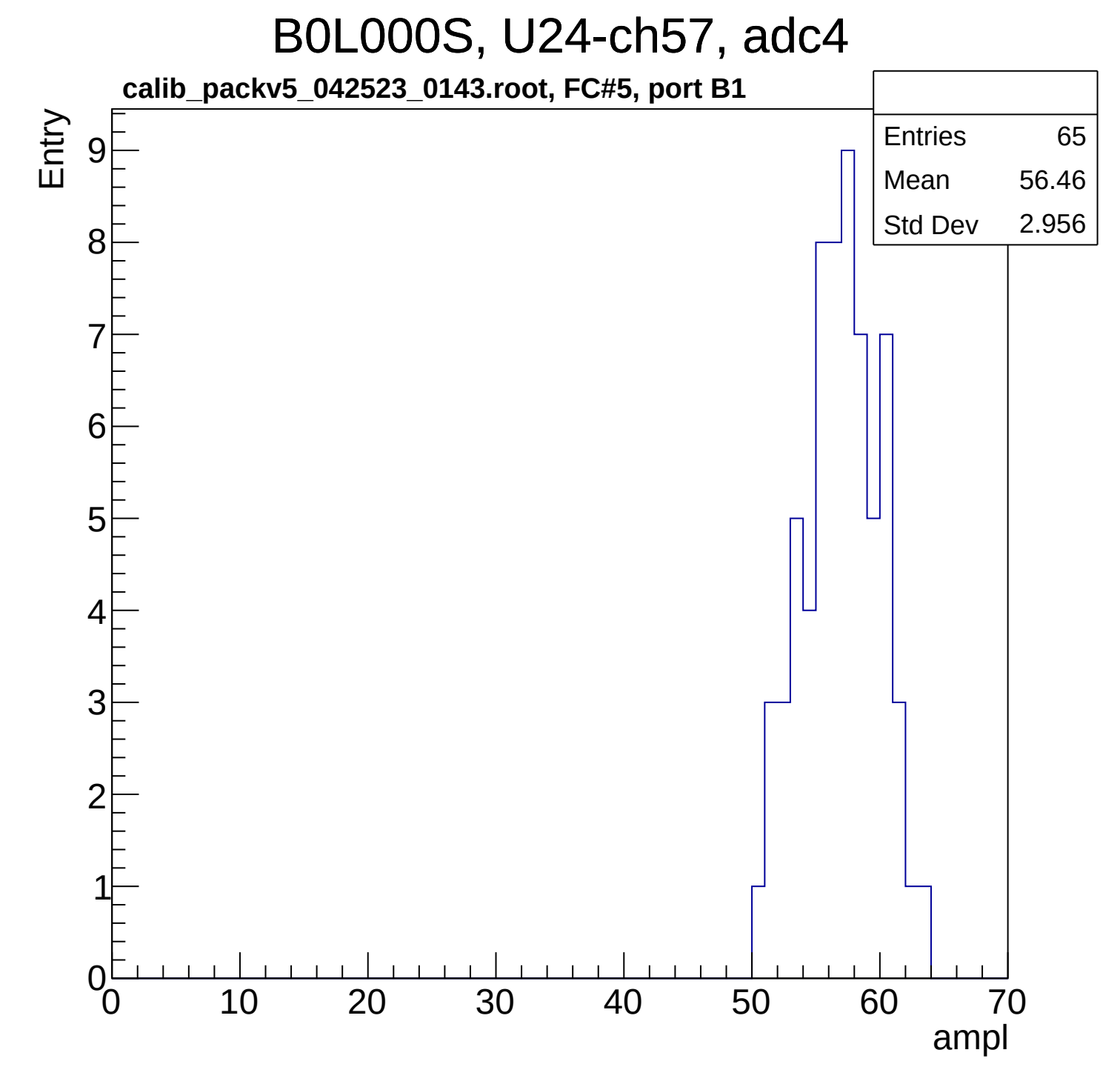
50
60
70
ampl


B0L000S, U24-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 56.46
Std Dev 2.956
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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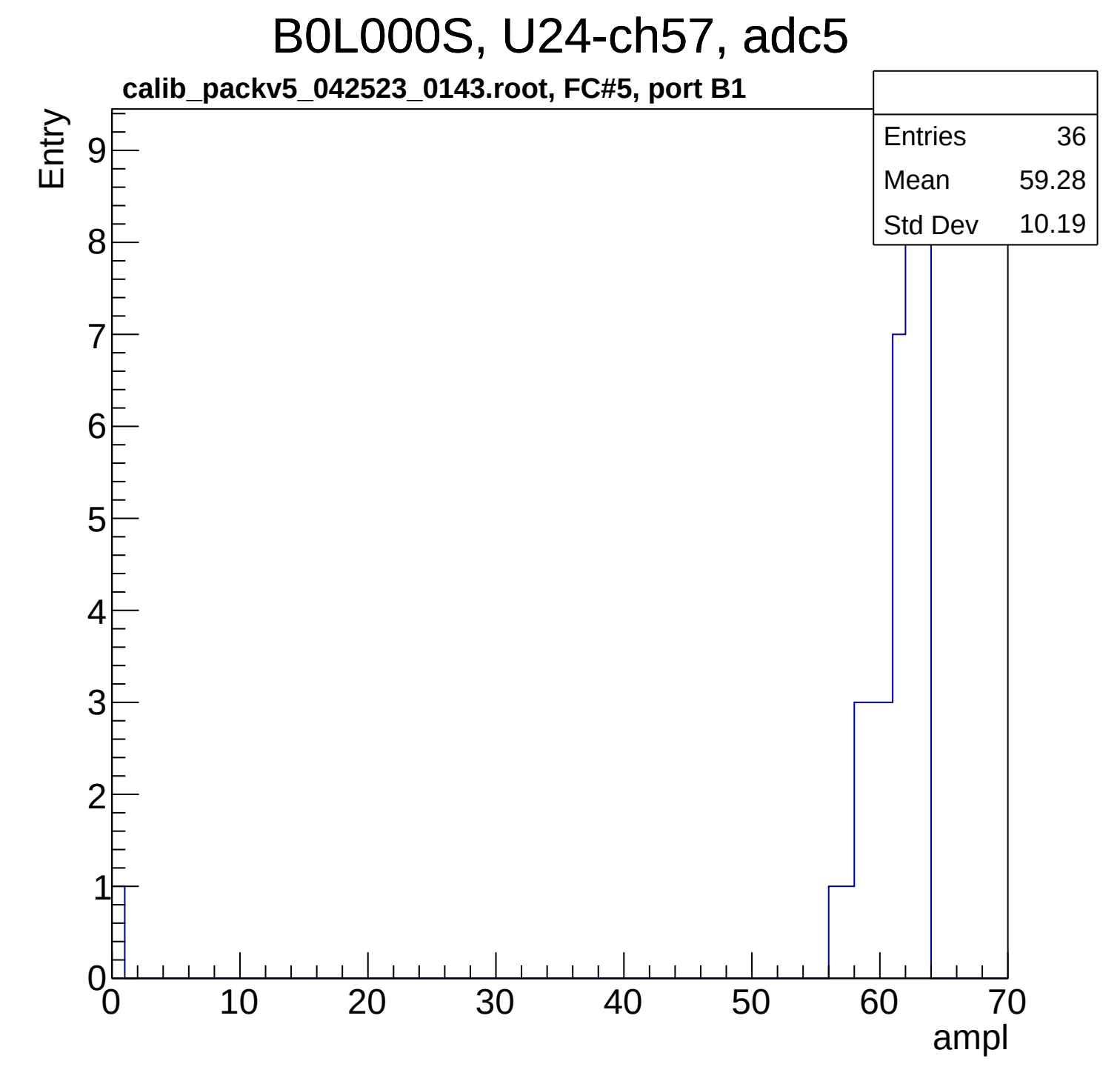
B0L000S, U24-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 59.28
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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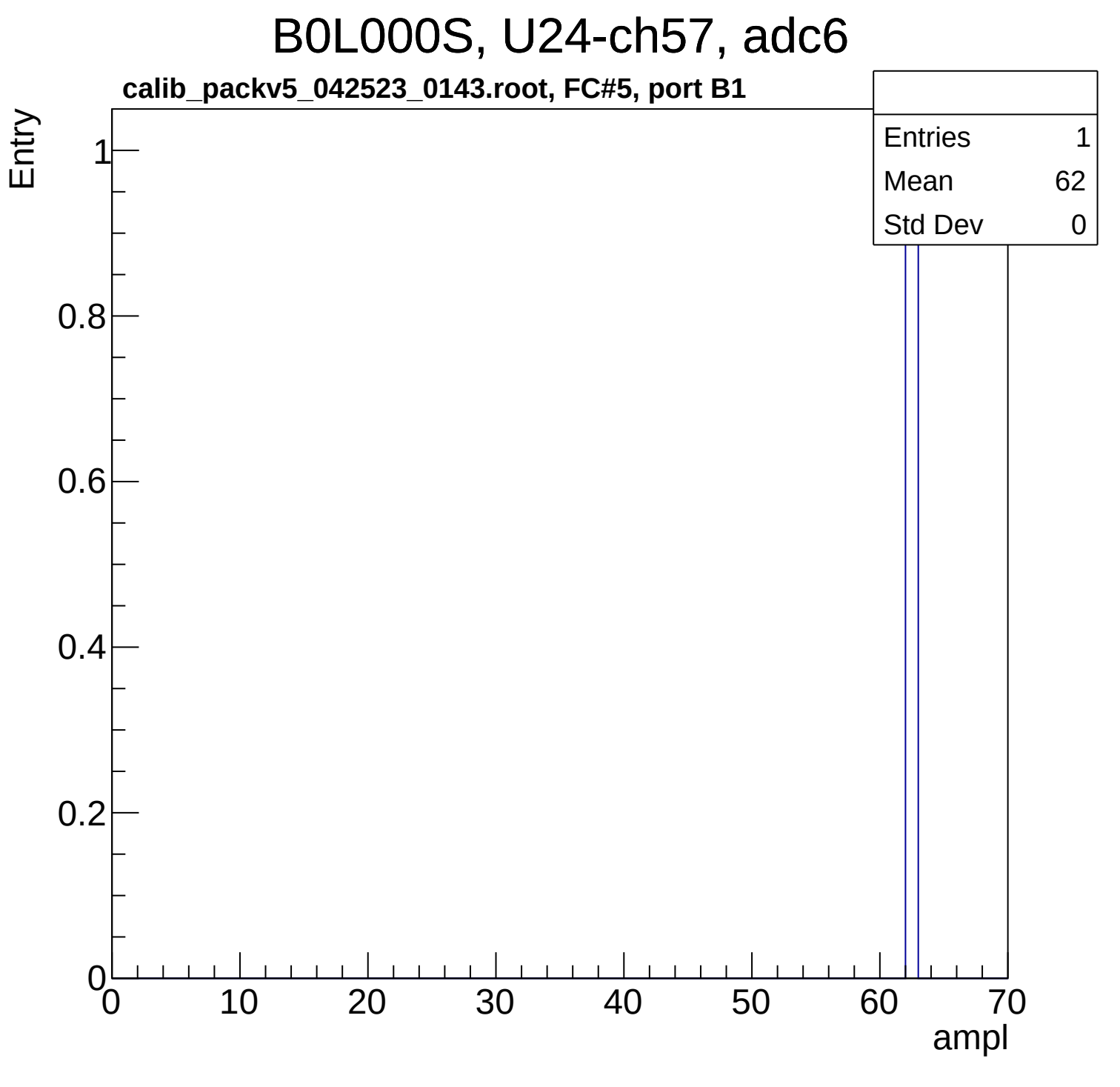
60
70


B0L000S, U24-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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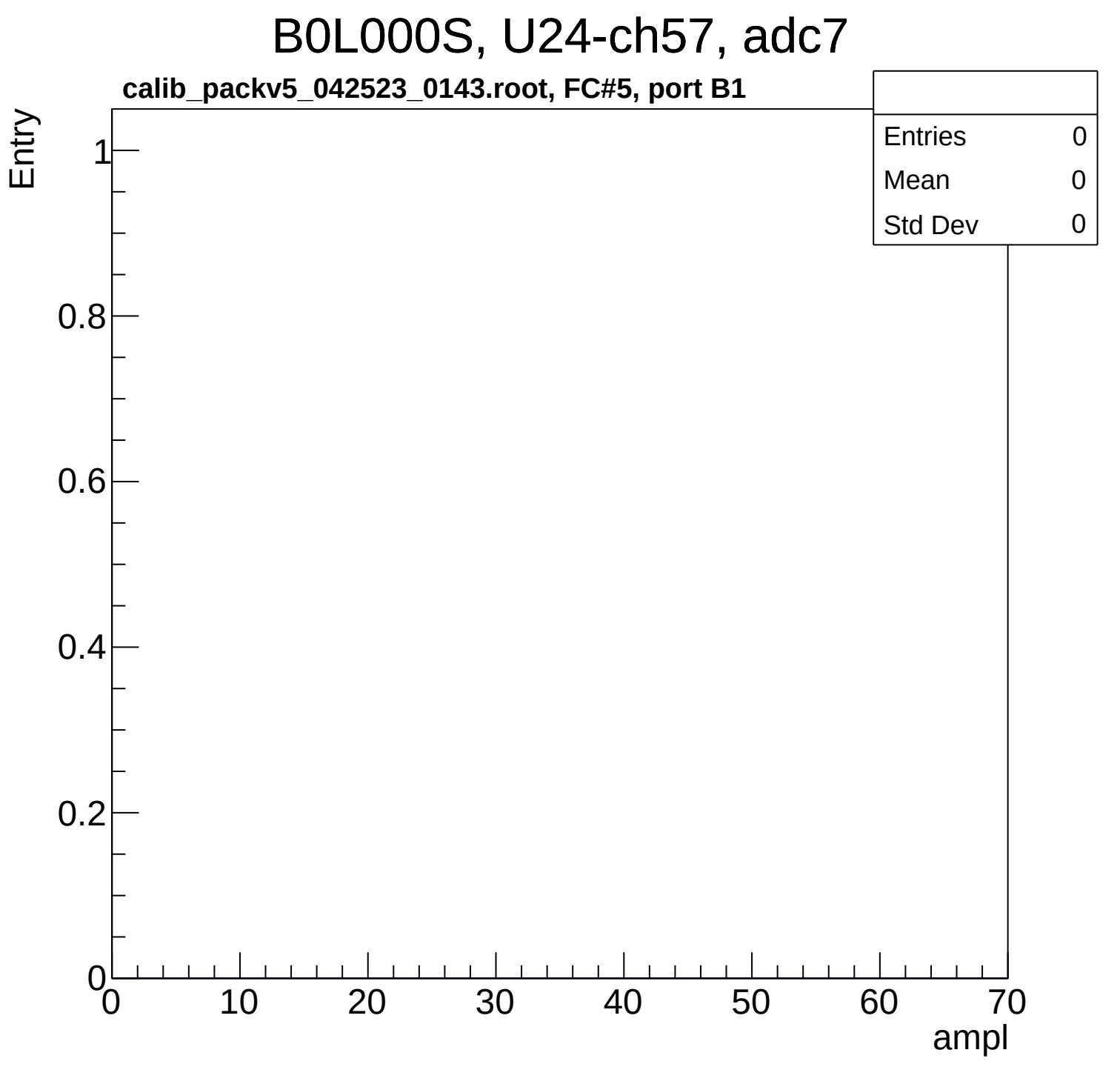
50
60
70
ampl


B0L000S, U24-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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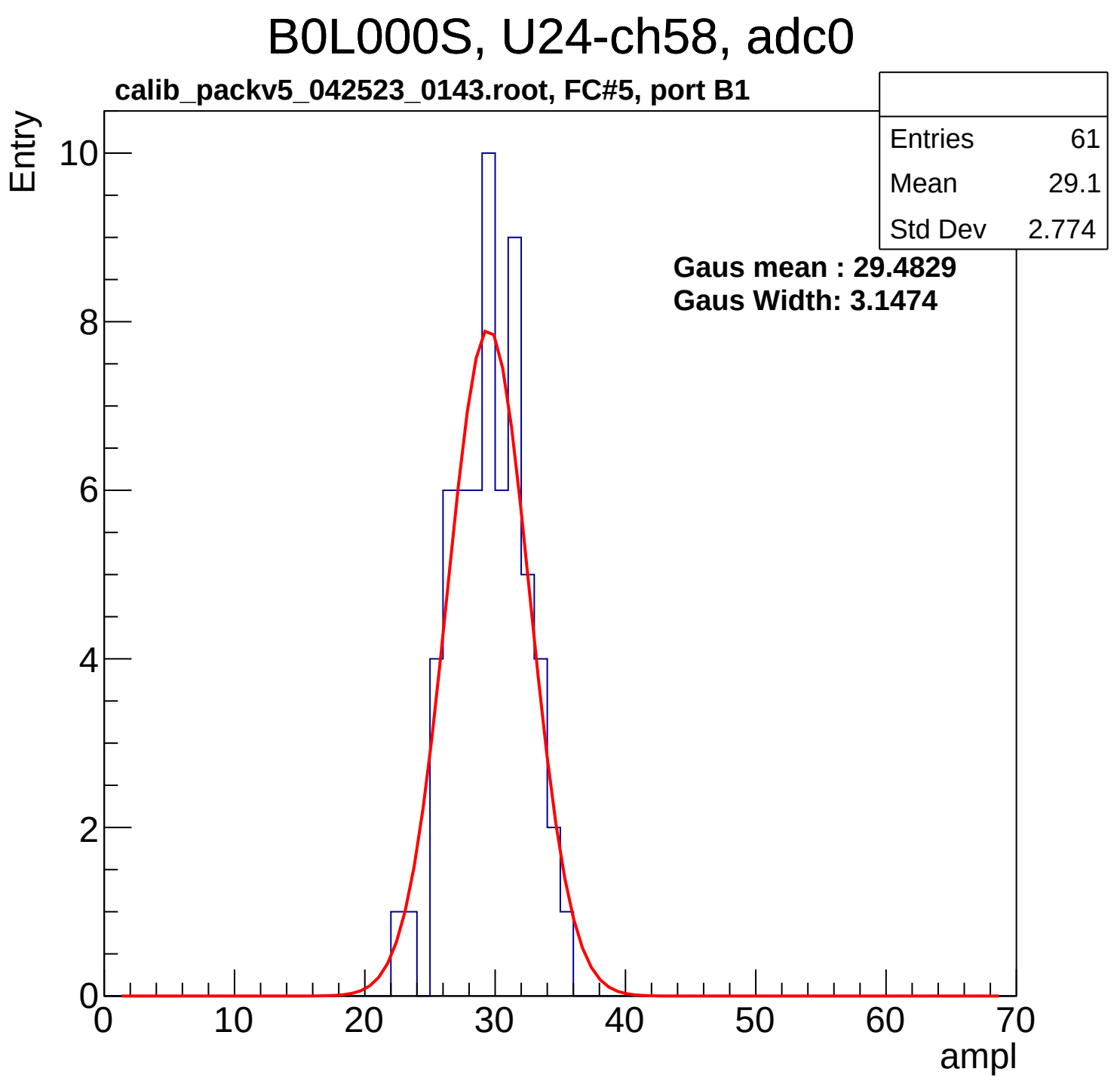
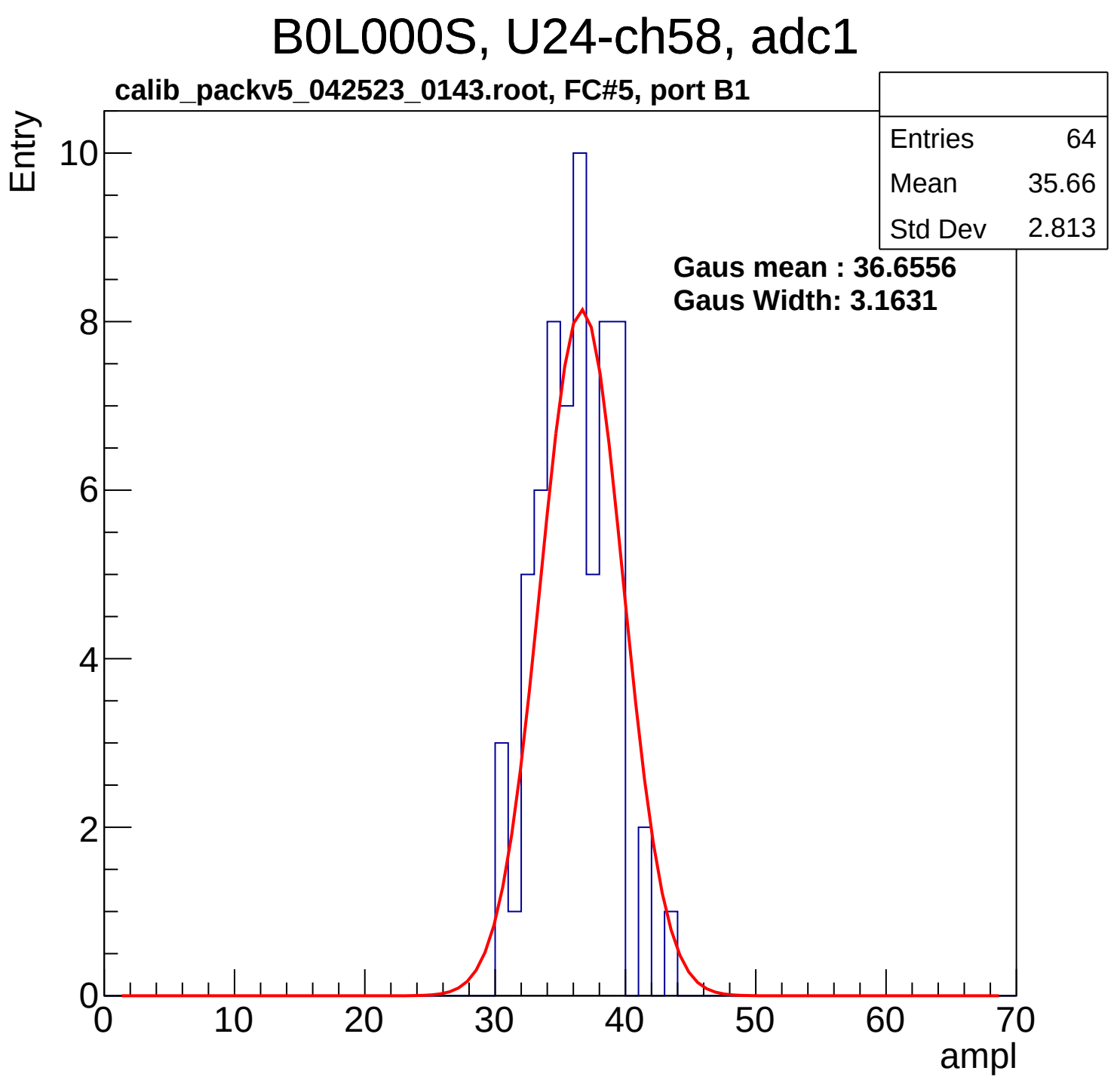
40
50
60
70
ampl


B0L000S, U24-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.1
Std Dev 2.774
Gaus mean : 29.4829
Gaus Width: 3.1474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.66
Std Dev 2.813
Gaus mean : 36.6556
Gaus Width: 3.1631
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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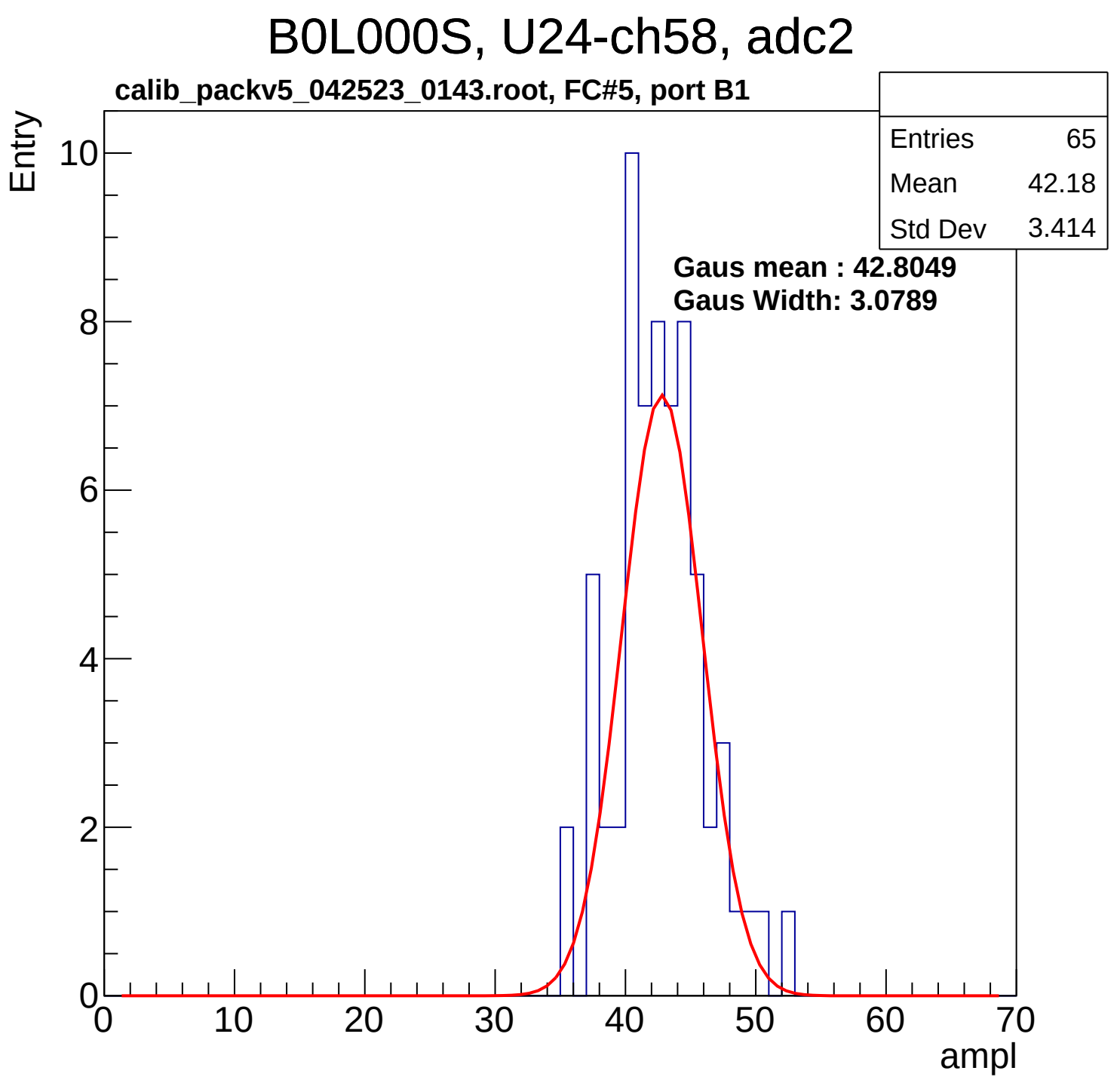
B0L000S, U24-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.18
Std Dev 3.414
Gaus mean : 42.8049
Gaus Width: 3.0789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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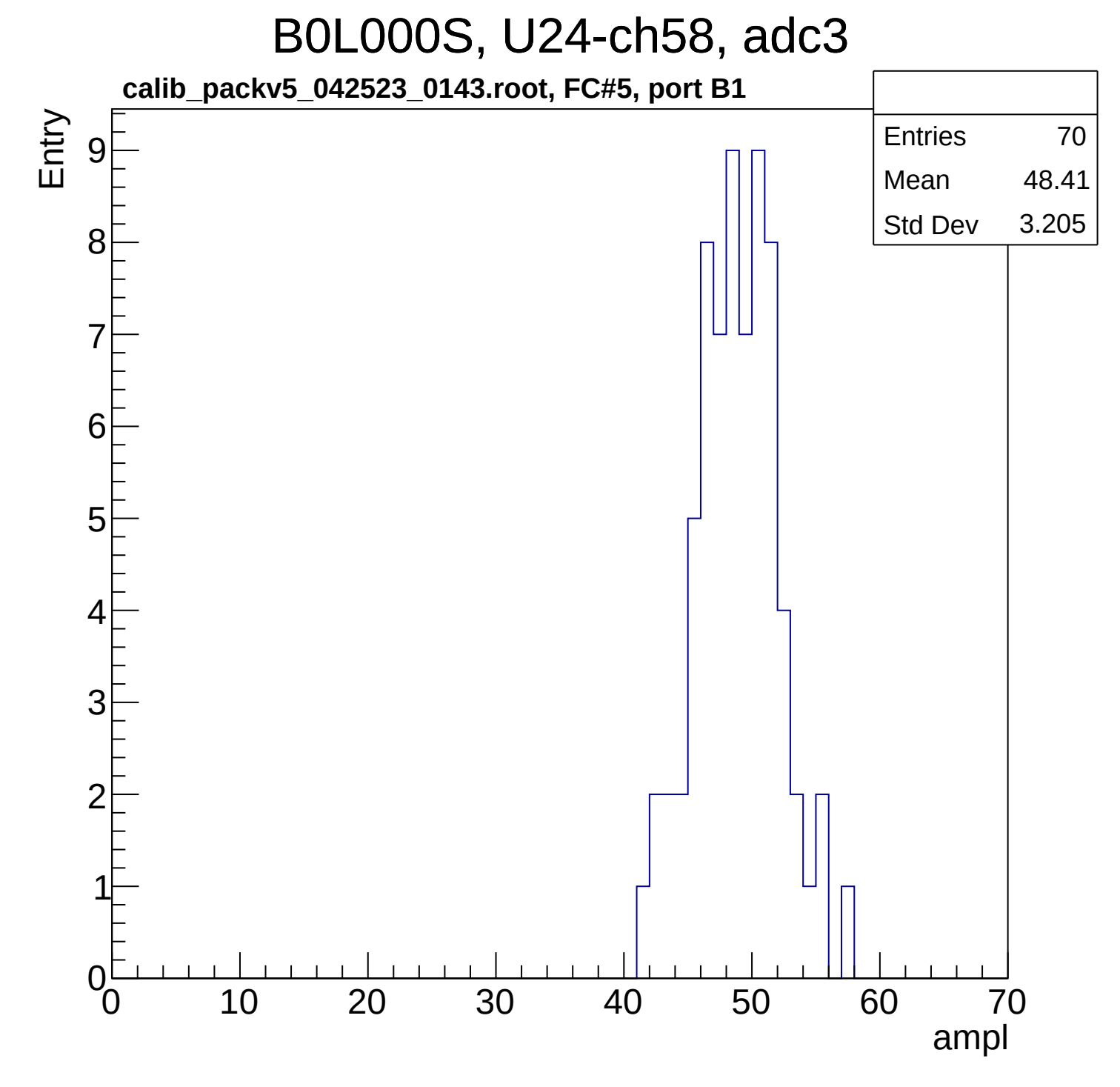
B0L000S, U24-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.41
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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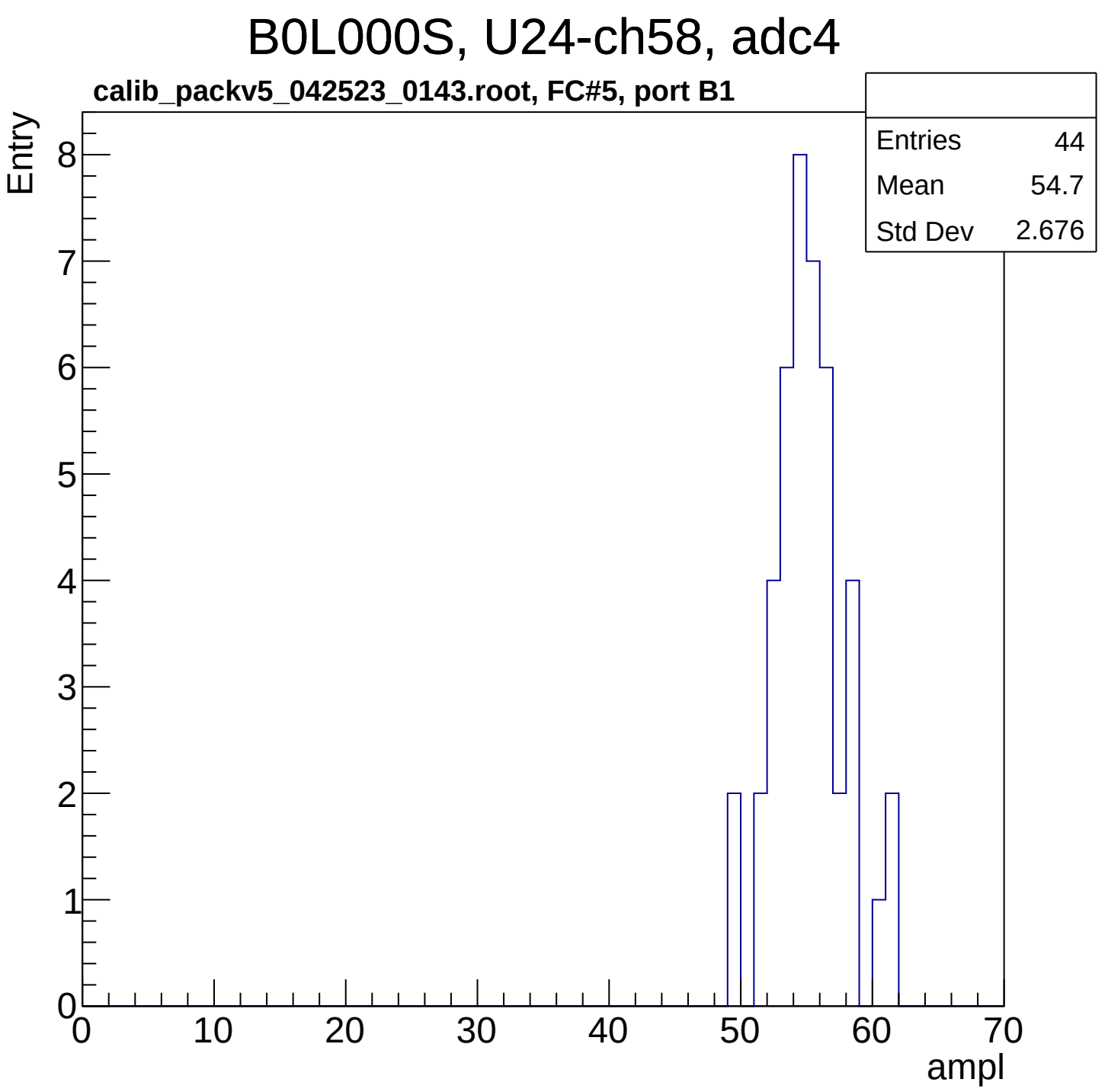
B0L000S, U24-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 54.7
Std Dev 2.676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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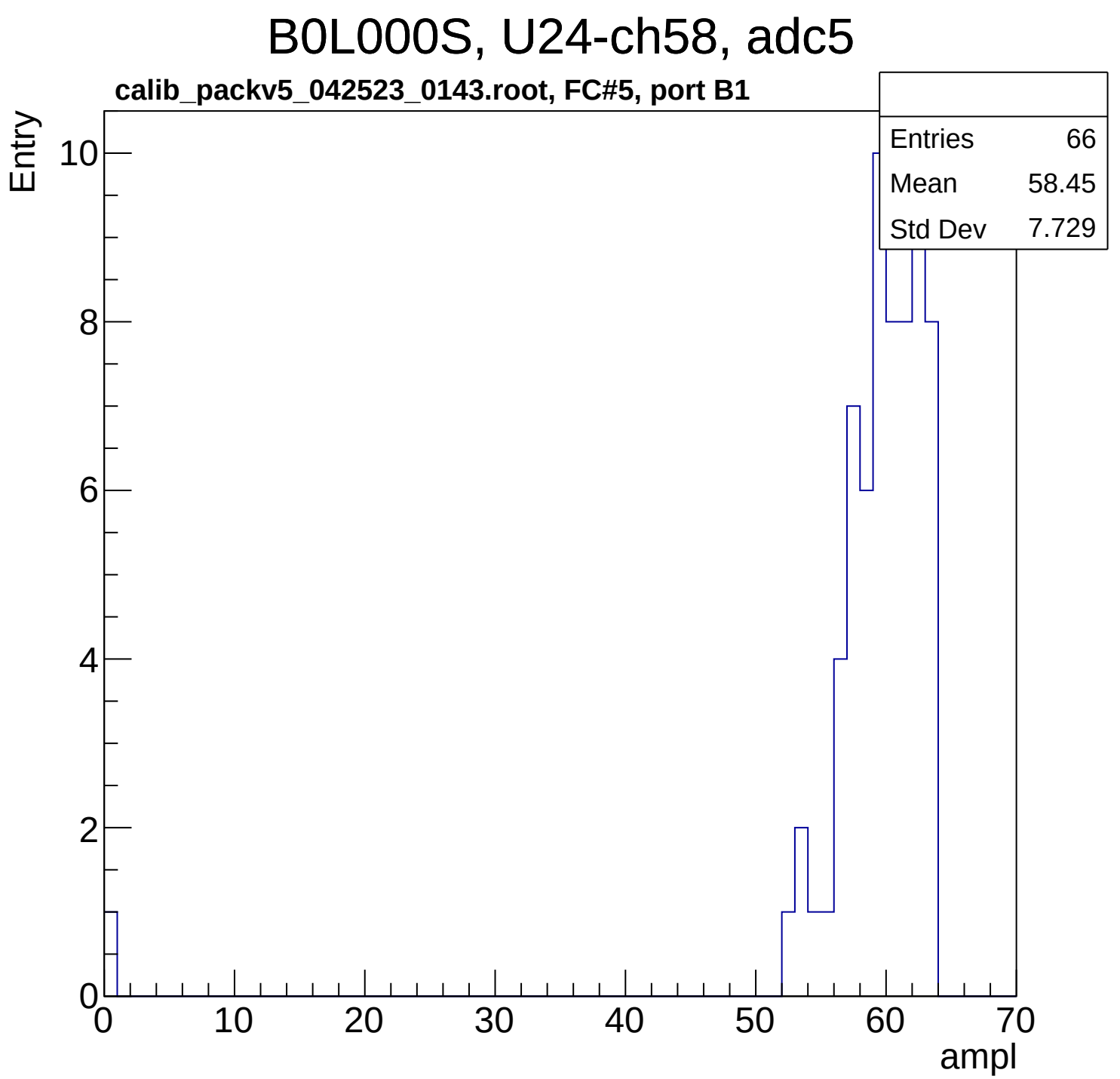
B0L000S, U24-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 58.45
Std Dev 7.729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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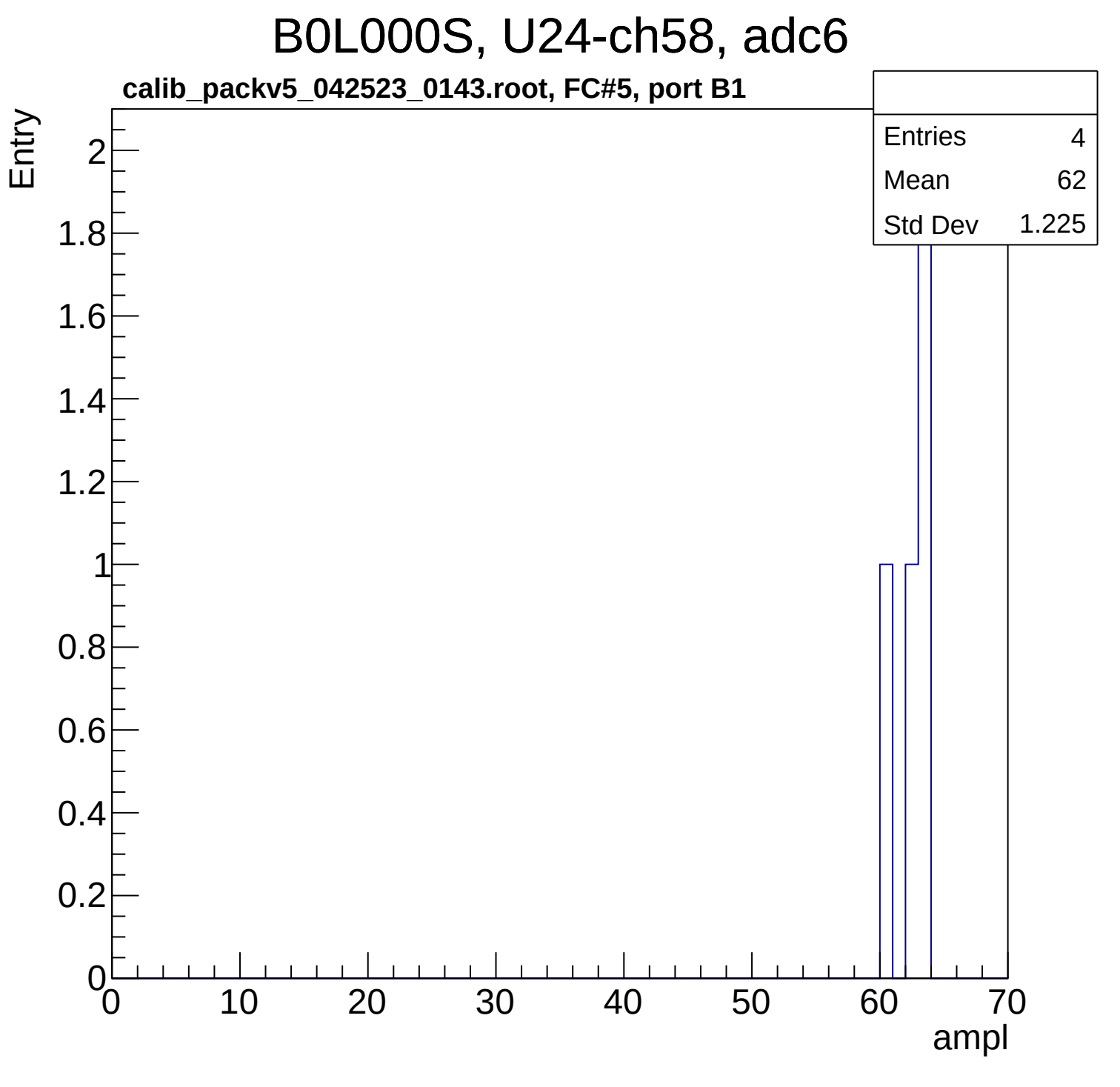
B0L000S, U24-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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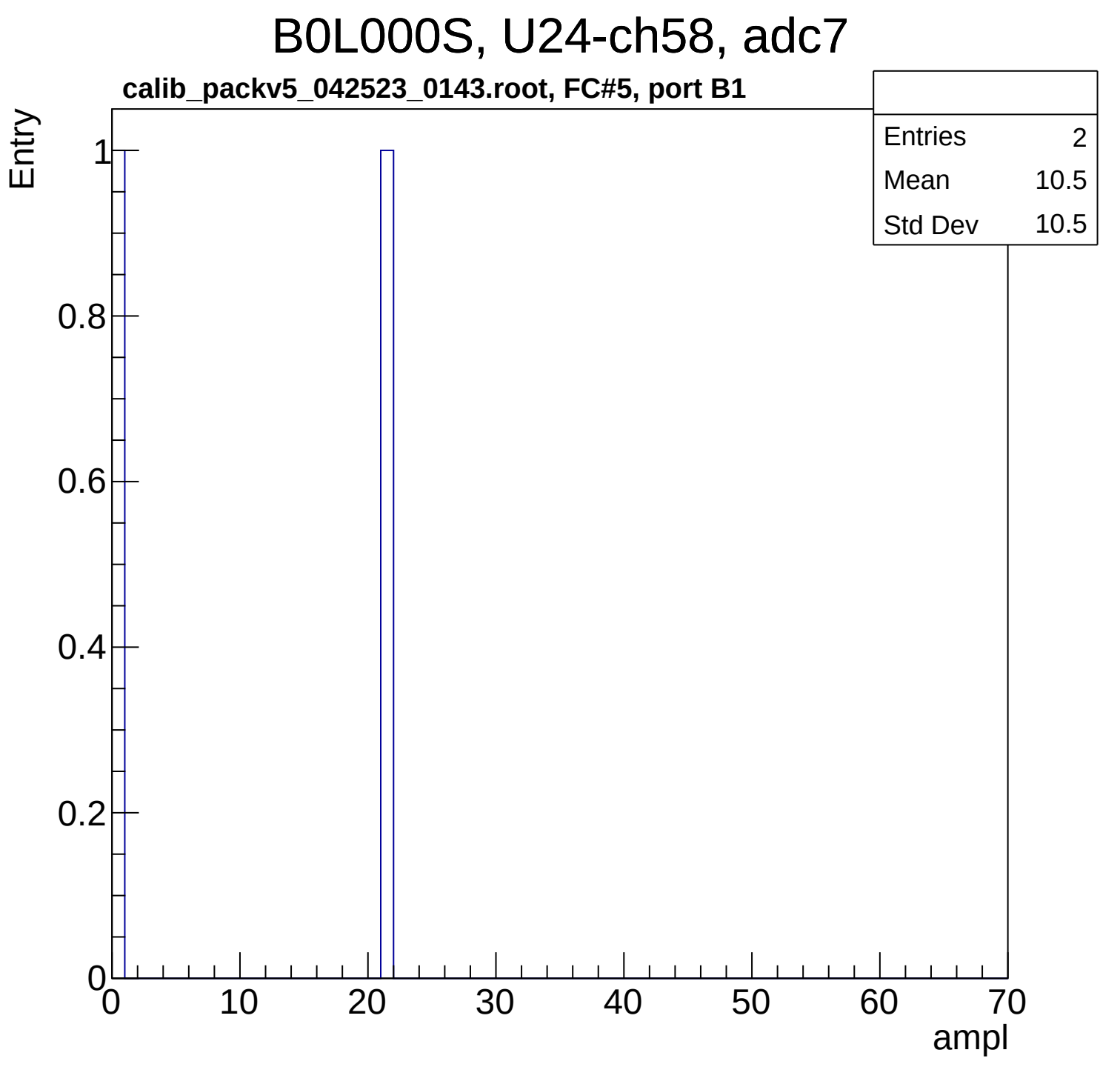
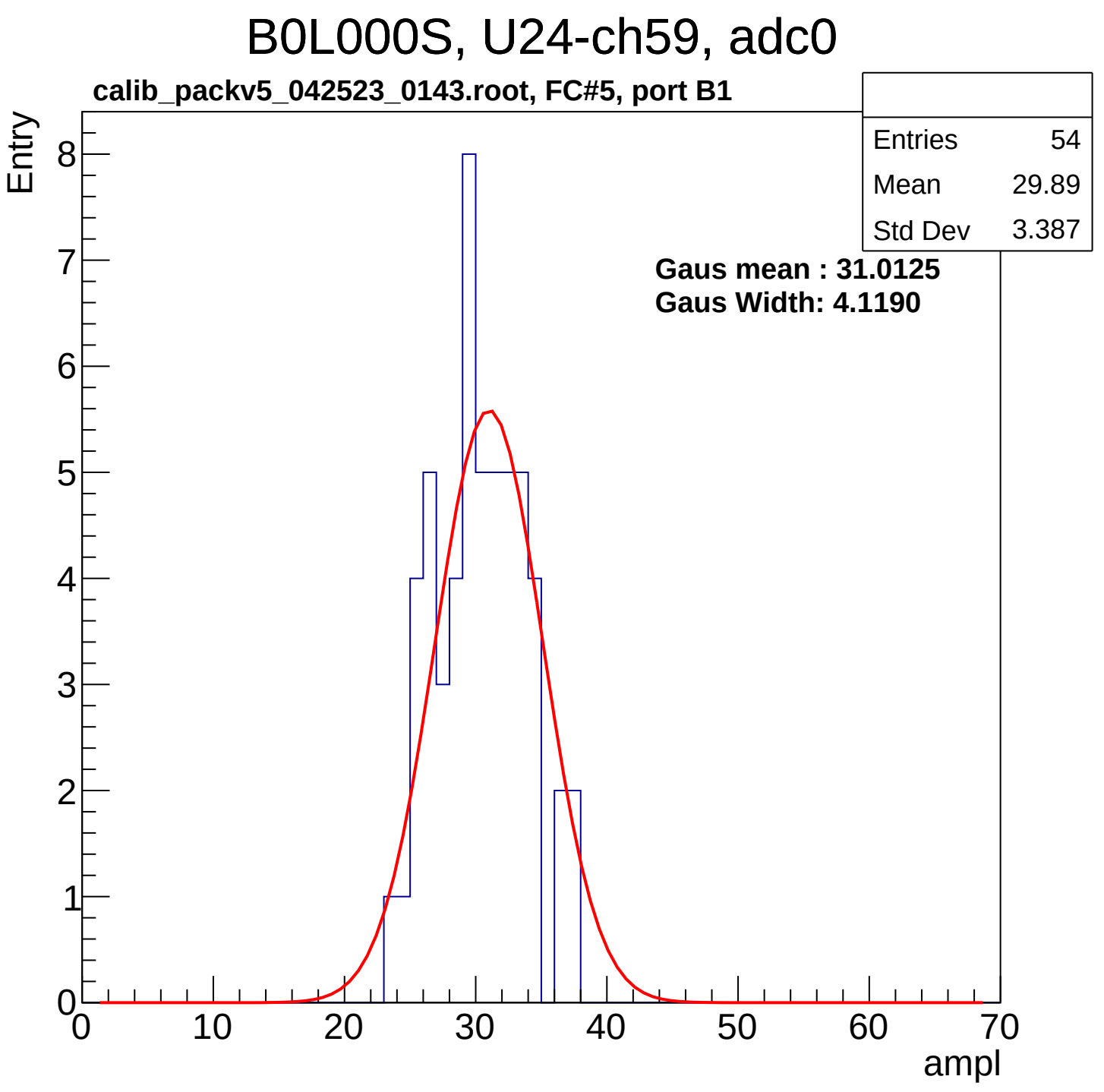
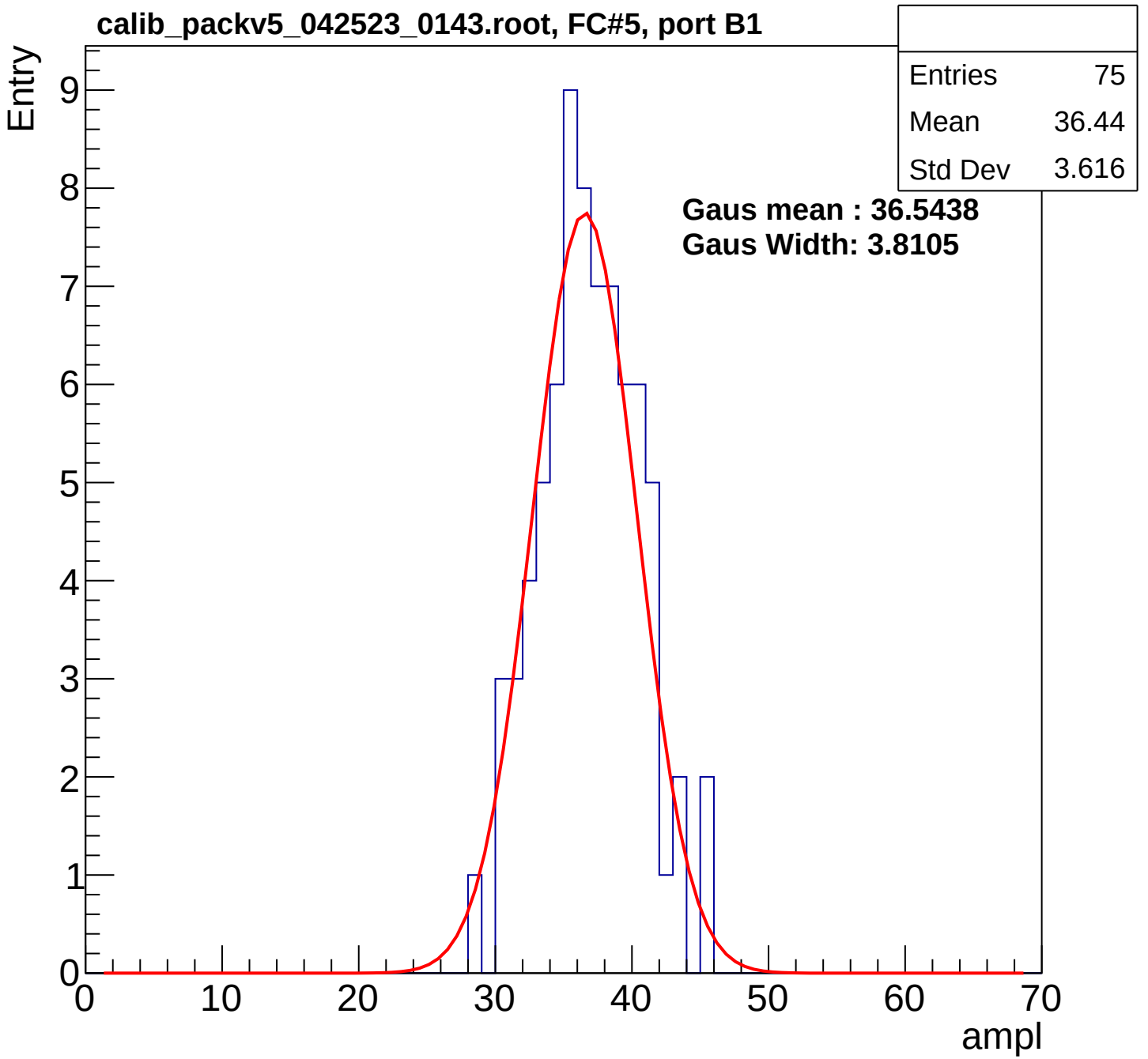
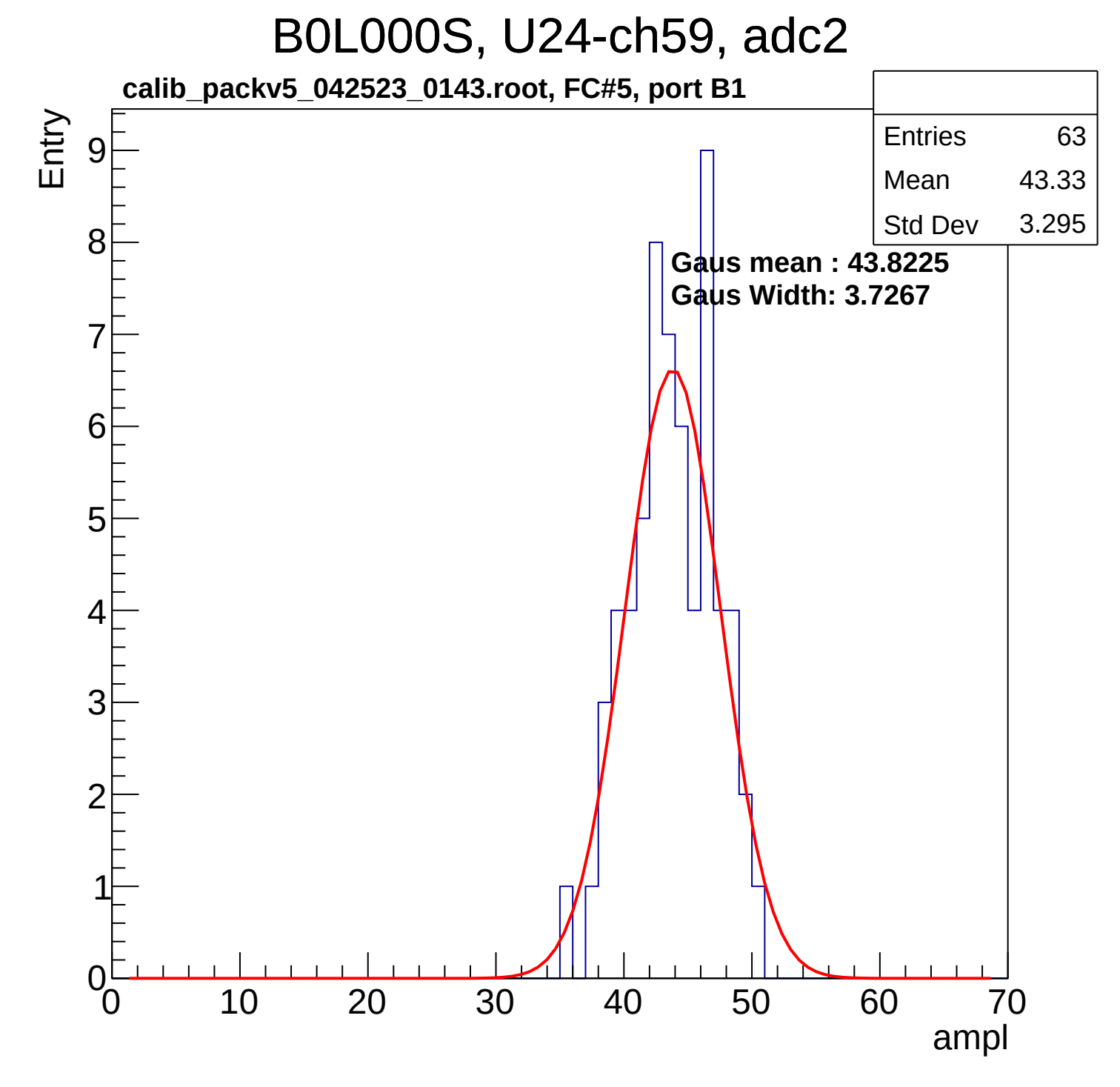
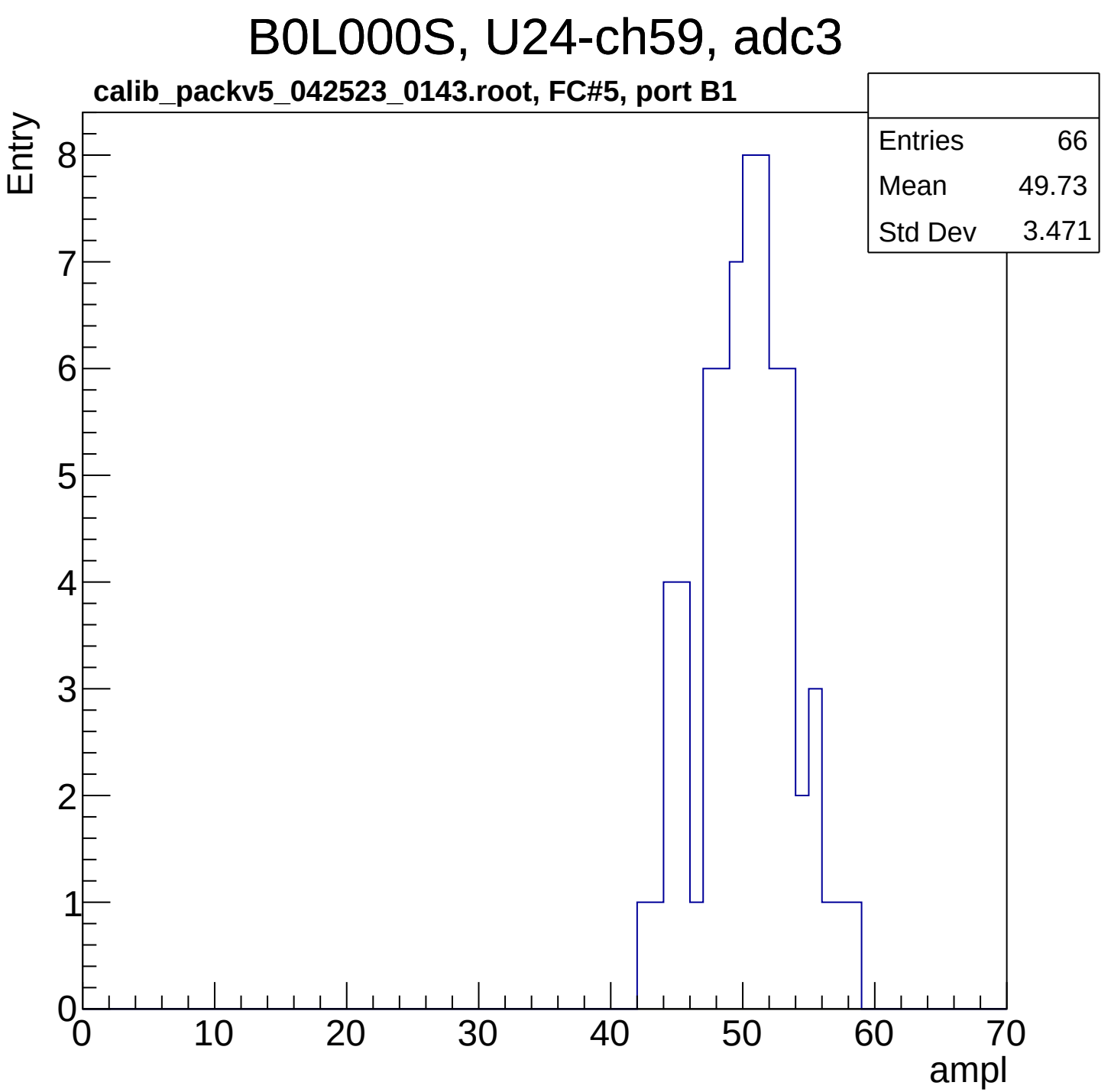

B0L000S, U24-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.89
Std Dev 3.387
Gaus mean : 31.0125
Gaus Width: 4.1190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.44
Std Dev 3.616
Gaus mean : 36.5438
Gaus Width: 3.8105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.33
Std Dev 3.295
Gaus mean : 43.8225
Gaus Width: 3.7267
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.73
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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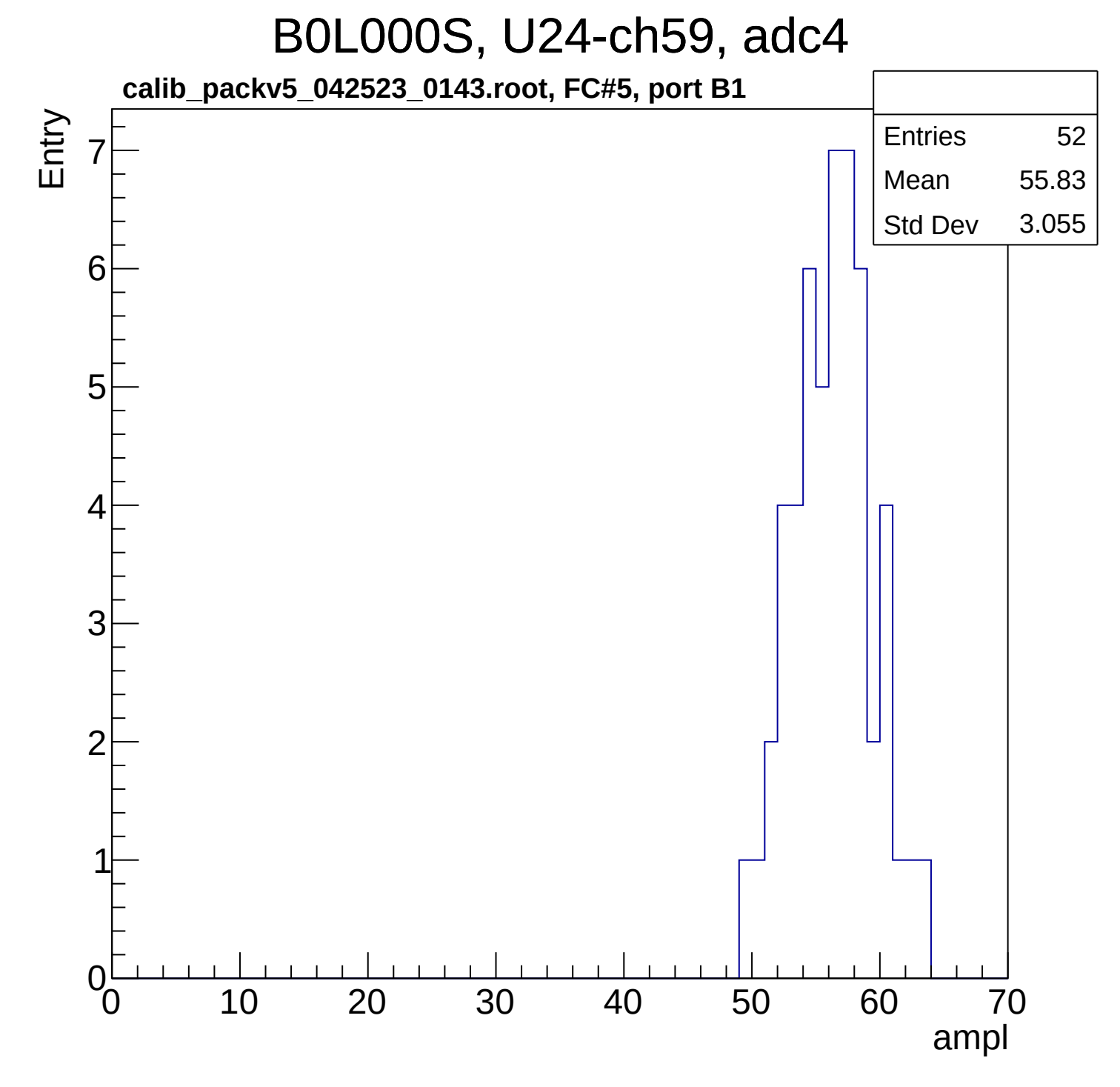
B0L000S, U24-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.83
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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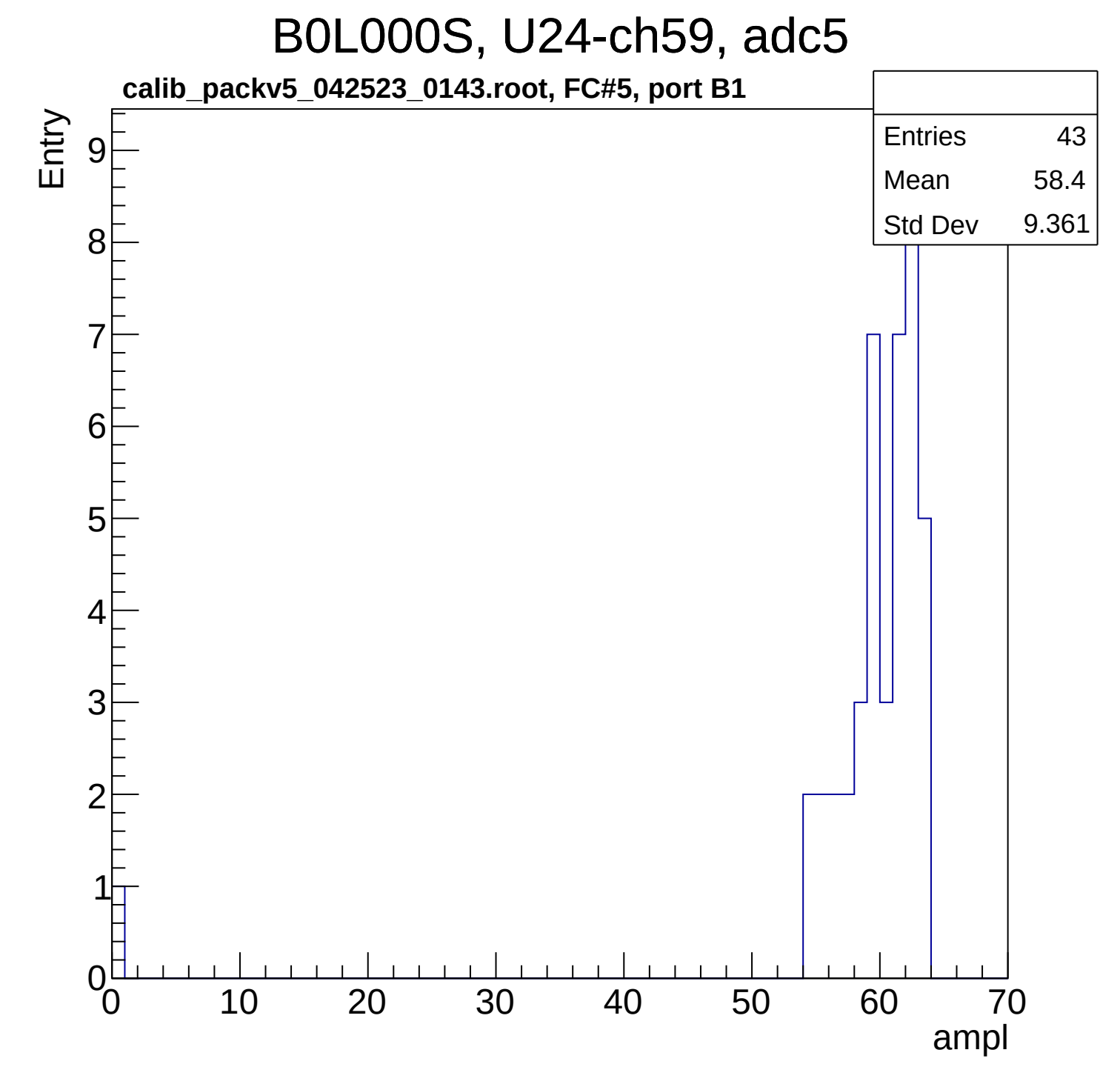
40
50
60
70


B0L000S, U24-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.4
Std Dev 9.361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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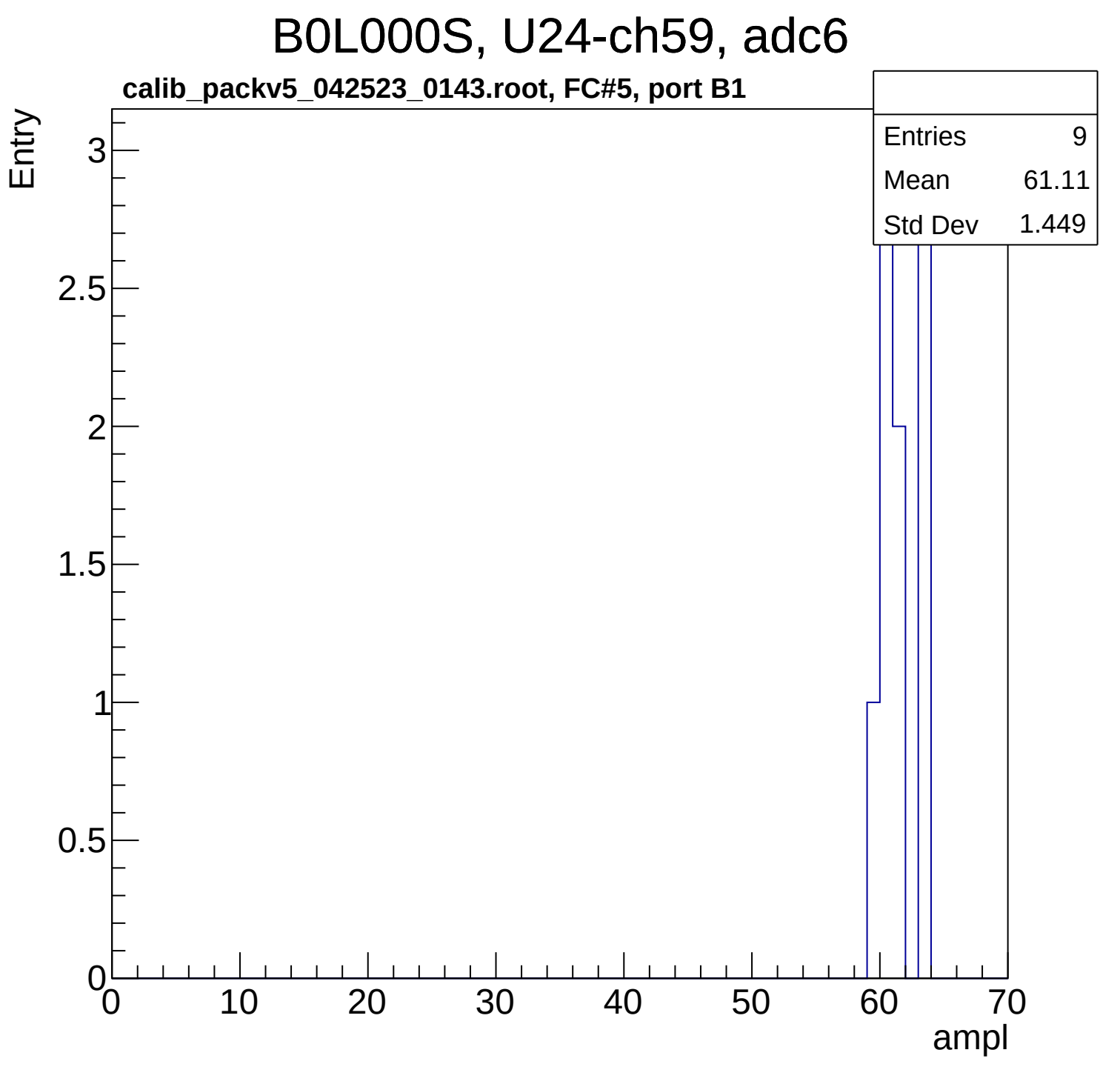
B0L000S, U24-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.11
Std Dev 1.449
Entry
ampl
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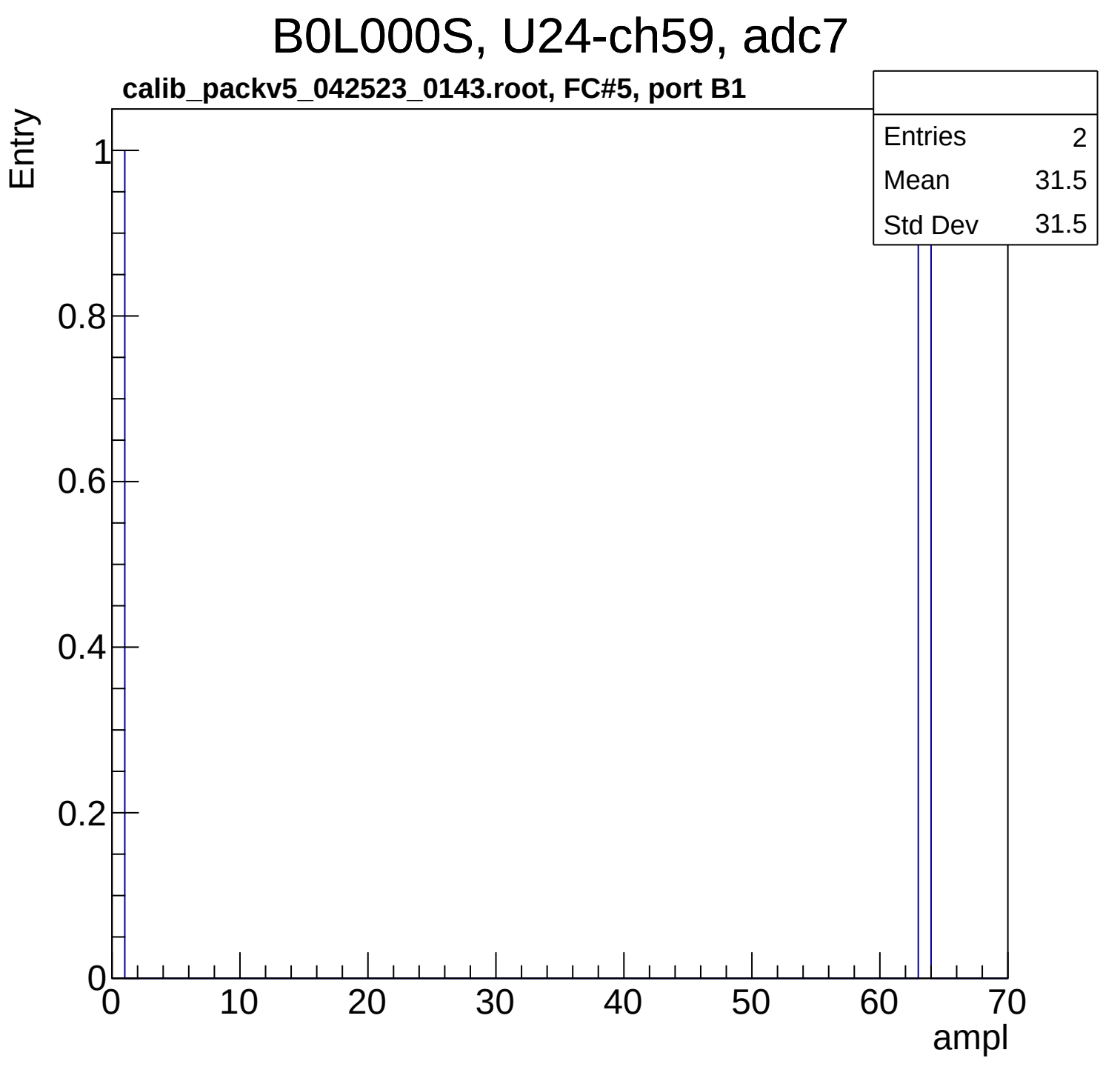
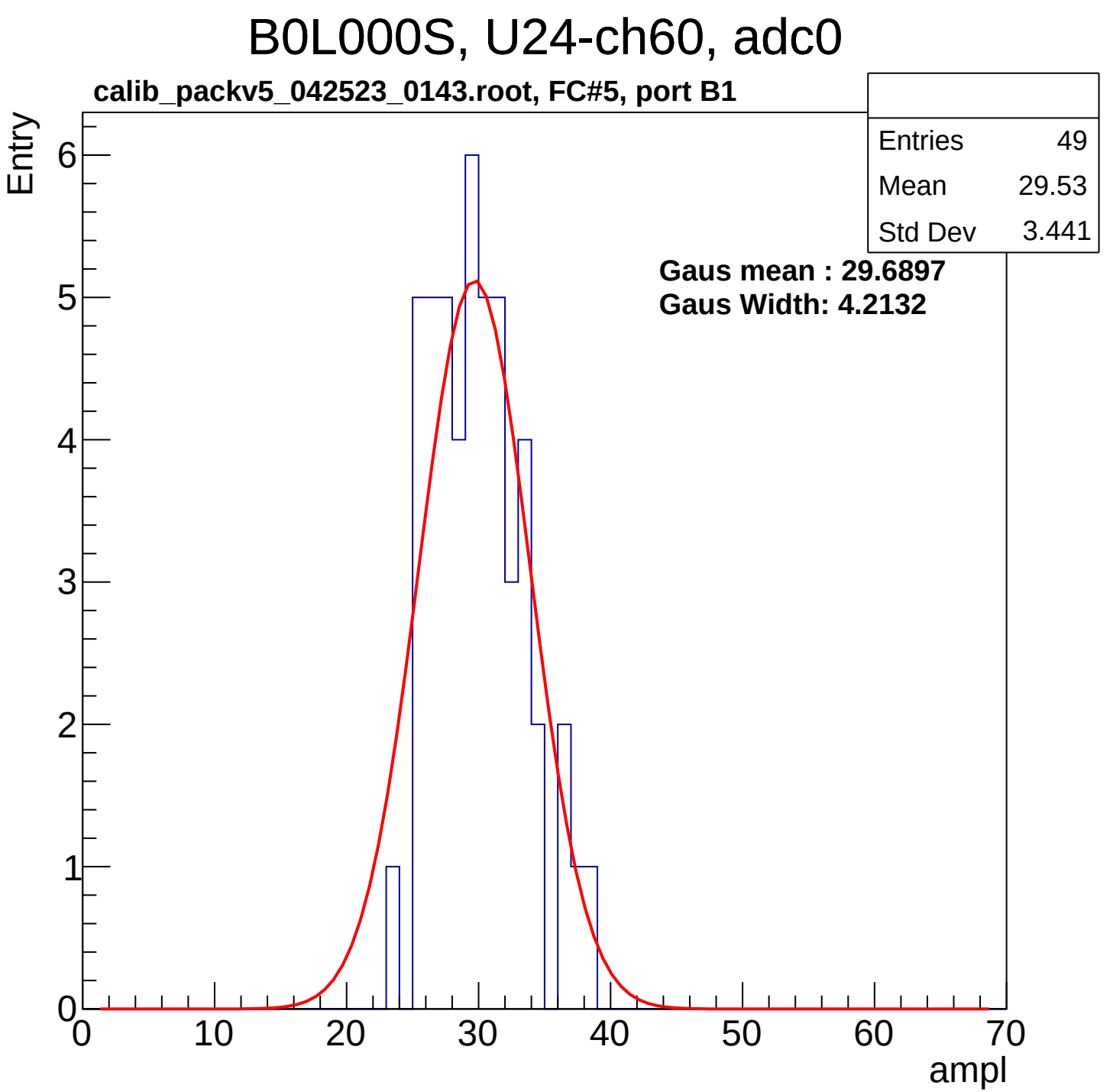
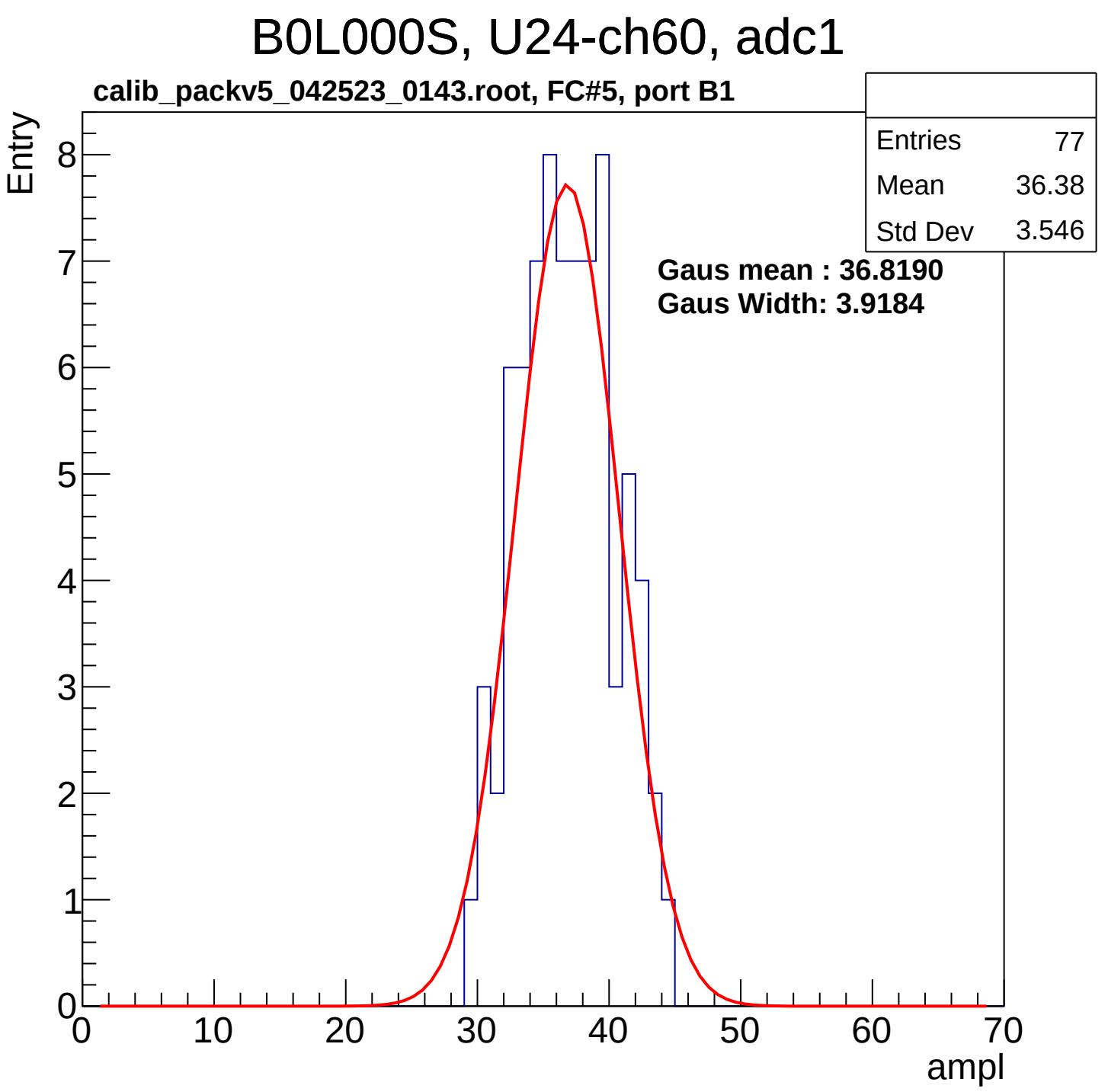
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 29.53
Std Dev 3.441
Gaus mean : 29.6897
Gaus Width: 4.2132
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.38
Std Dev 3.546
Gaus mean : 36.8190
Gaus Width: 3.9184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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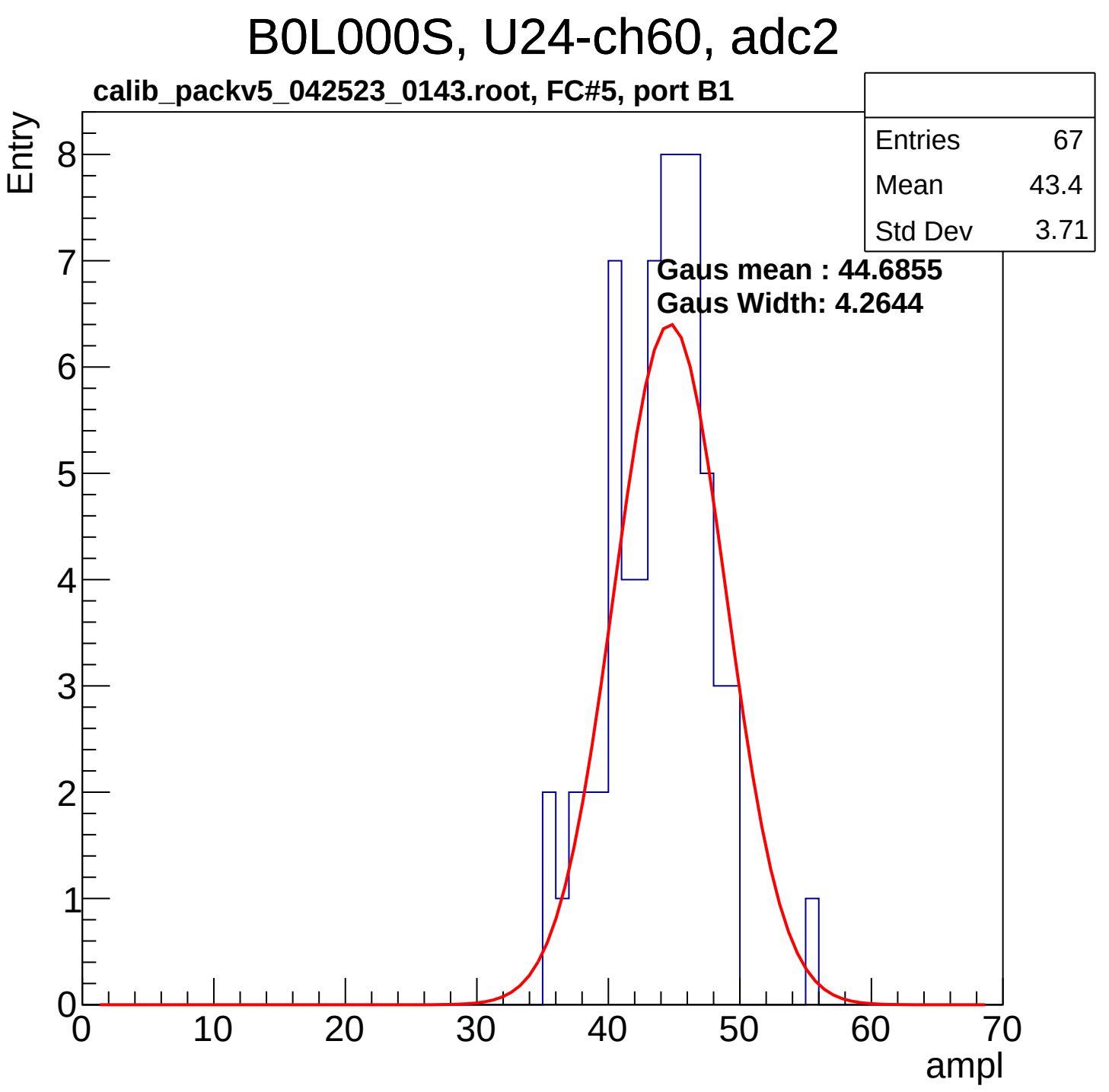
40
50
60
70


B0L000S, U24-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.4
Std Dev 3.71
Gaus mean : 44.6855
Gaus Width: 4.2644
Entry
ampl
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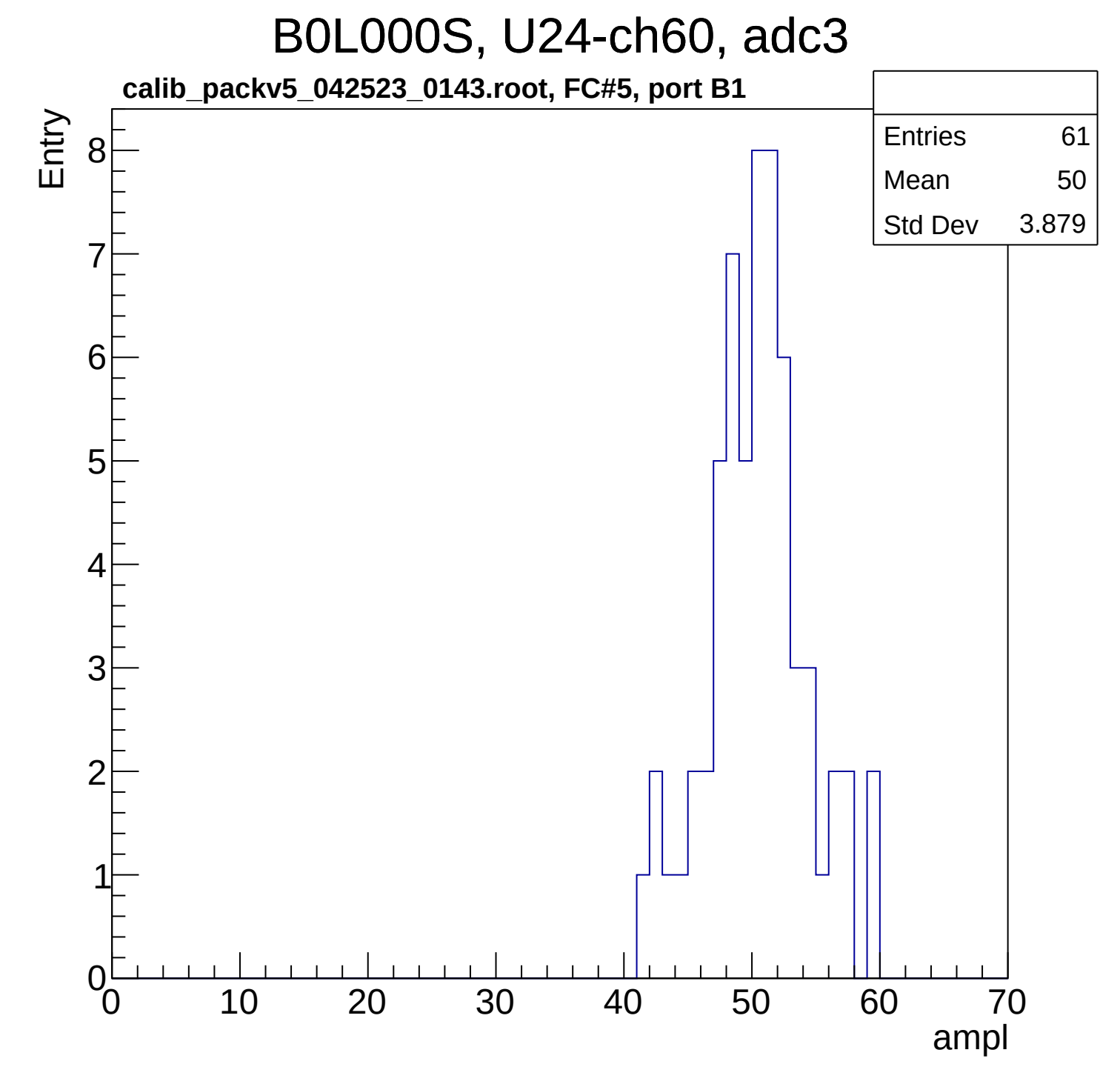
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50
Std Dev 3.879
Entry
ampl
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1
2
3
4
5
6
7
8
0
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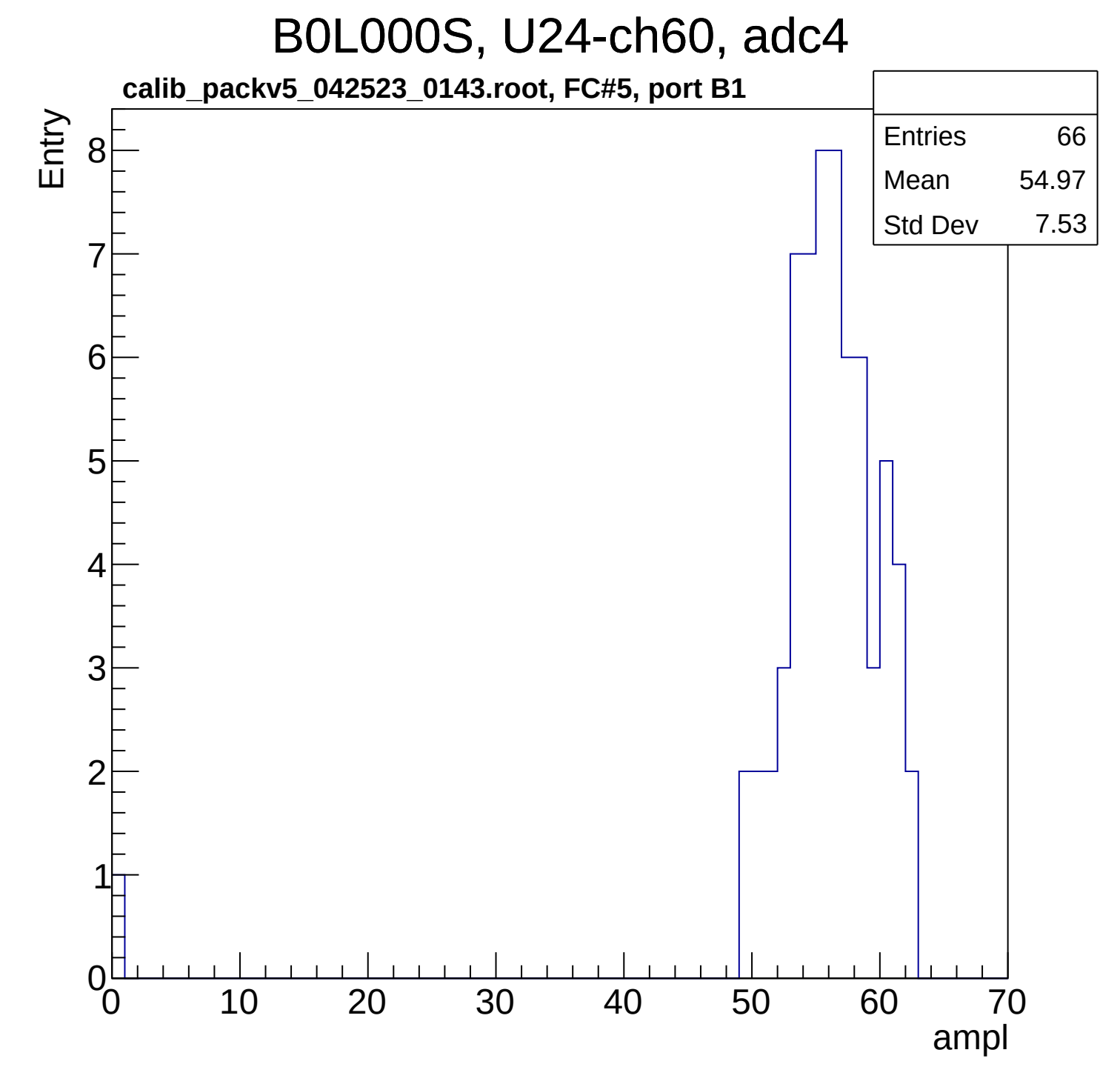
20
30
40
50
60
70


B0L000S, U24-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.97
Std Dev 7.53
Entry
ampl
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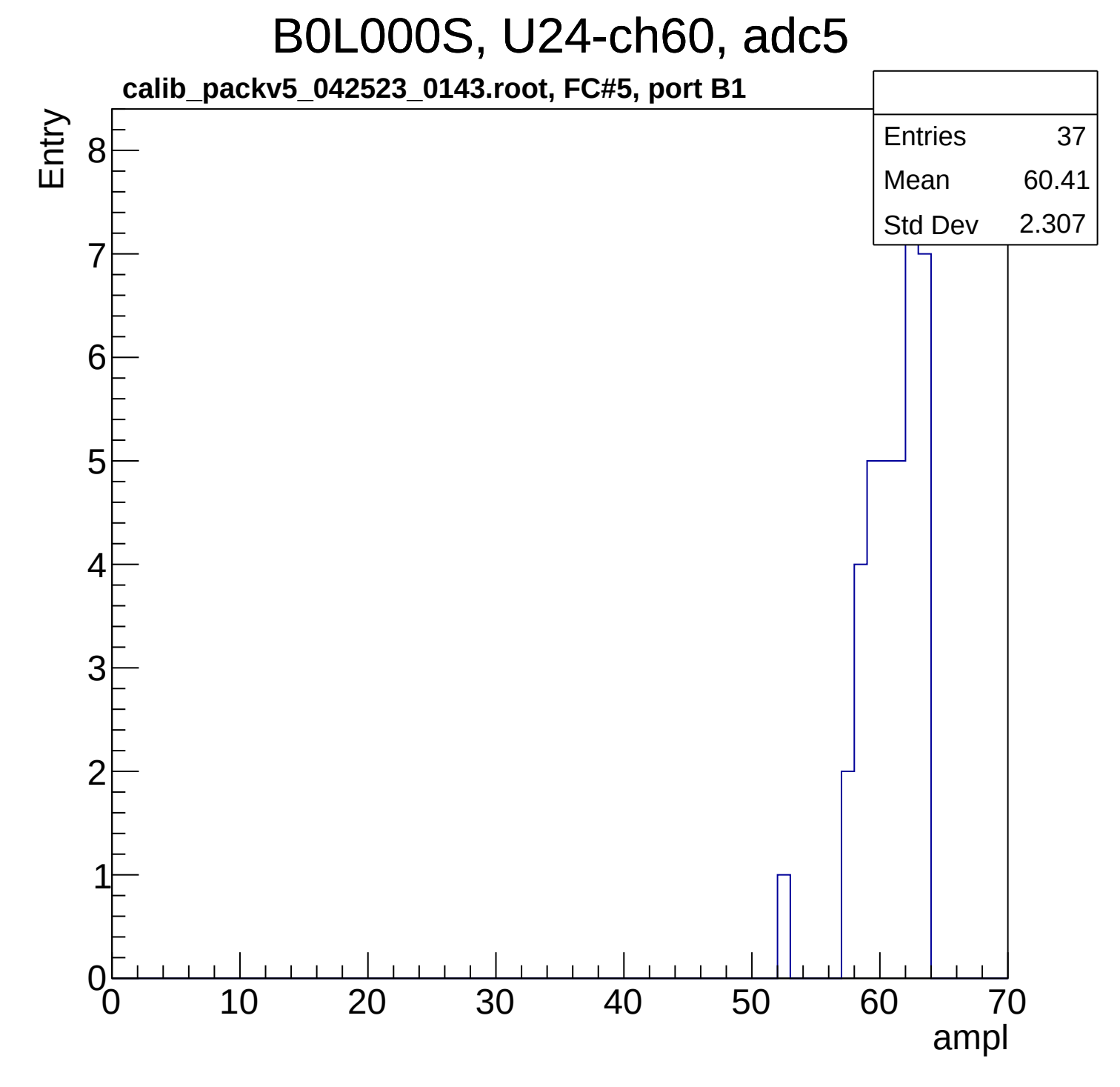
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.41
Std Dev 2.307
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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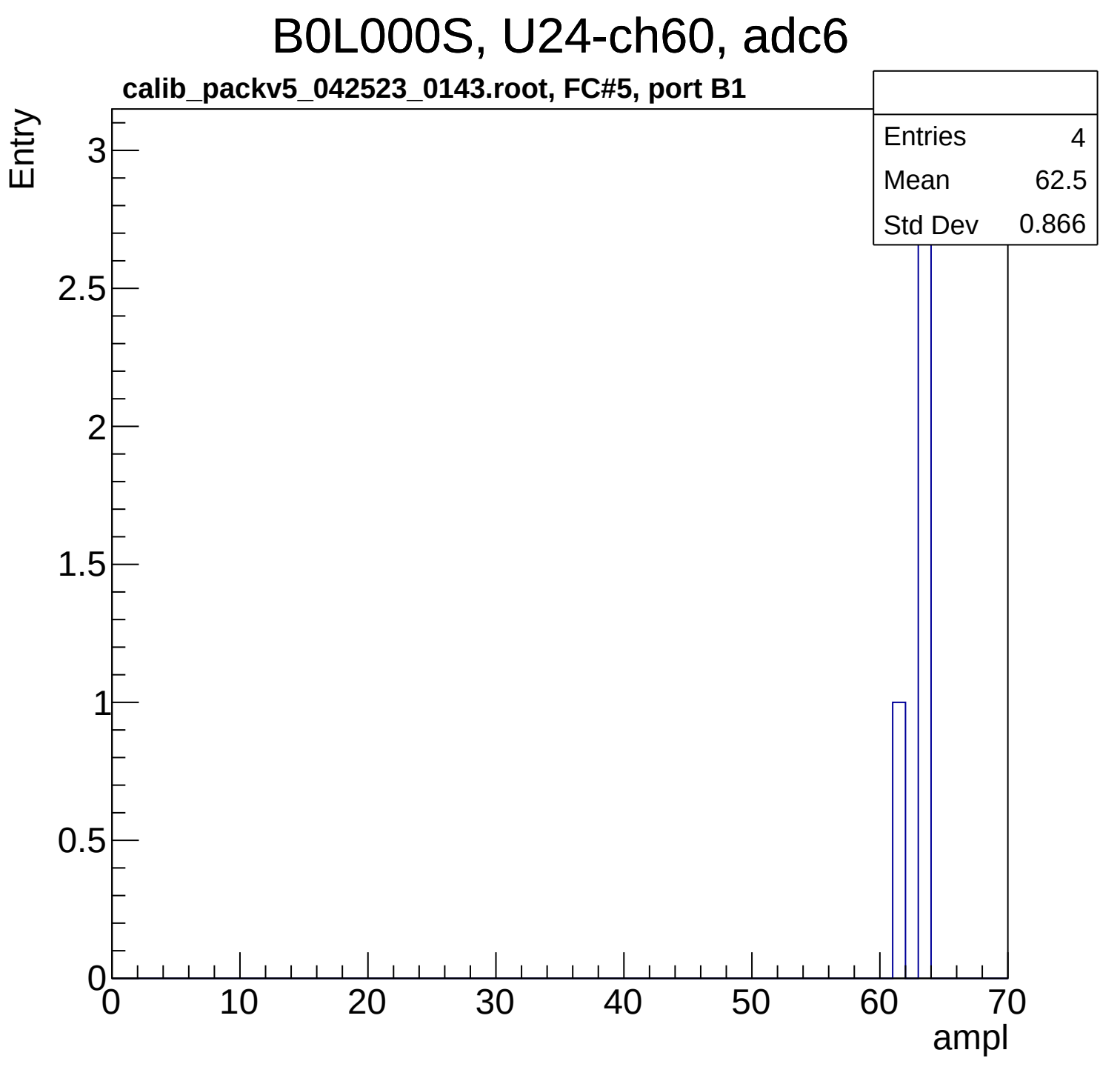
B0L000S, U24-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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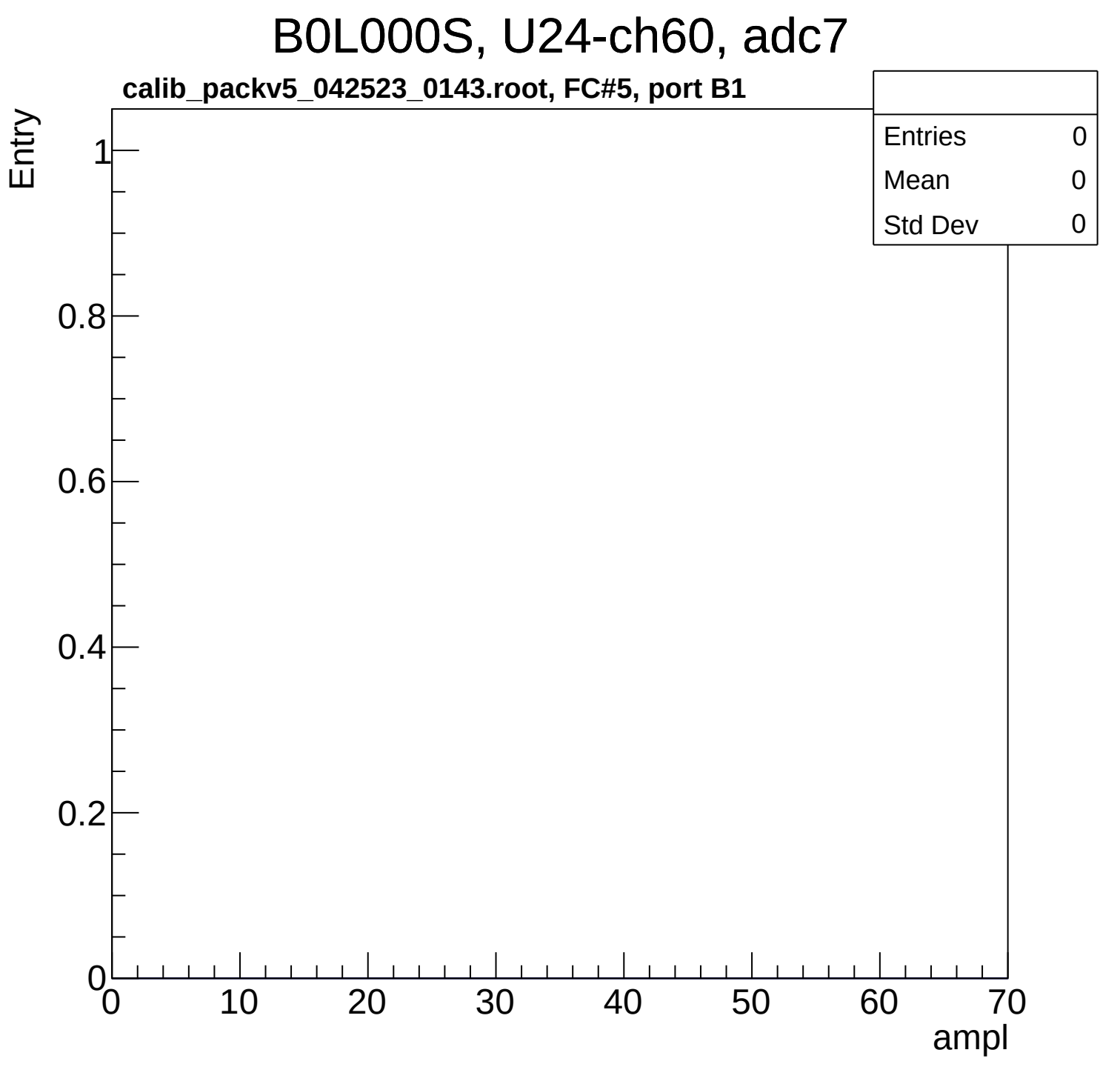
40
50
60
70


B0L000S, U24-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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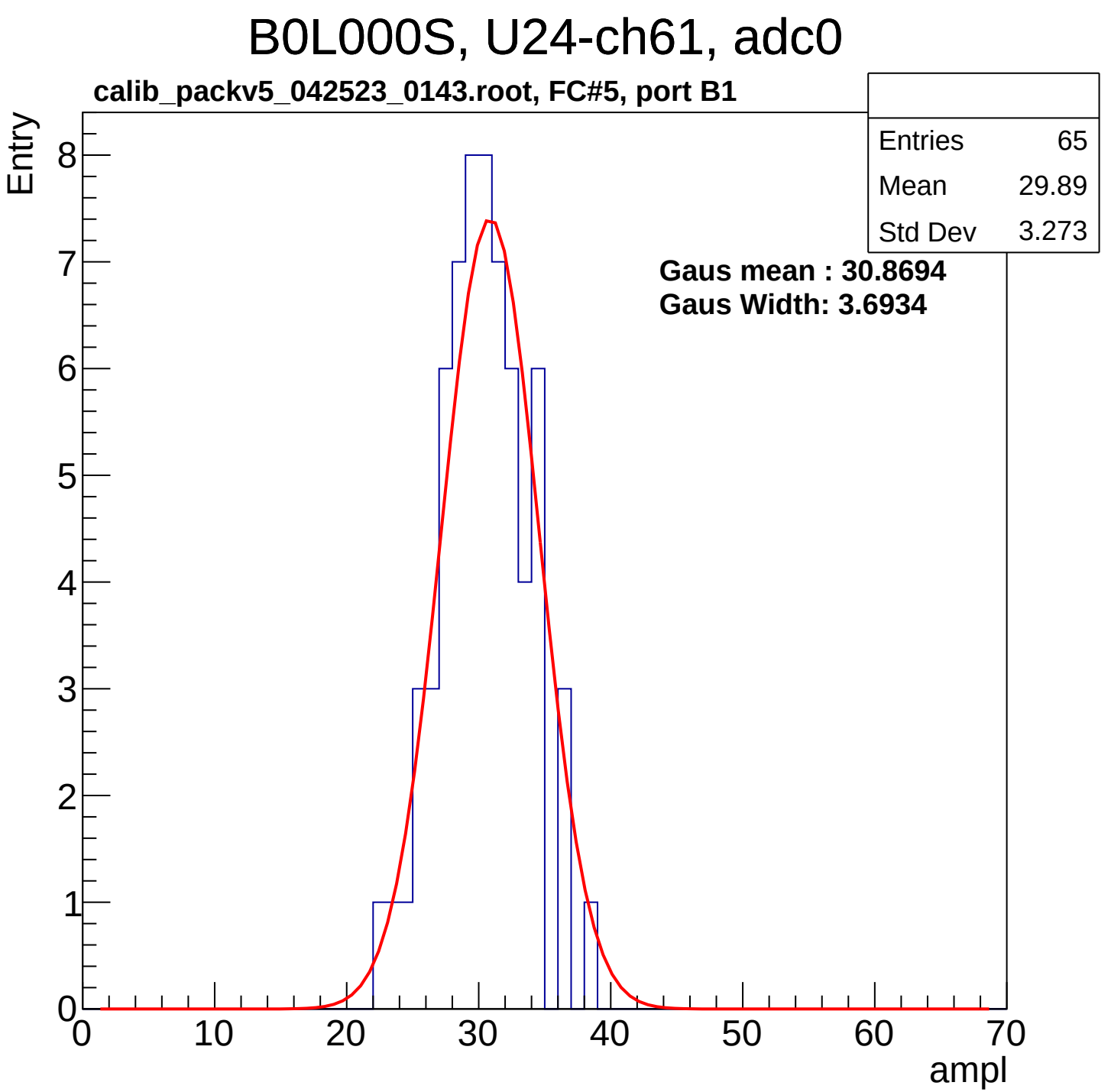
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.89
Std Dev 3.273
Gaus mean : 30.8694
Gaus Width: 3.6934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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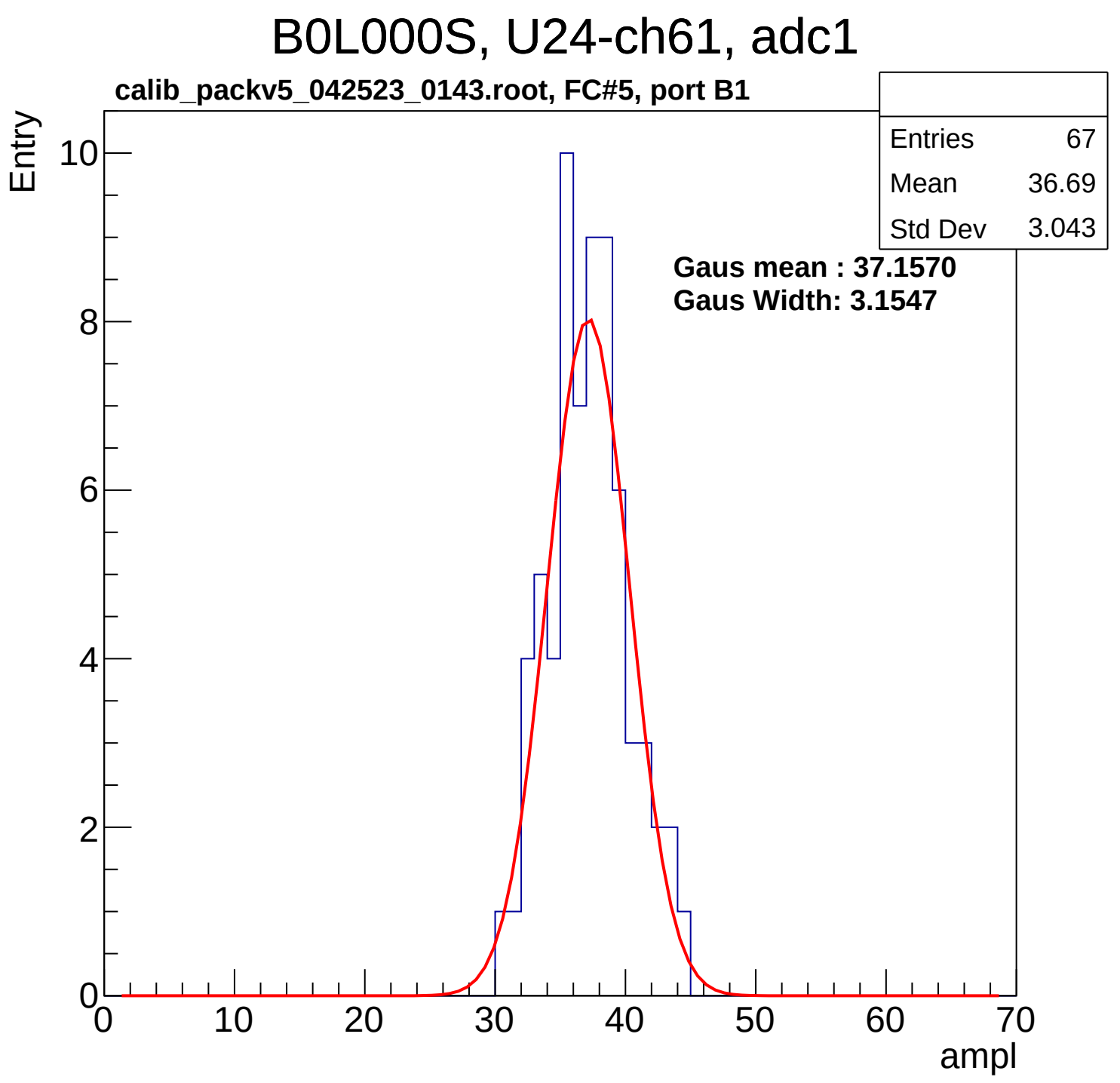
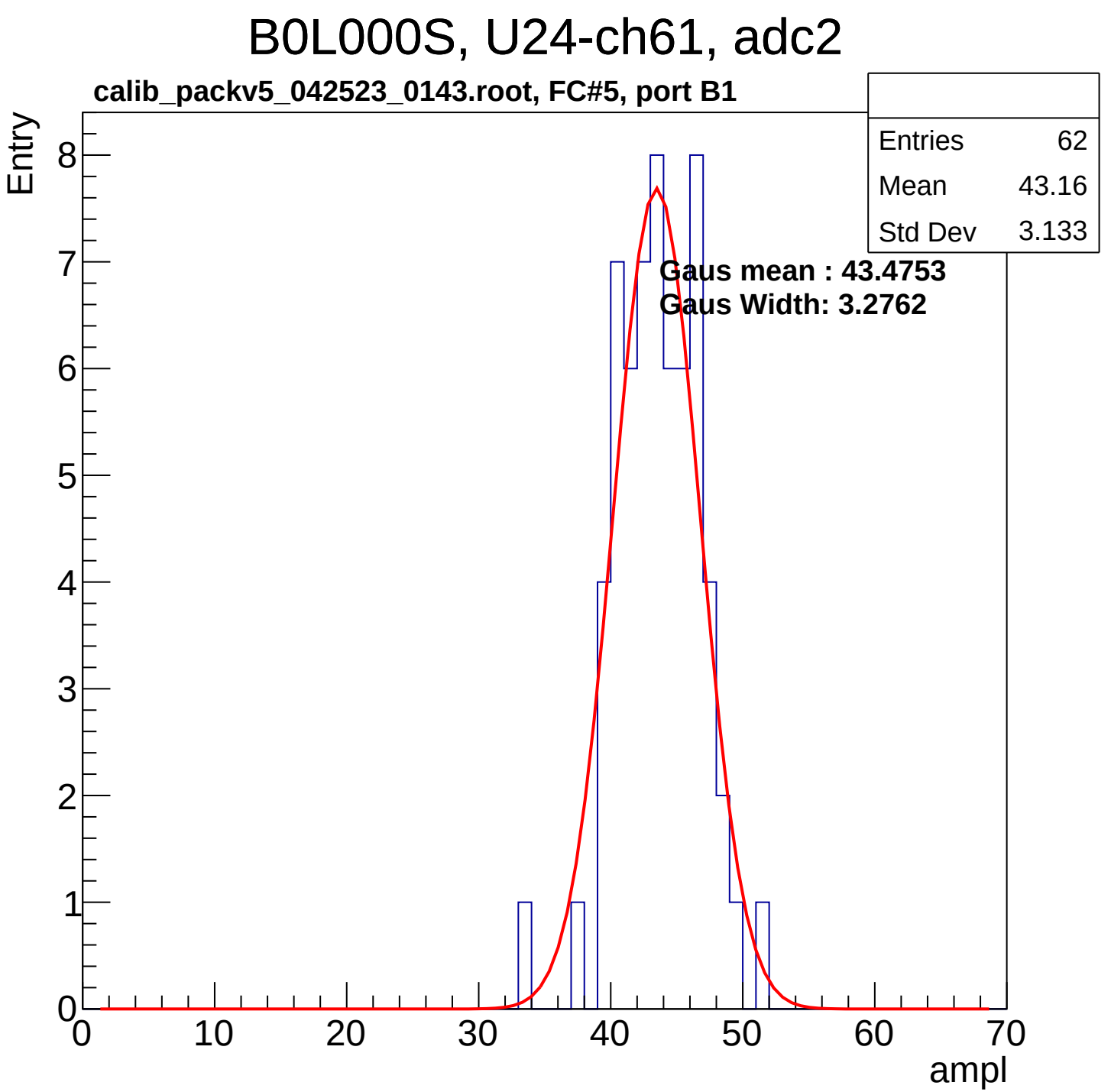

B0L000S, U24-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.69
Std Dev 3.043
Gaus mean : 37.1570
Gaus Width: 3.1547
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.16
Std Dev 3.133
Gaus mean : 43.4753
Gaus Width: 3.2762
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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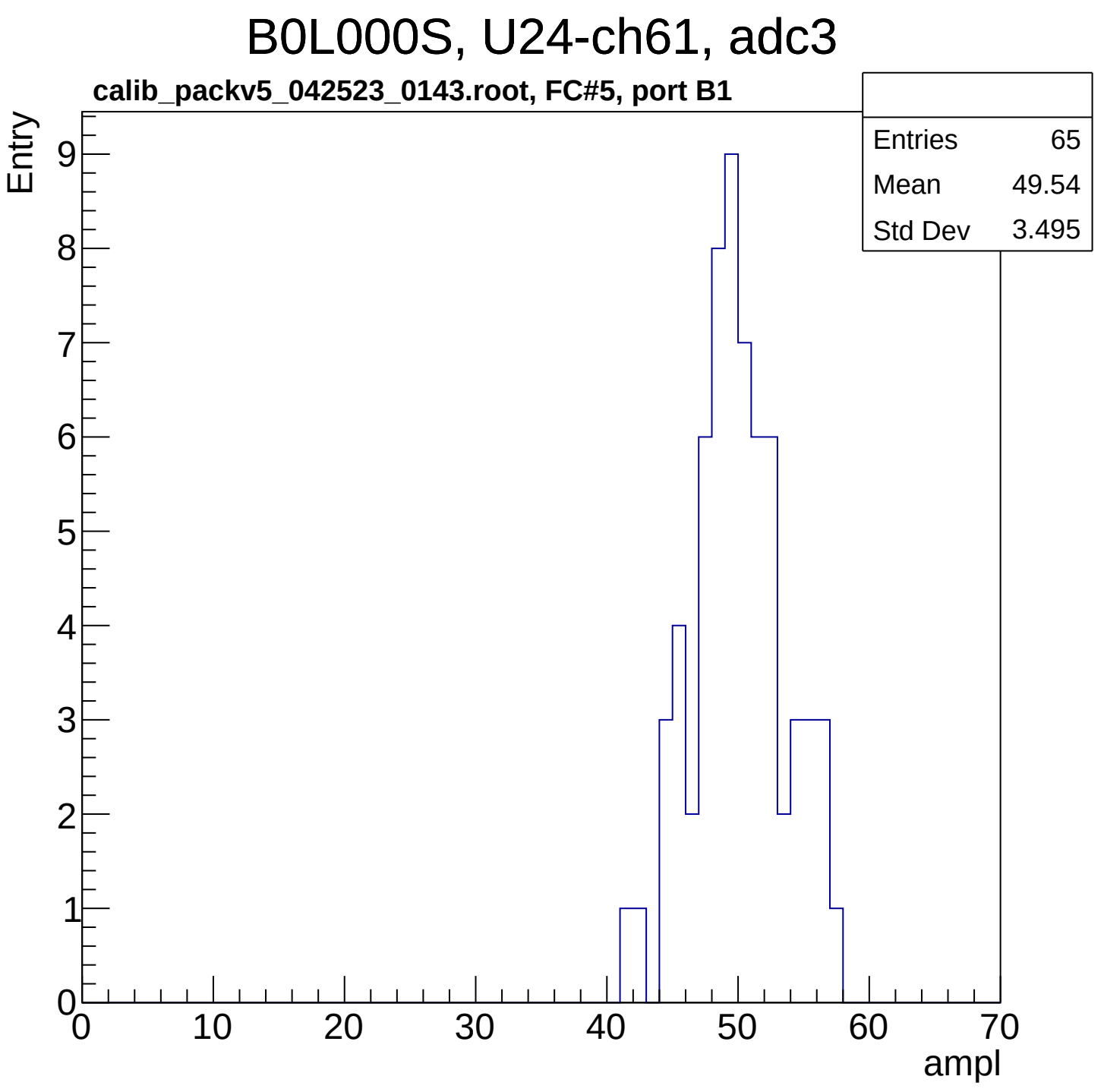
B0L000S, U24-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.54
Std Dev 3.495
Entry
ampl
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7
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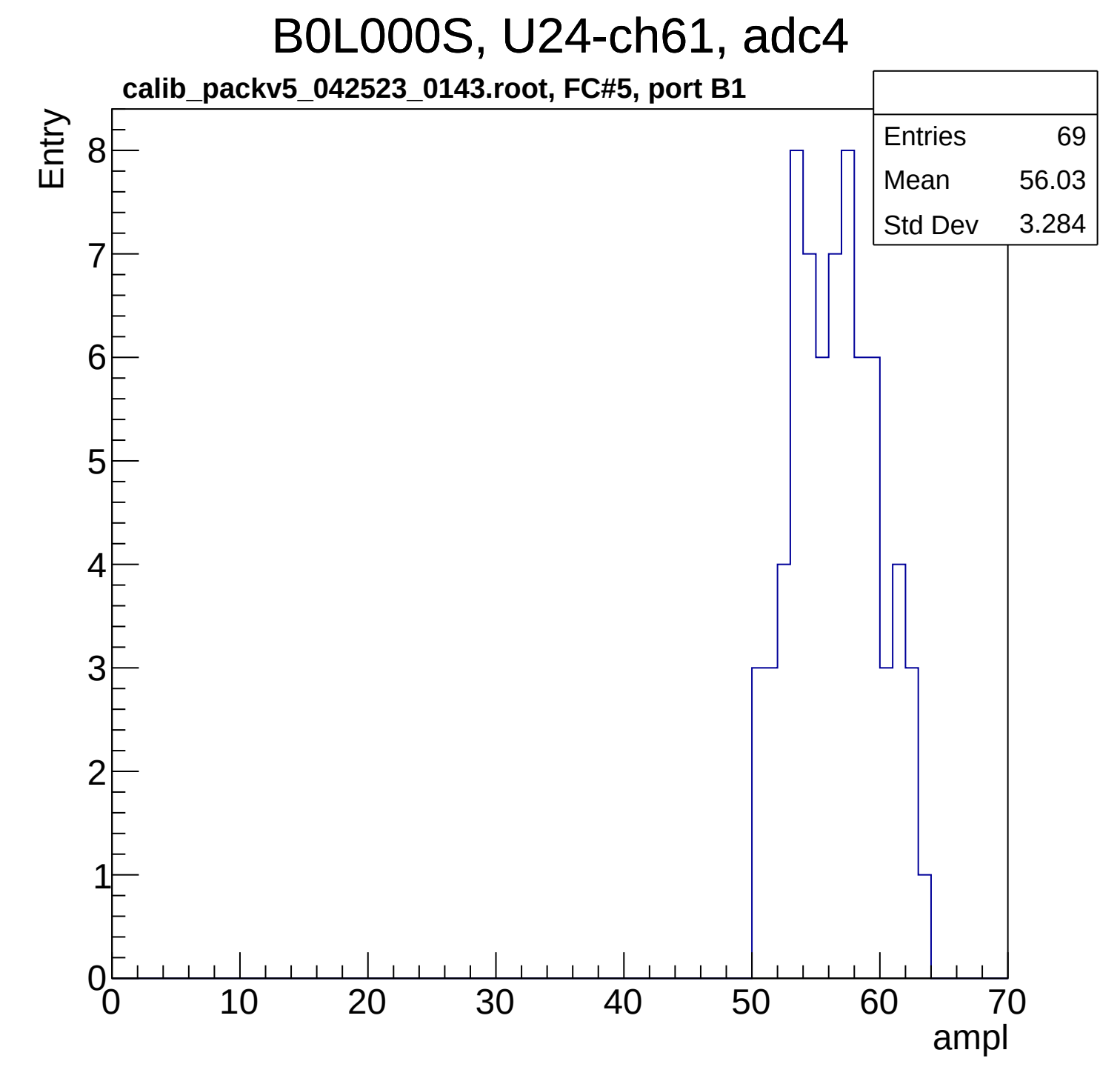
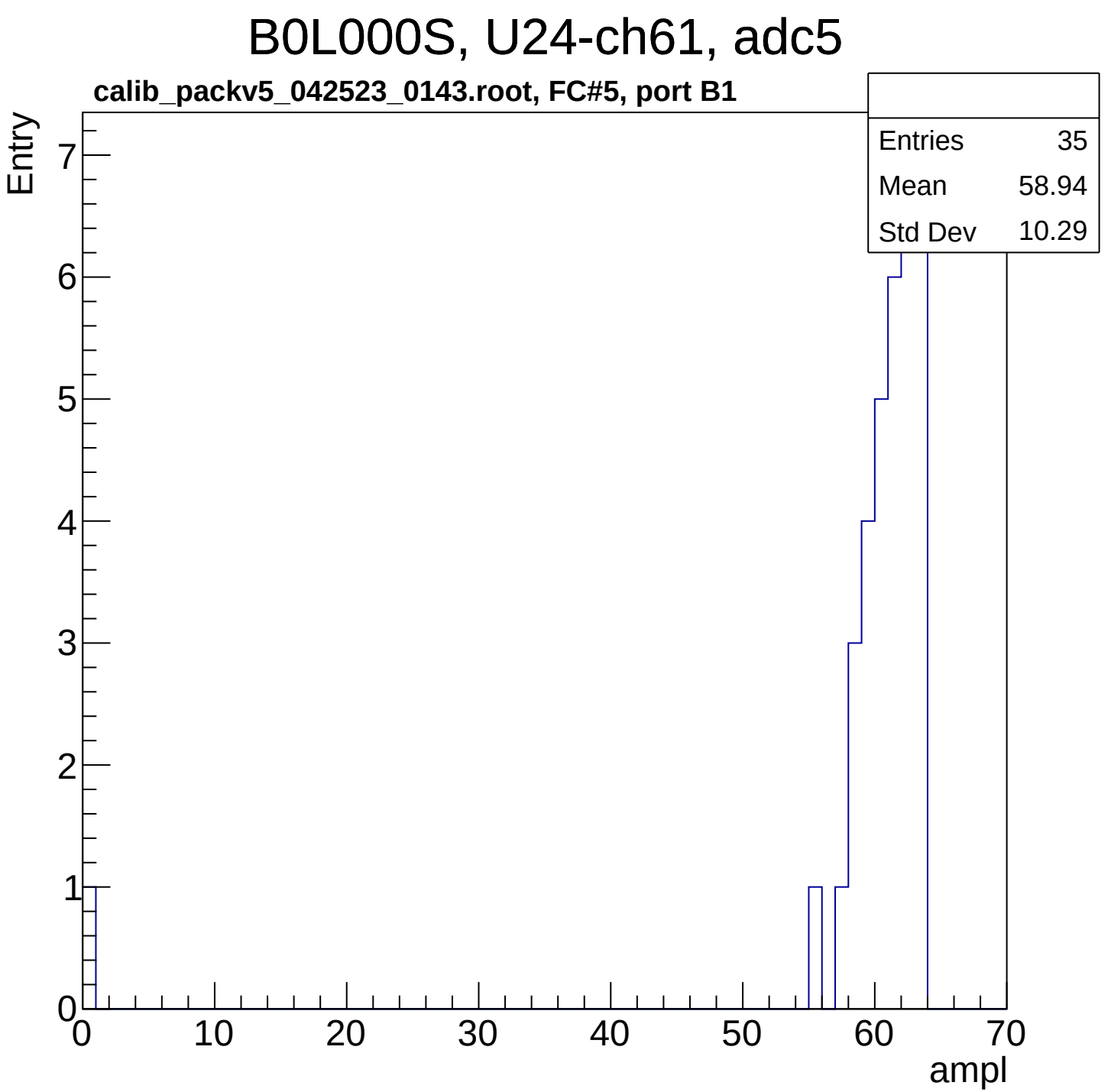

B0L000S, U24-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 56.03
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.94
Std Dev 10.29
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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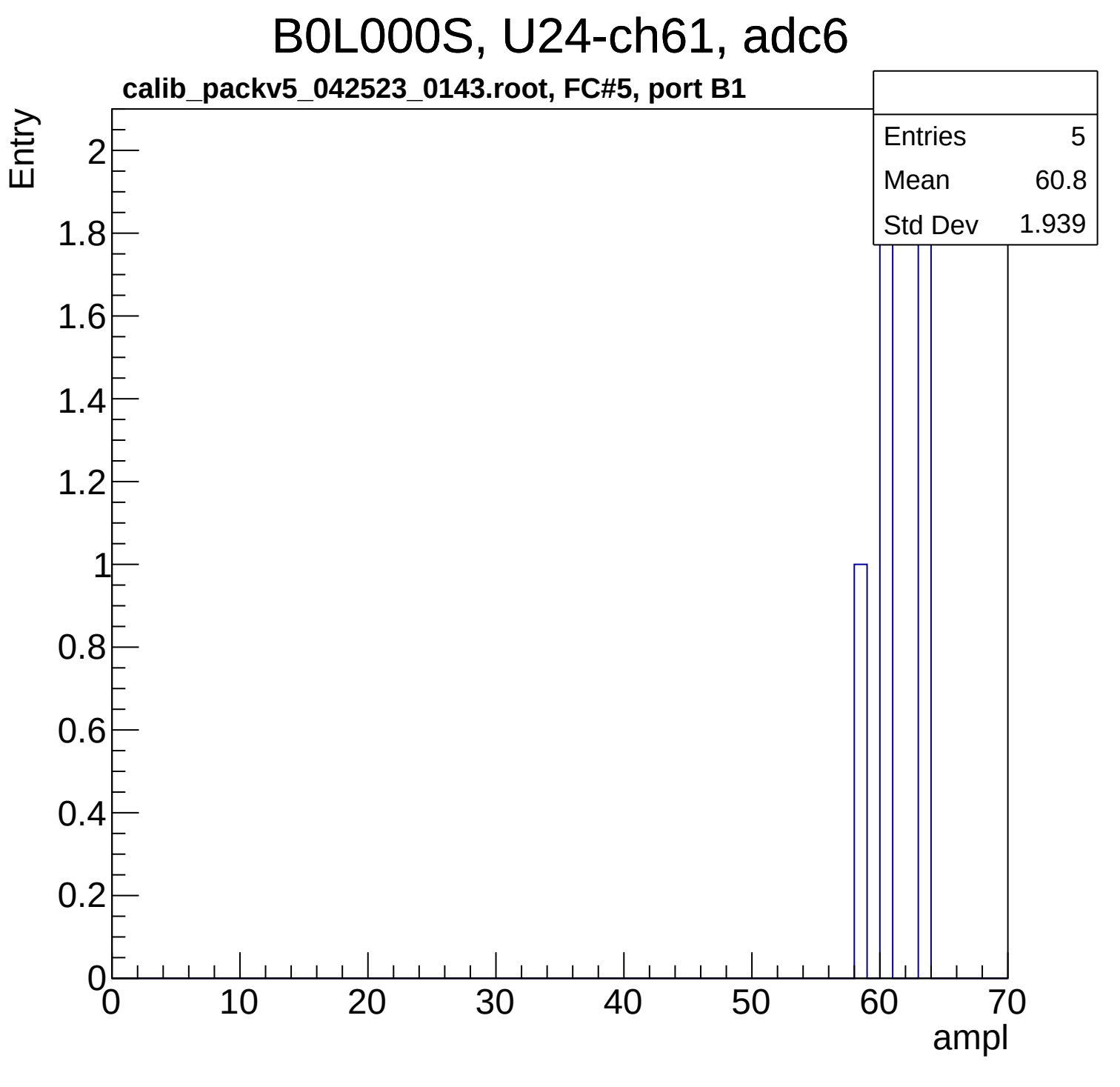
B0L000S, U24-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 60.8
Std Dev 1.939
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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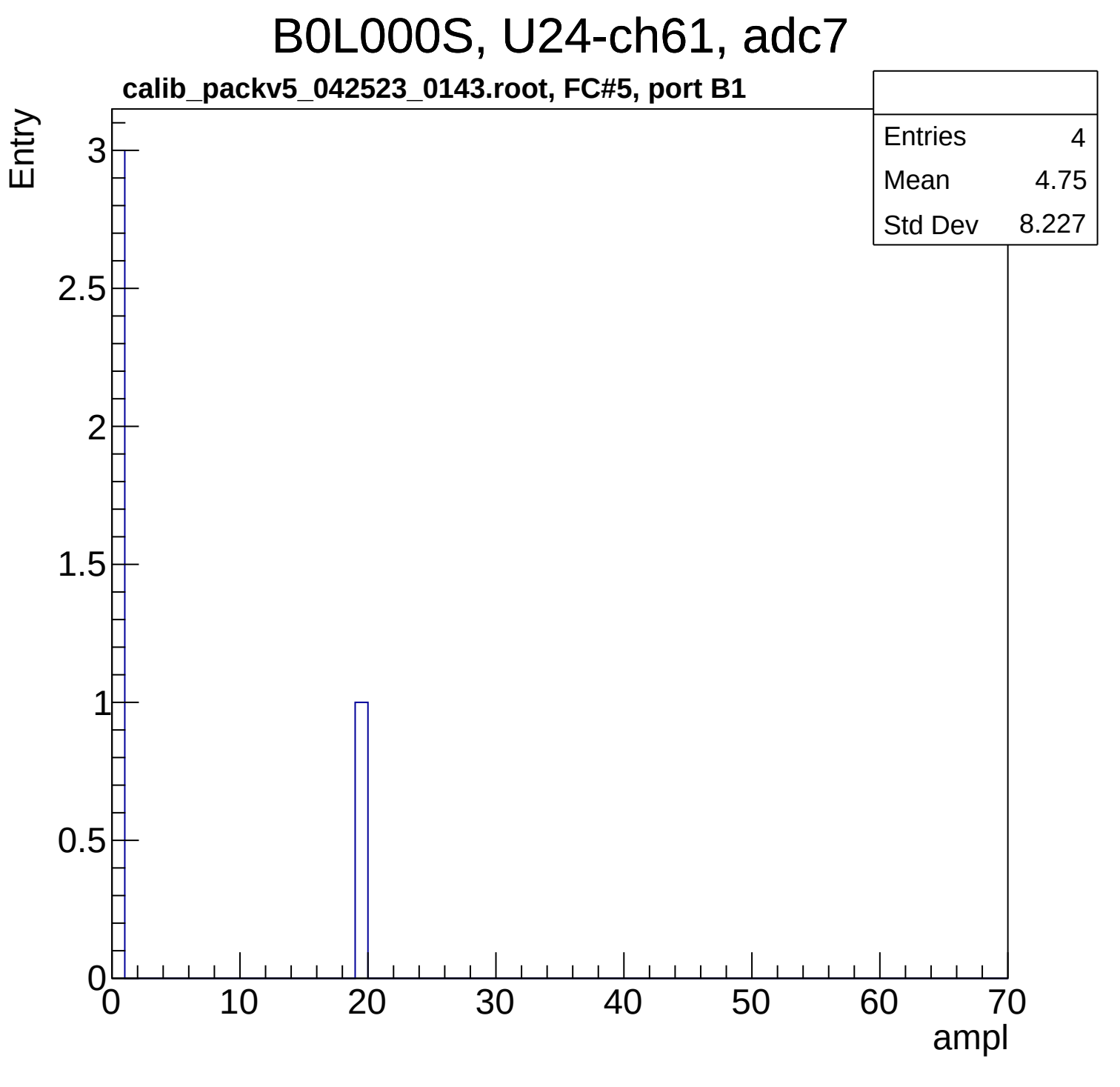
B0L000S, U24-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 4.75
Std Dev 8.227
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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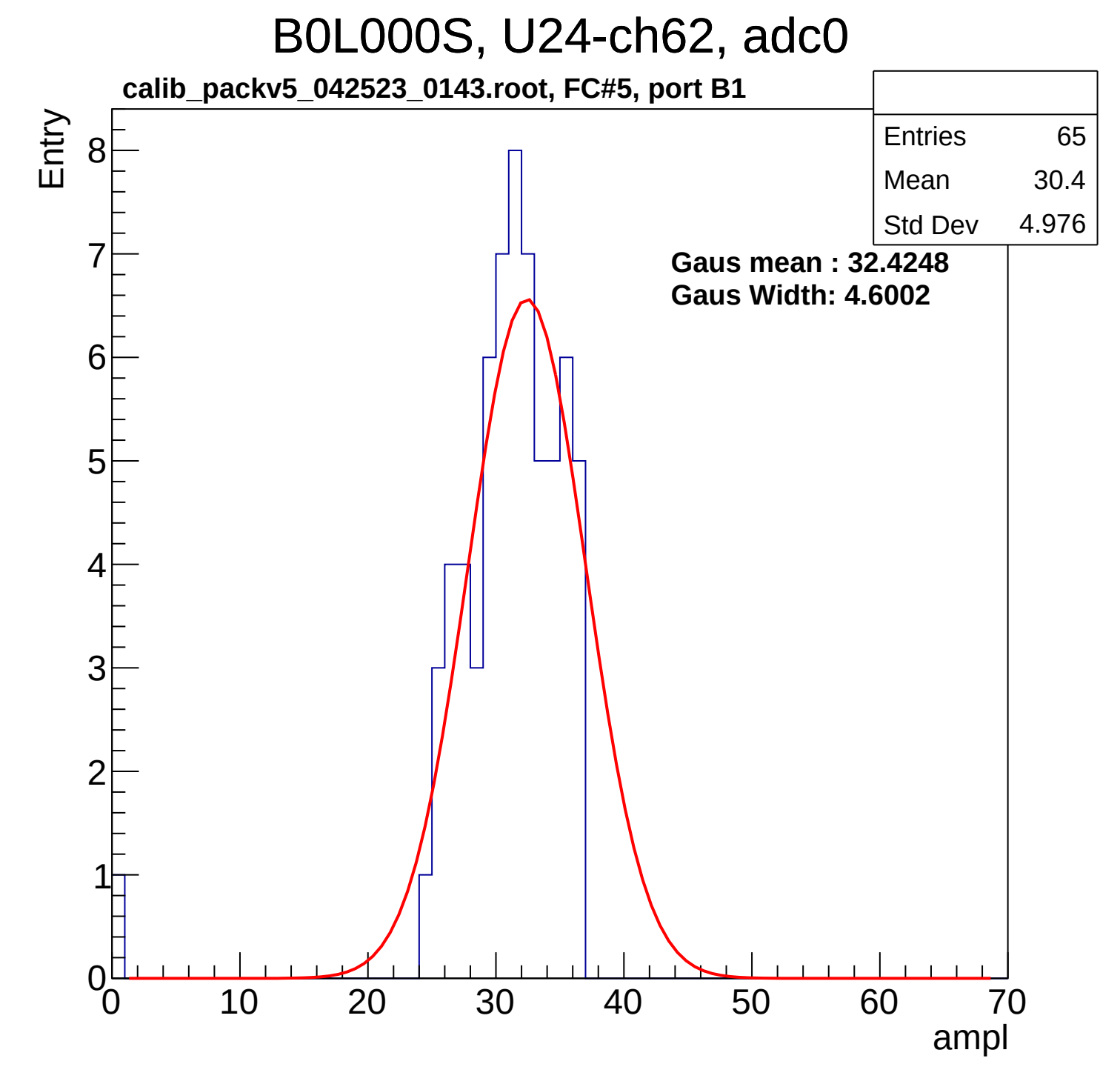
B0L000S, U24-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.4
Std Dev 4.976
Gaus mean : 32.4248
Gaus Width: 4.6002
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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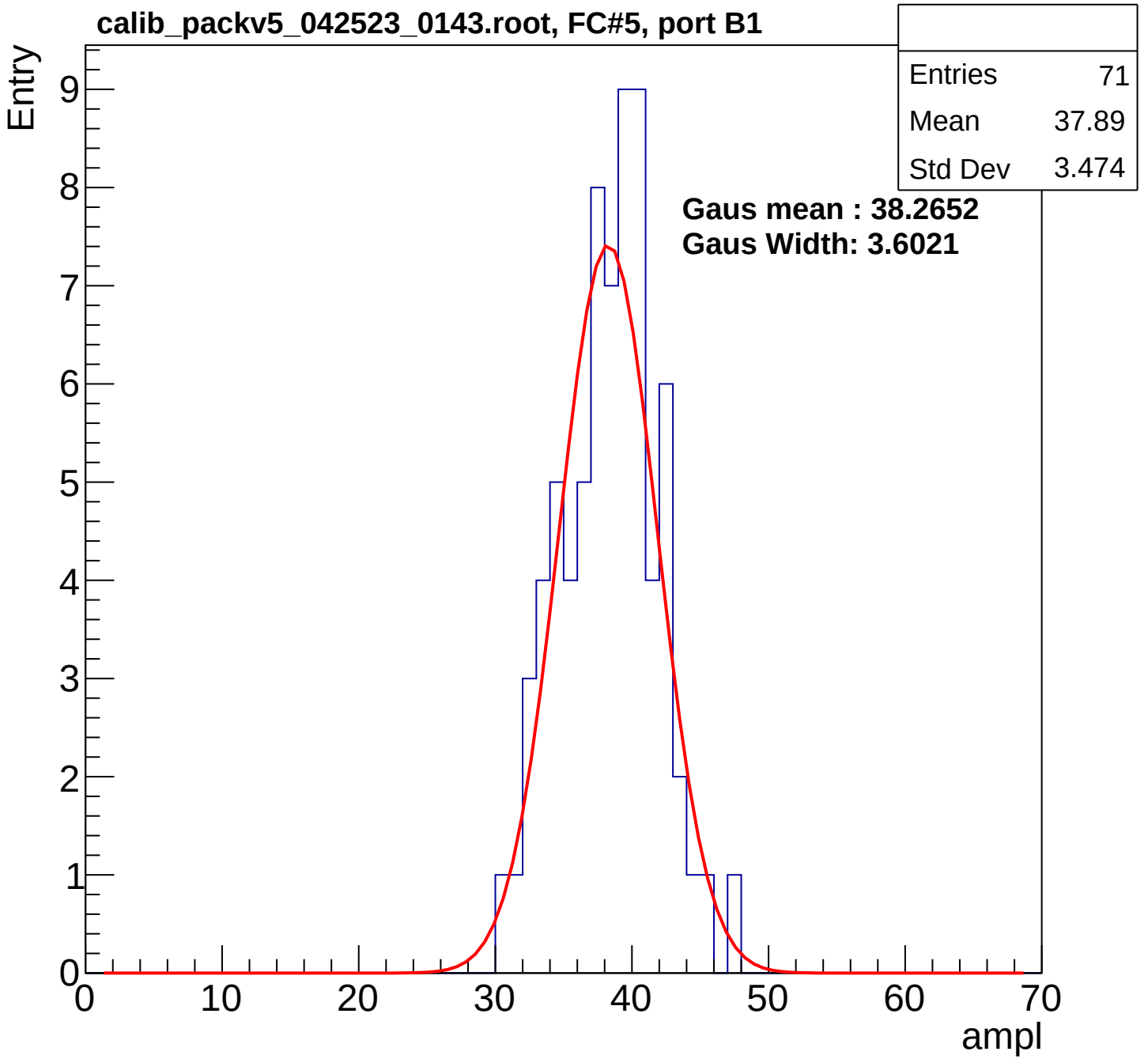
B0L000S, U24-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.89
Std Dev 3.474
Gaus mean : 38.2652
Gaus Width: 3.6021
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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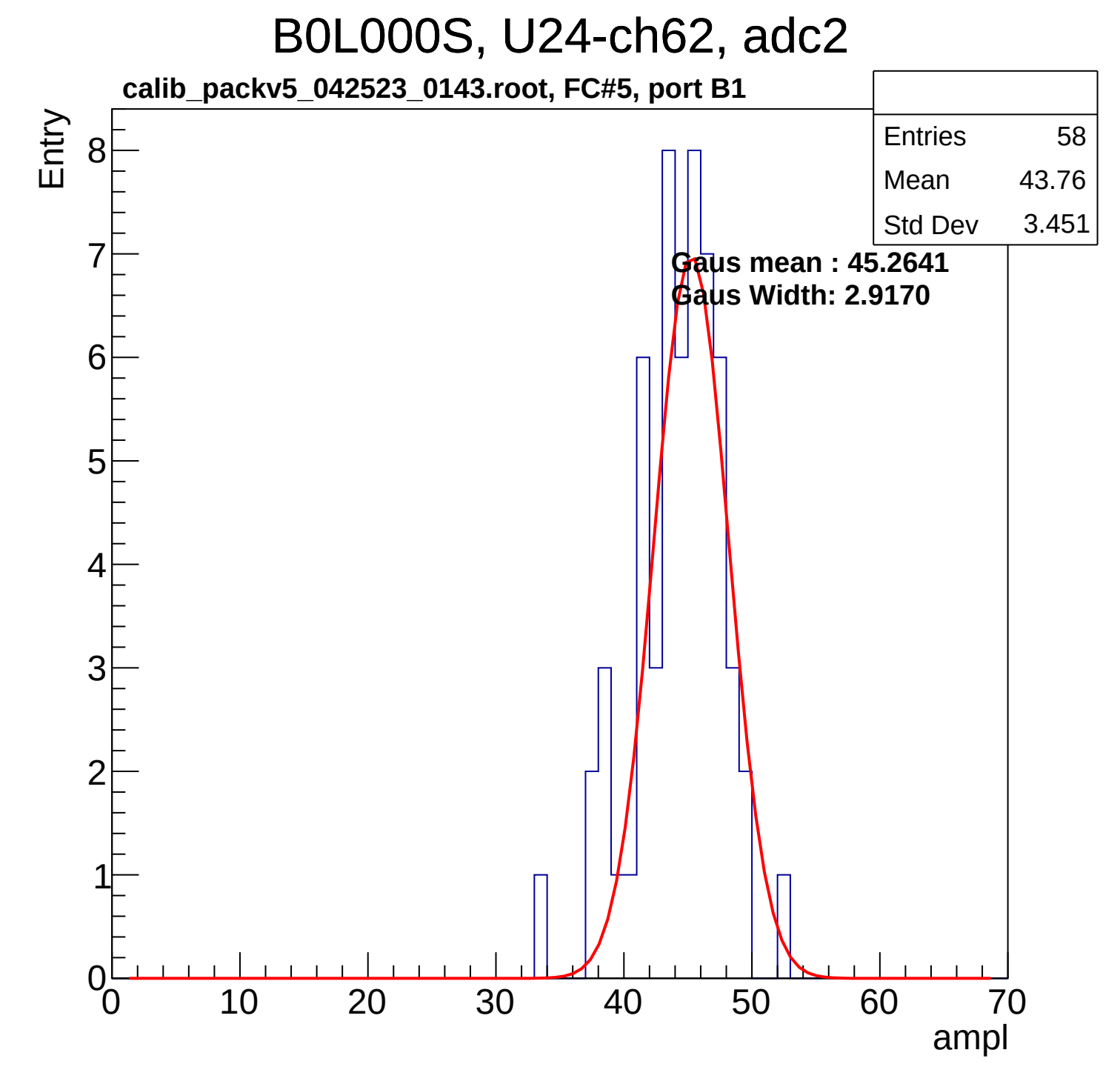
B0L000S, U24-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.76
Std Dev 3.451
Gaus mean : 45.2641
Gaus Width: 2.9170
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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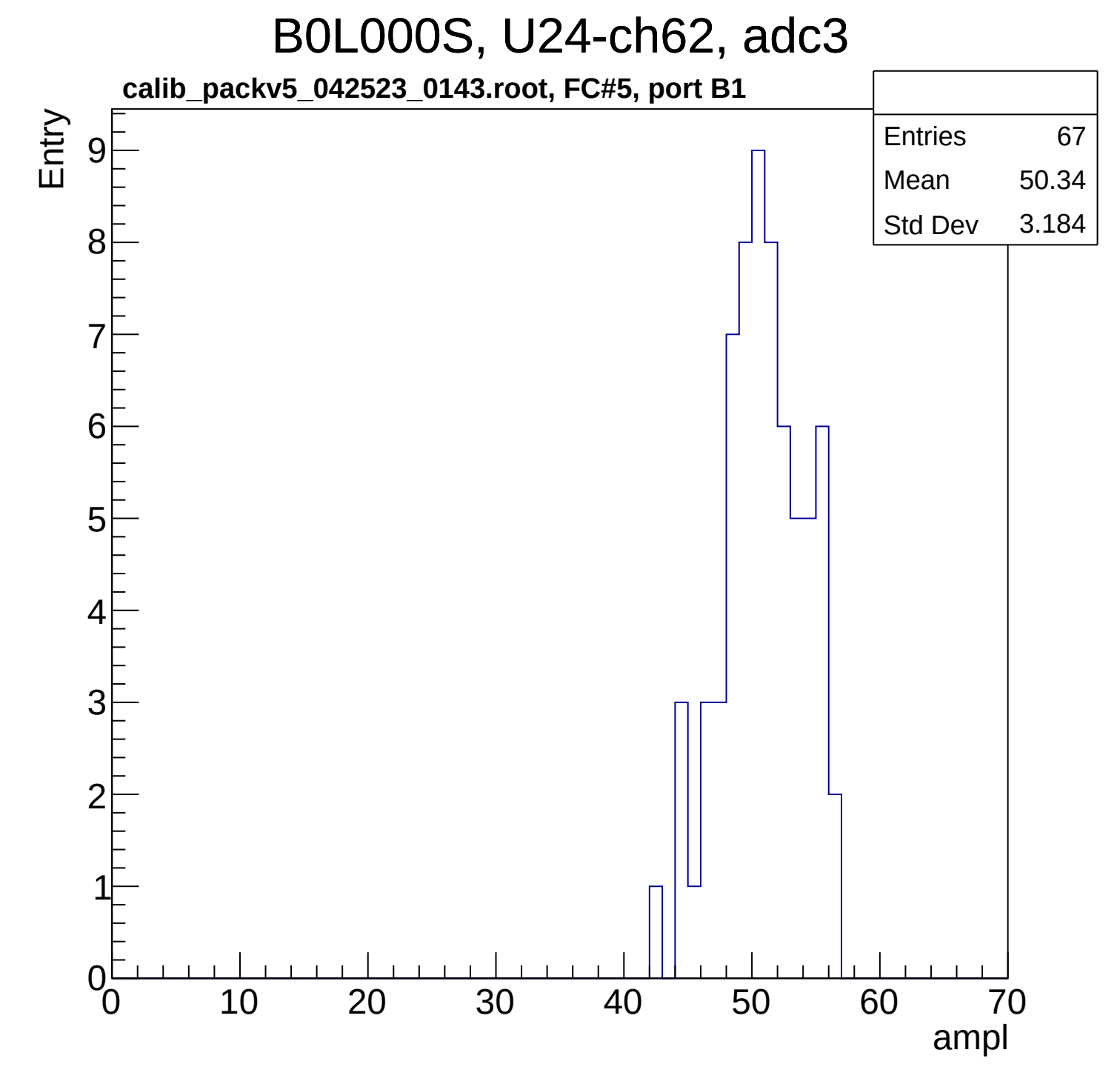
B0L000S, U24-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.34
Std Dev 3.184
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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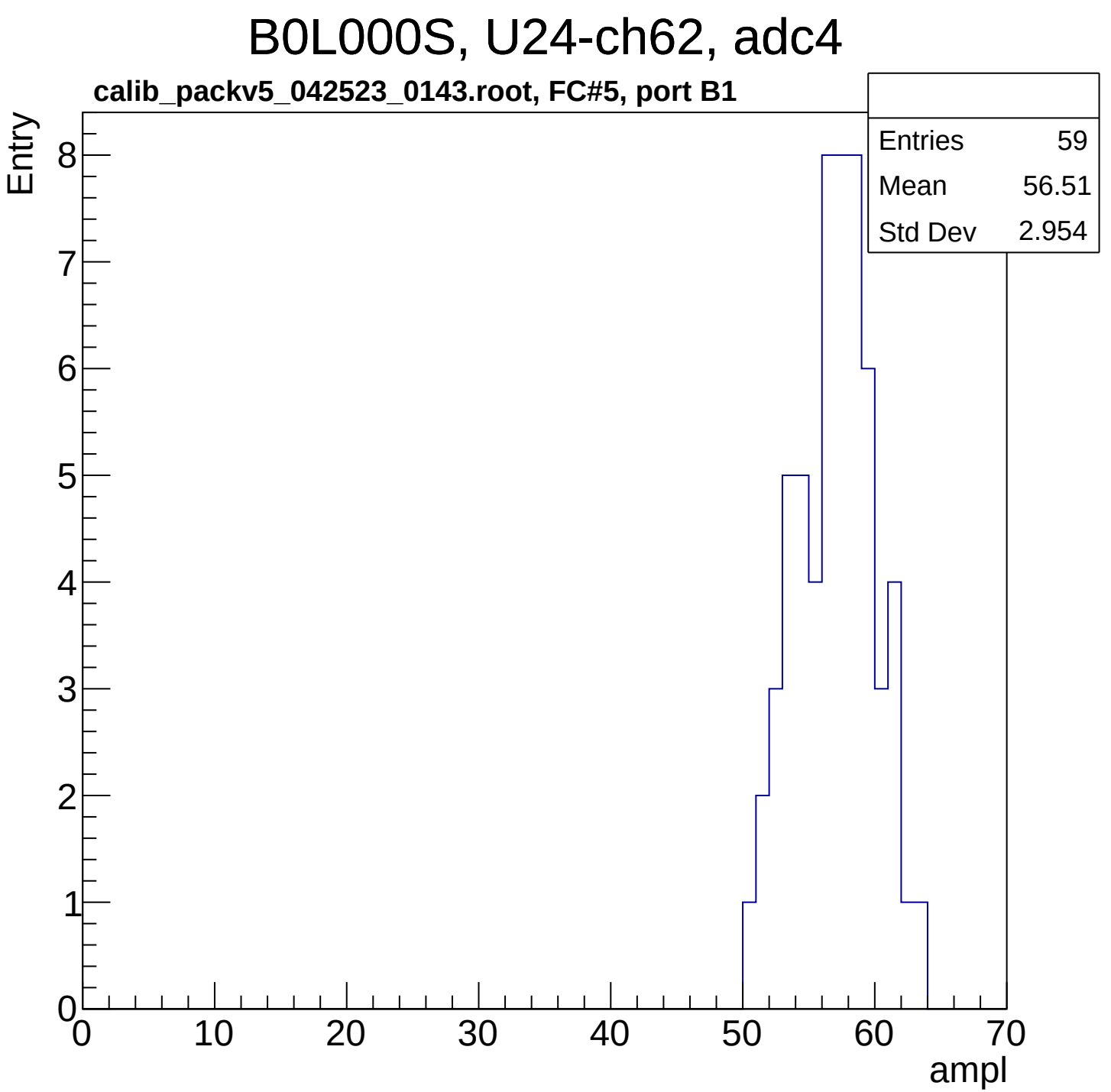
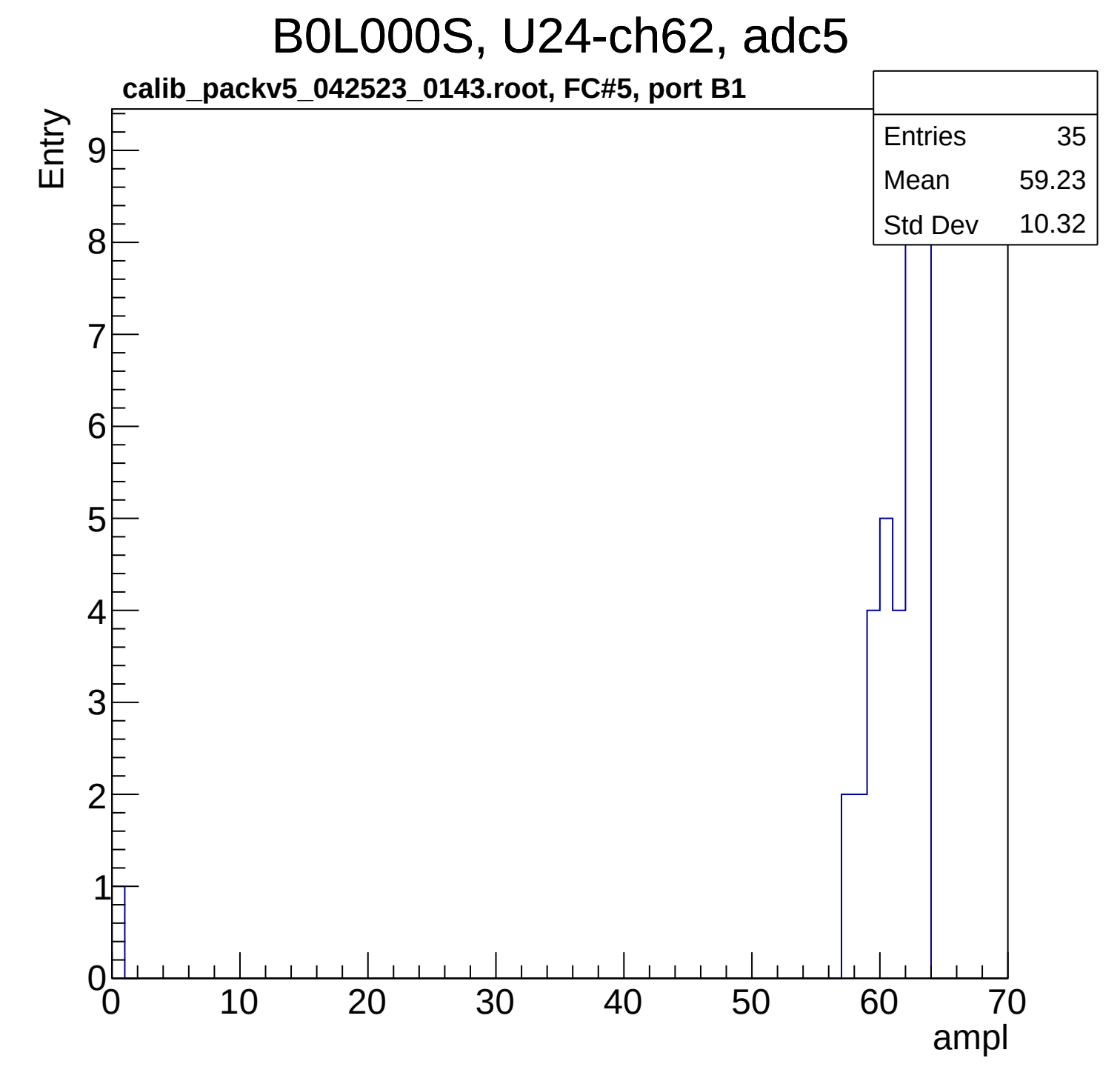
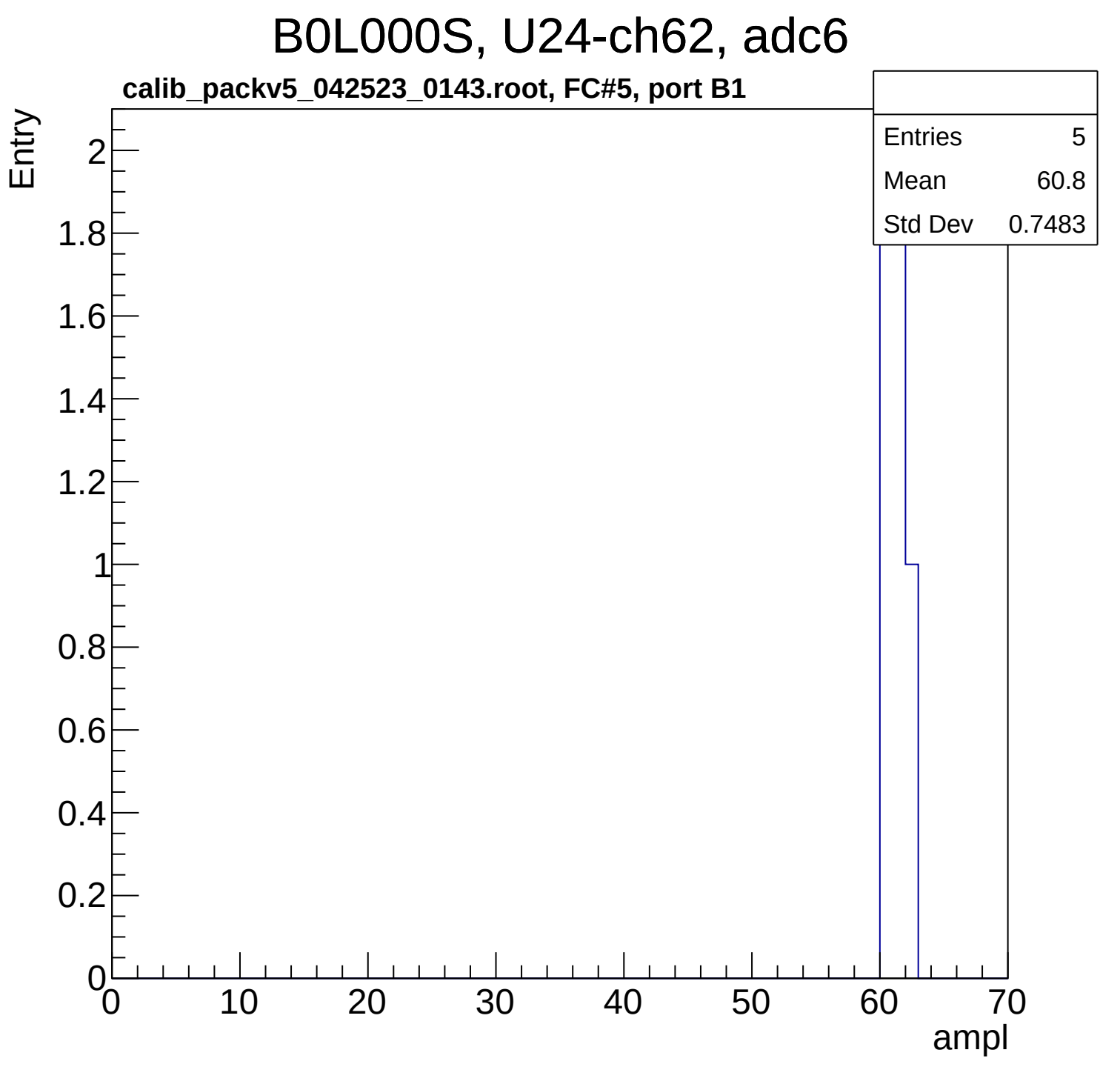

B0L000S, U24-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.51
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.23
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 60.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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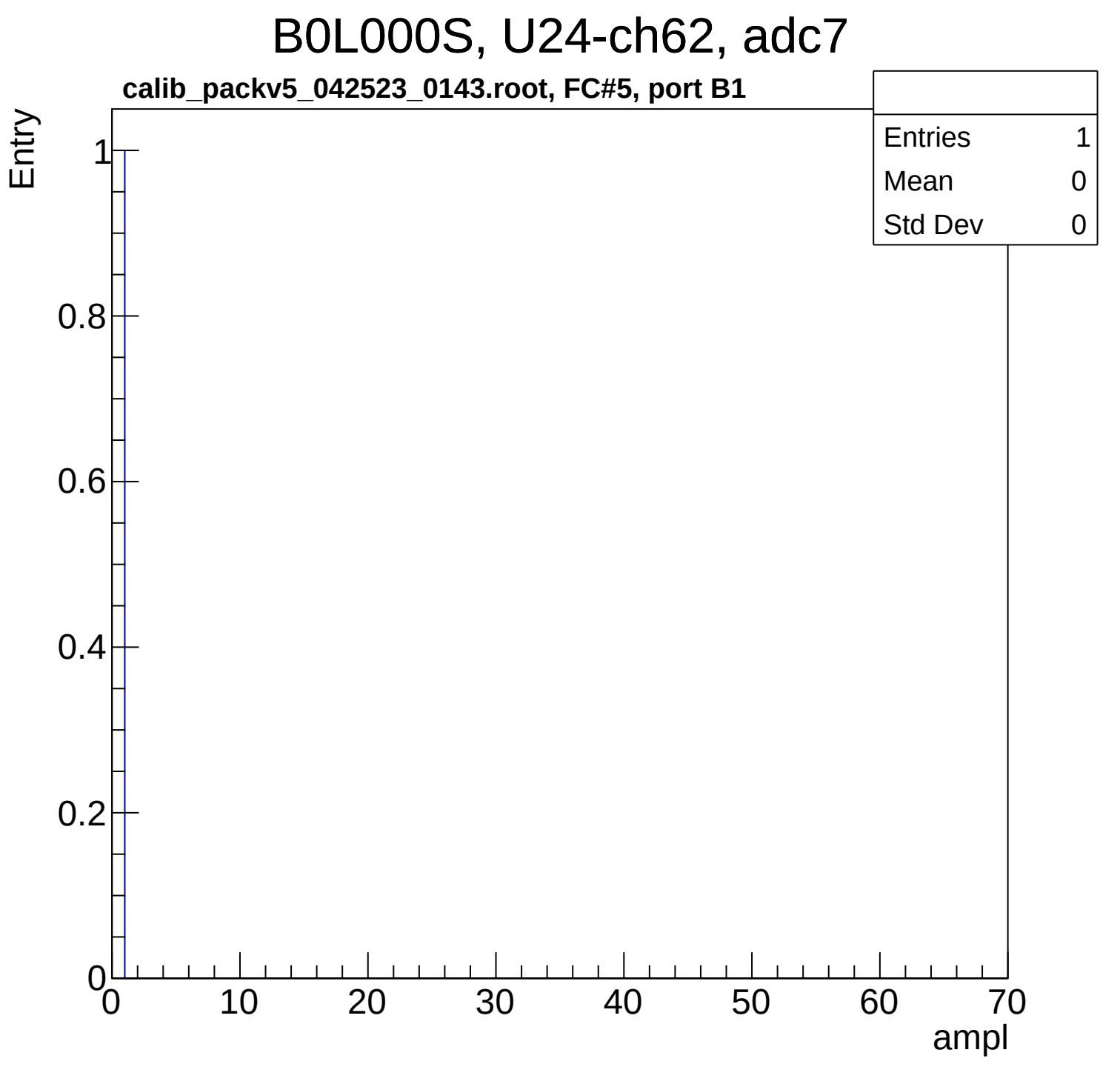
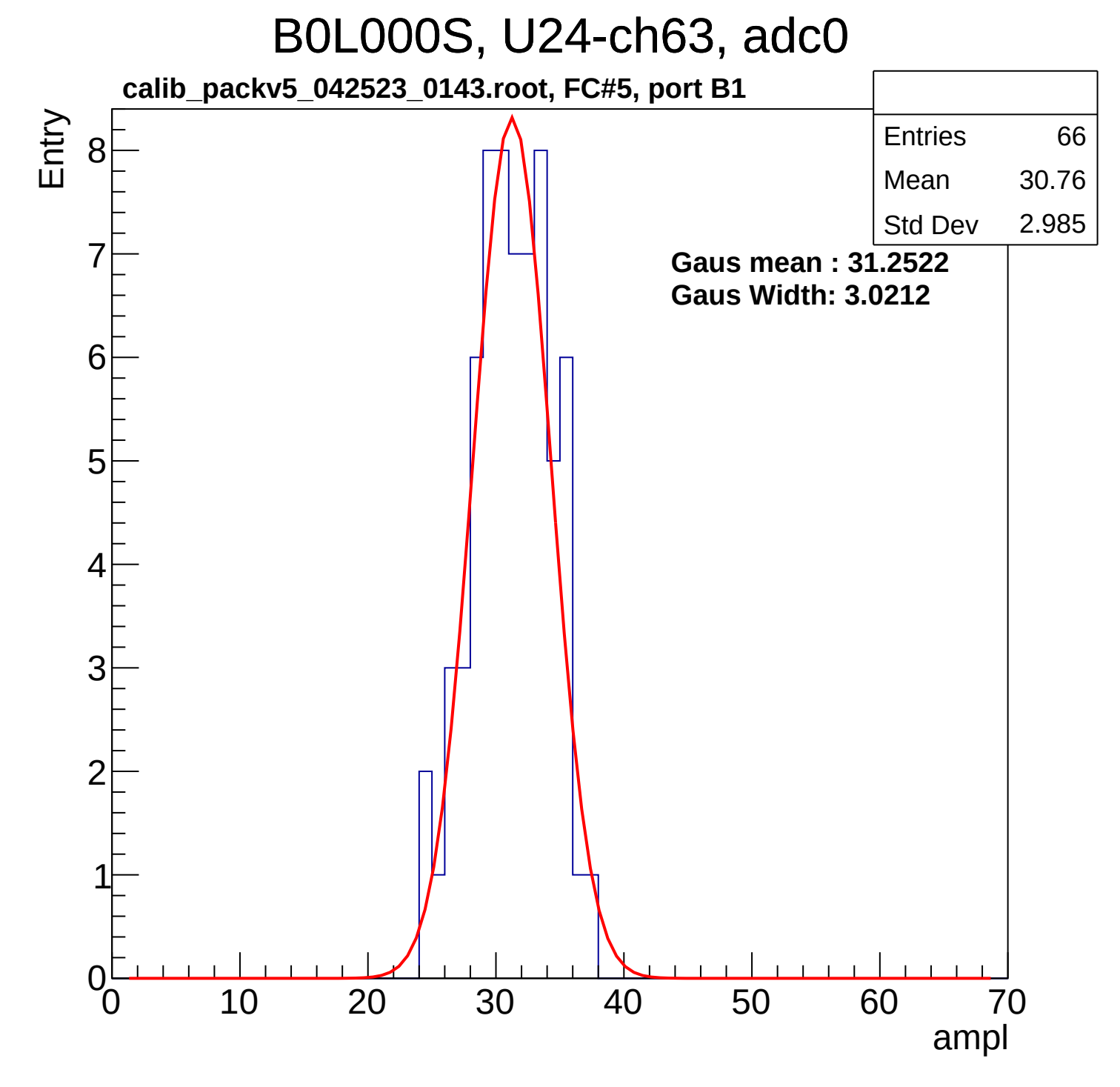
20
30
40
50
60
70


B0L000S, U24-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.76
Std Dev 2.985
Gaus mean : 31.2522
Gaus Width: 3.0212
Entry
ampl
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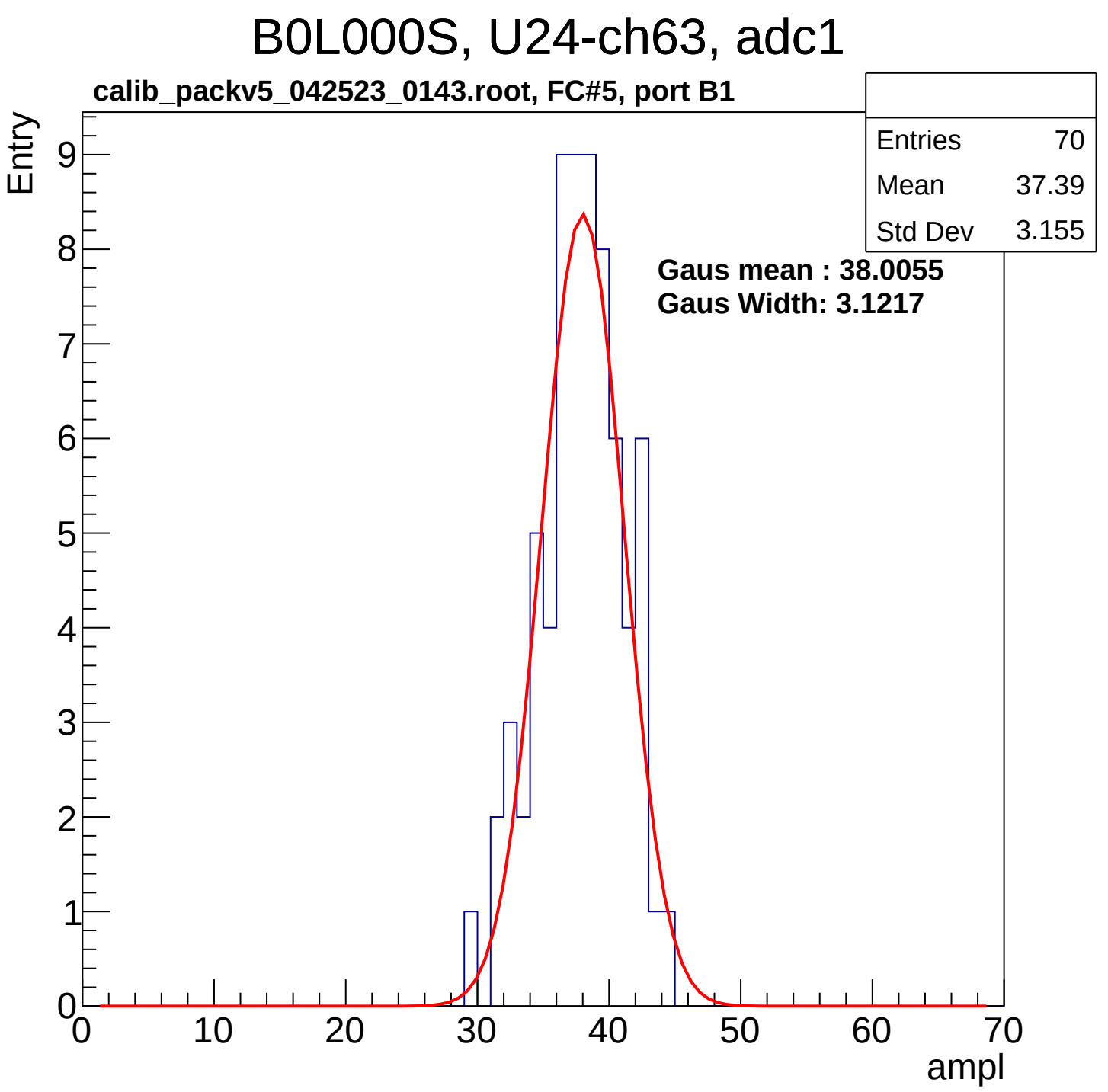
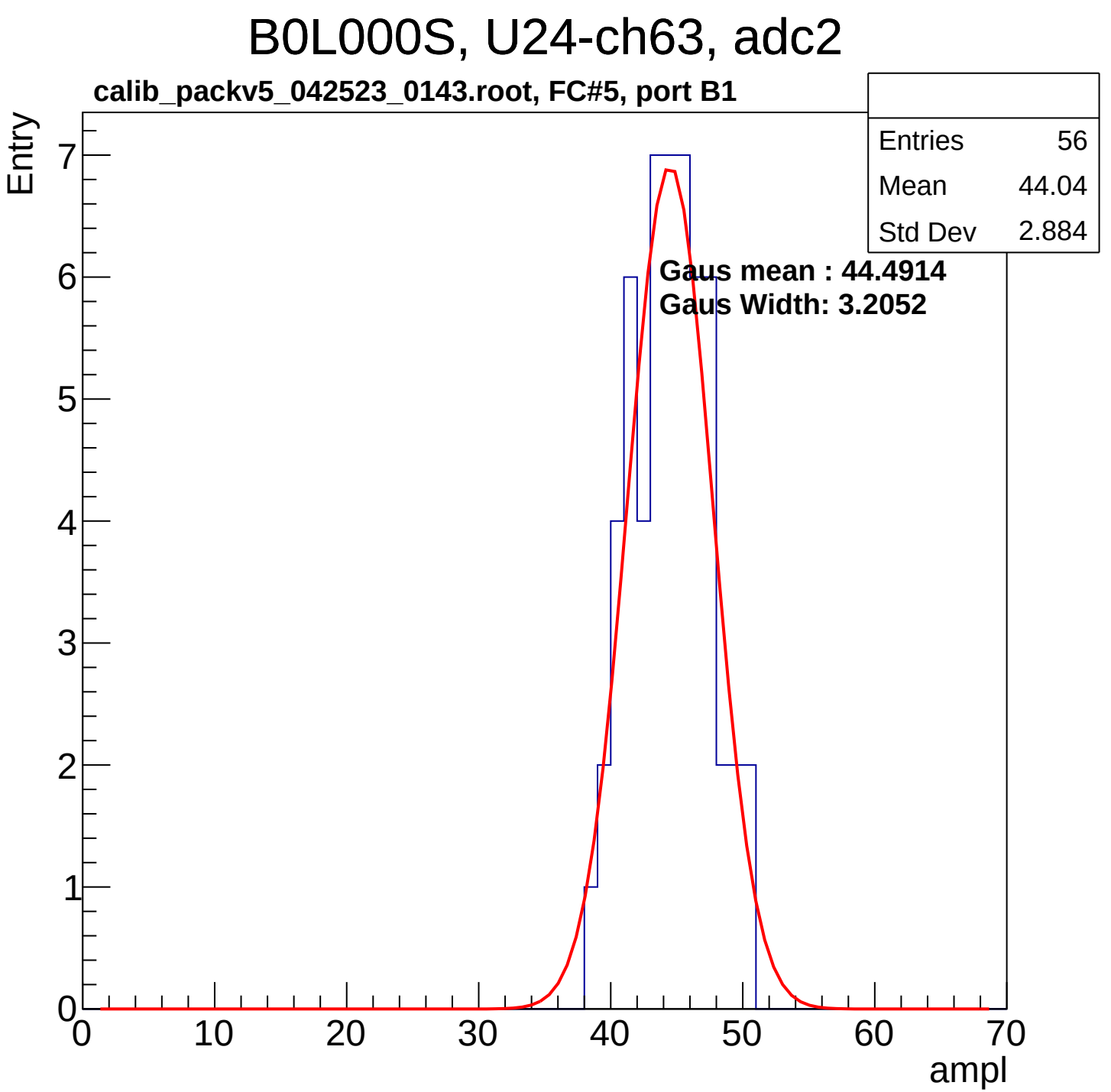
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.39
Std Dev 3.155
Gaus mean : 38.0055
Gaus Width: 3.1217
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.04
Std Dev 2.884
Gaus mean : 44.4914
Gaus Width: 3.2052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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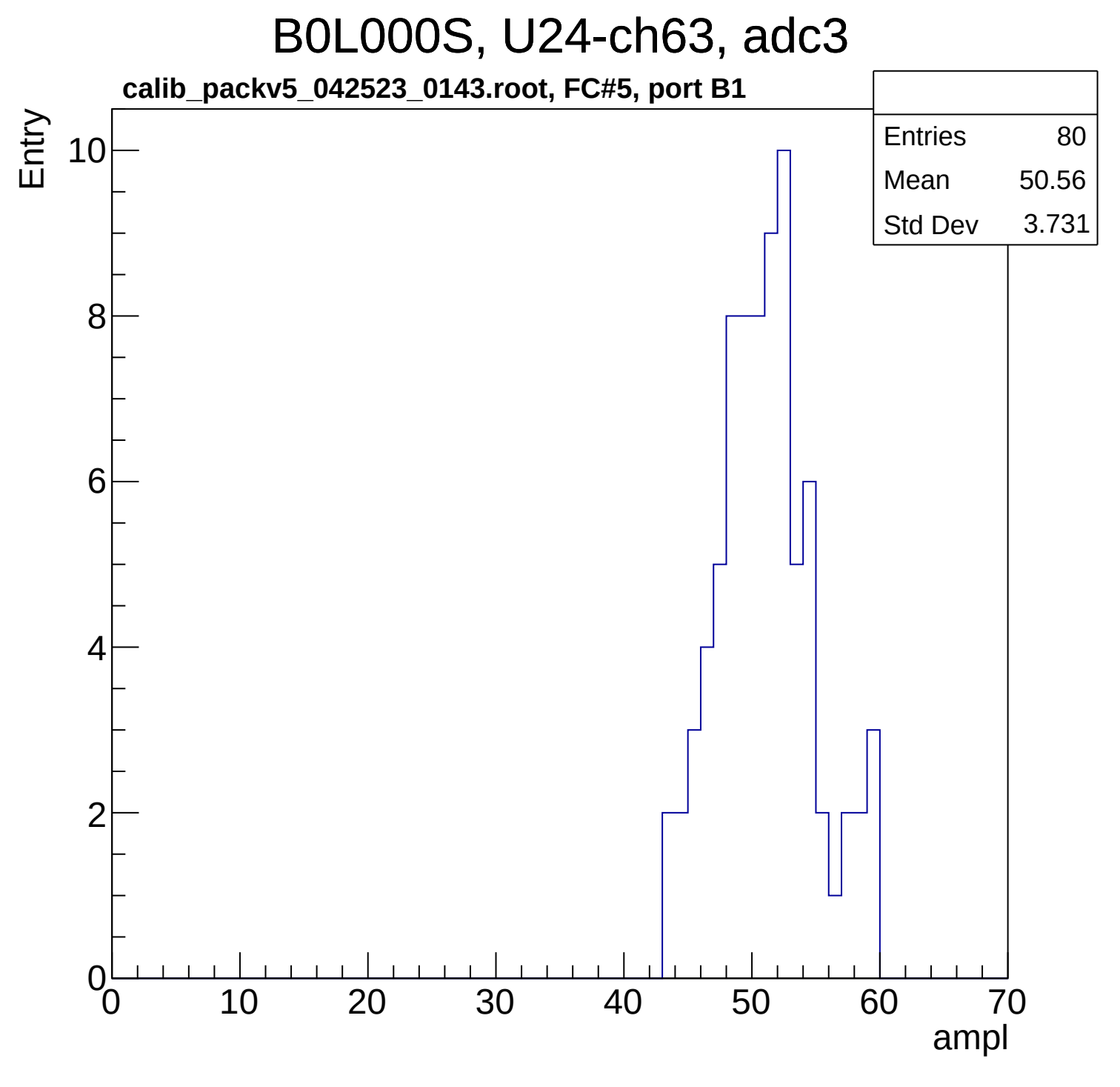
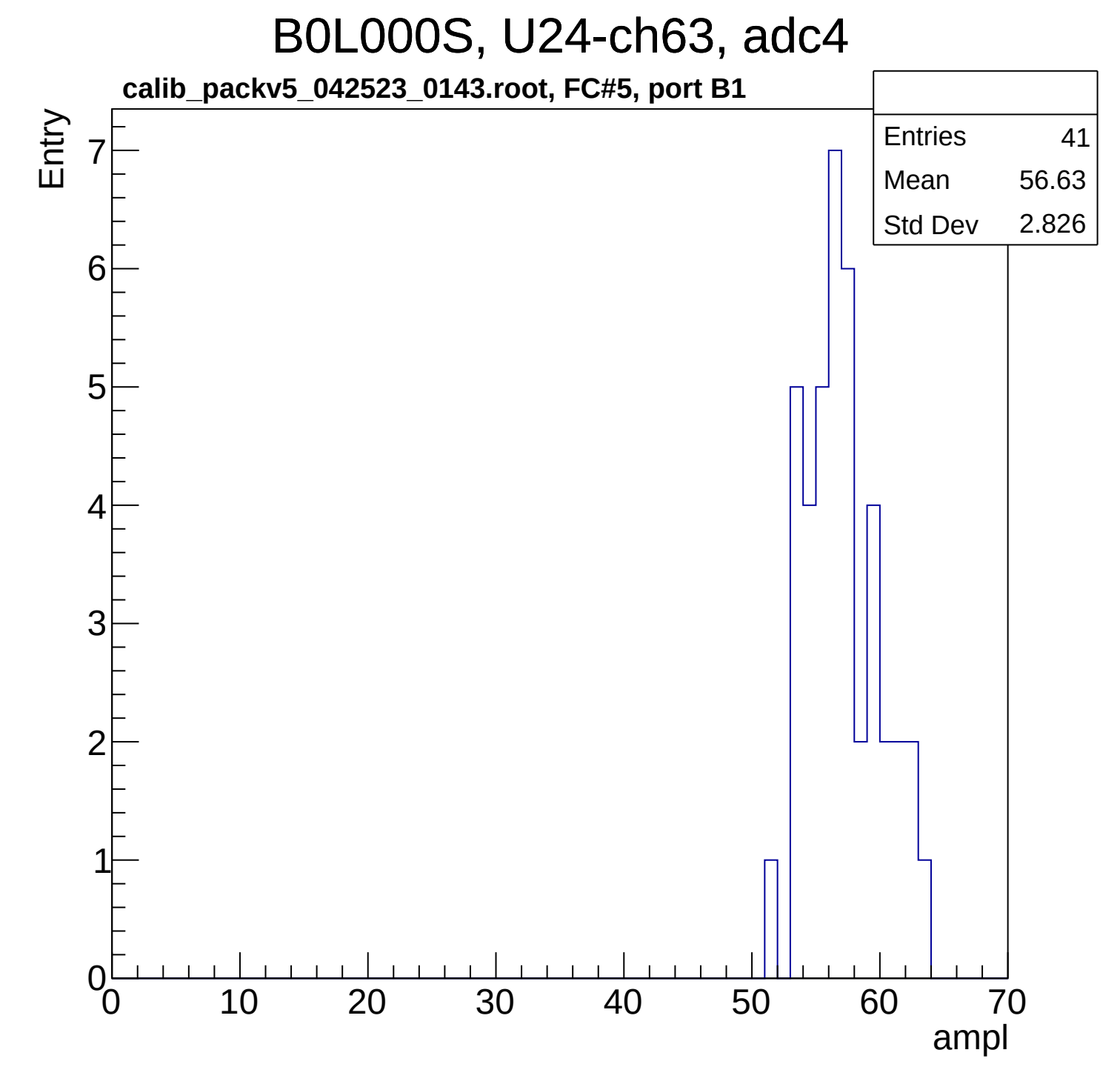
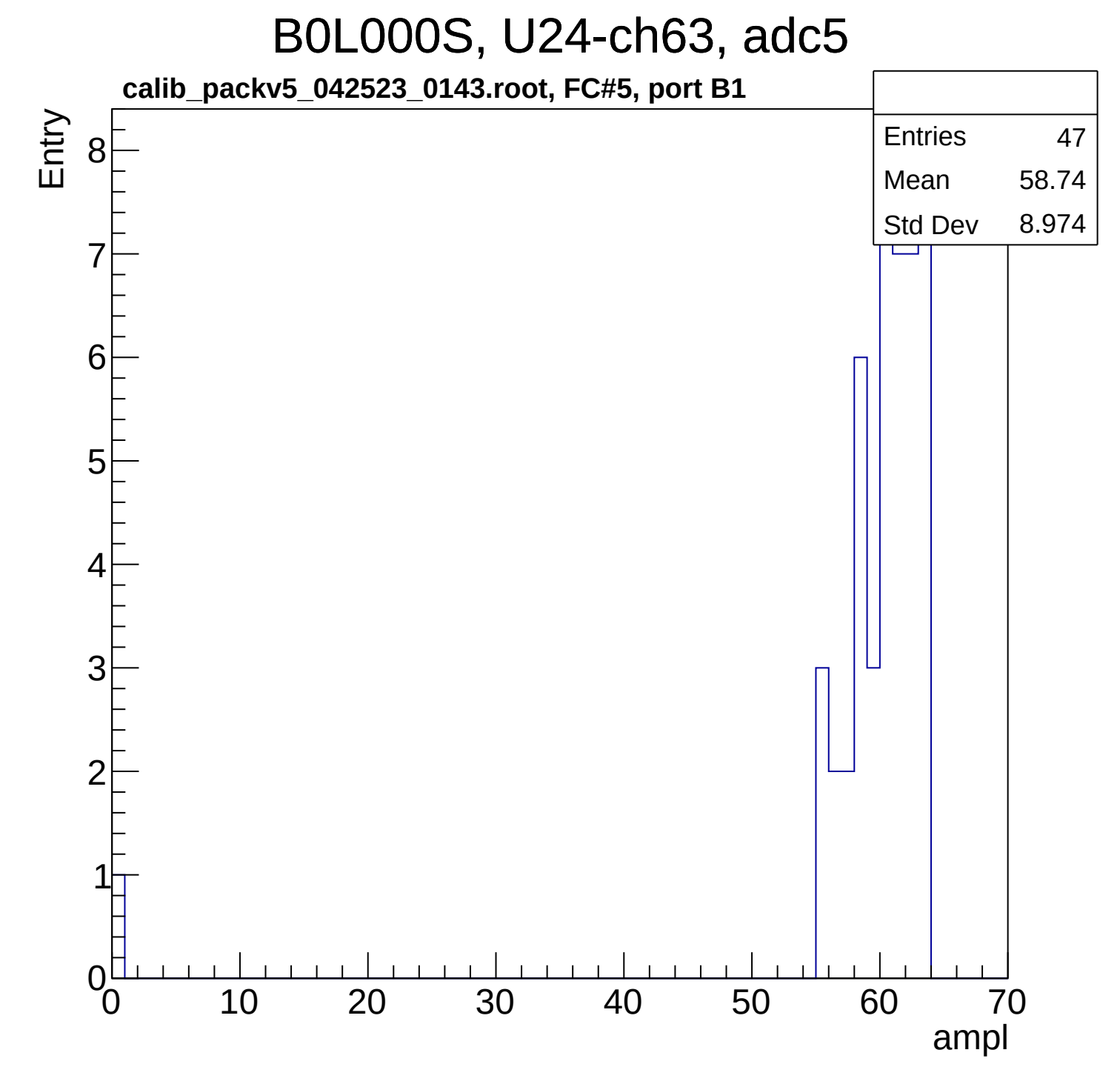

B0L000S, U24-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 50.56
Std Dev 3.731
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 56.63
Std Dev 2.826
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.74
Std Dev 8.974
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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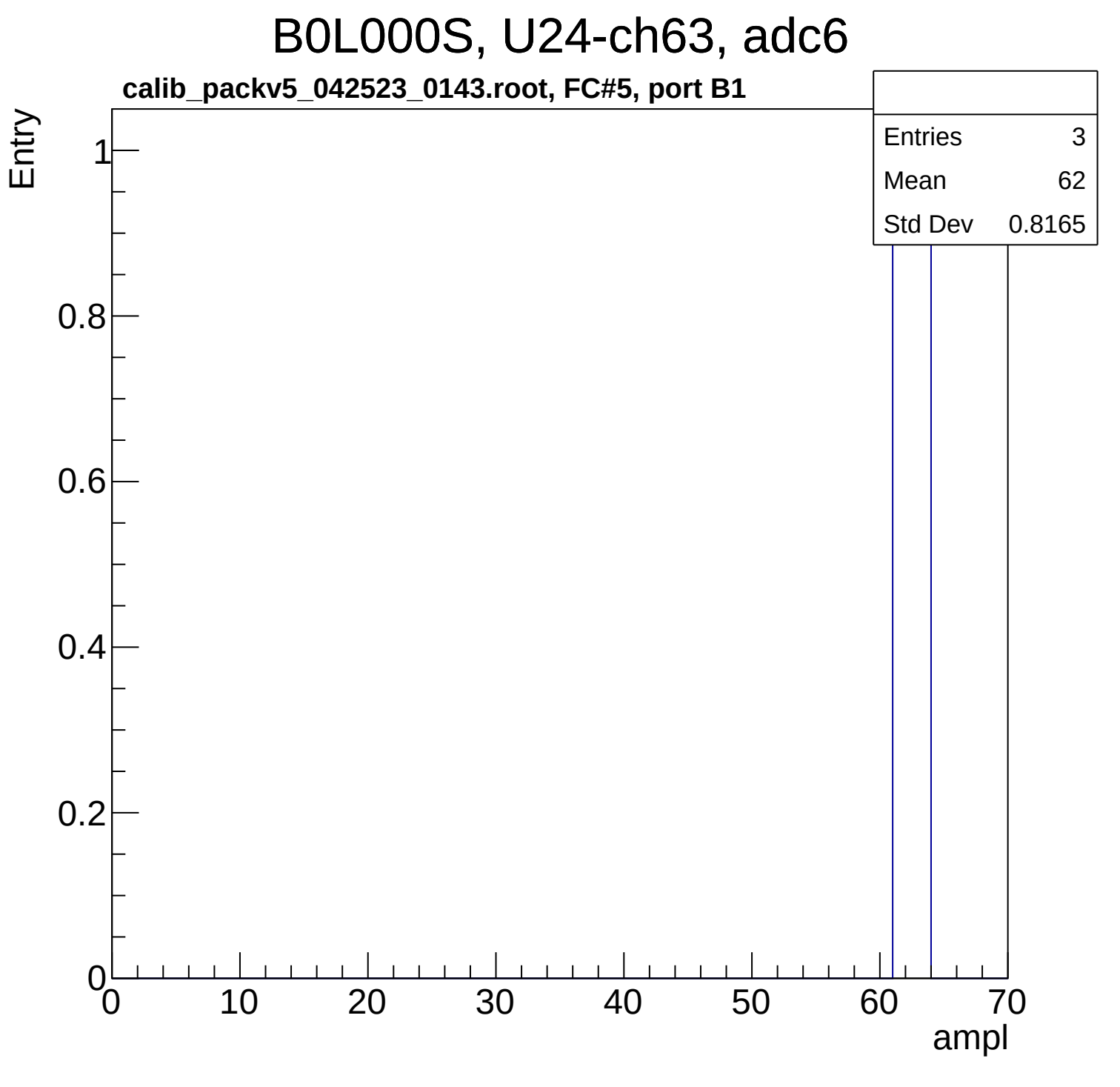
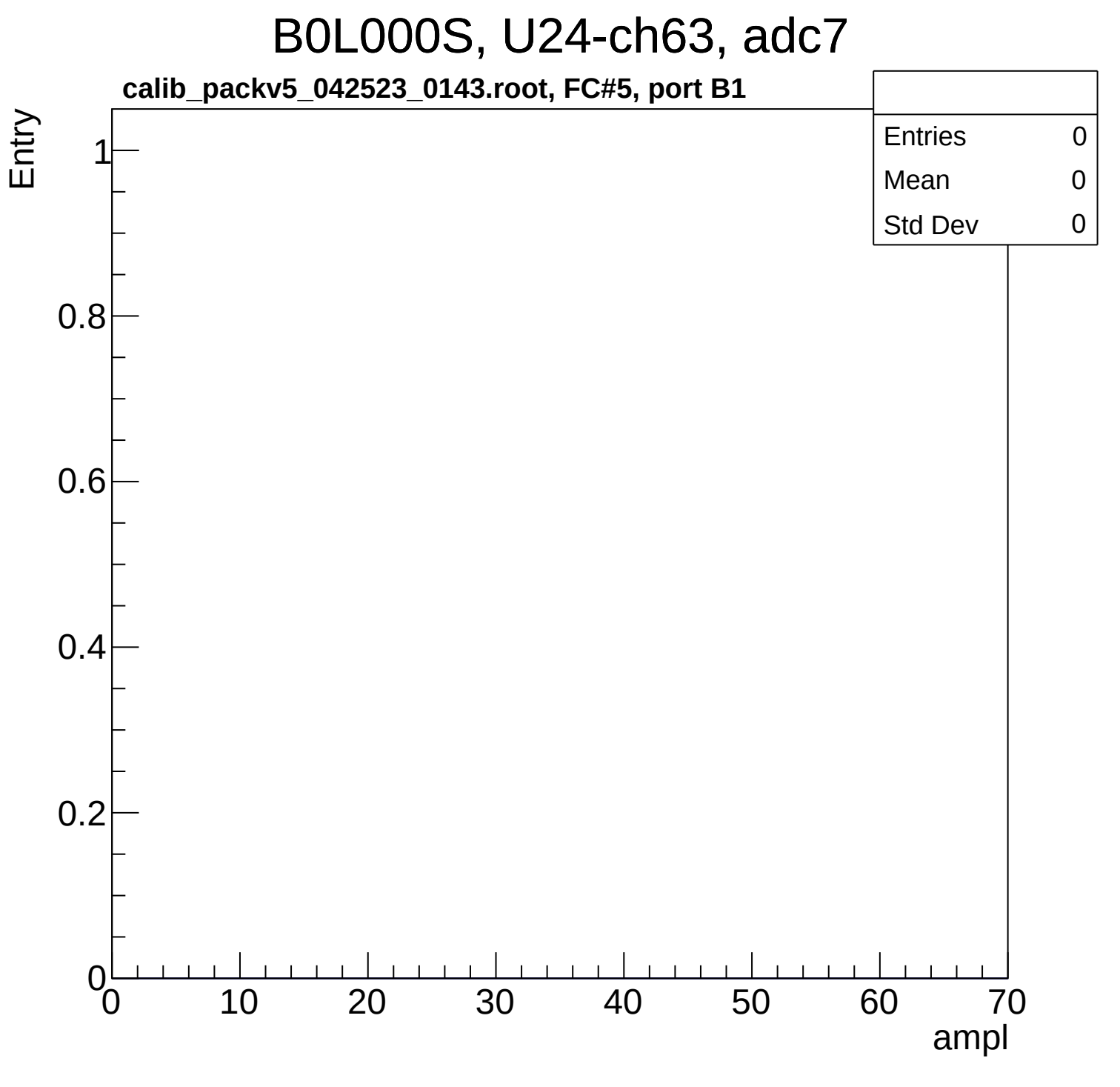
40
50
60
70


B0L000S, U24-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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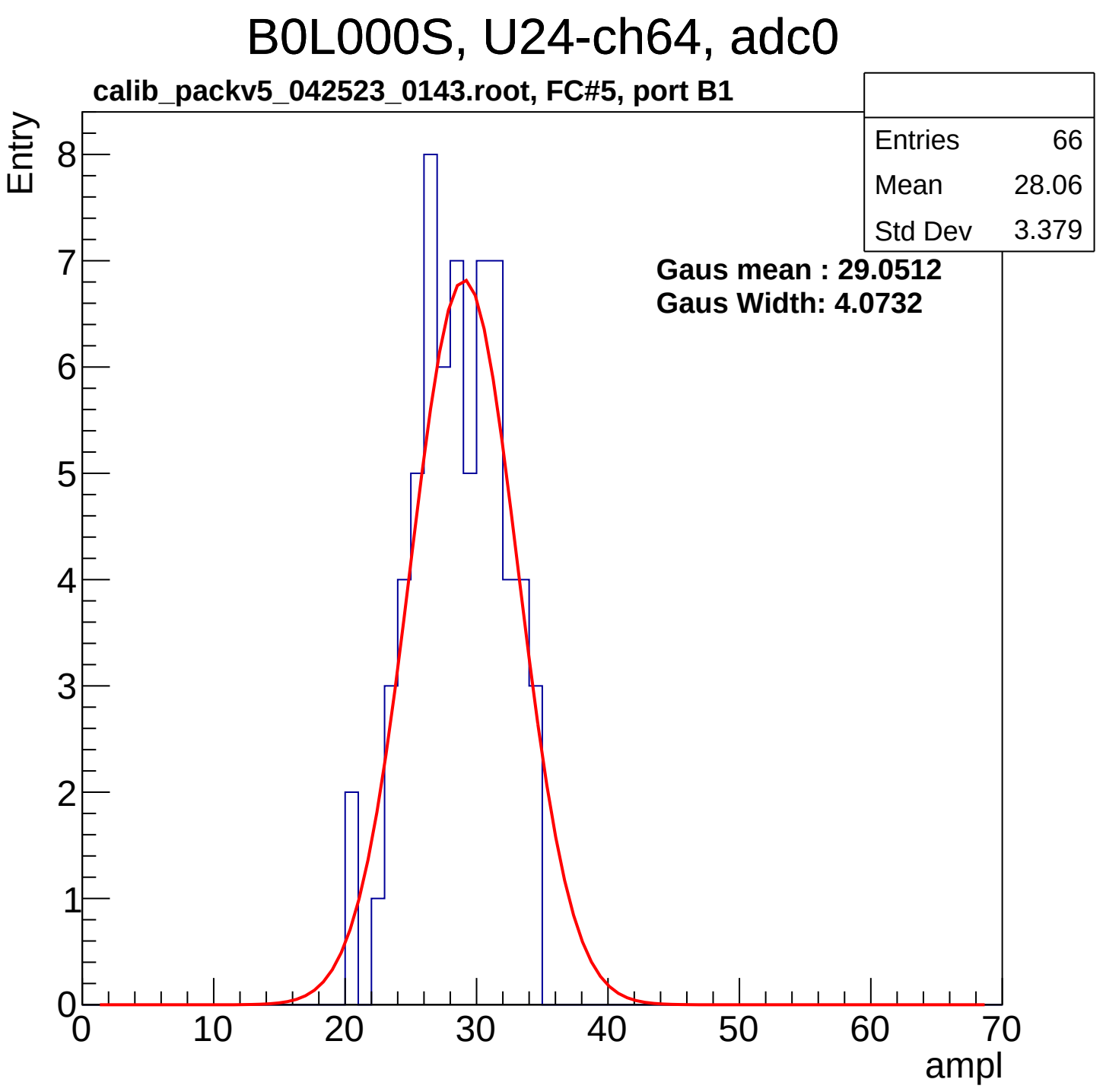
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.06
Std Dev 3.379
Gaus mean : 29.0512
Gaus Width: 4.0732
Entry
ampl
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1
2
3
4
5
6
7
8
0
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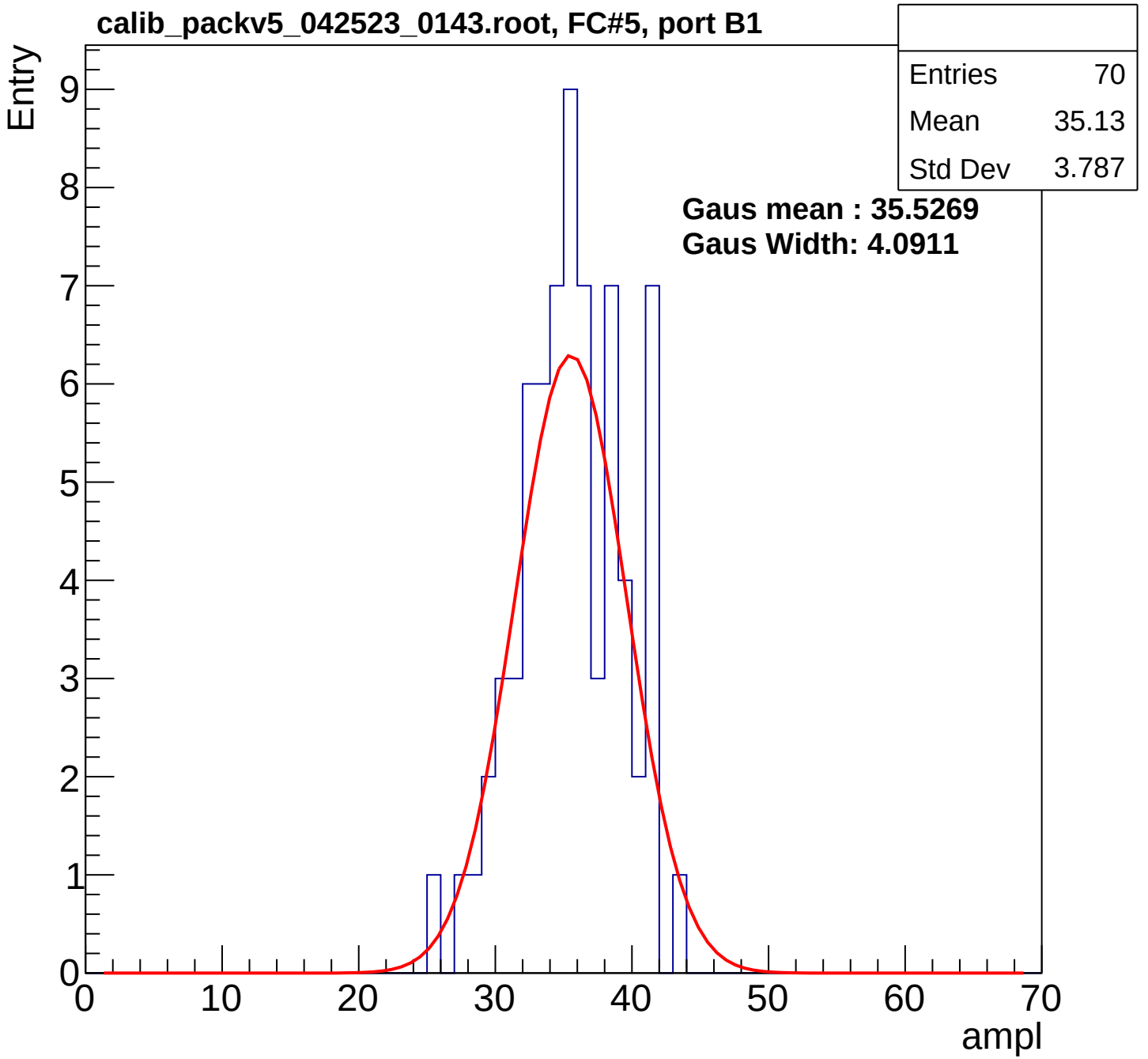
20
30
40
50
60
70


B0L000S, U24-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 35.13
Std Dev 3.787
Gaus mean : 35.5269
Gaus Width: 4.0911
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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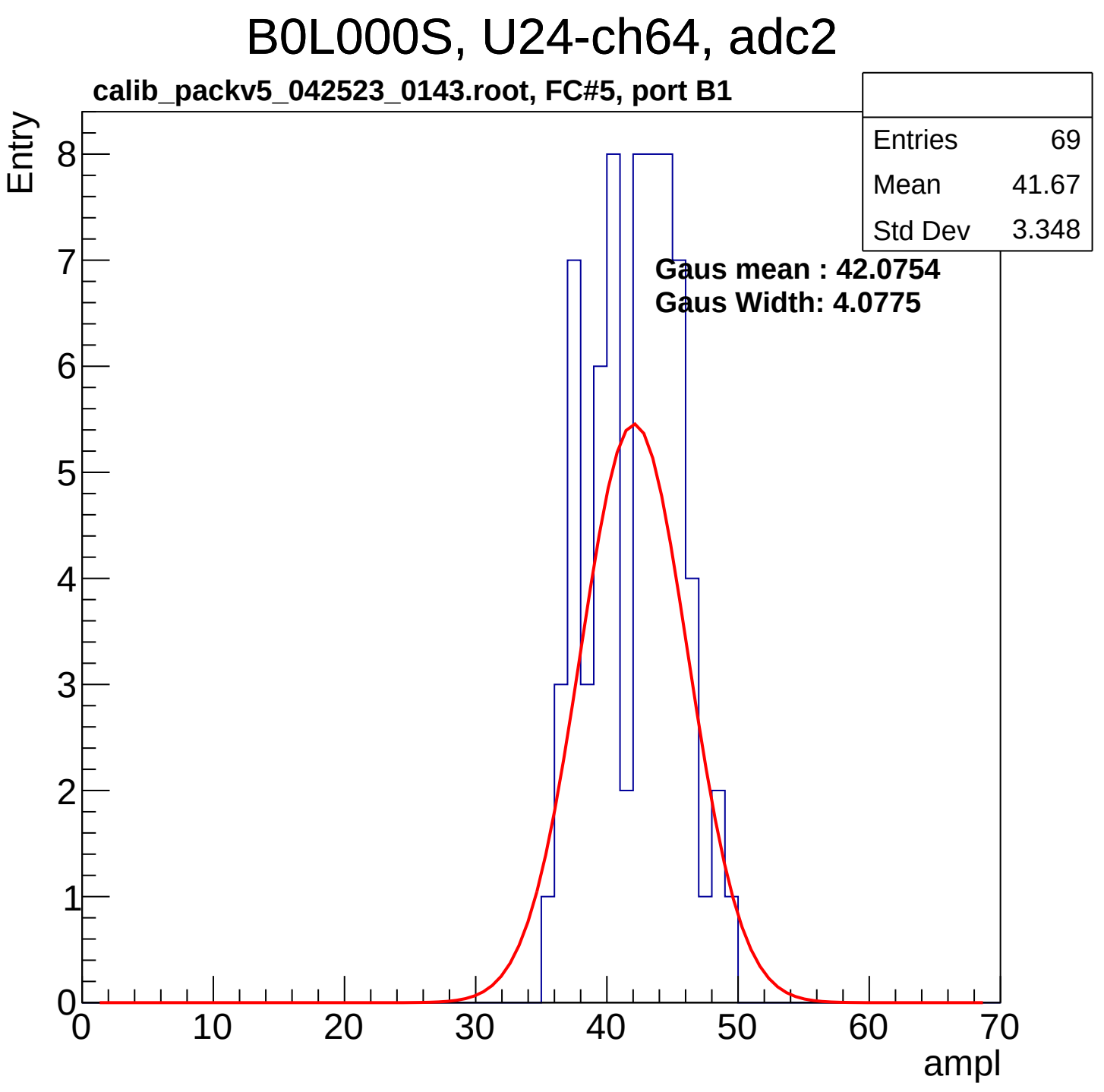
40
50
60
70


B0L000S, U24-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 41.67
Std Dev 3.348
Gaus mean : 42.0754
Gaus Width: 4.0775
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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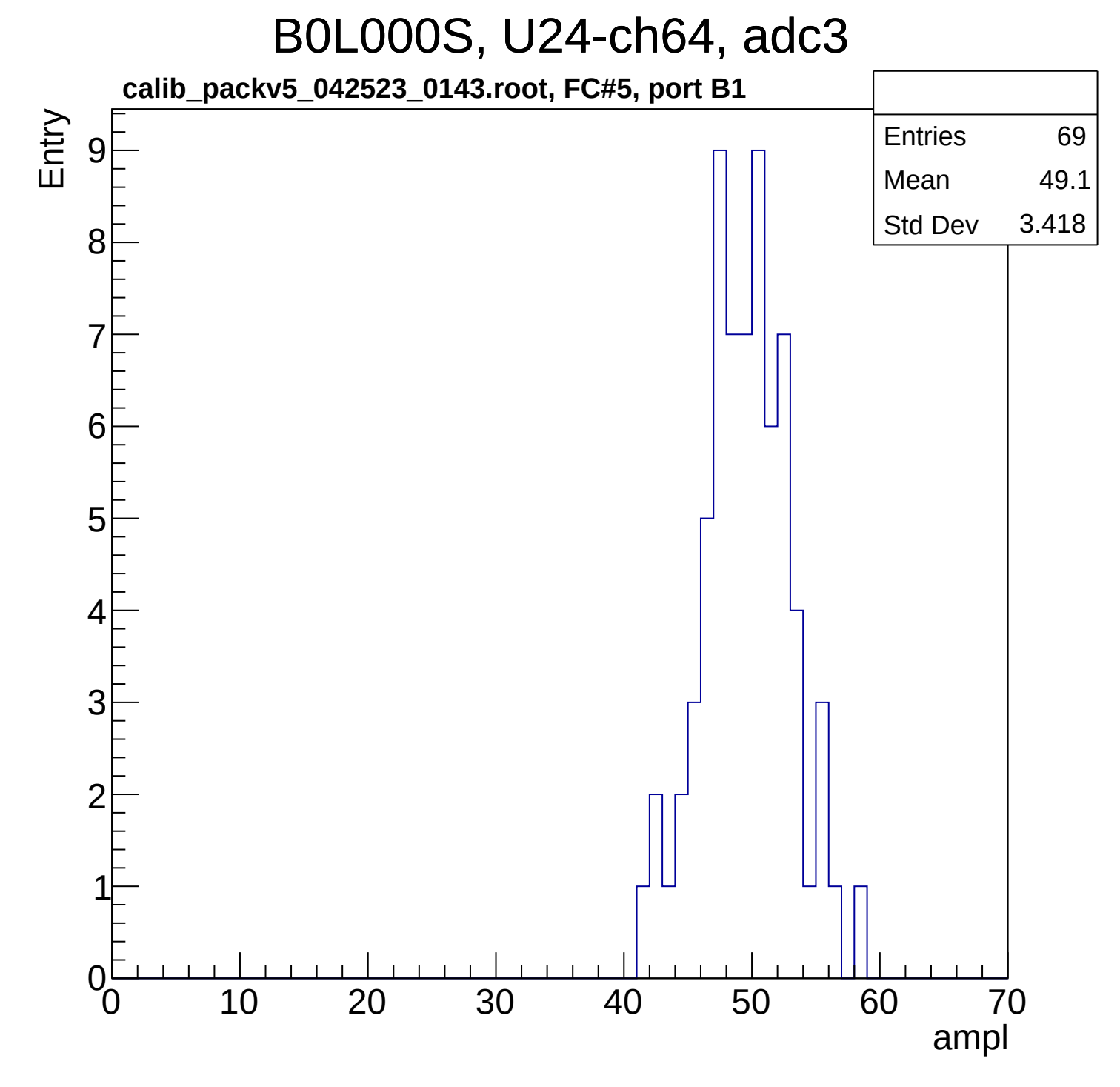
B0L000S, U24-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.1
Std Dev 3.418
Entry
ampl
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3
4
5
6
7
8
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0
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40
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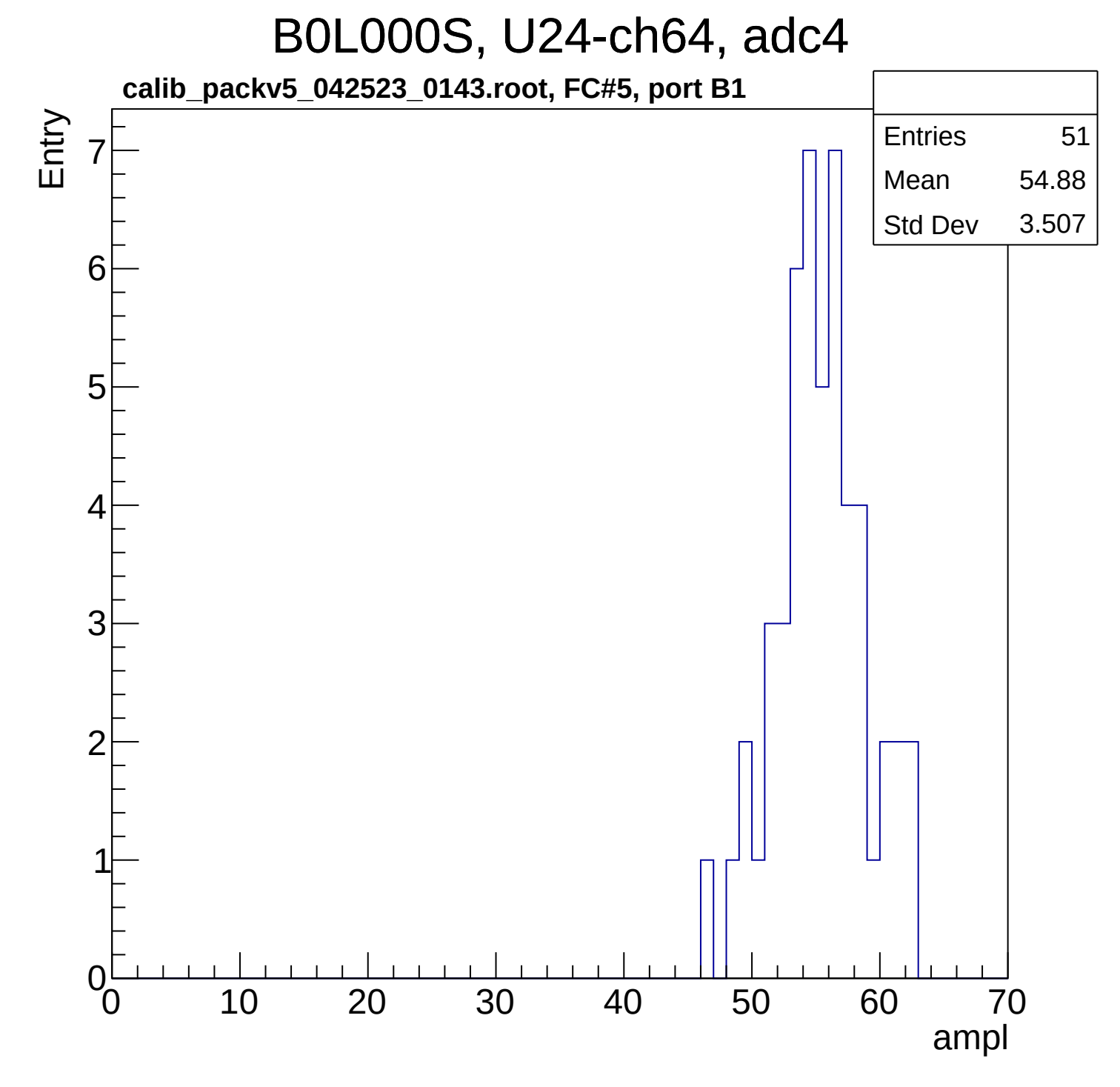
60
70


B0L000S, U24-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.88
Std Dev 3.507
Entry
ampl
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3
4
5
6
7
0
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40
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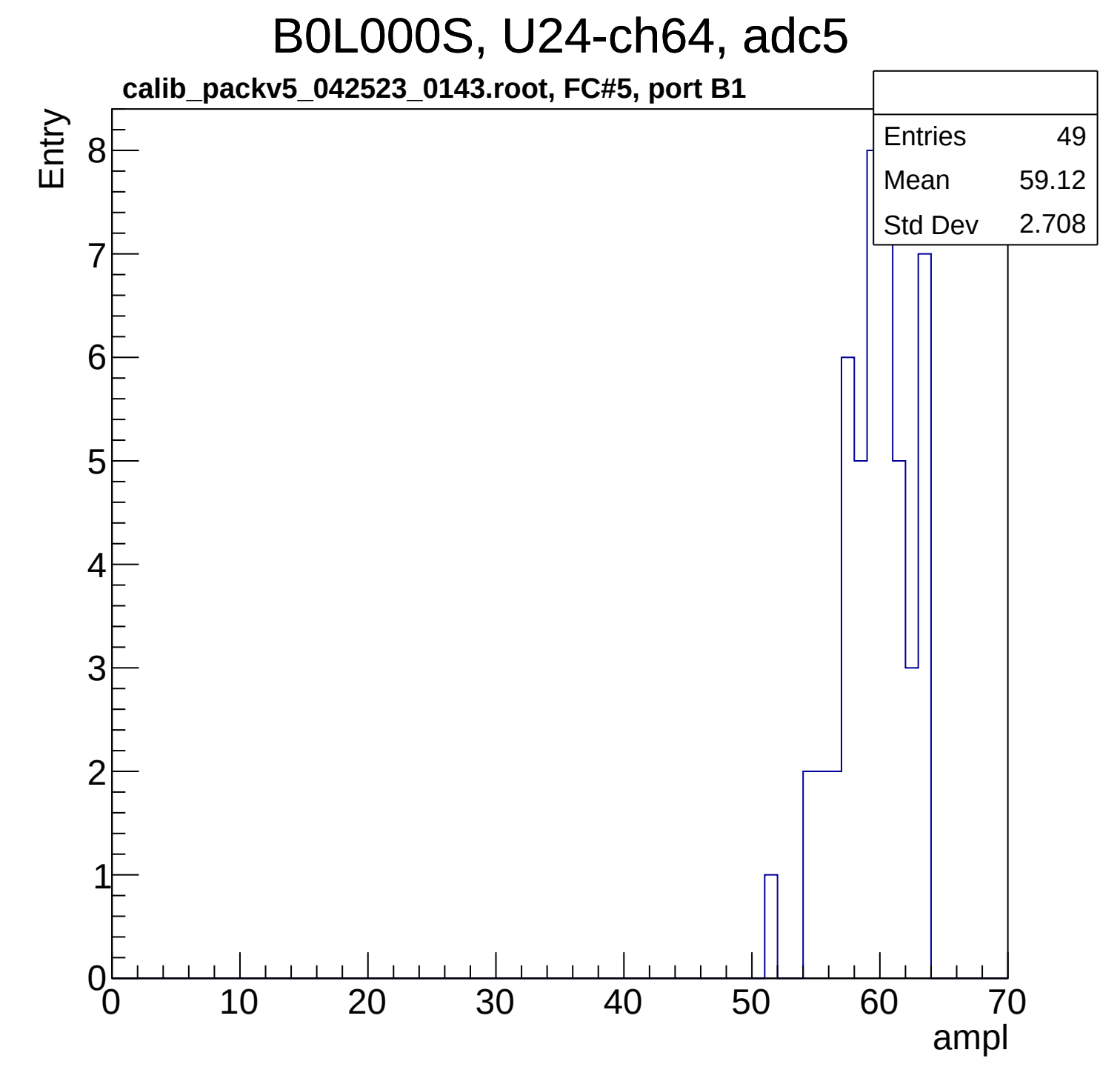
60
70


B0L000S, U24-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.12
Std Dev 2.708
Entry
ampl
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1
2
3
4
5
6
7
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20
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40
50
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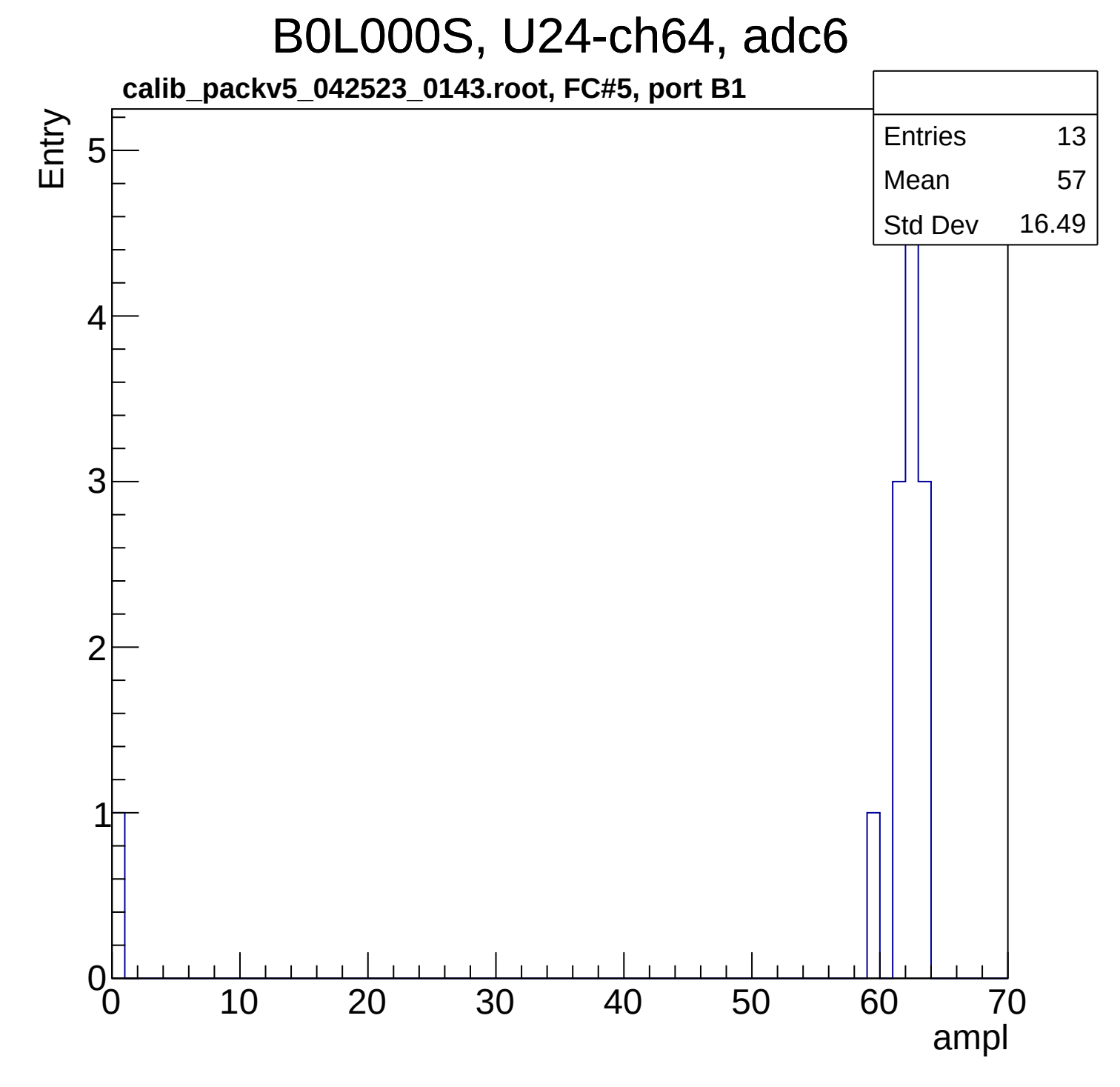
B0L000S, U24-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57
Std Dev 16.49
Entry
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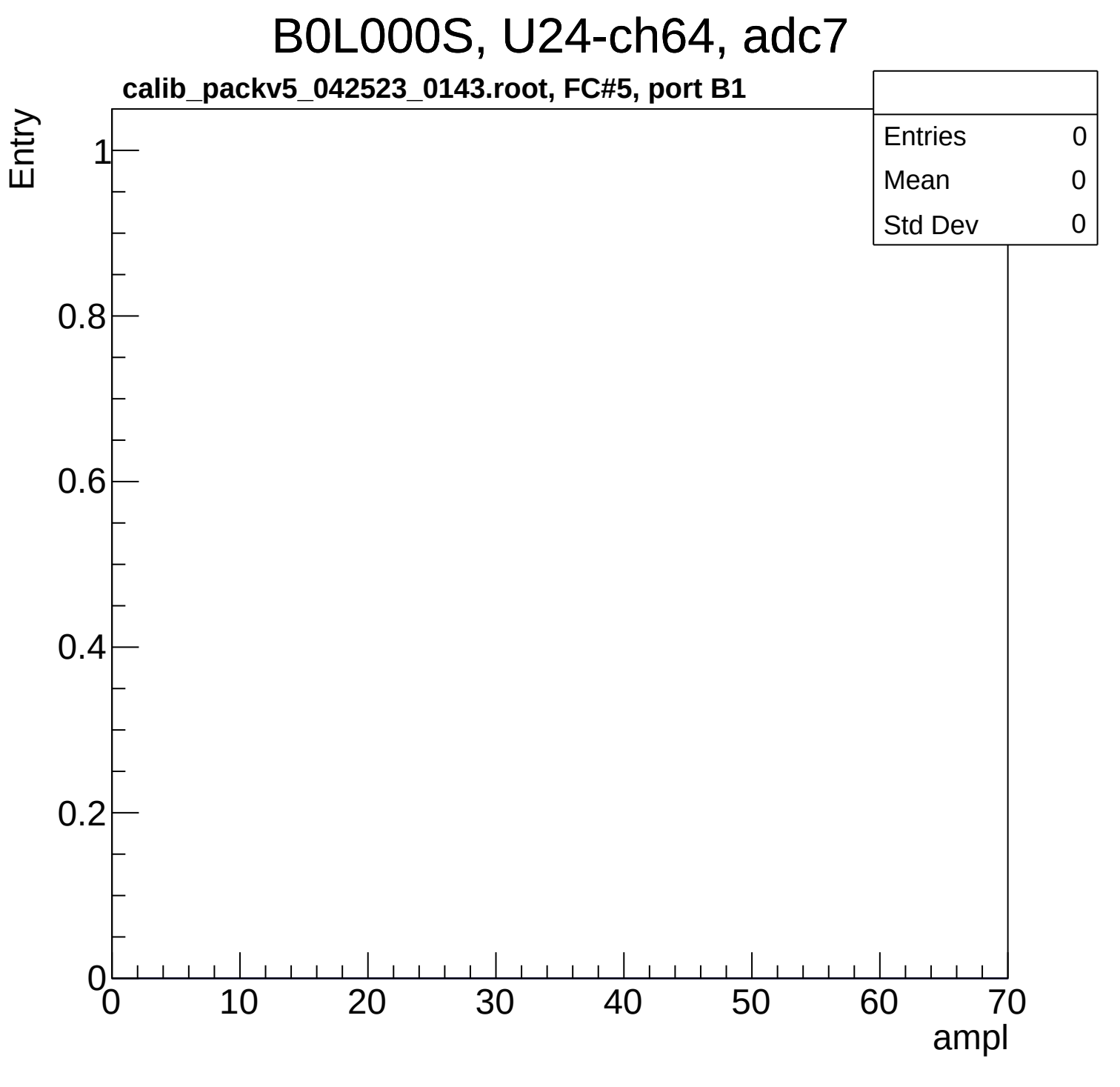
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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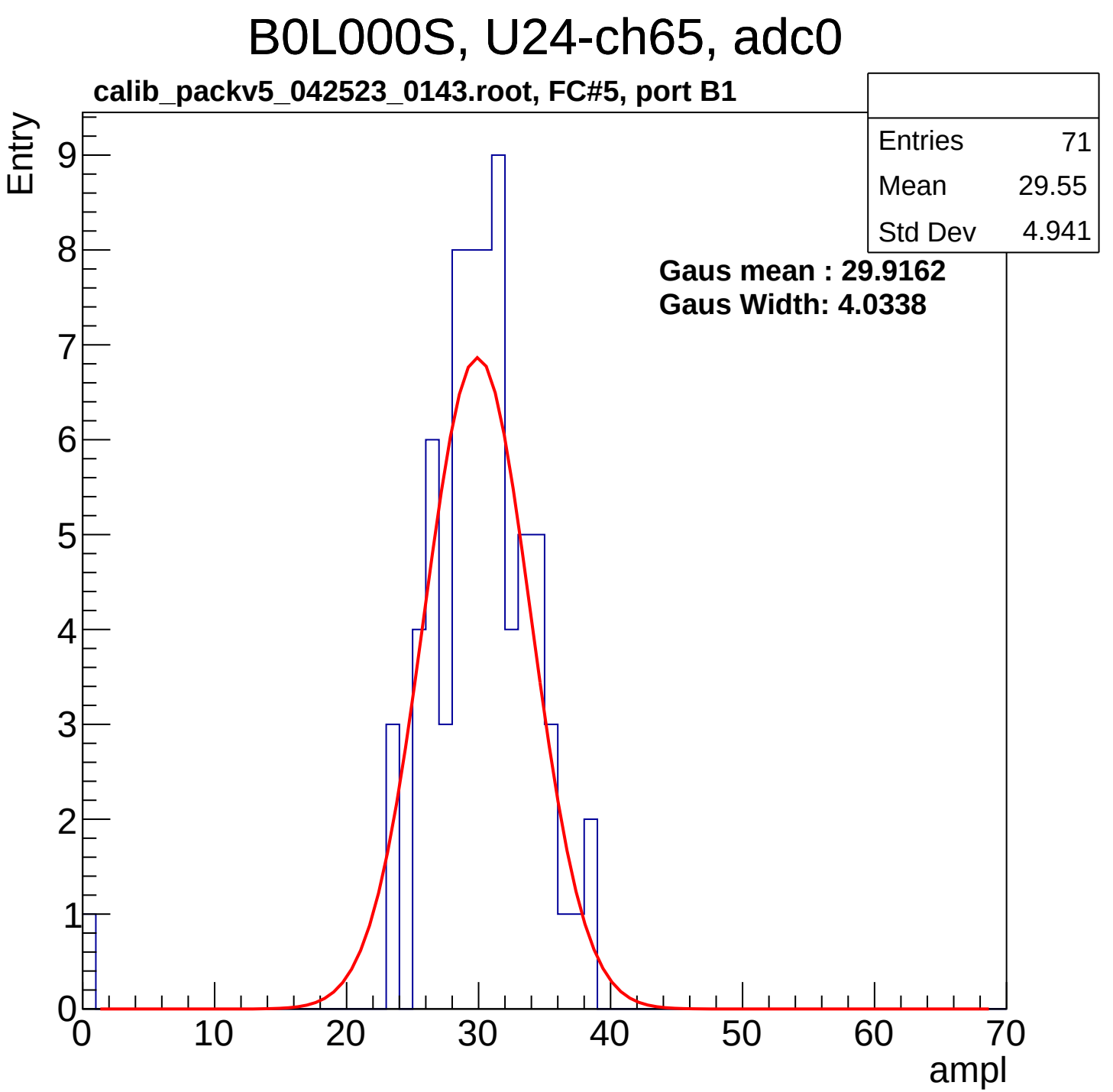
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.55
Std Dev 4.941
Gaus mean : 29.9162
Gaus Width: 4.0338
Entry
ampl
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2
3
4
5
6
7
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0
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60
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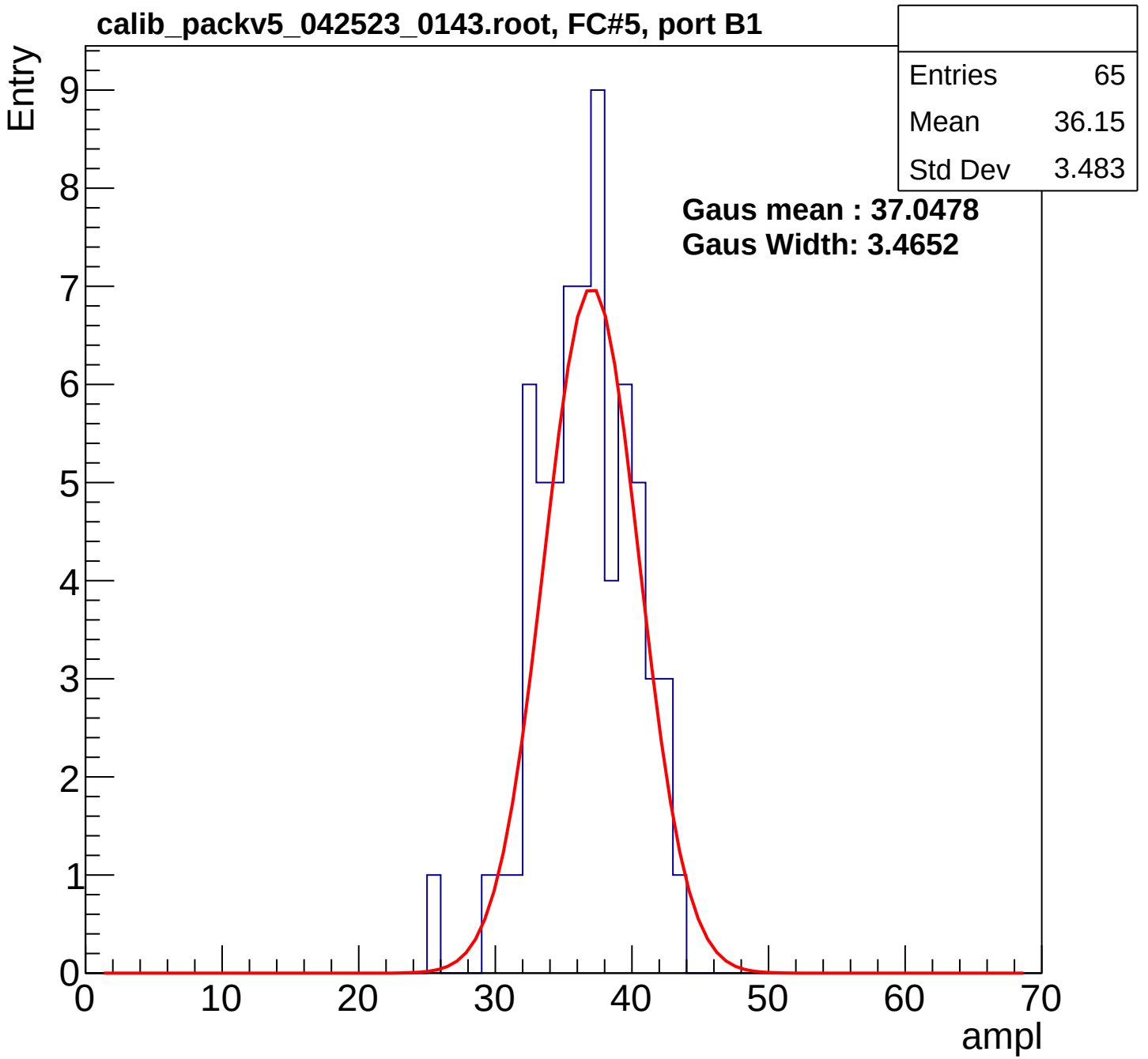
B0L000S, U24-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.15
Std Dev 3.483
Gaus mean : 37.0478
Gaus Width: 3.4652
Entry
ampl
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2
3
4
5
6
7
8
9
0
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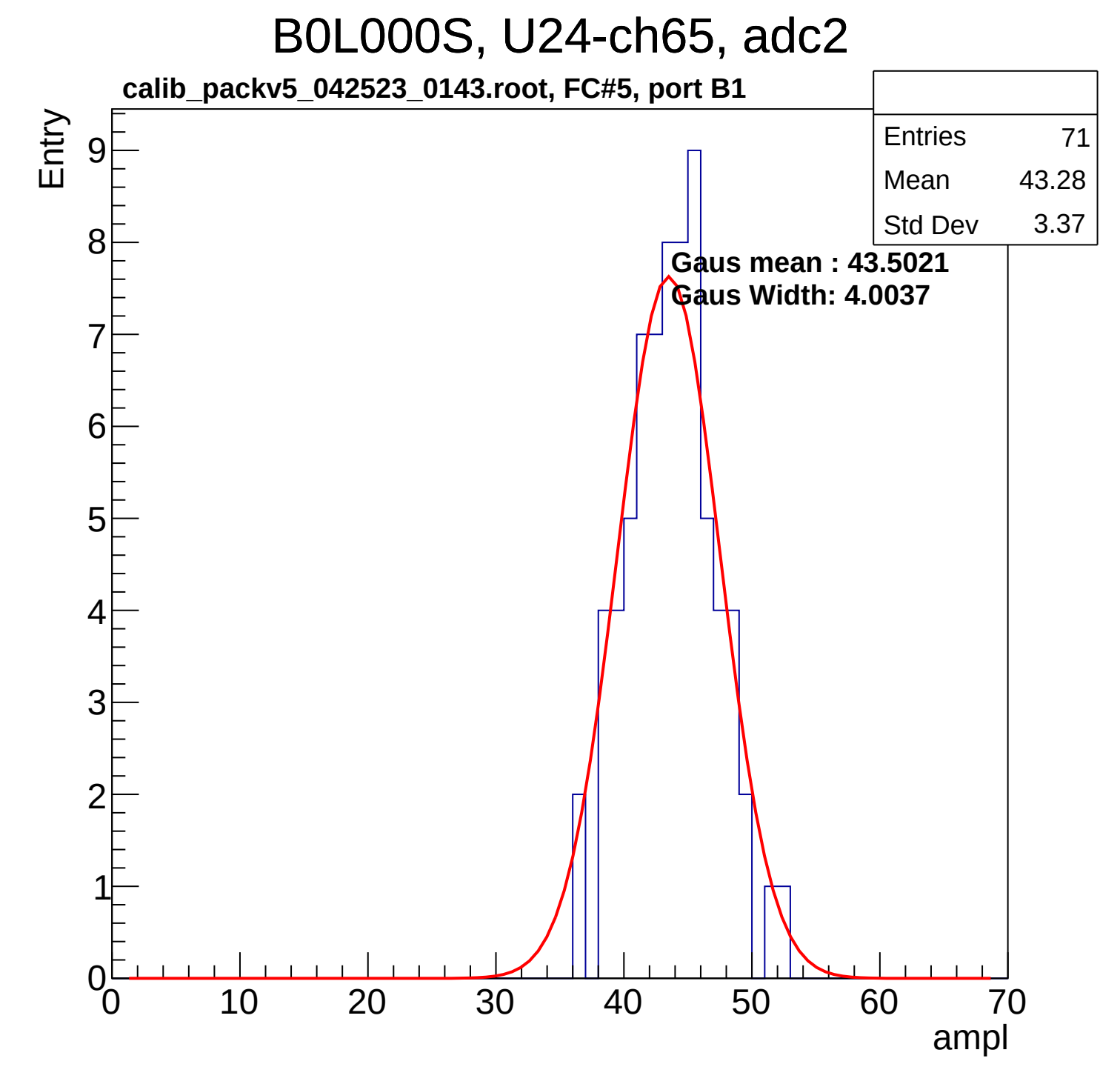
B0L000S, U24-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.28
Std Dev 3.37
Gaus mean : 43.5021
Gaus Width: 4.0037
Entry
ampl
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3
4
5
6
7
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0
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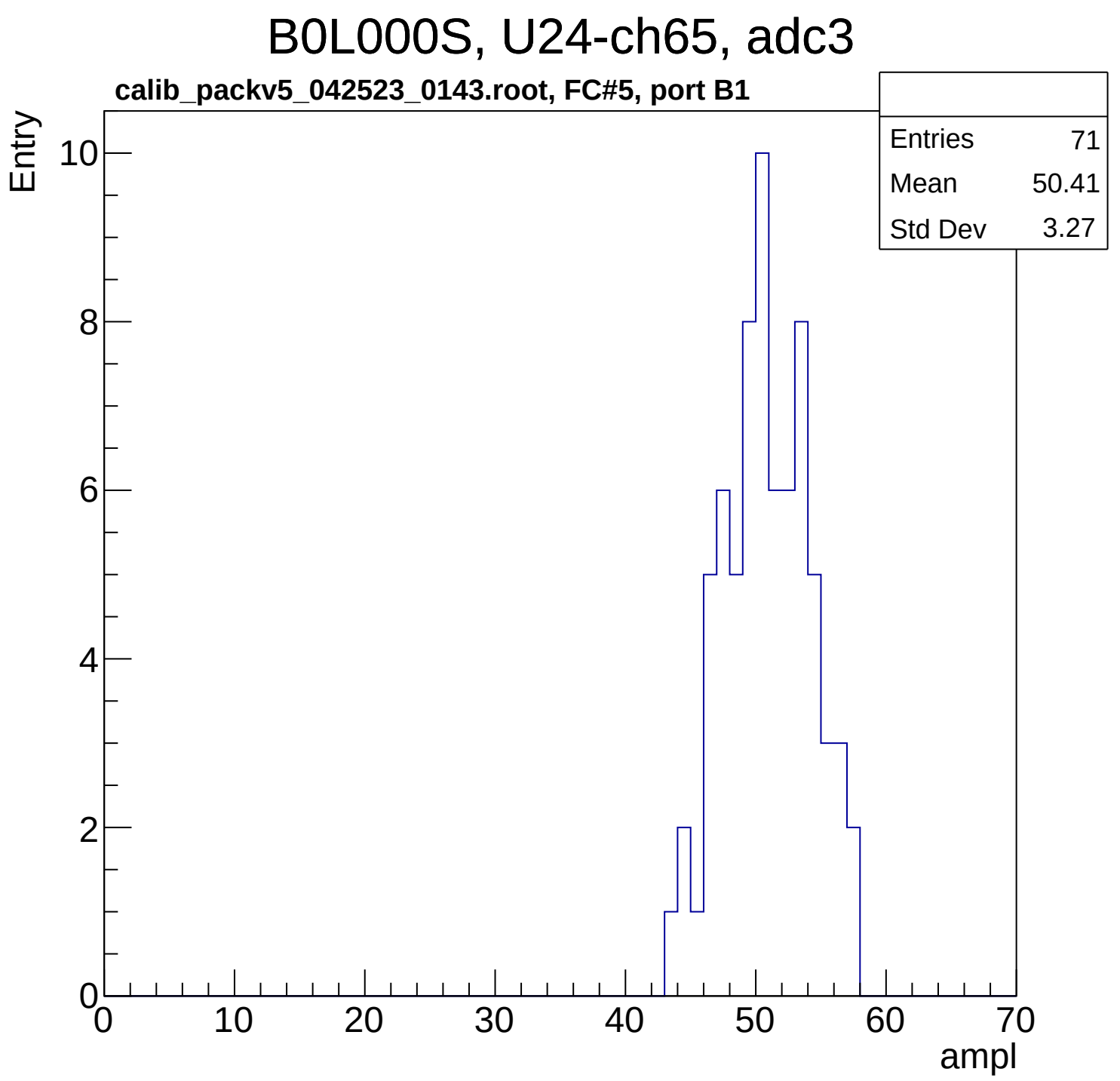
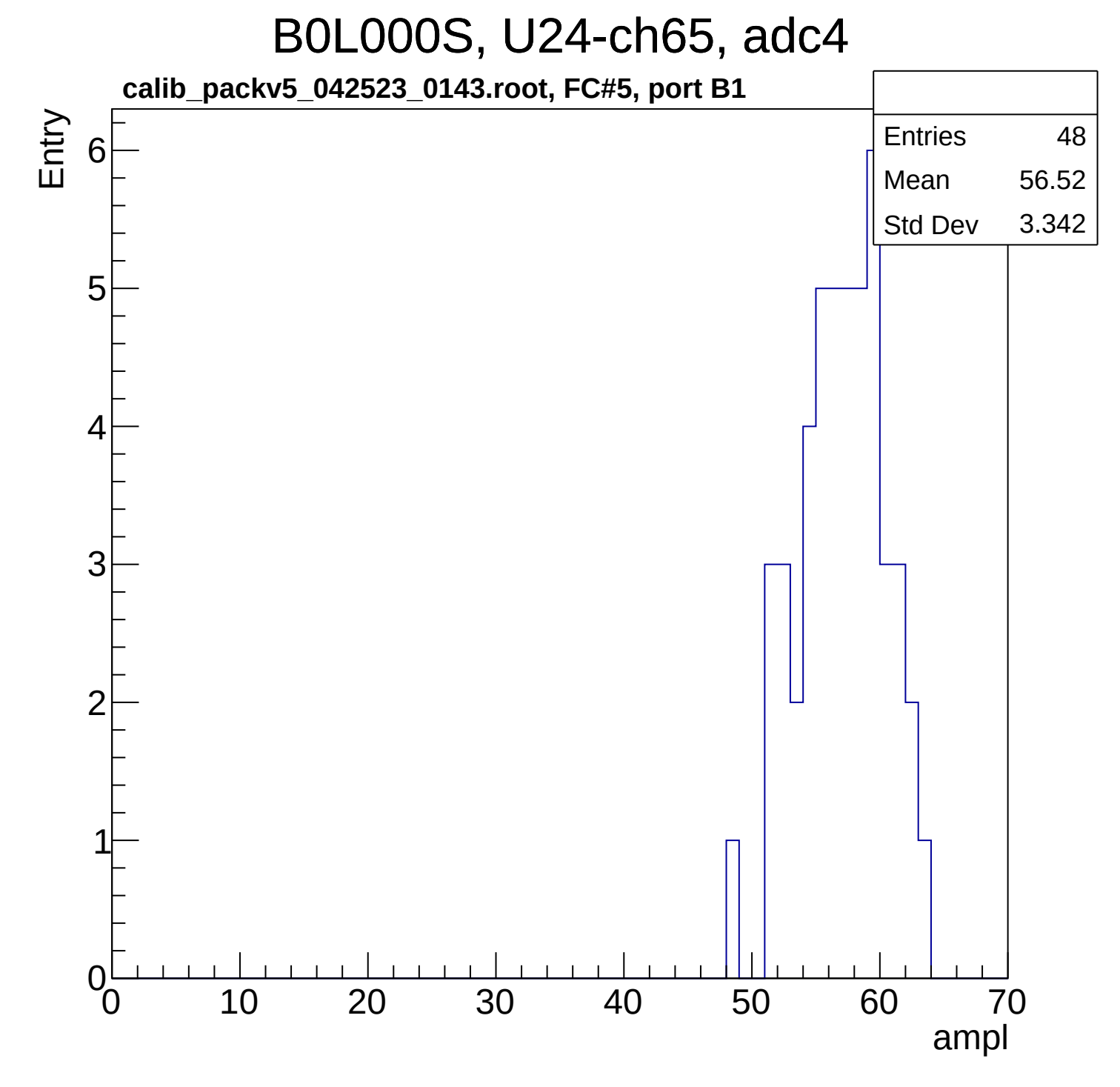
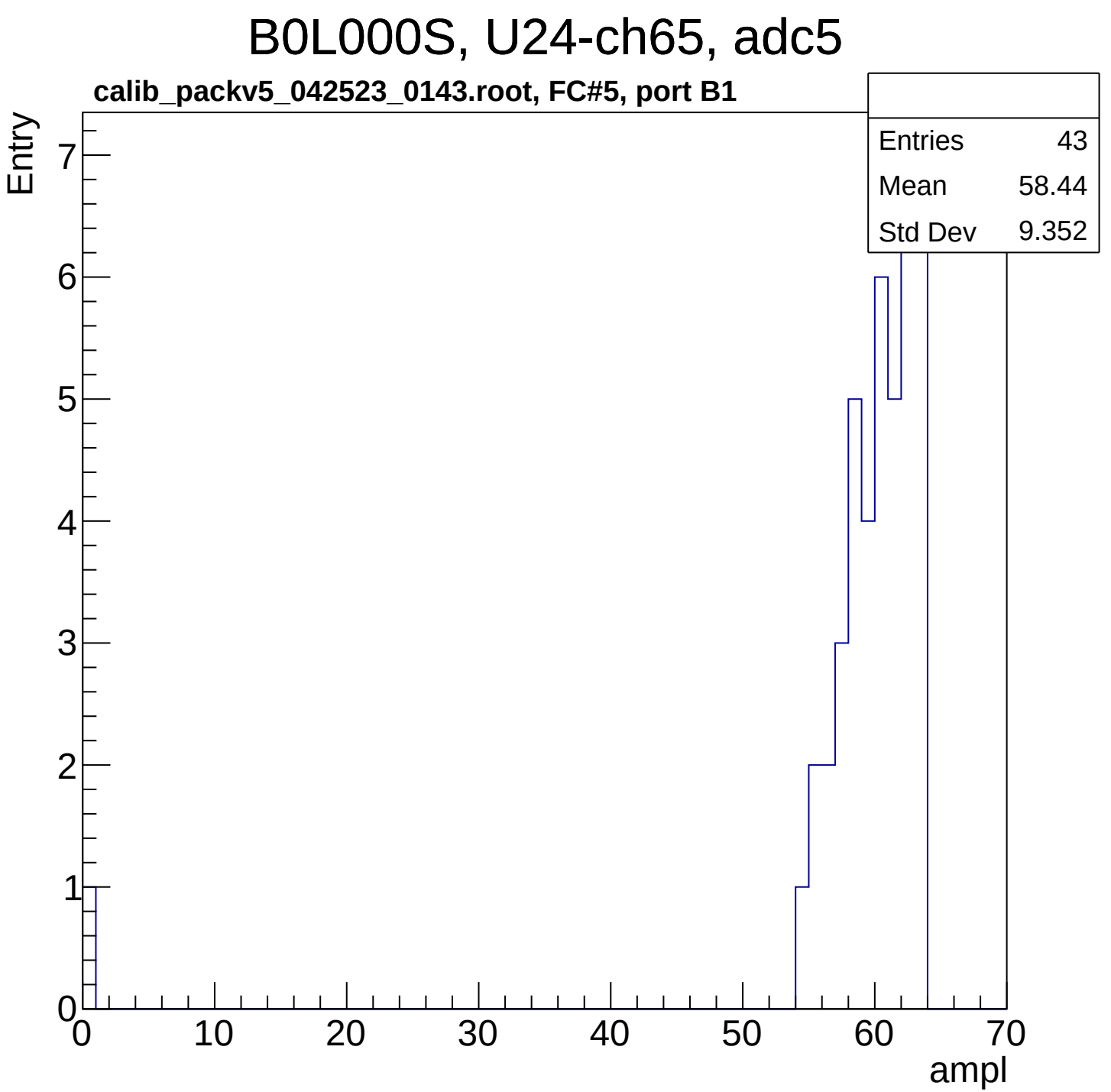
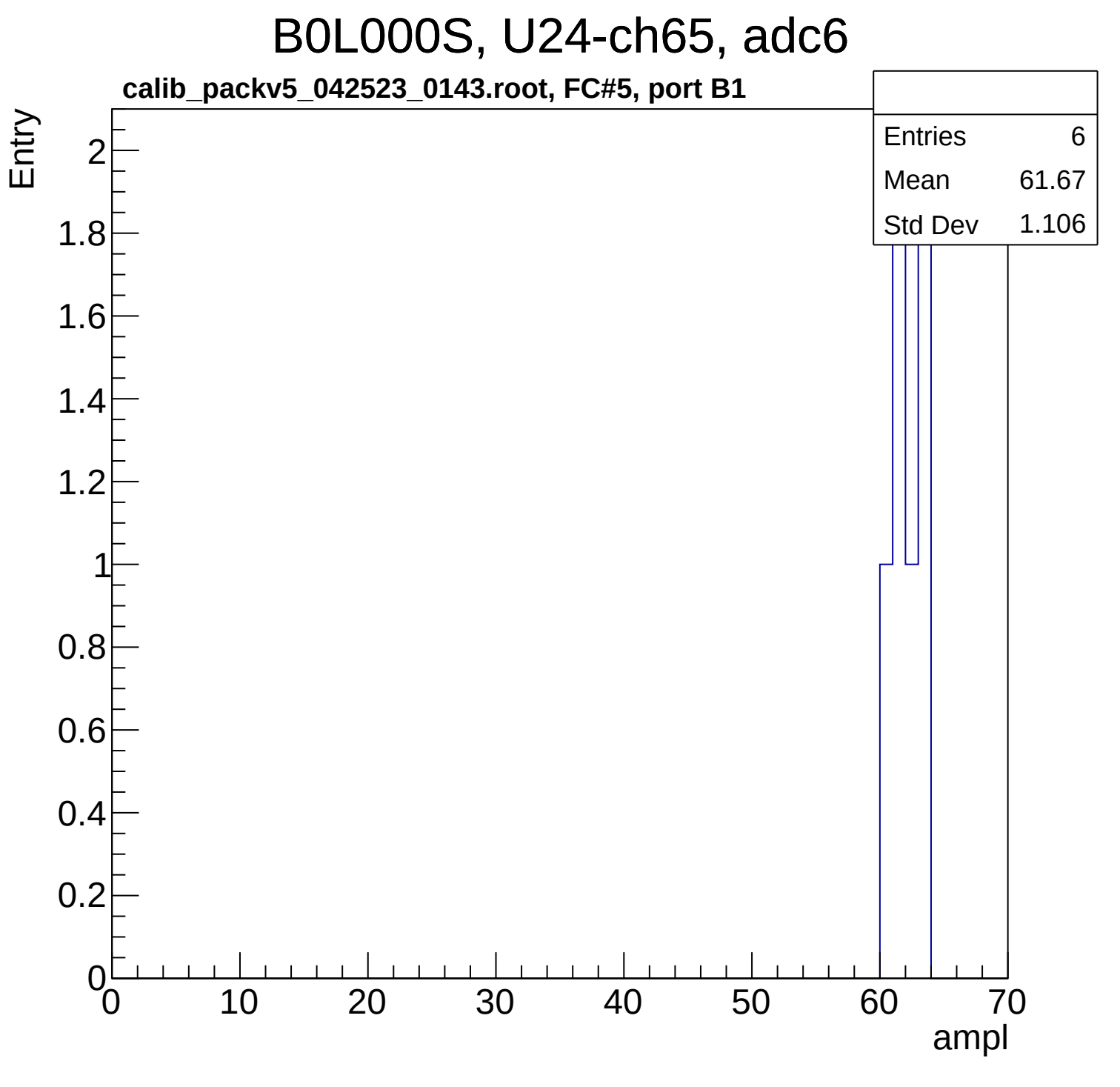
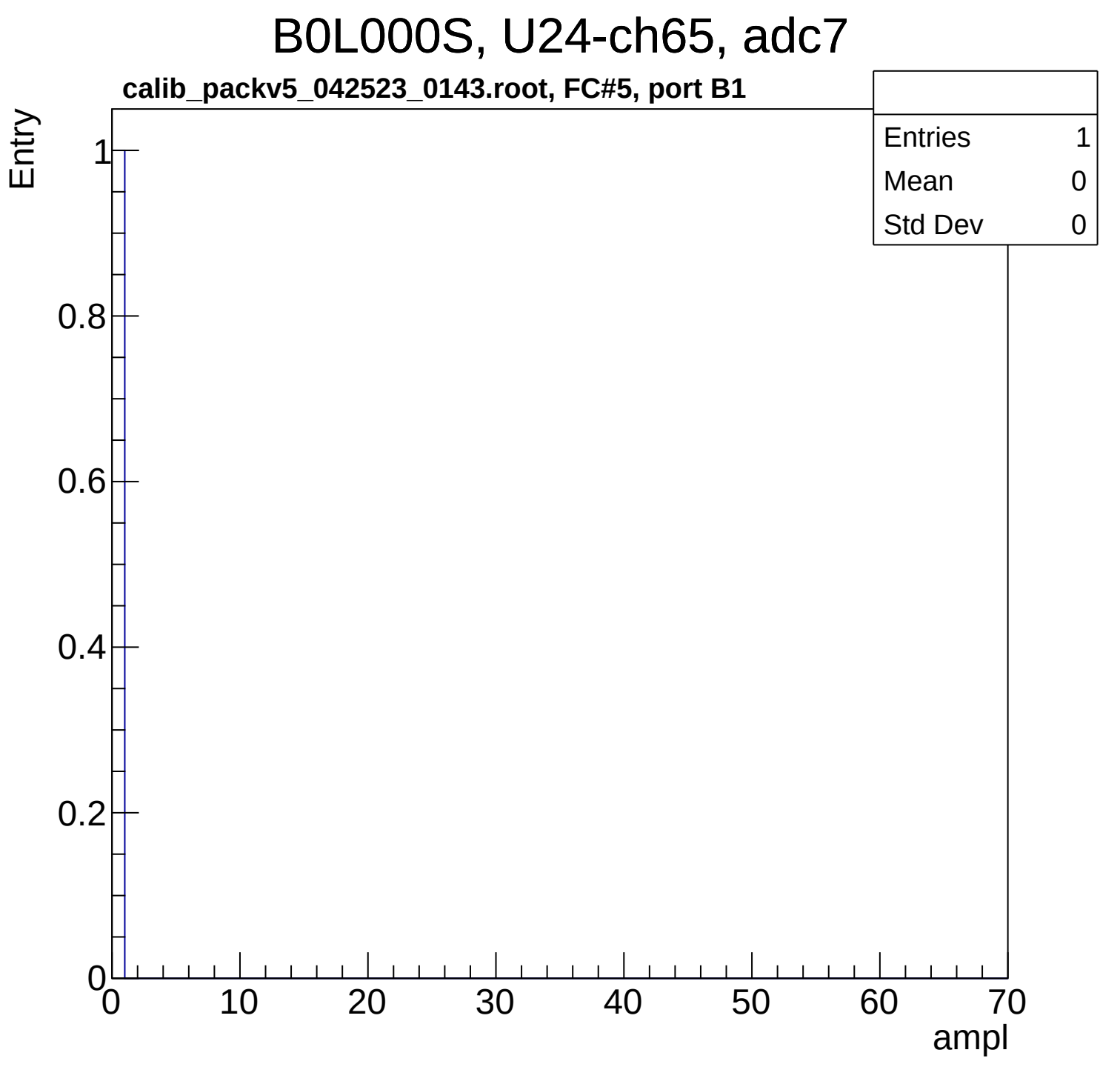
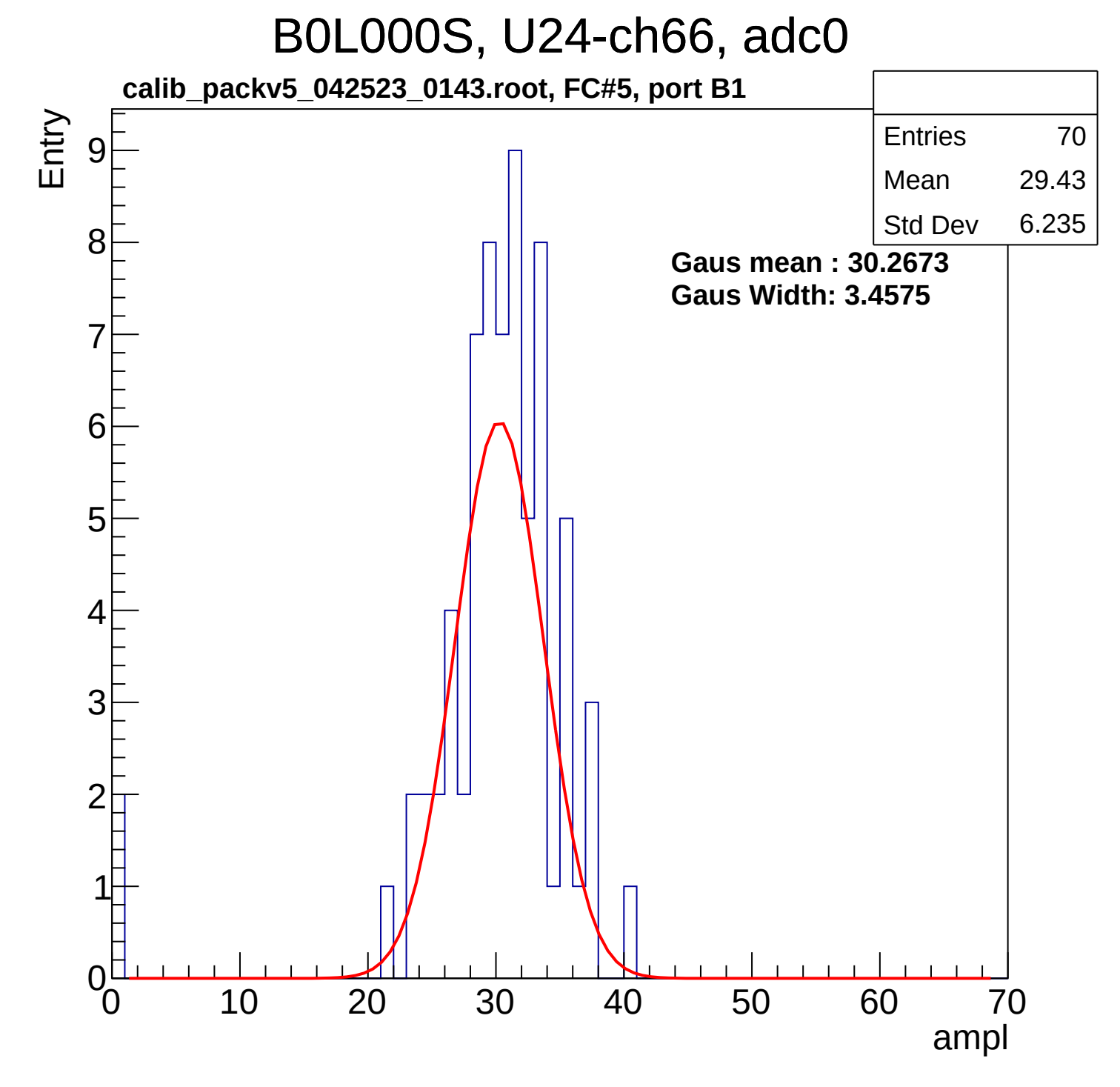

B0L000S, U24-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.41
Std Dev 3.27
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

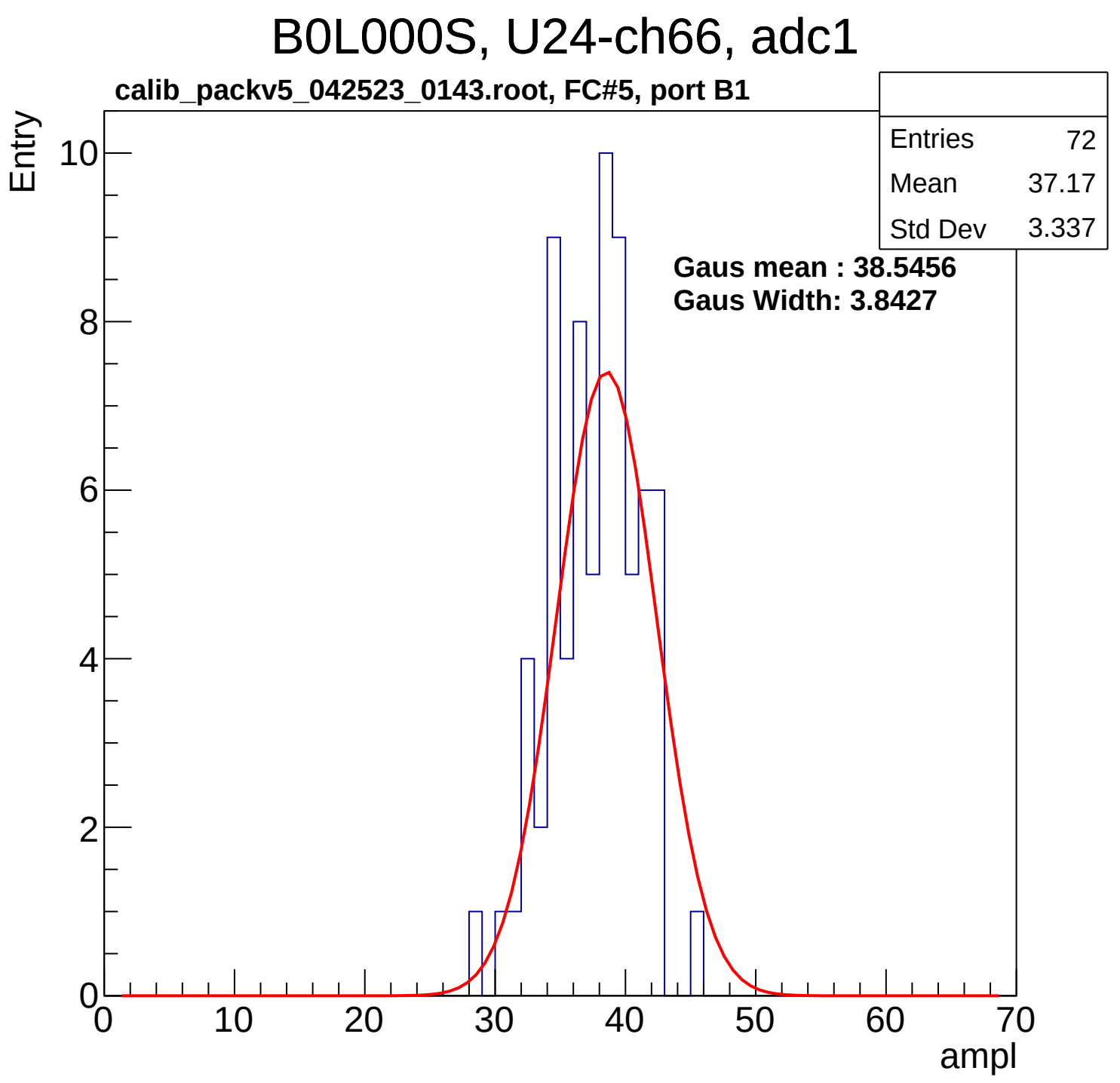

B0L000S, U24-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.52
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

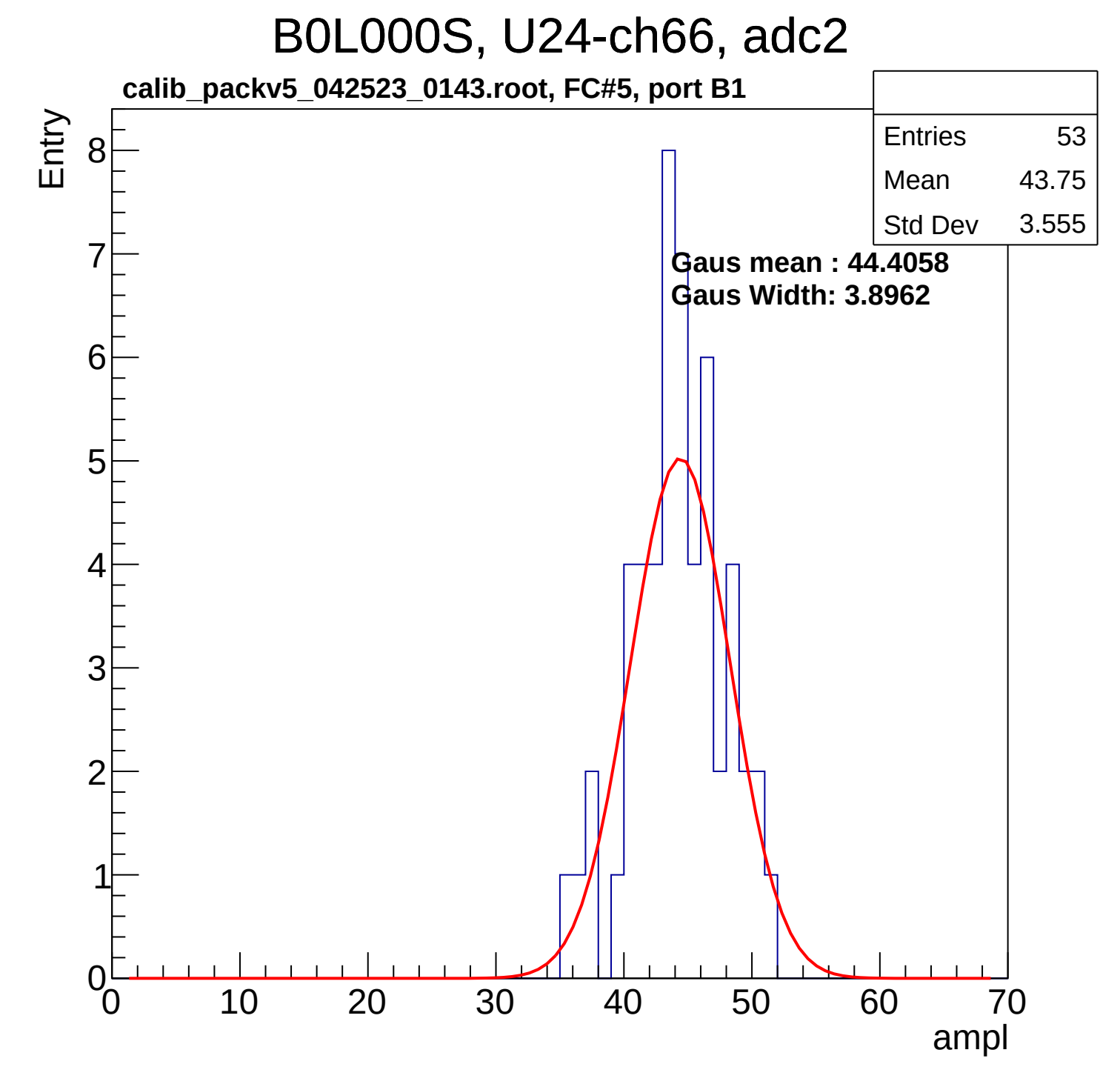

B0L000S, U24-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.44
Std Dev 9.352
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

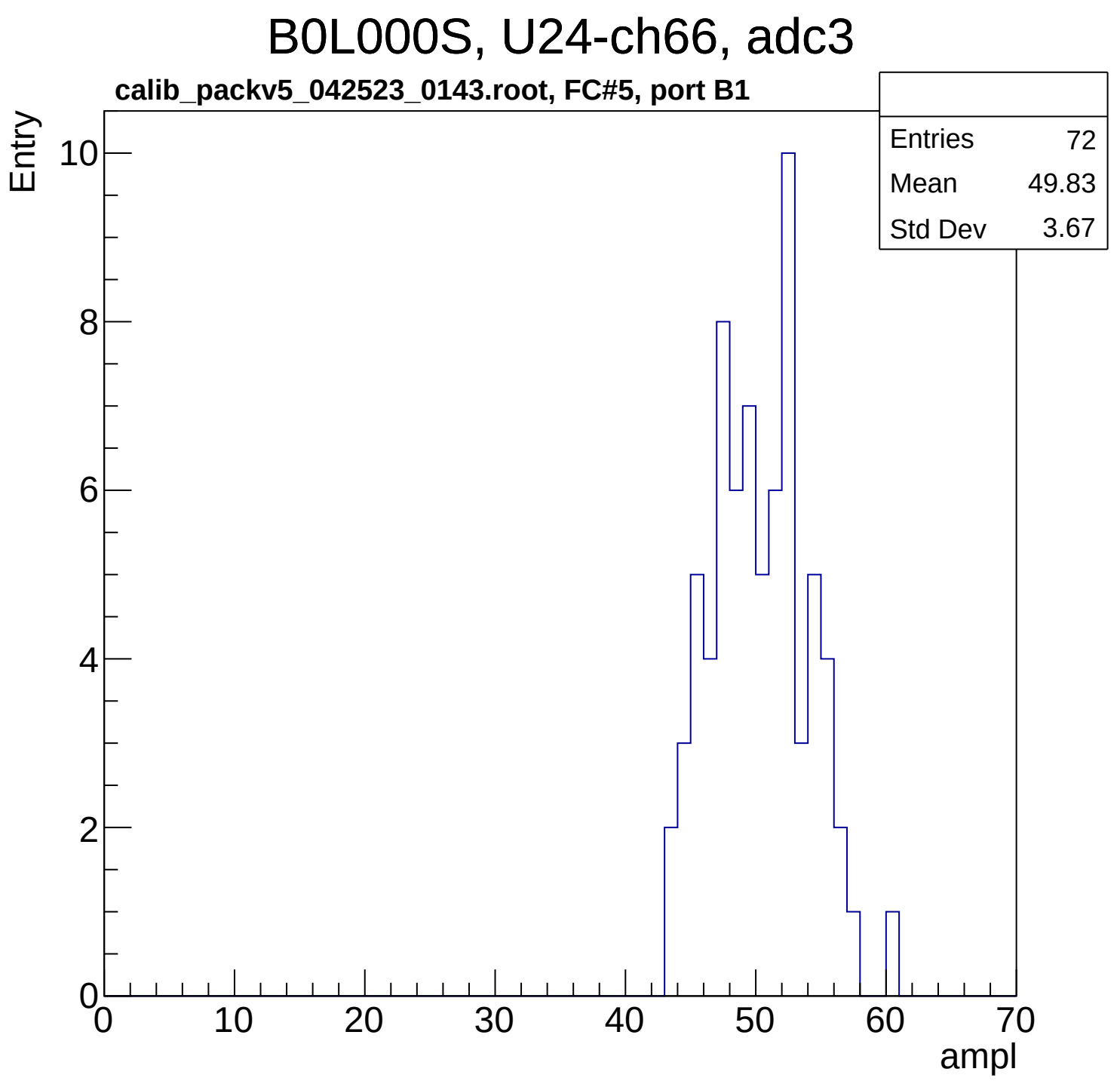

B0L000S, U24-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

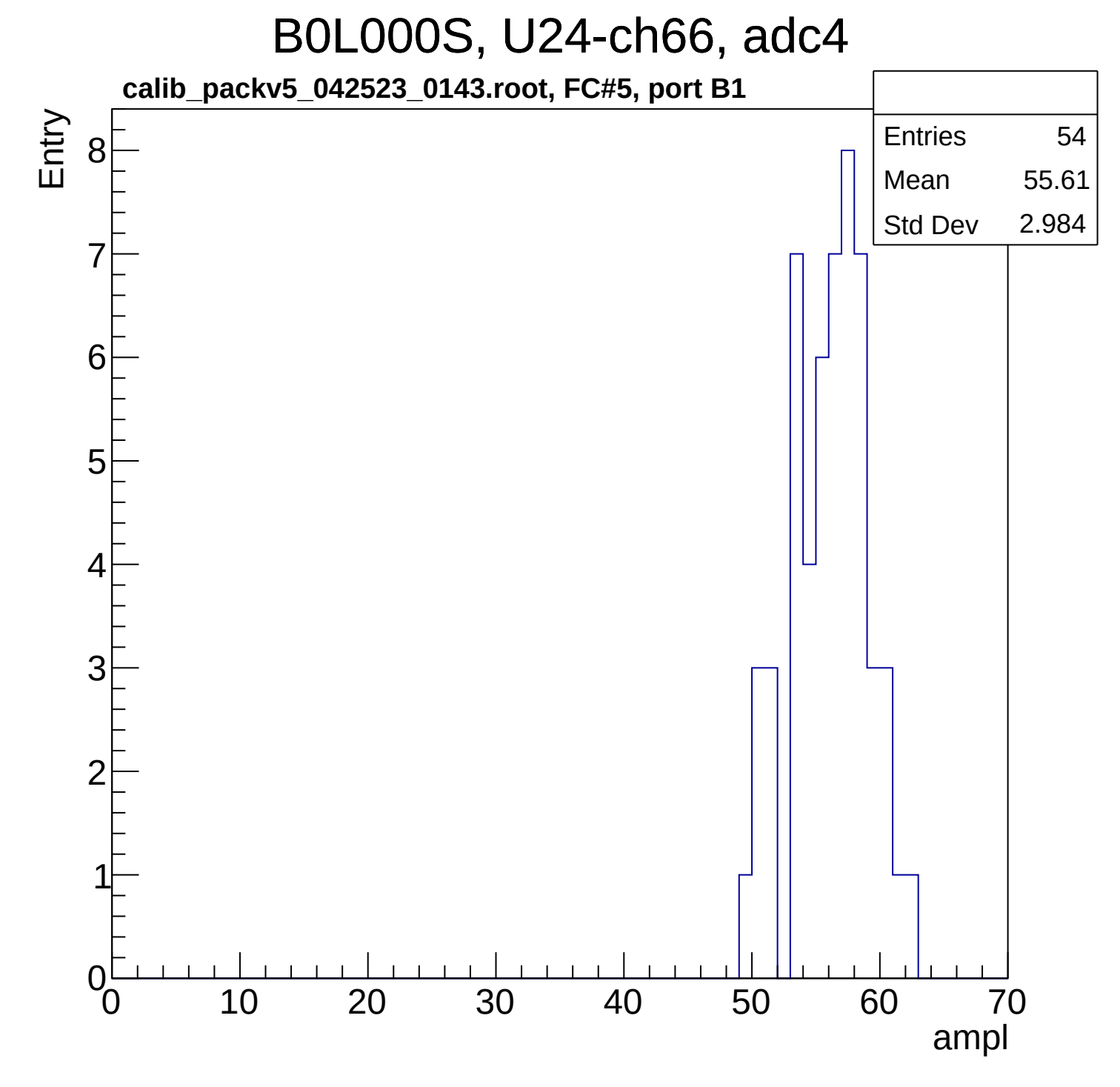

B0L000S, U24-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

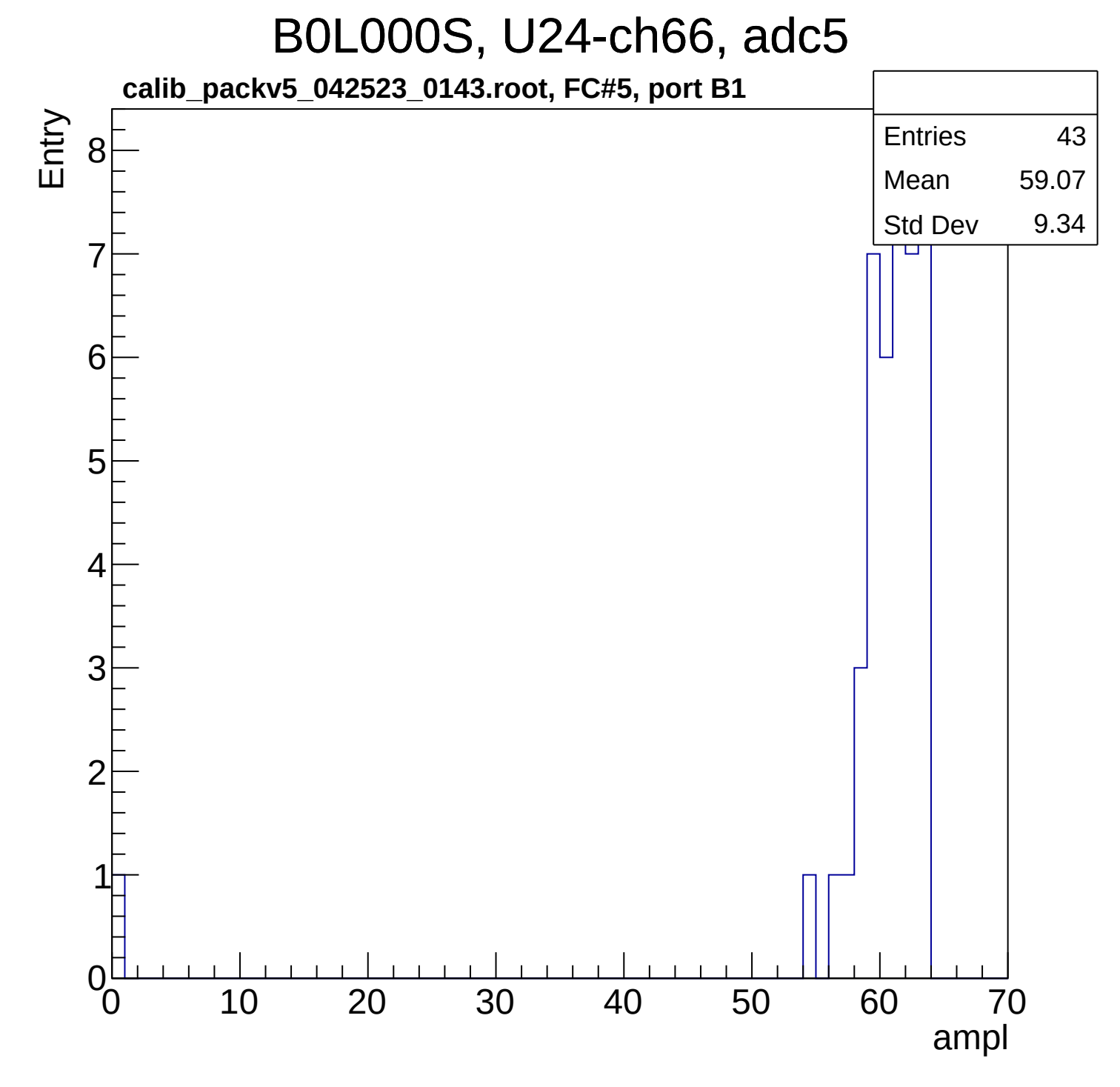

B0L000S, U24-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.43
Std Dev 6.235
Gaus mean : 30.2673
Gaus Width: 3.4575
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

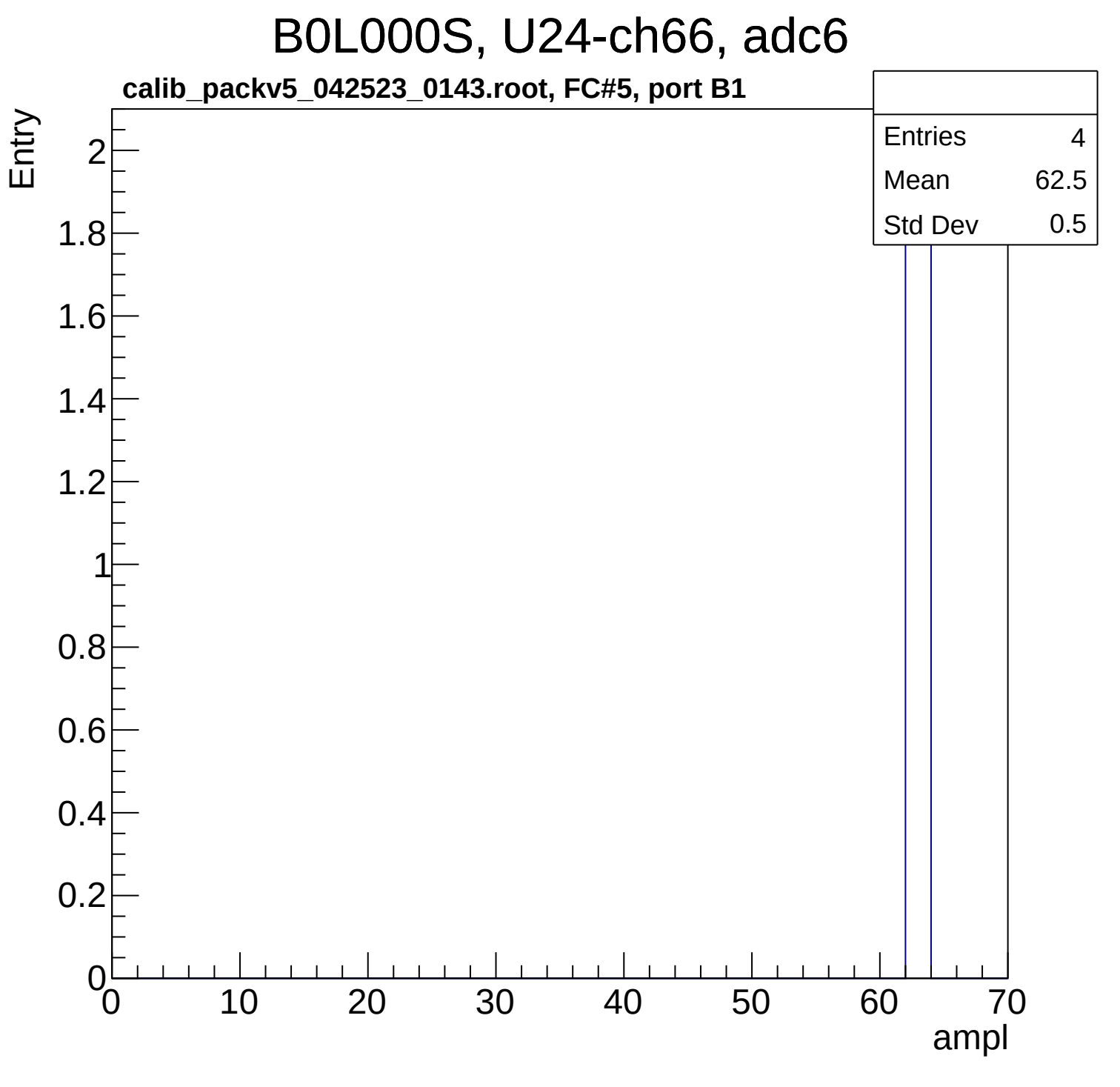

B0L000S, U24-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.17
Std Dev 3.337
Gaus mean : 38.5456
Gaus Width: 3.8427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

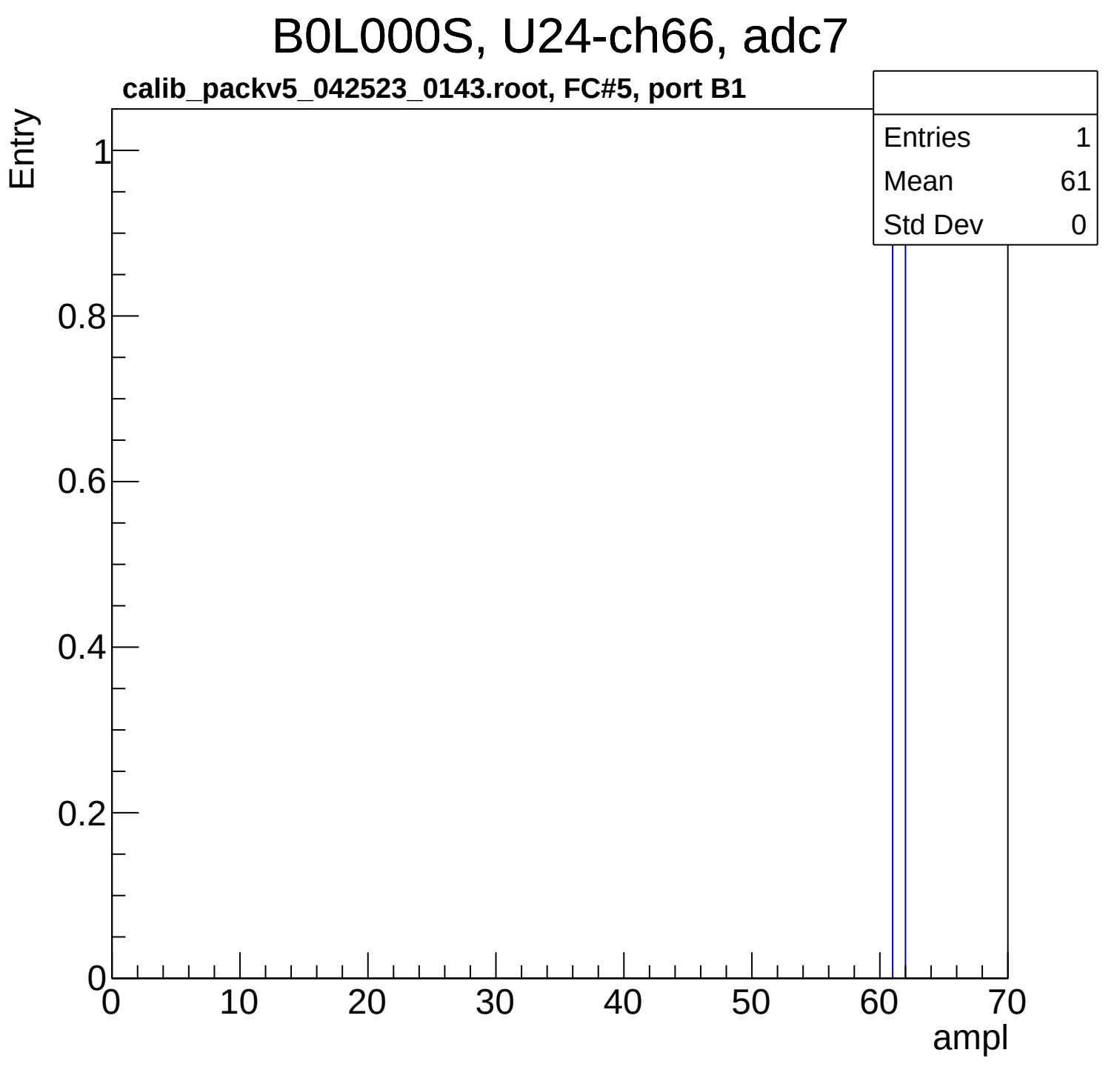

B0L000S, U24-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 43.75
Std Dev 3.555
Gaus mean : 44.4058
Gaus Width: 3.8962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

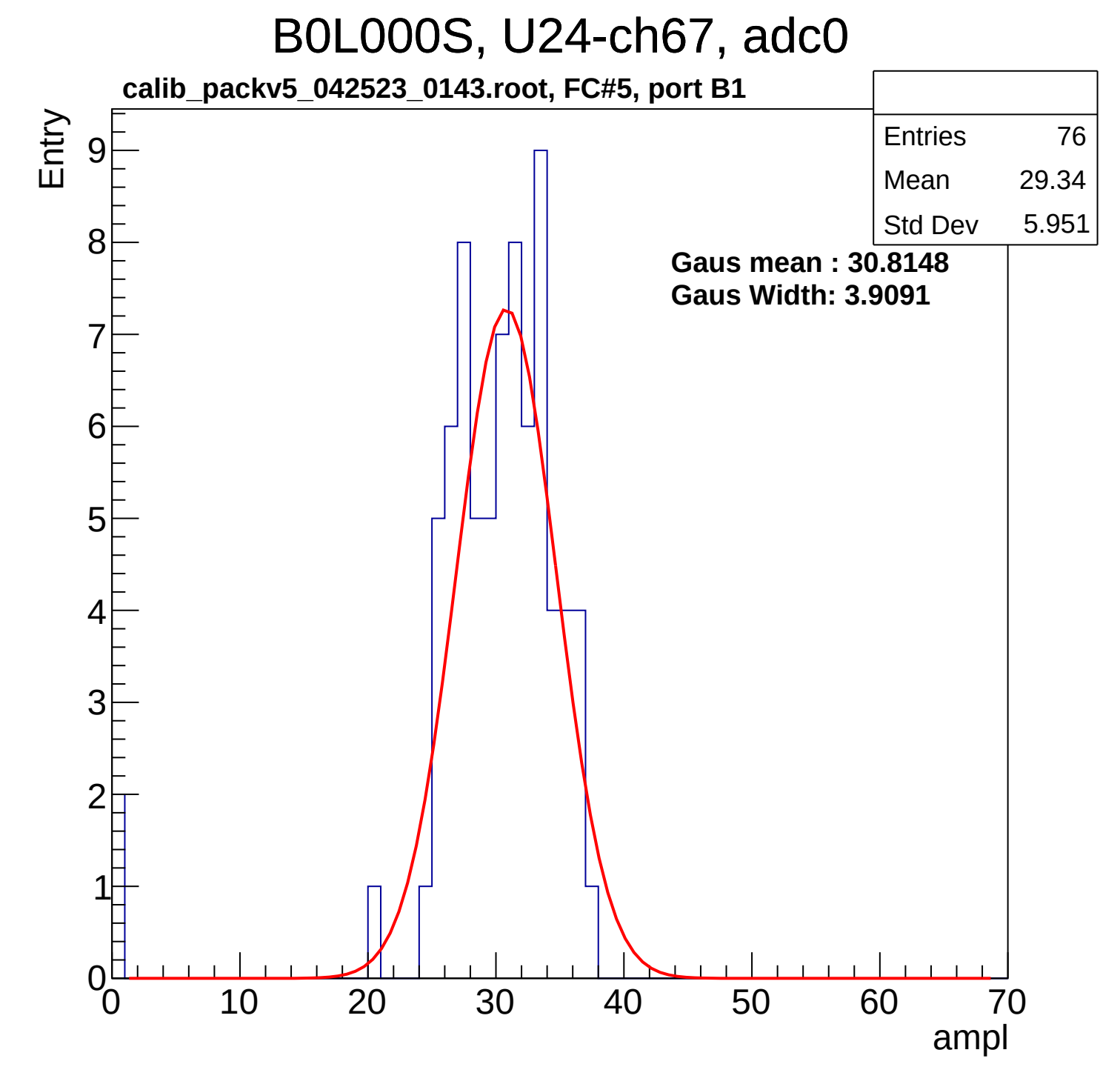

B0L000S, U24-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.83
Std Dev 3.67
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

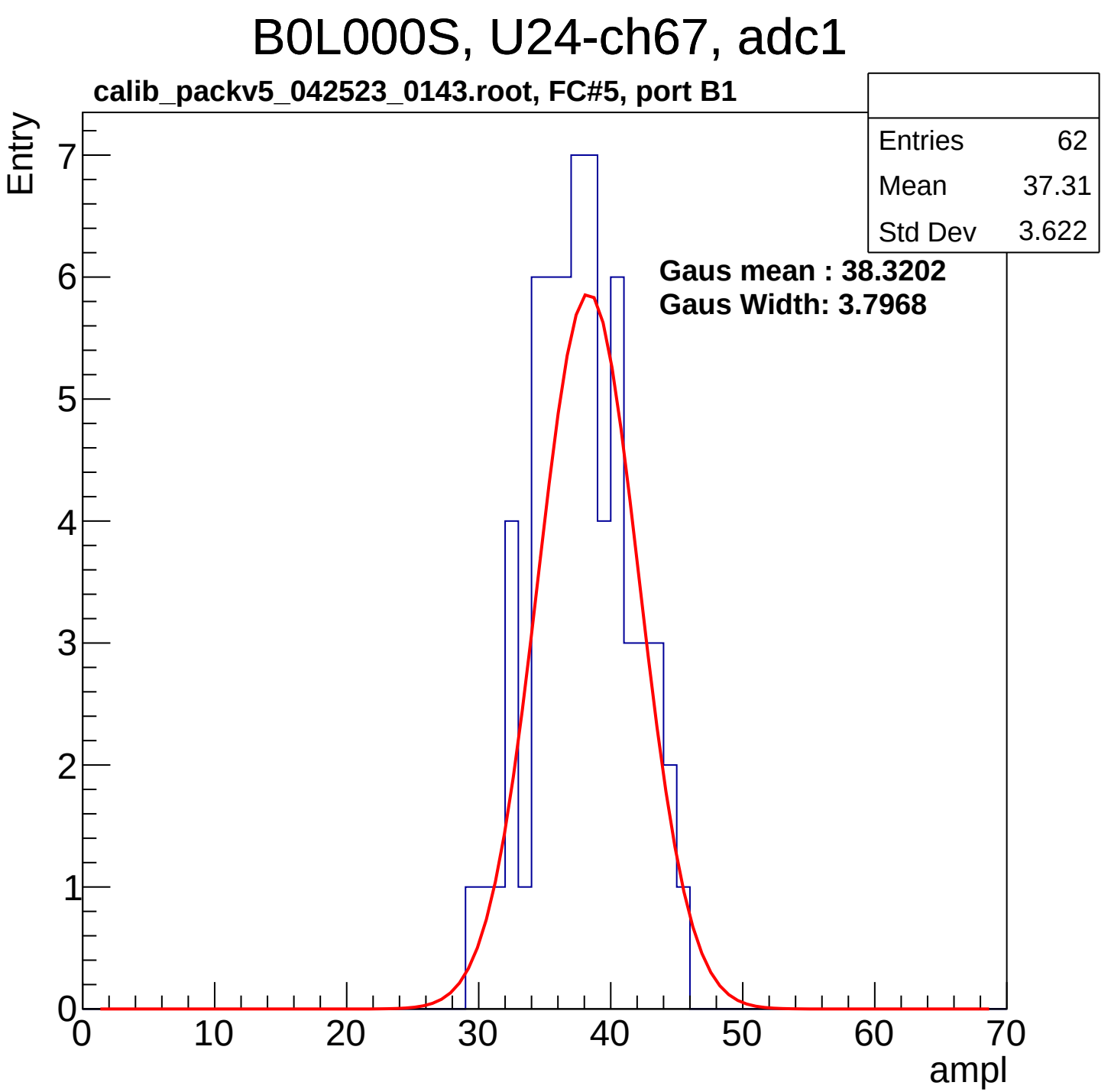

B0L000S, U24-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.61
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

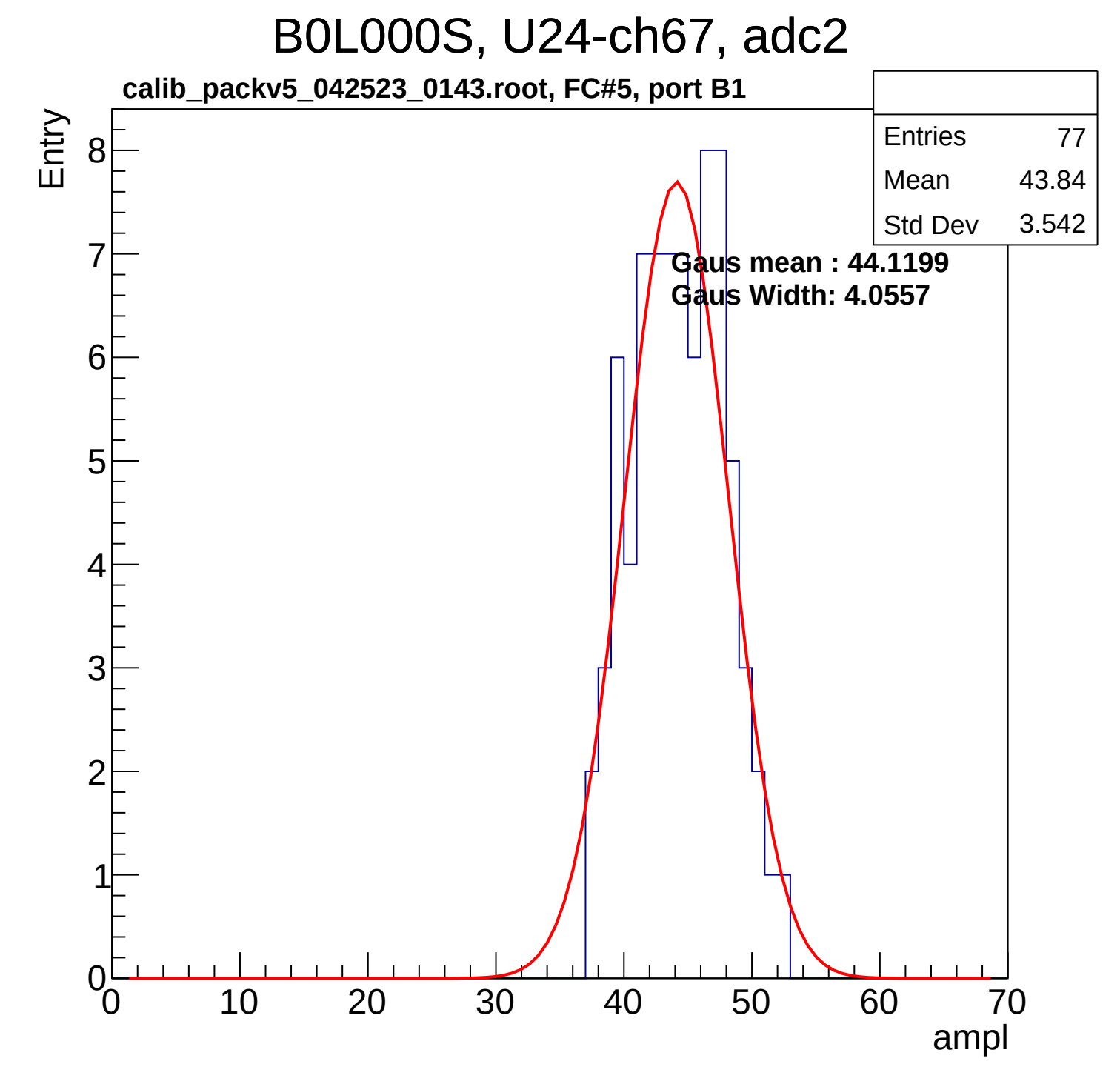

B0L000S, U24-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.07
Std Dev 9.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

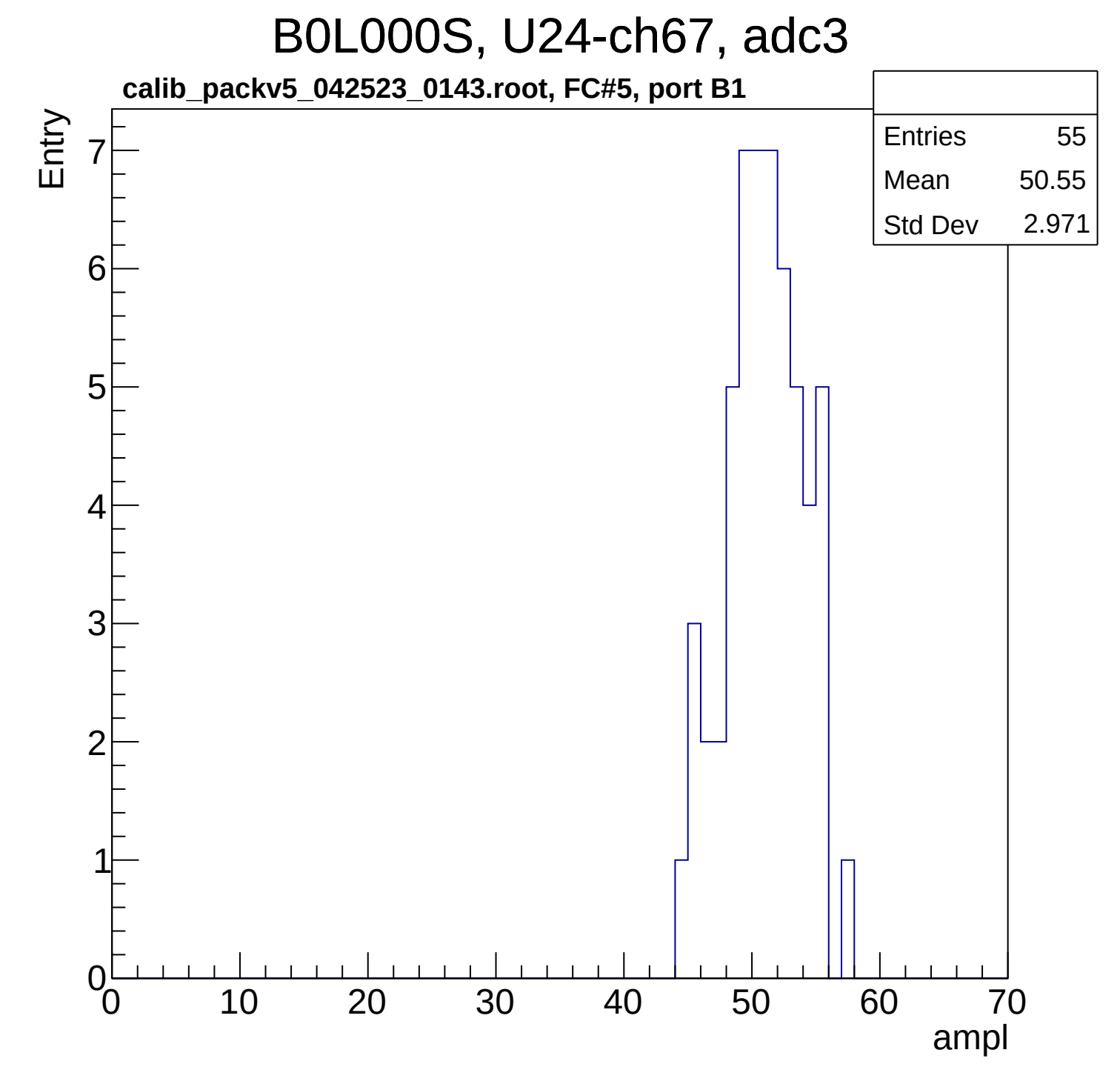

B0L000S, U24-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

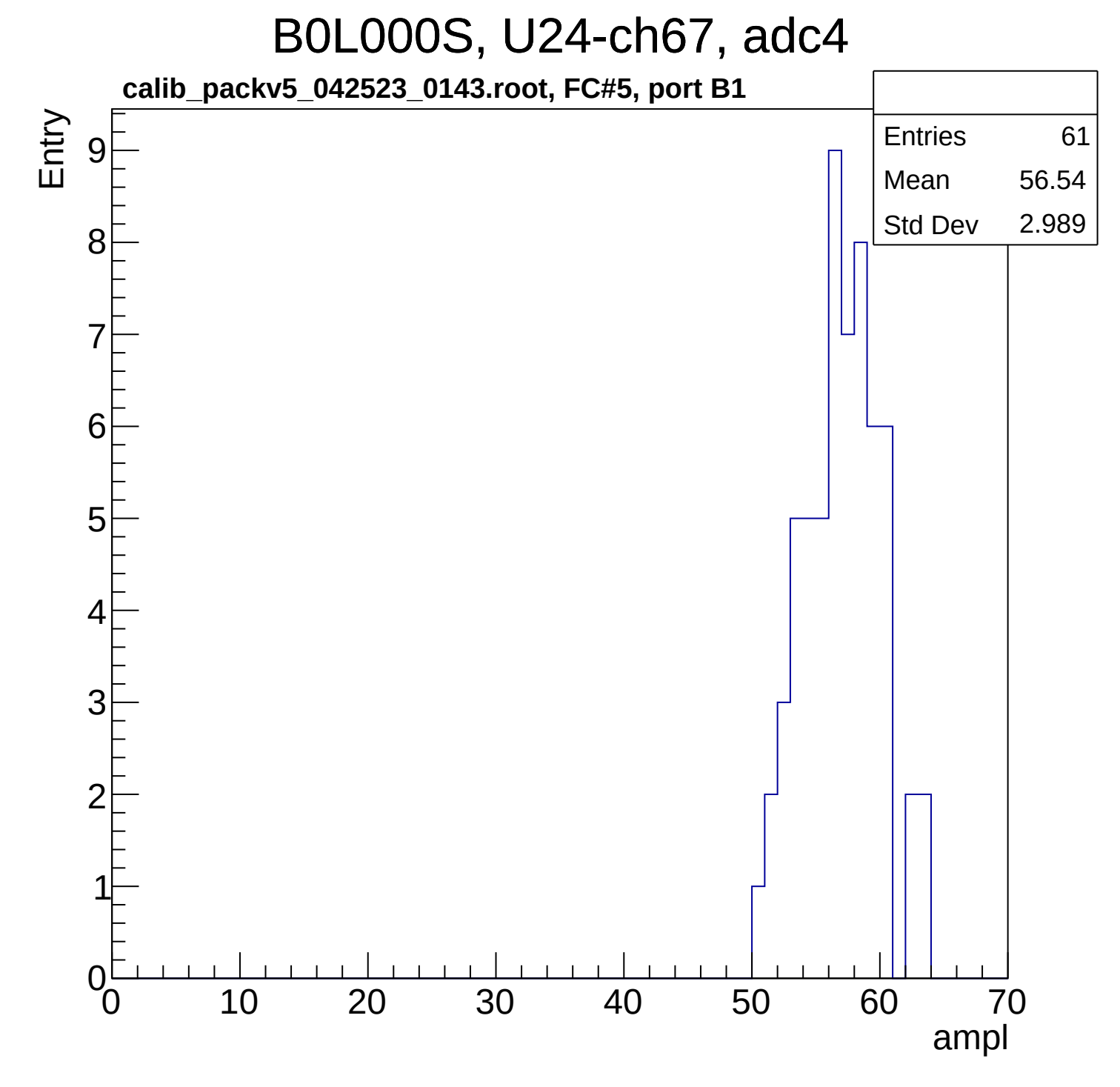

B0L000S, U24-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

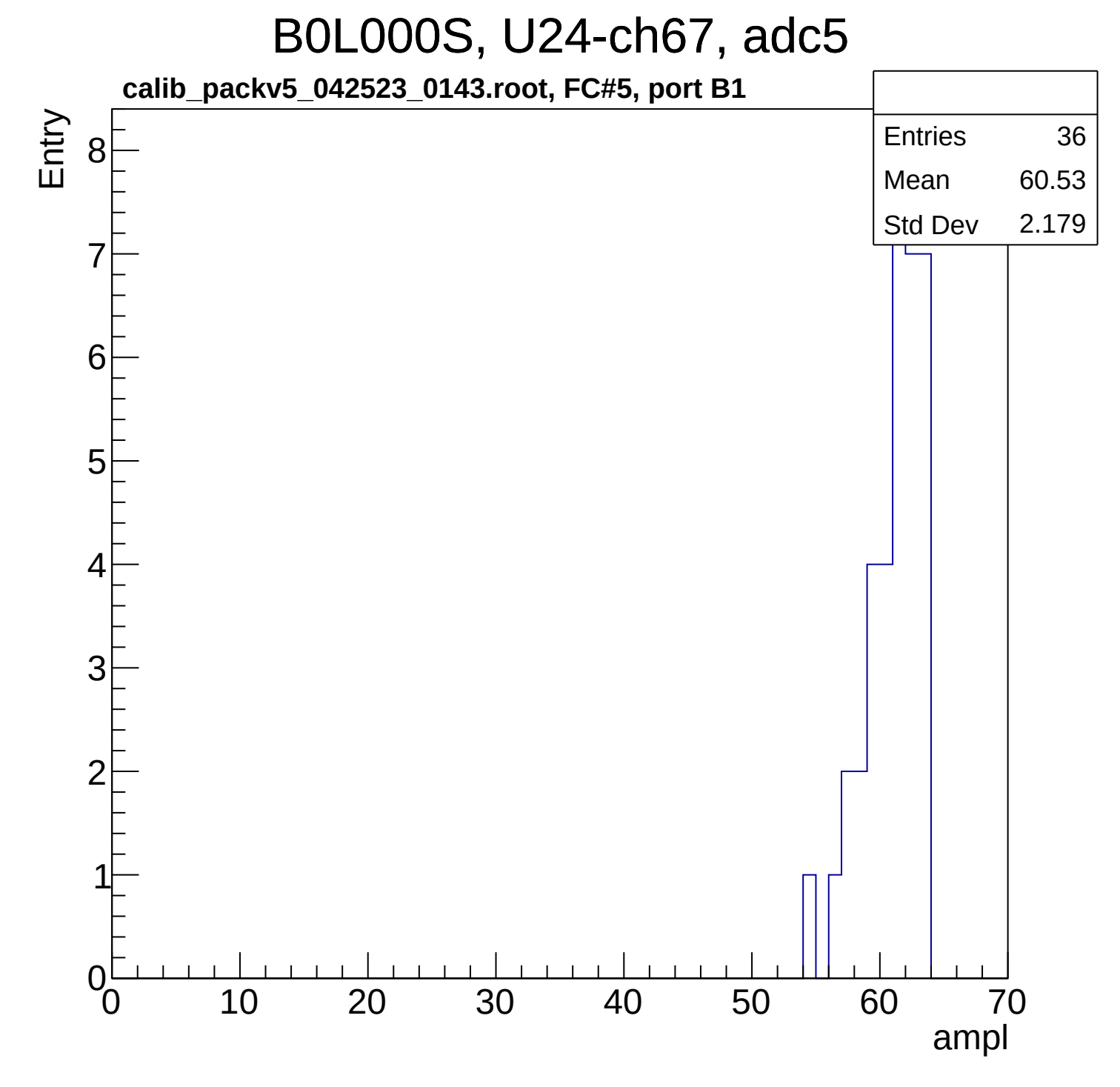

B0L000S, U24-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.34
Std Dev 5.951
Gaus mean : 30.8148
Gaus Width: 3.9091
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

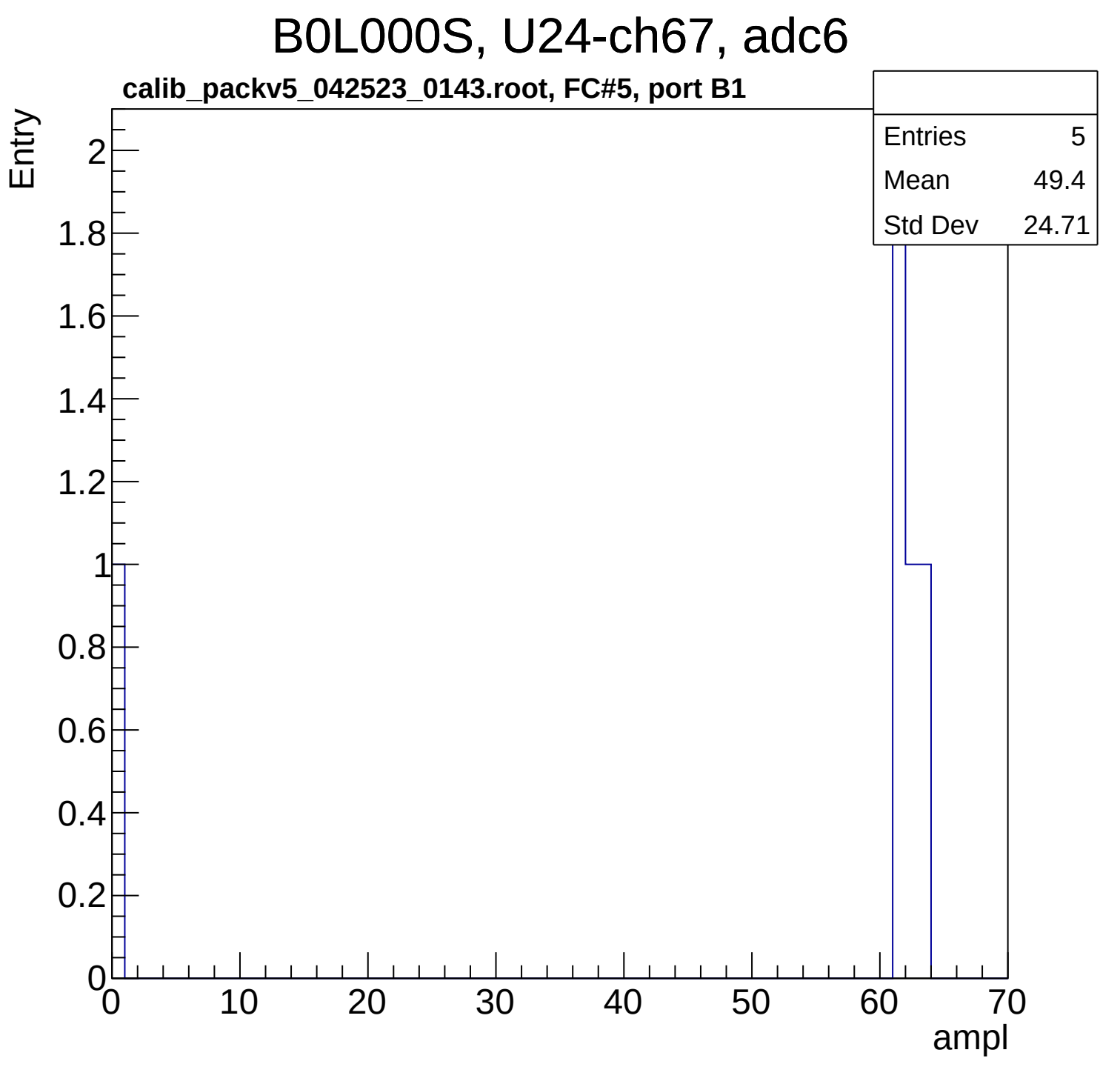

B0L000S, U24-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.31
Std Dev 3.622
Gaus mean : 38.3202
Gaus Width: 3.7968
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

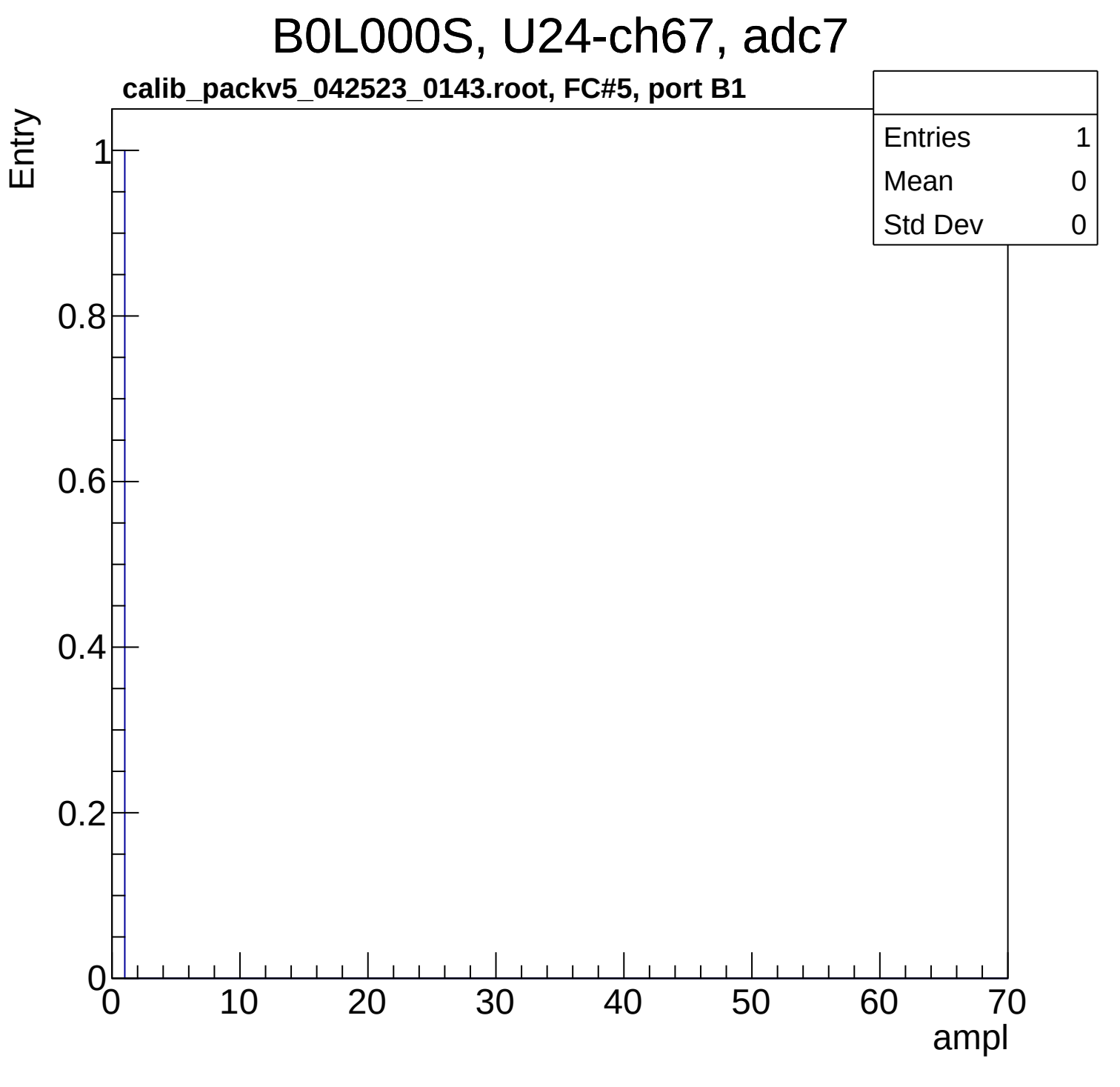

B0L000S, U24-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 43.84
Std Dev 3.542
Gaus mean : 44.1199
Gaus Width: 4.0557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

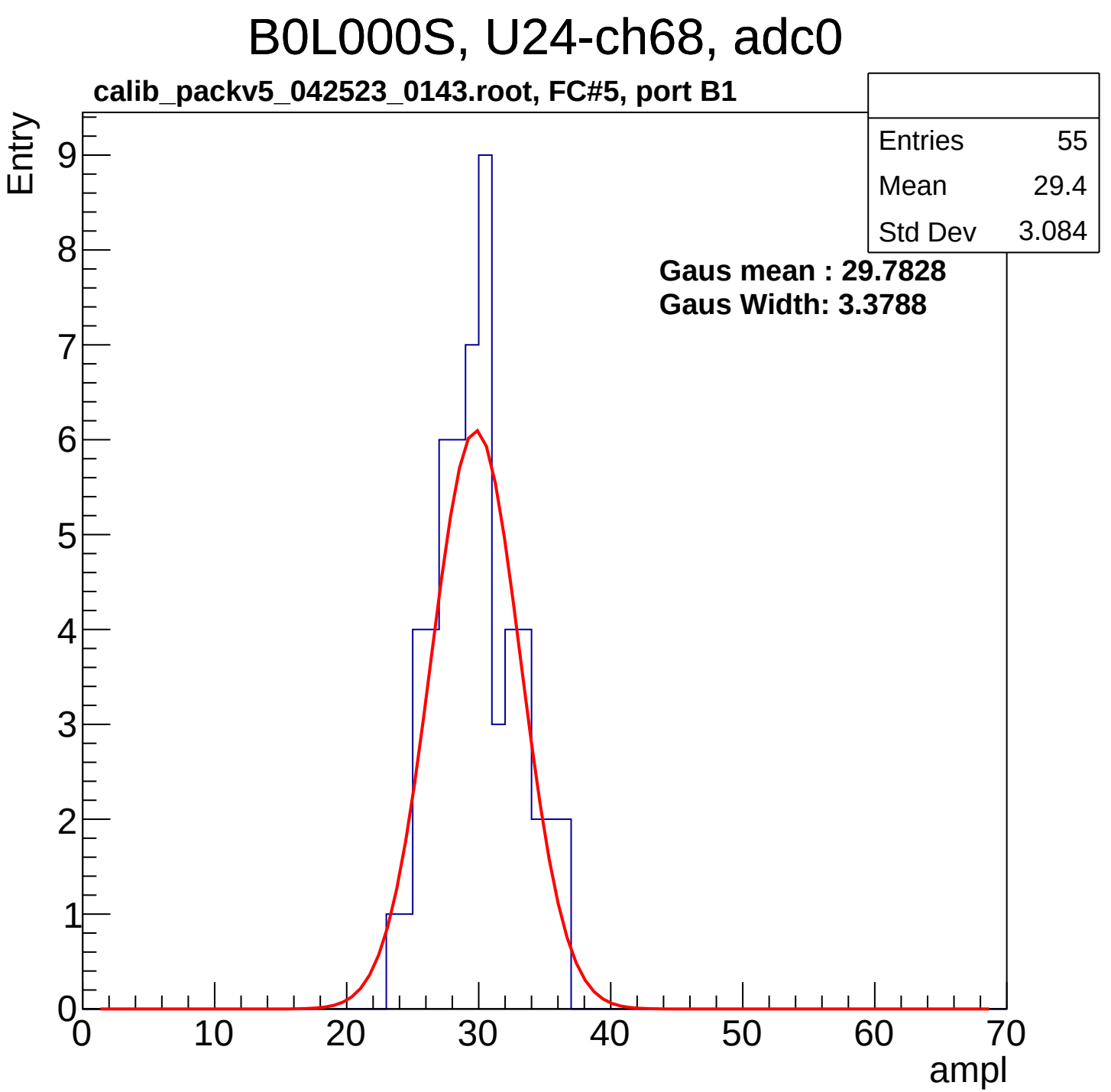

B0L000S, U24-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 50.55
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

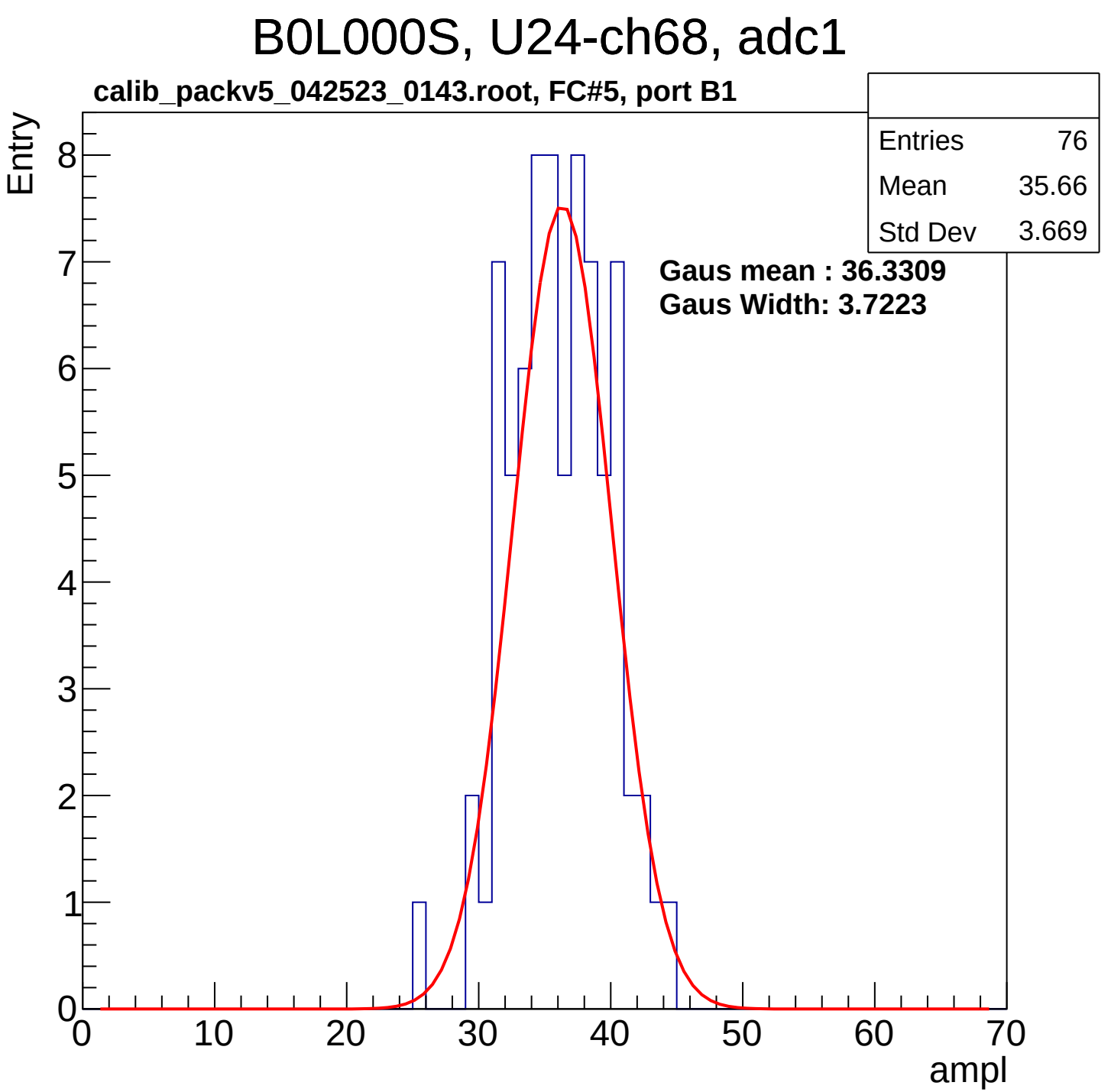

B0L000S, U24-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.54
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

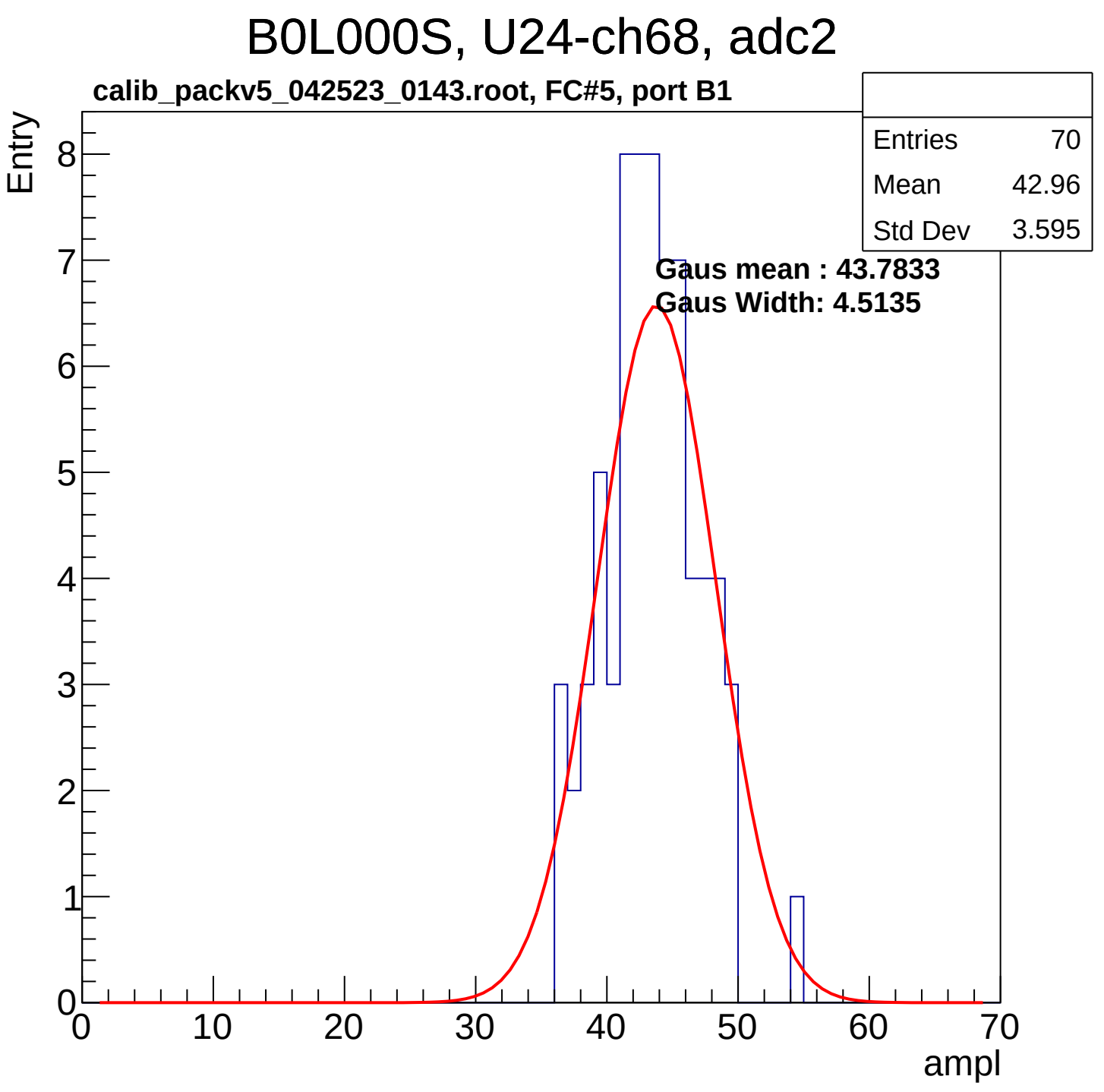

B0L000S, U24-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 60.53
Std Dev 2.179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

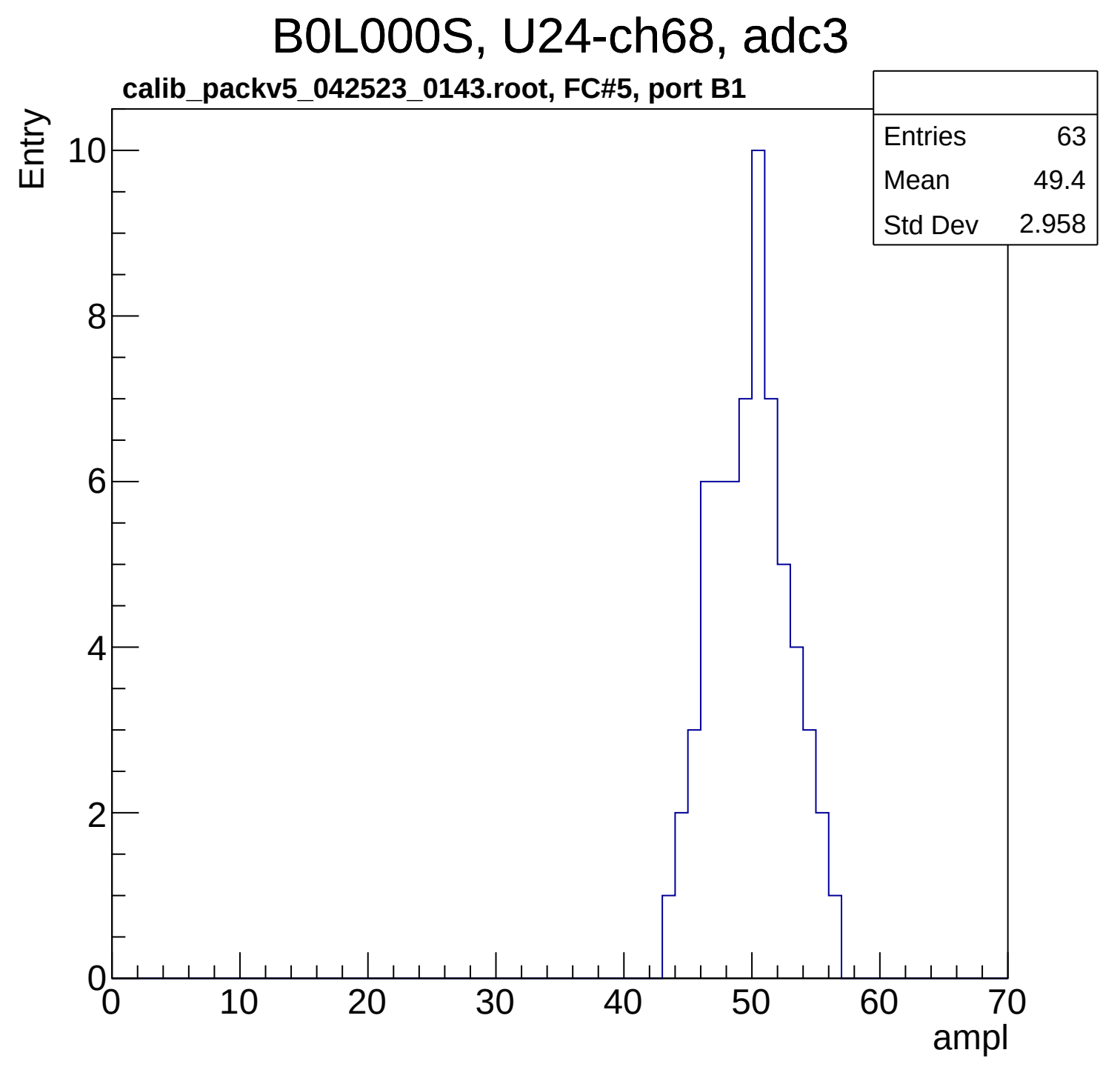

B0L000S, U24-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.4
Std Dev 24.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

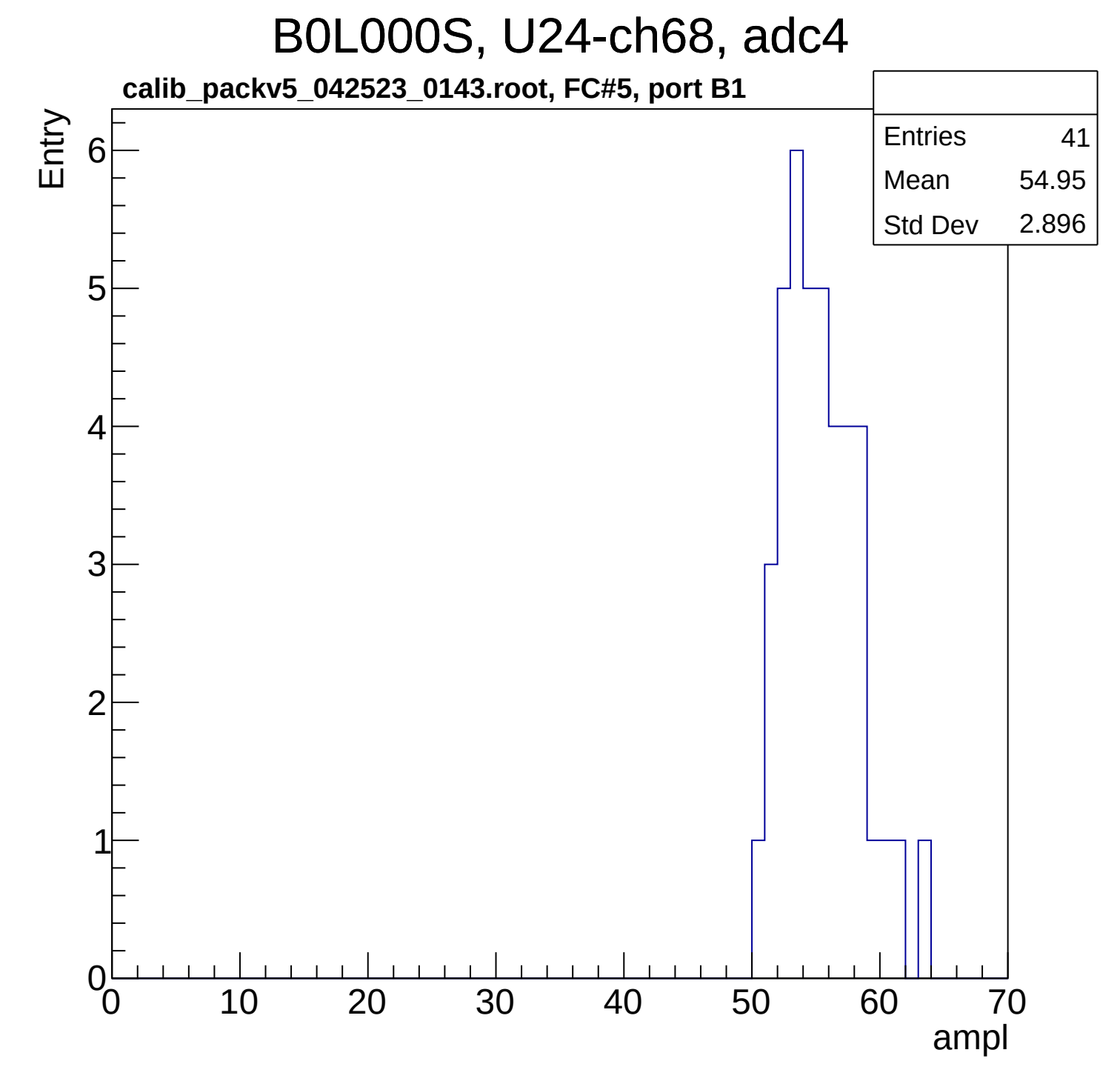

B0L000S, U24-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

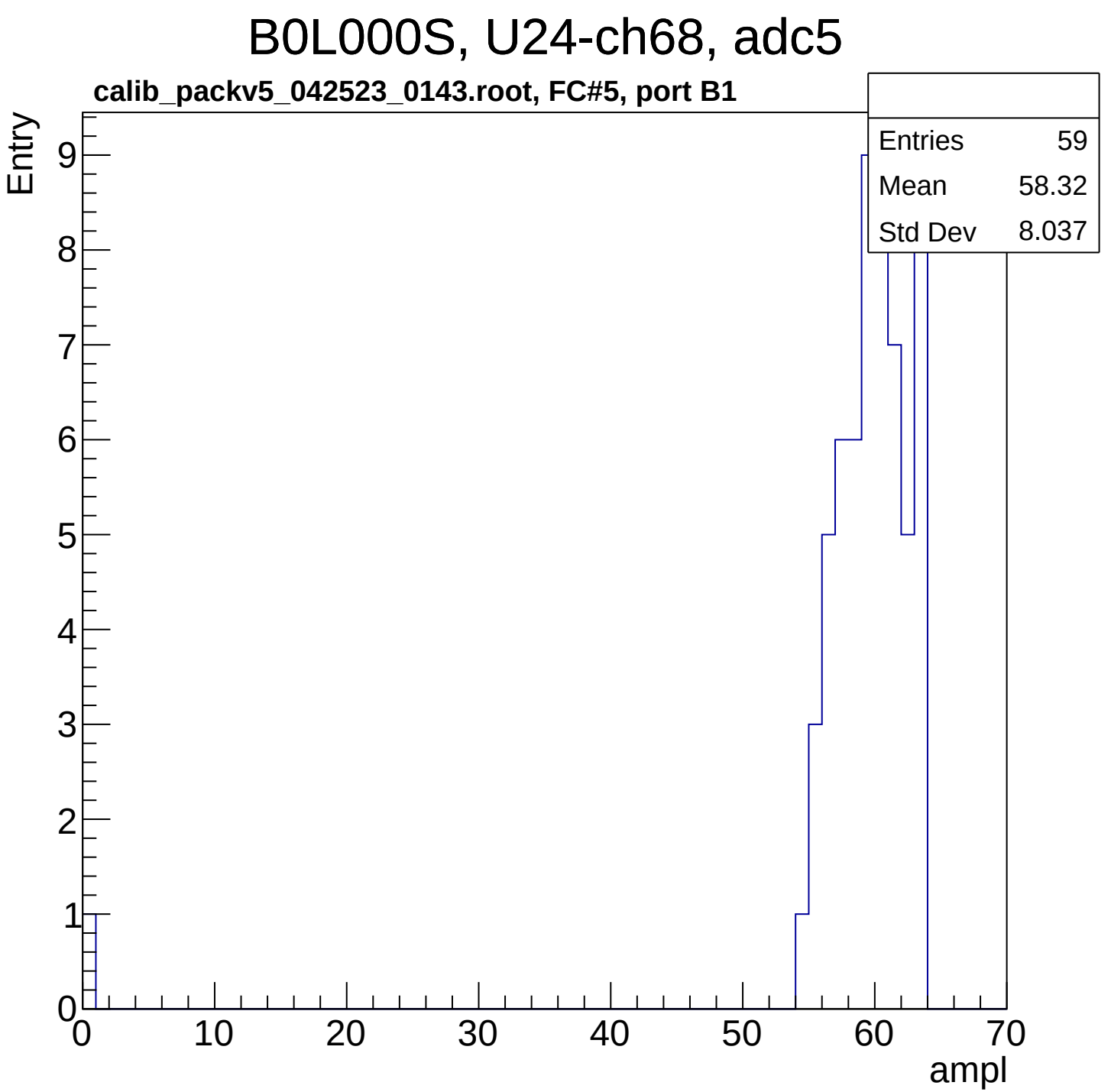

B0L000S, U24-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 29.4
Std Dev 3.084
Gaus mean : 29.7828
Gaus Width: 3.3788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

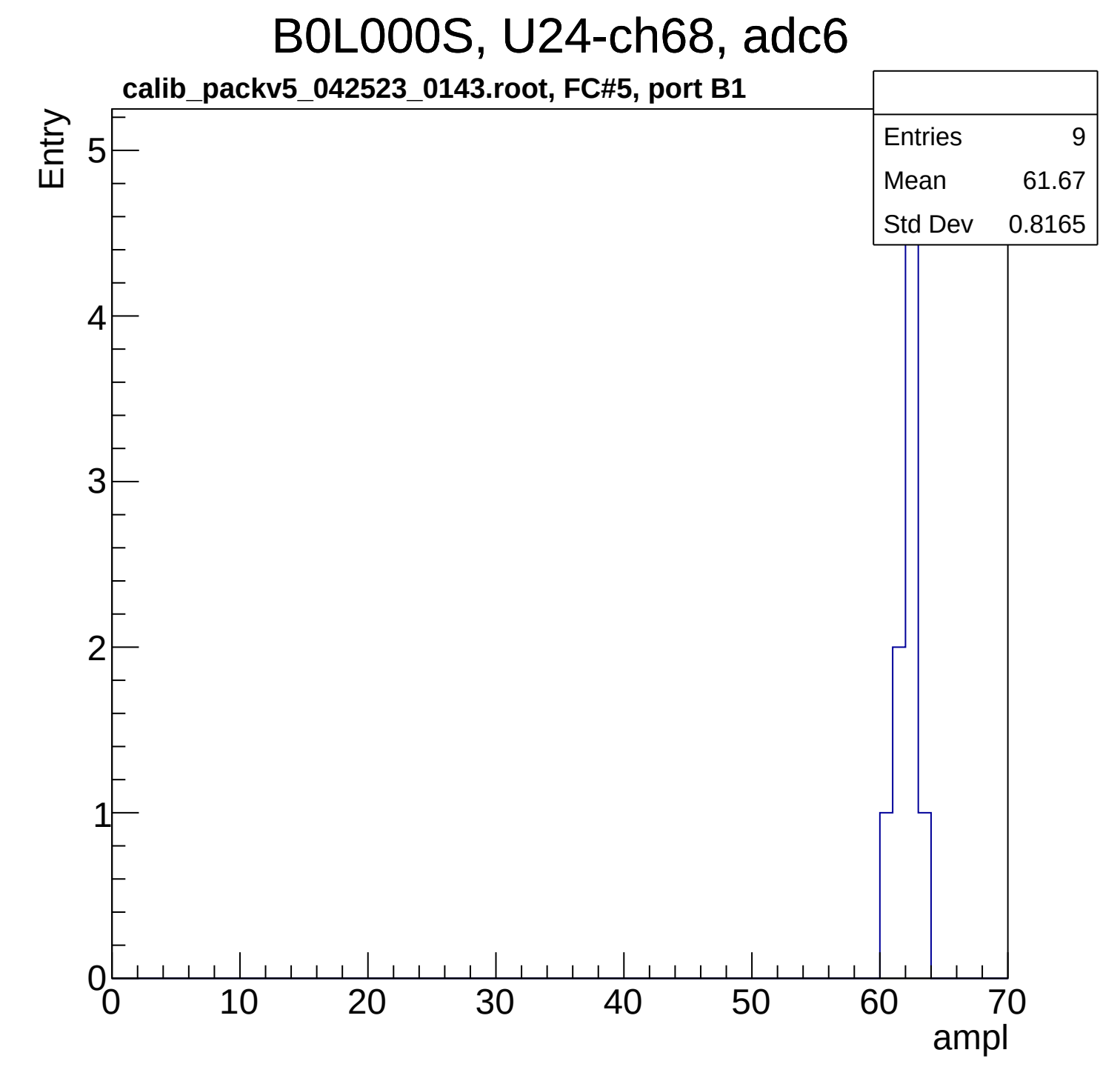

B0L000S, U24-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.66
Std Dev 3.669
Gaus mean : 36.3309
Gaus Width: 3.7223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

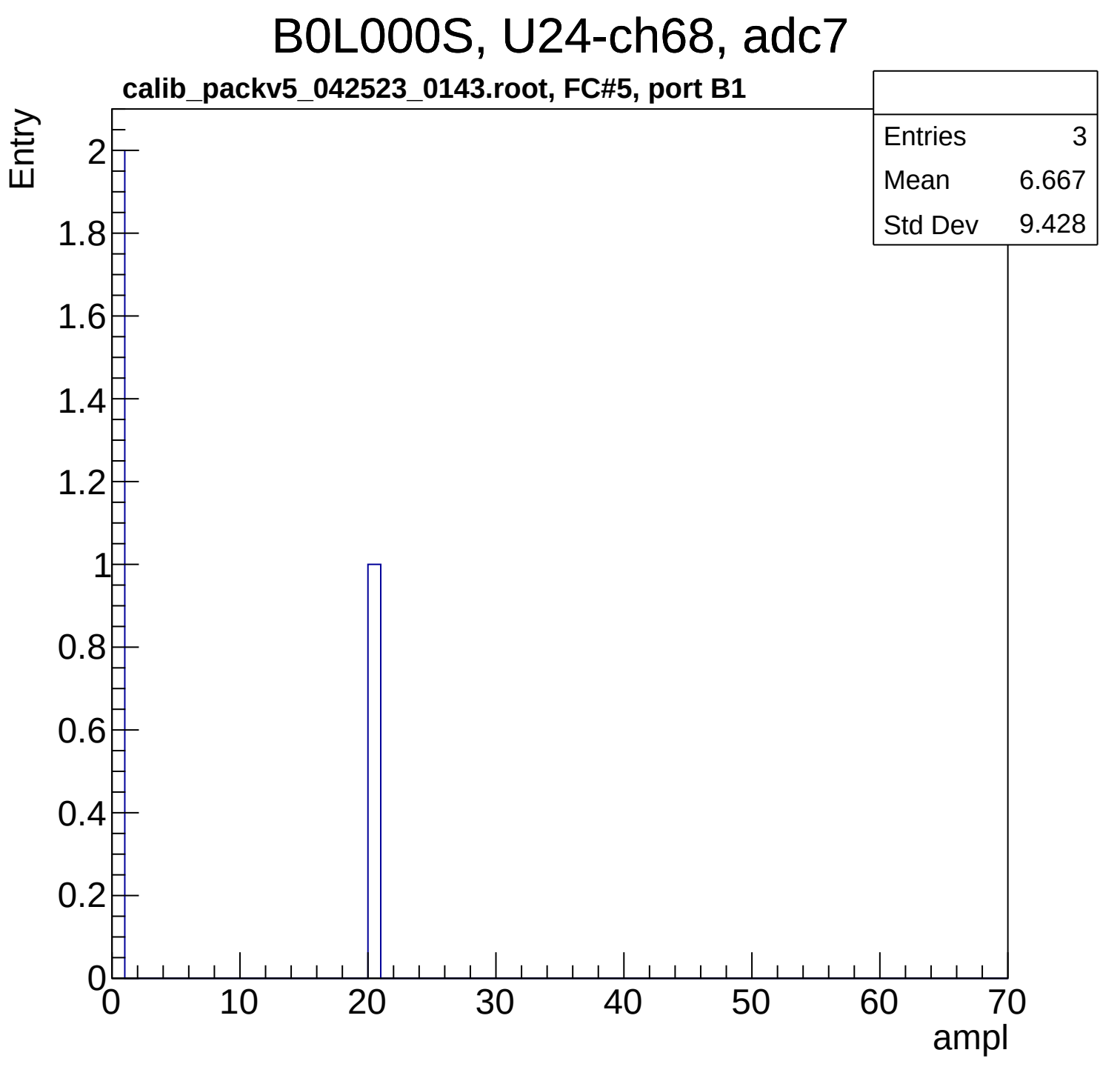

B0L000S, U24-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.96
Std Dev 3.595
Gaus mean : 43.7833
Gaus Width: 4.5135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

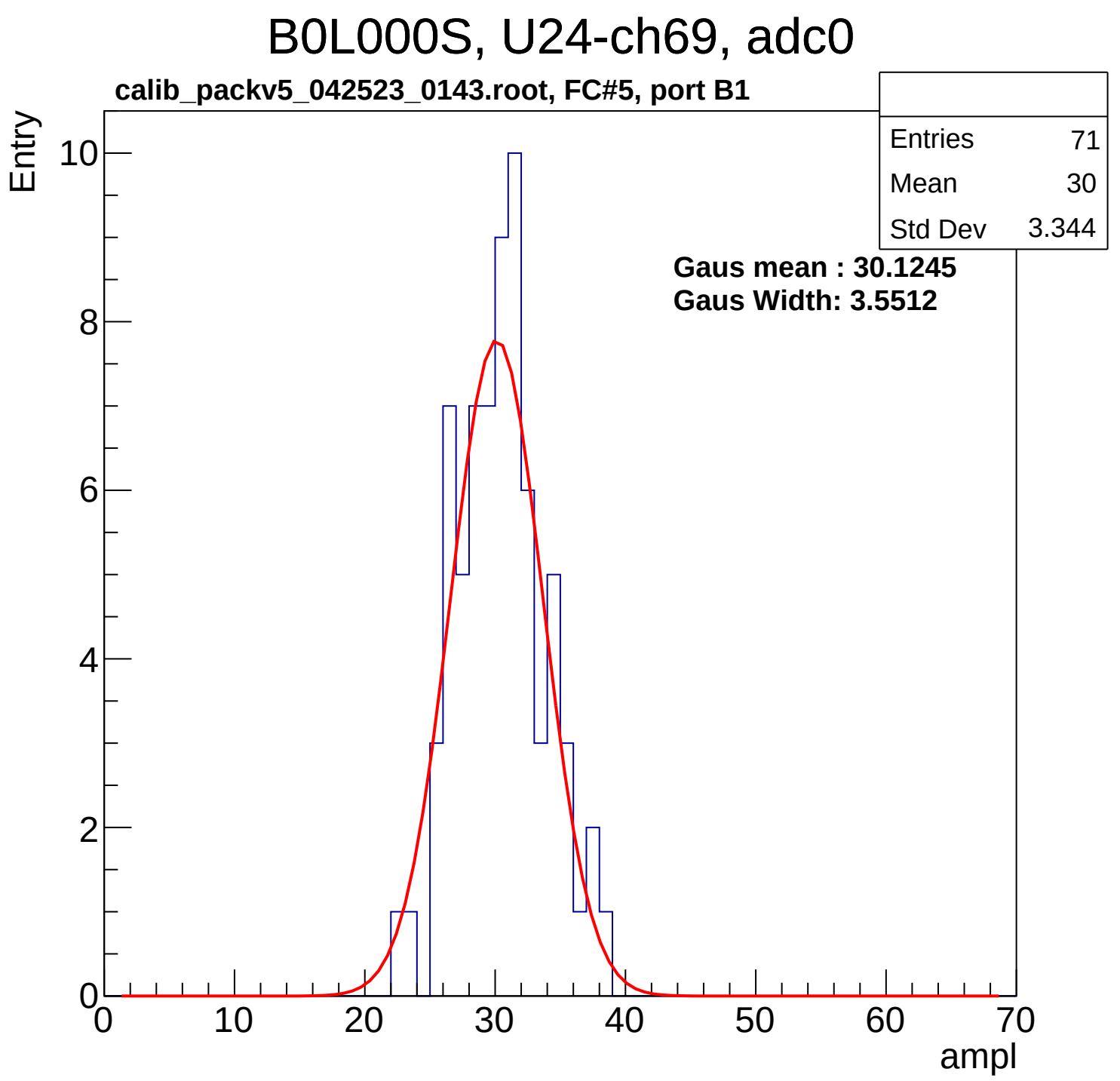

B0L000S, U24-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.4
Std Dev 2.958
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

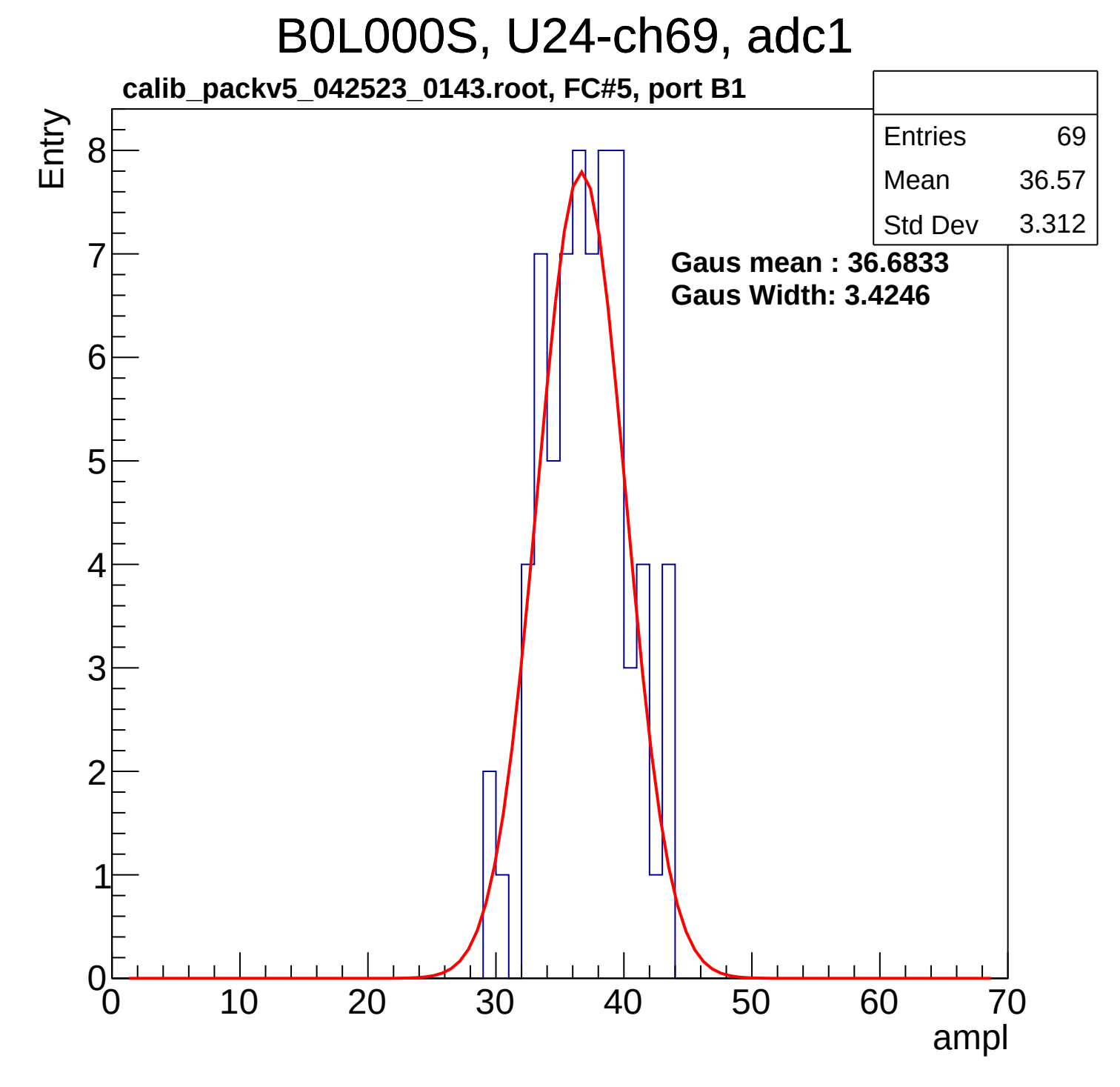

B0L000S, U24-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 54.95
Std Dev 2.896
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

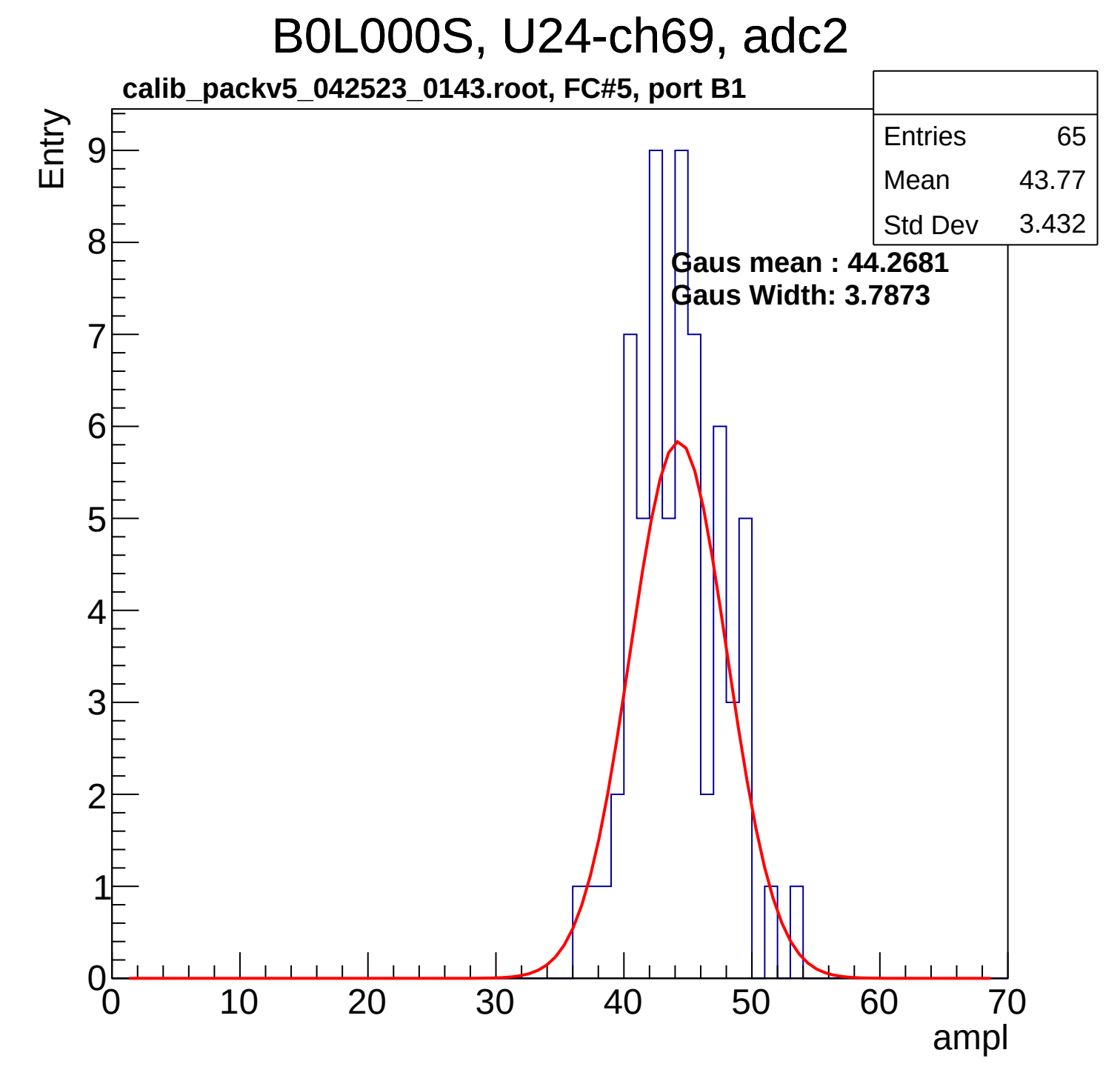

B0L000S, U24-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 58.32
Std Dev 8.037
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

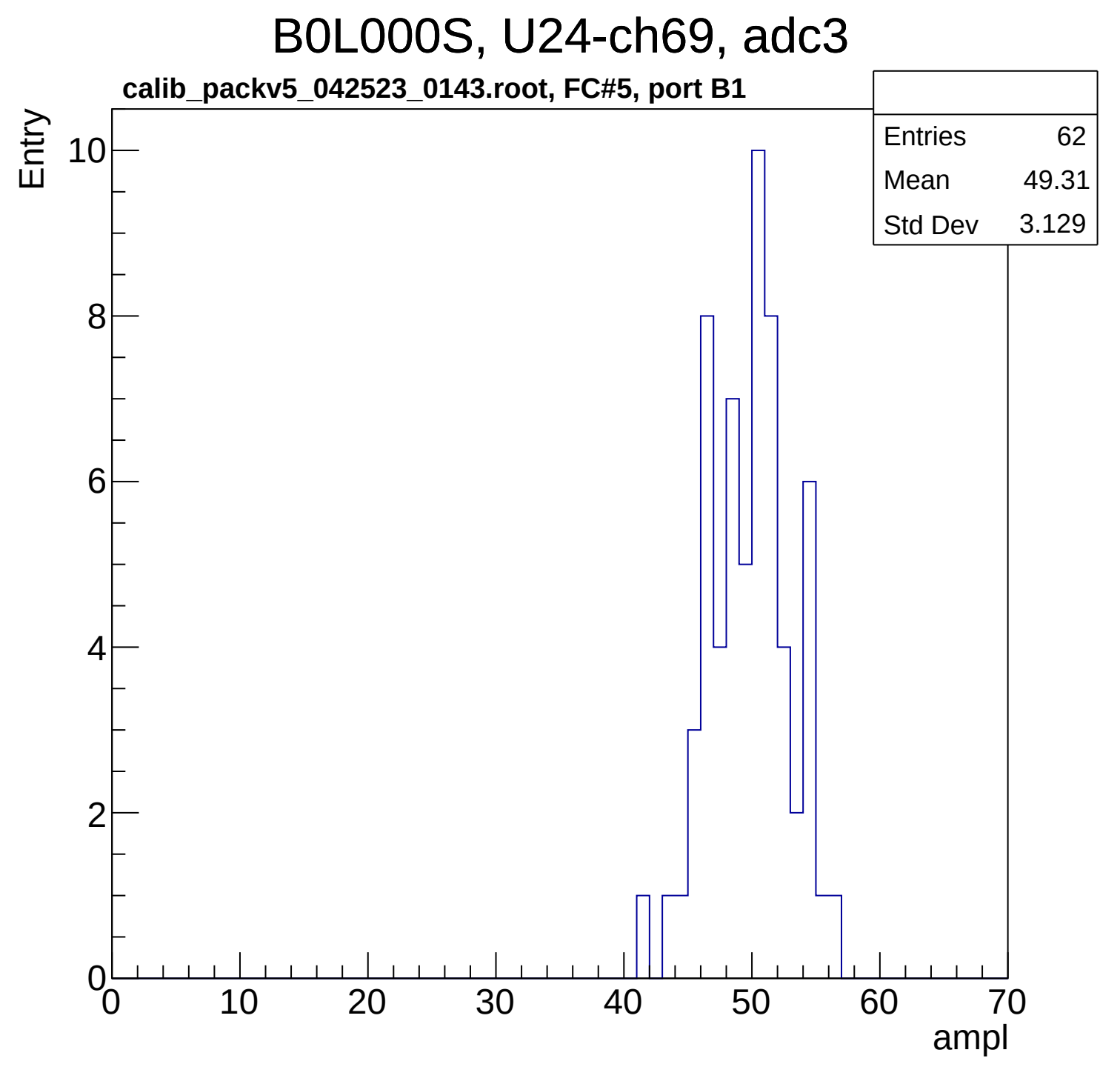

B0L000S, U24-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.67
Std Dev 0.8165
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30
Std Dev 3.344
Gaus mean : 30.1245
Gaus Width: 3.5512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.57
Std Dev 3.312
Gaus mean : 36.6833
Gaus Width: 3.4246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.77
Std Dev 3.432
Gaus mean : 44.2681
Gaus Width: 3.7873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.31
Std Dev 3.129
Entry
ampl
0
2
4
6
8
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0
10
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40
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60
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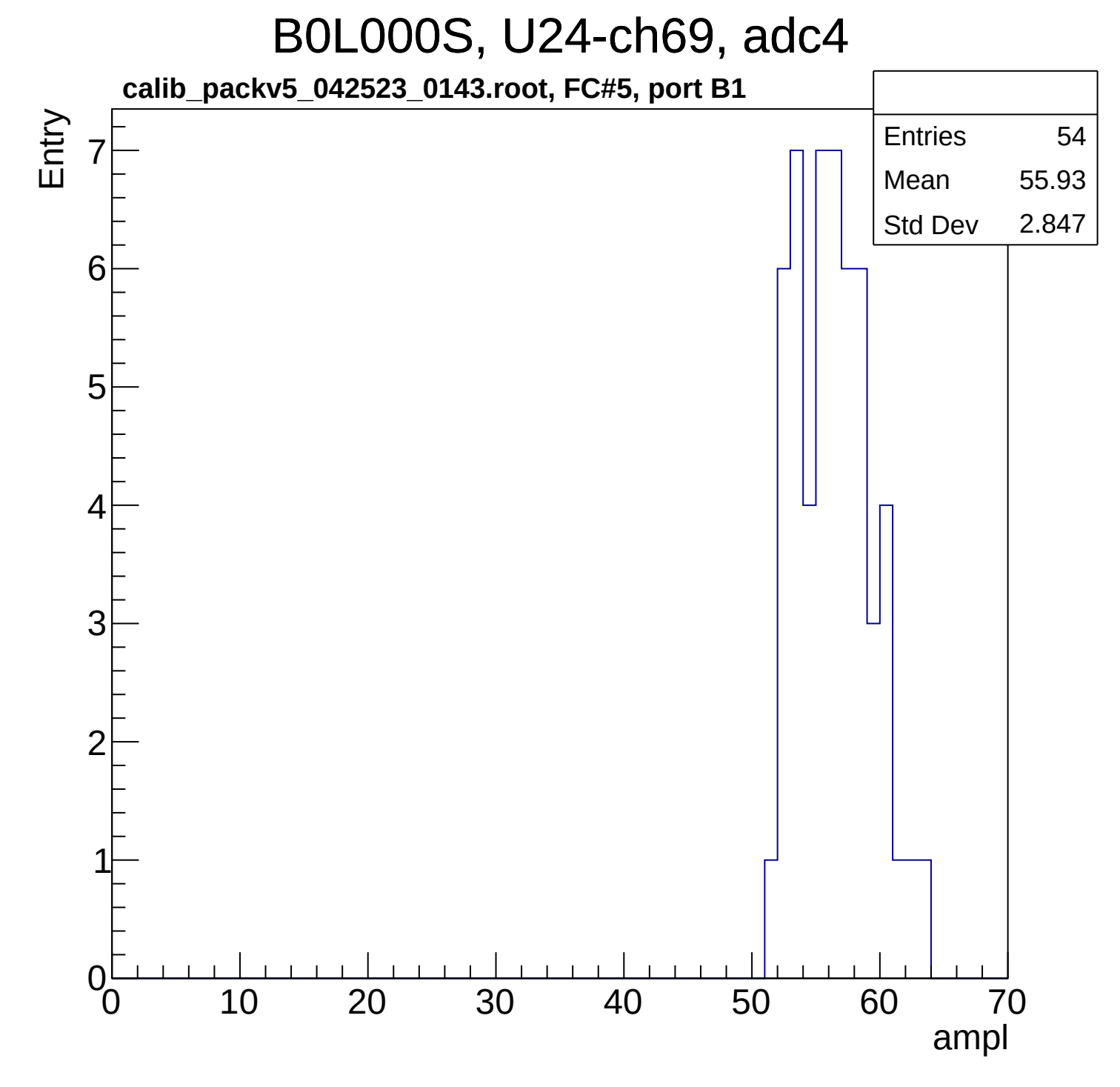
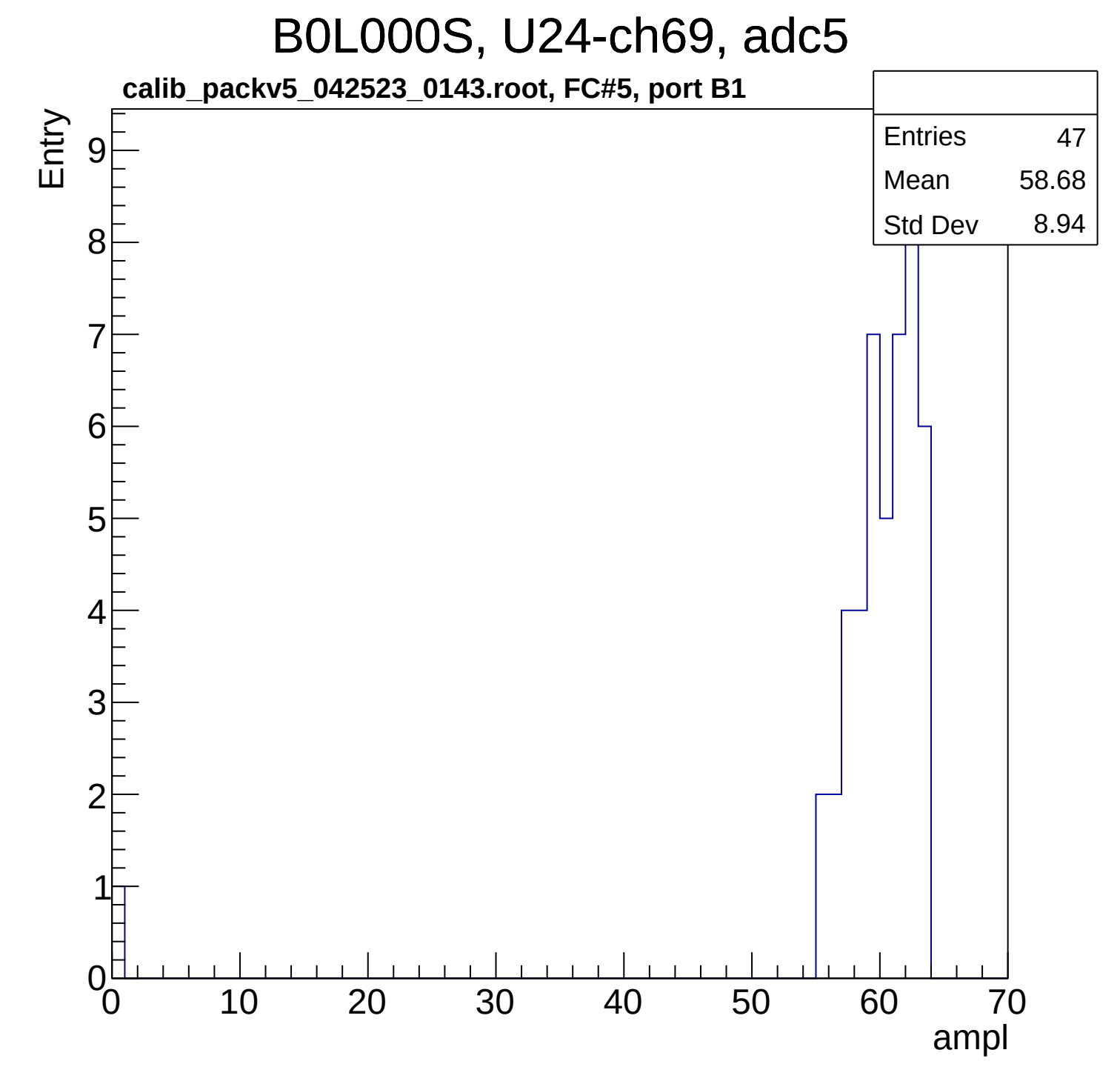

B0L000S, U24-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.93
Std Dev 2.847
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.68
Std Dev 8.94
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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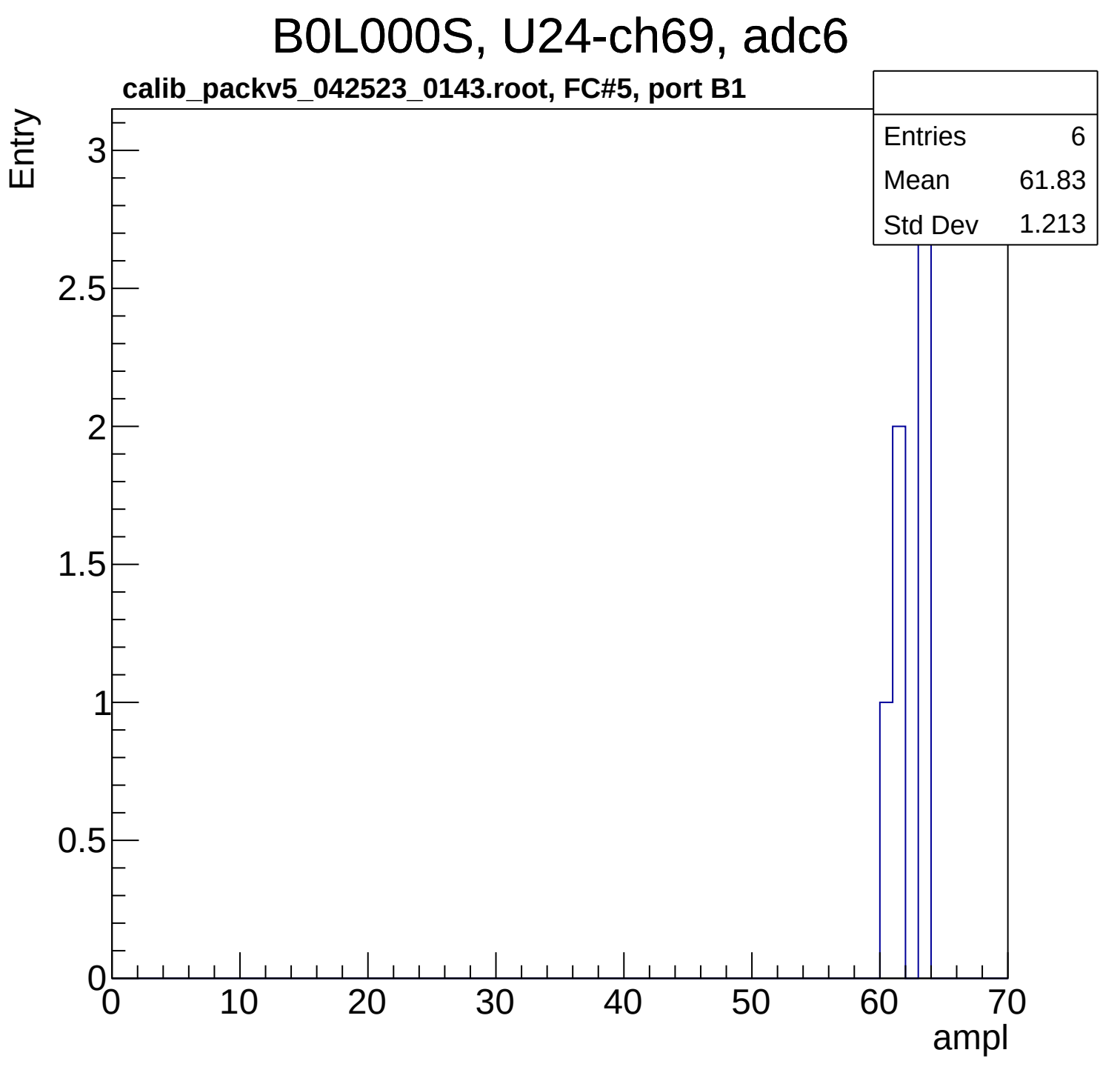
B0L000S, U24-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.83
Std Dev 1.213
Entry
ampl
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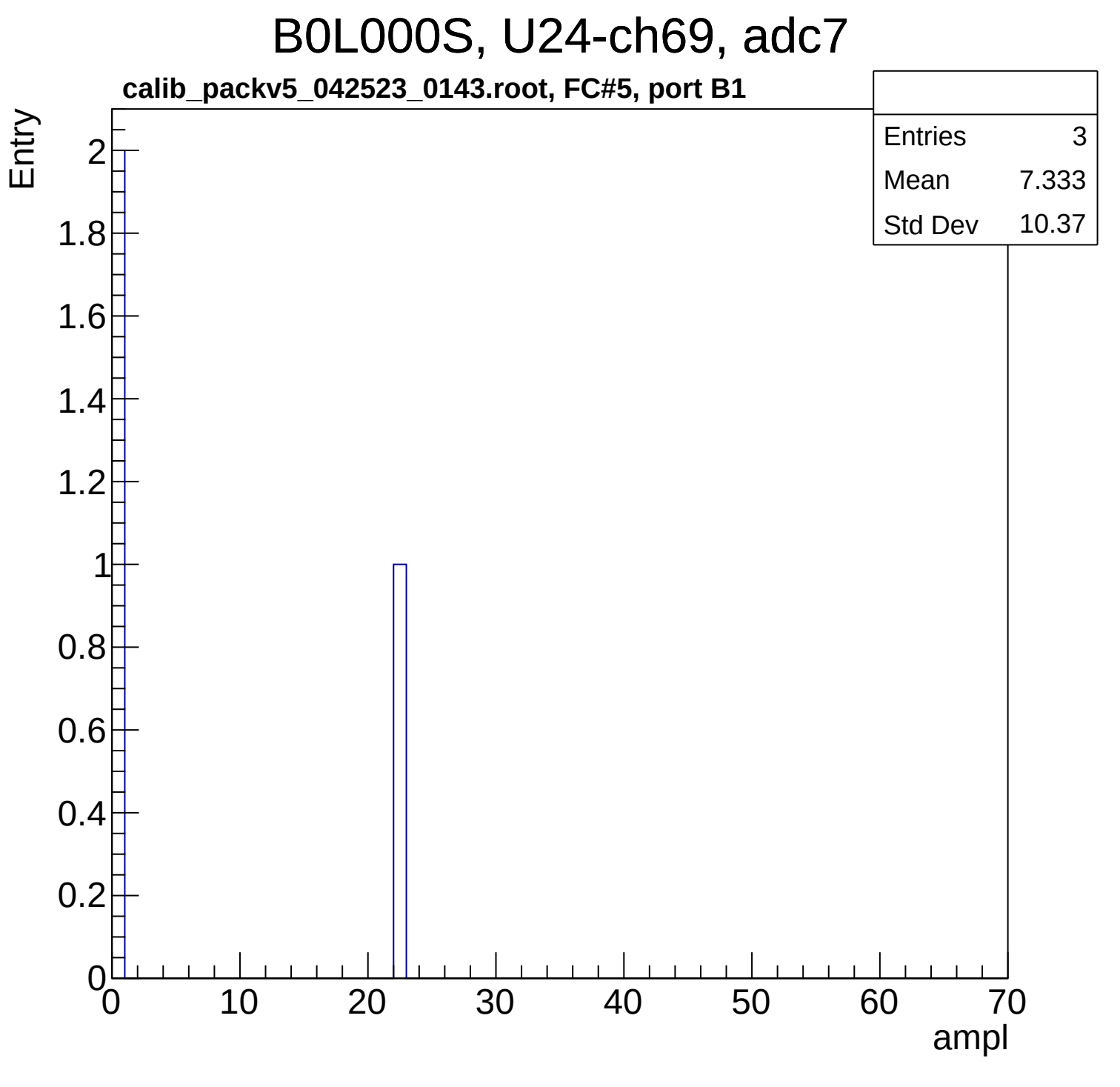
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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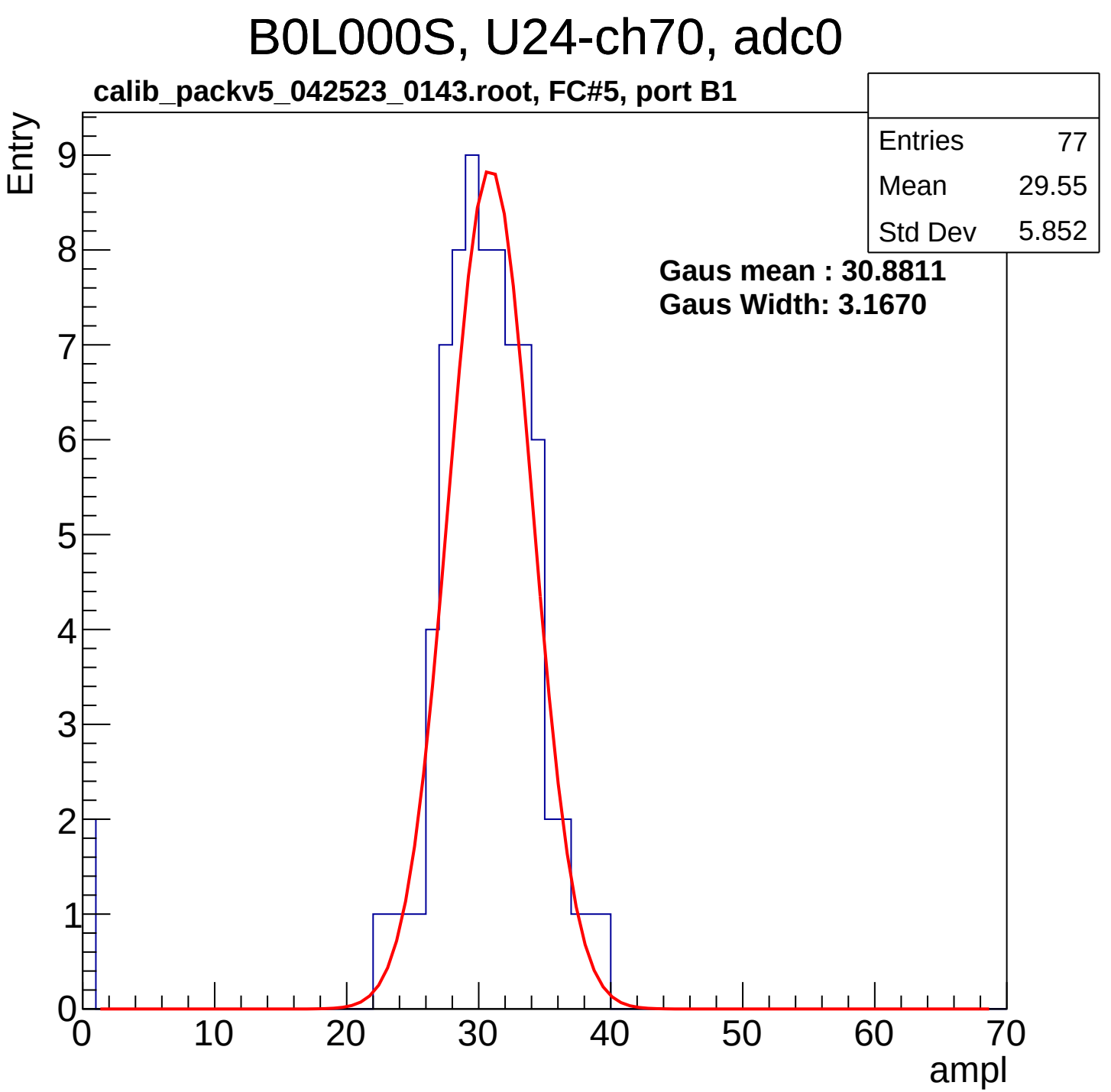
B0L000S, U24-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.55
Std Dev 5.852
Gaus mean : 30.8811
Gaus Width: 3.1670
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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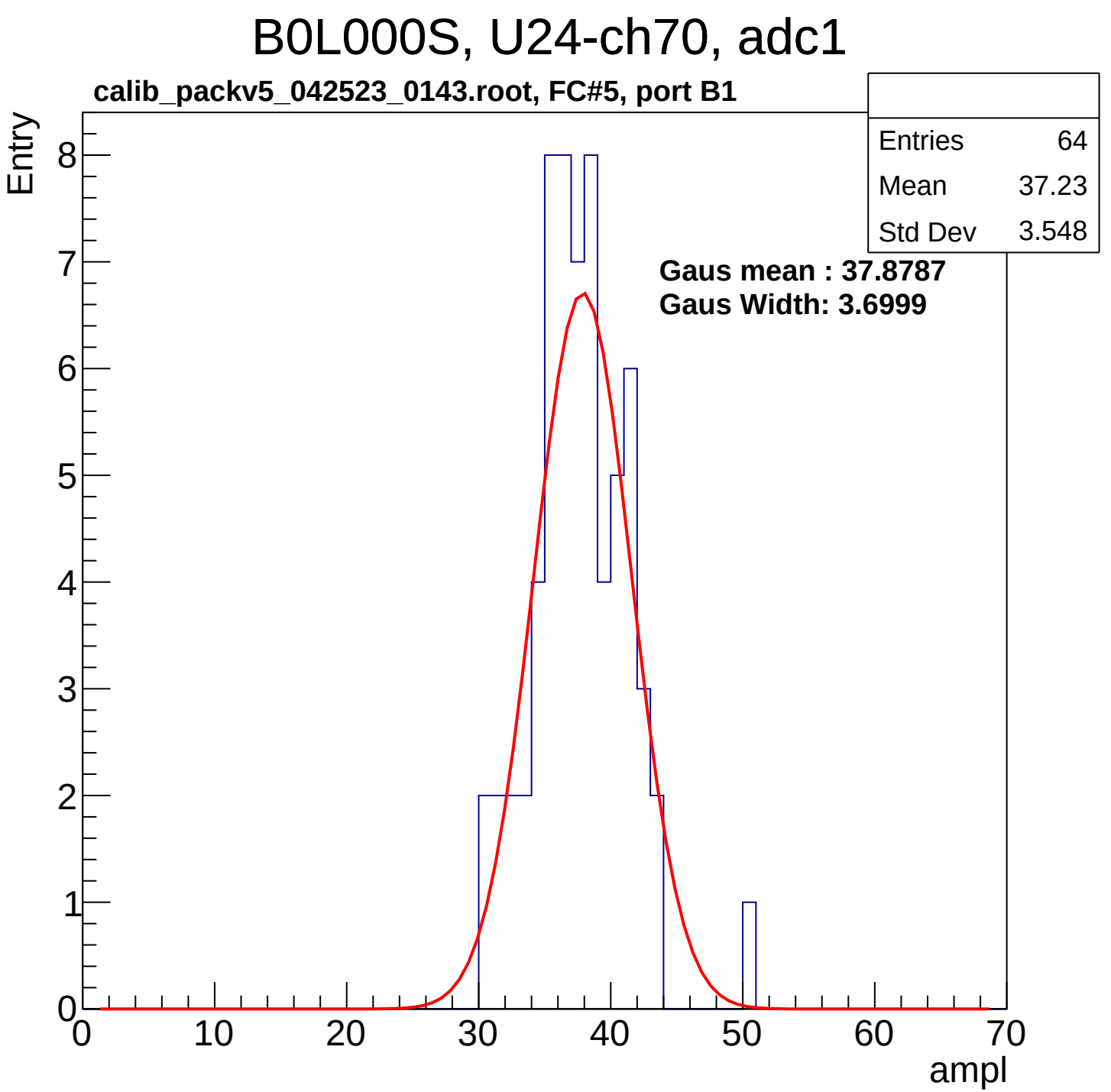
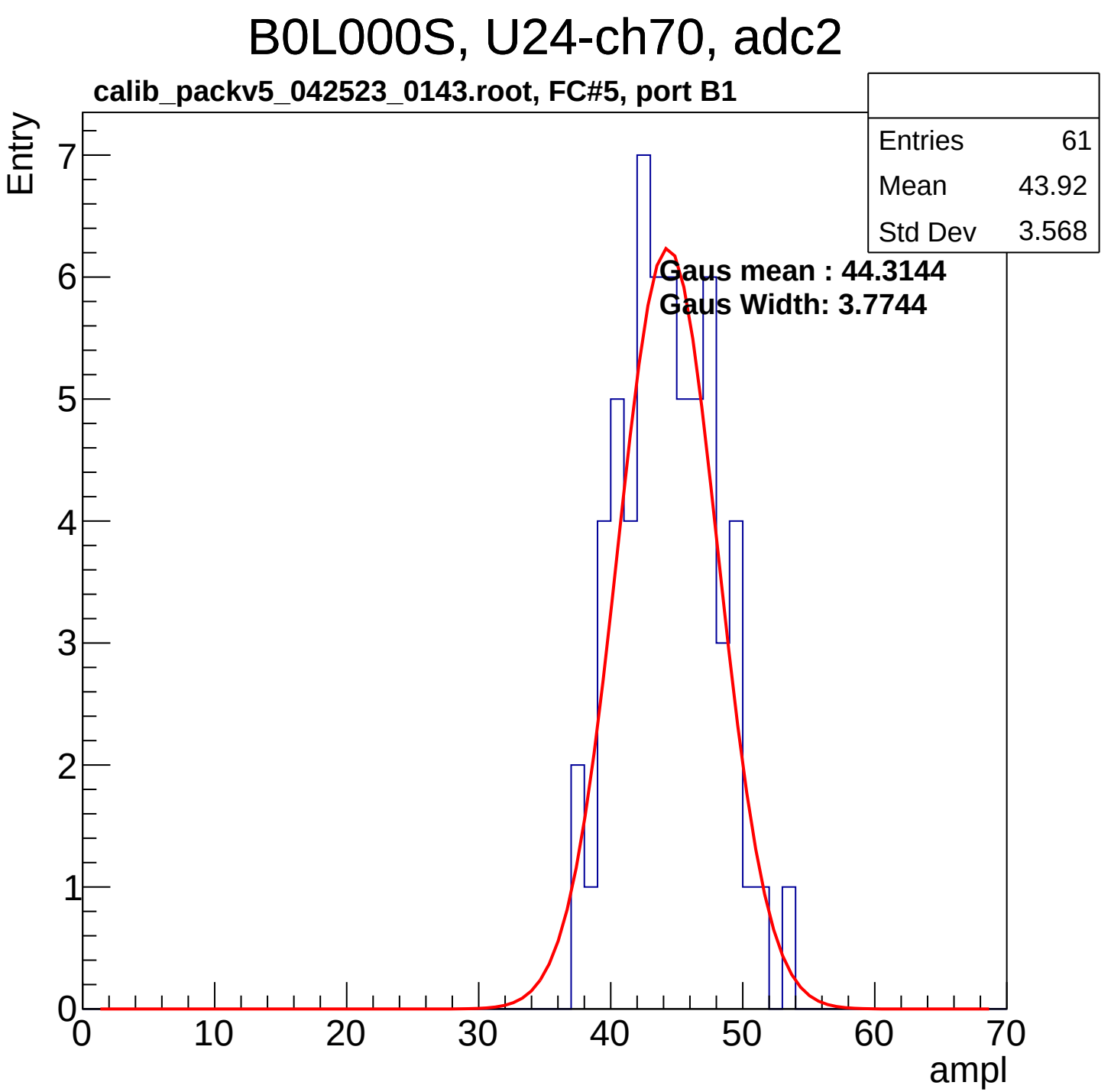
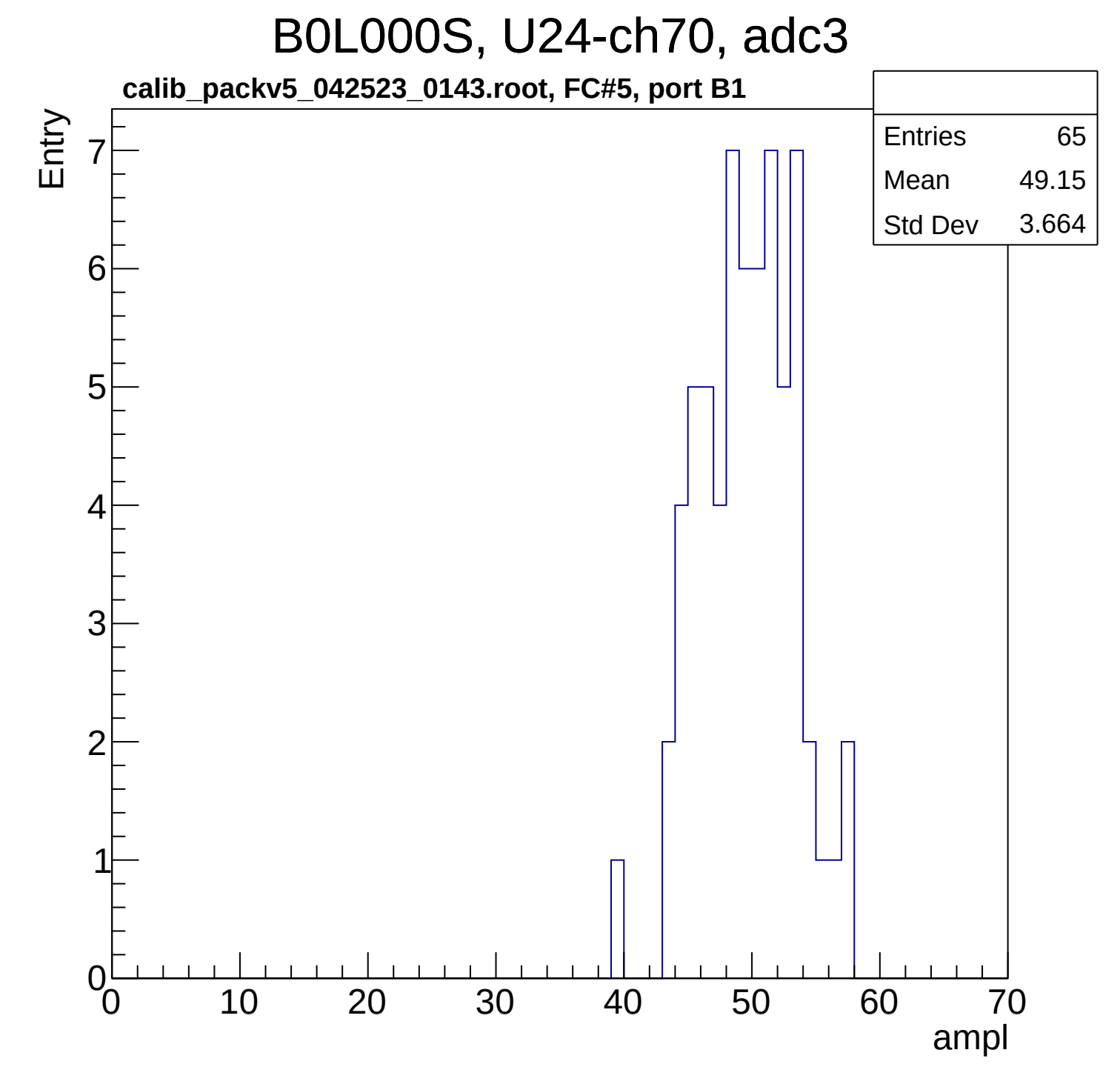
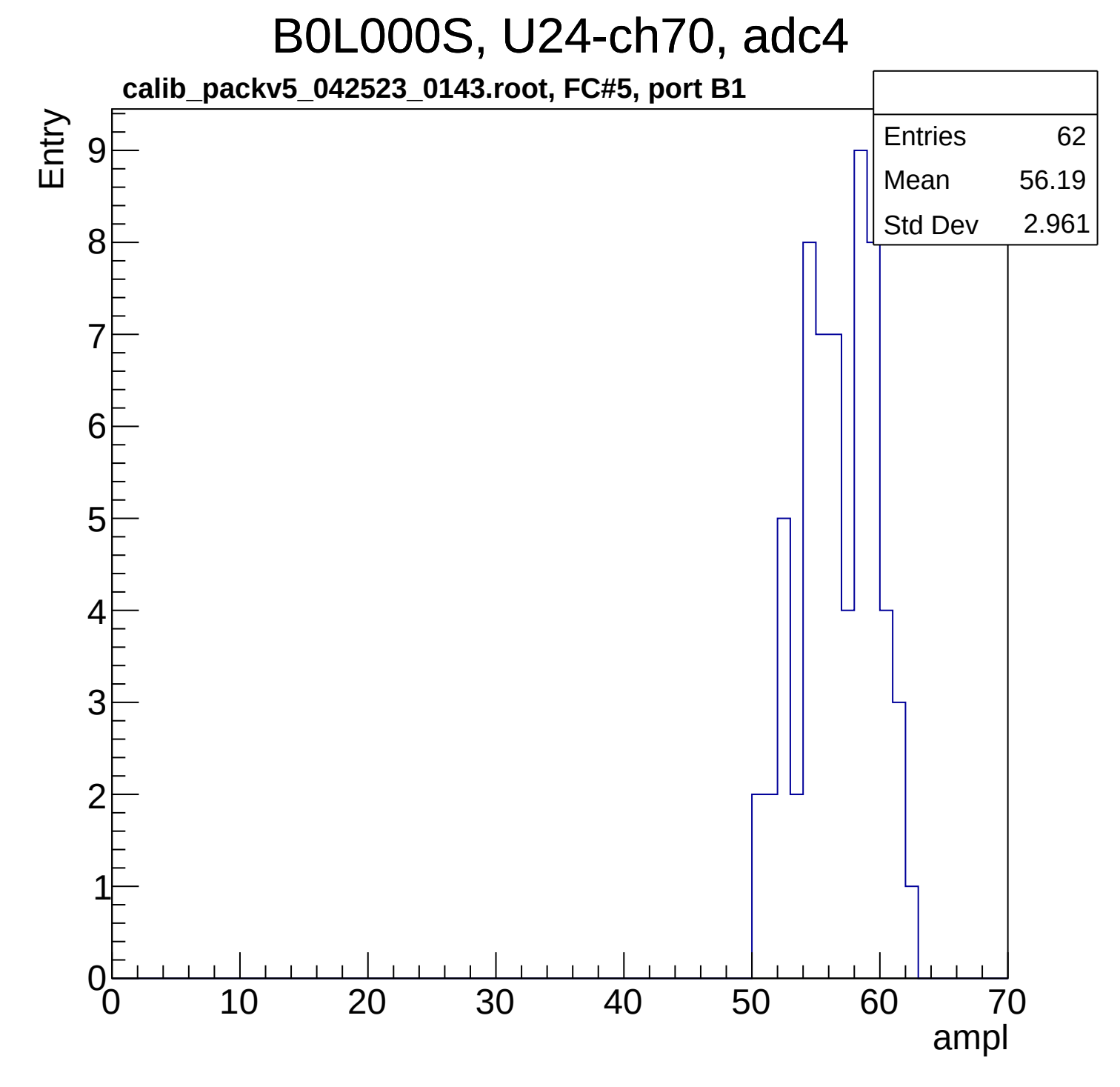
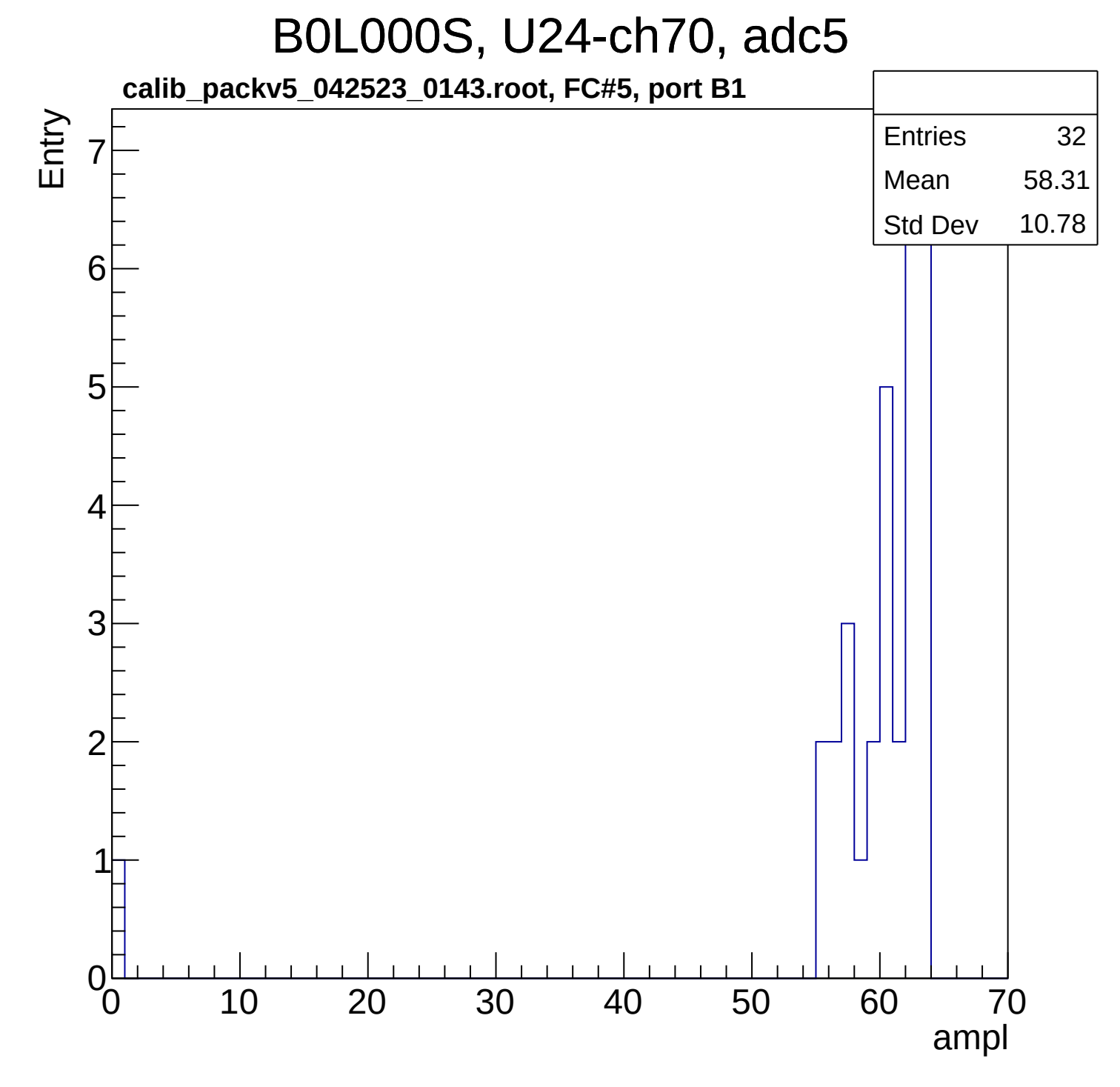
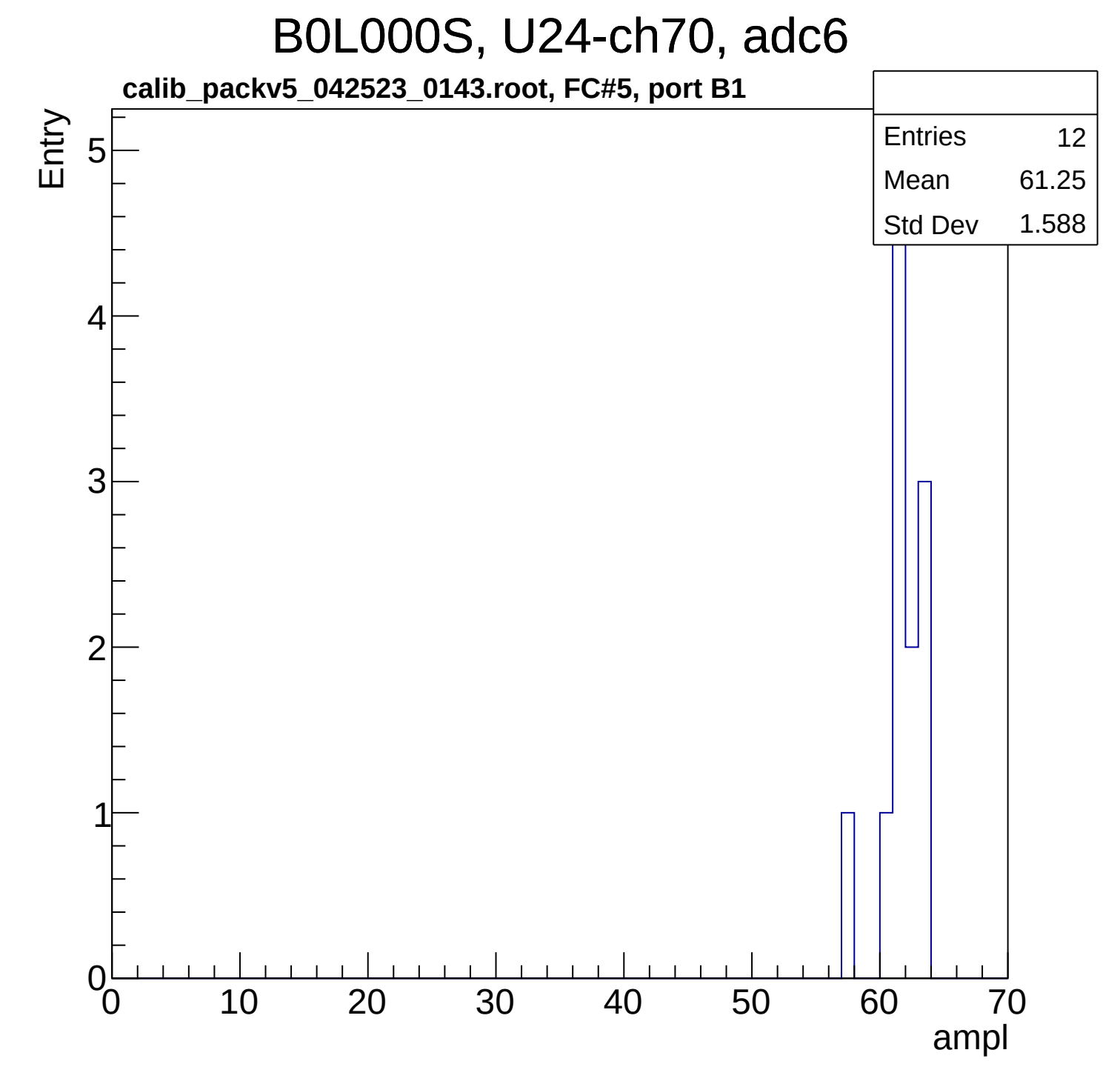

B0L000S, U24-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 37.23
Std Dev 3.548
Gaus mean : 37.8787
Gaus Width: 3.6999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

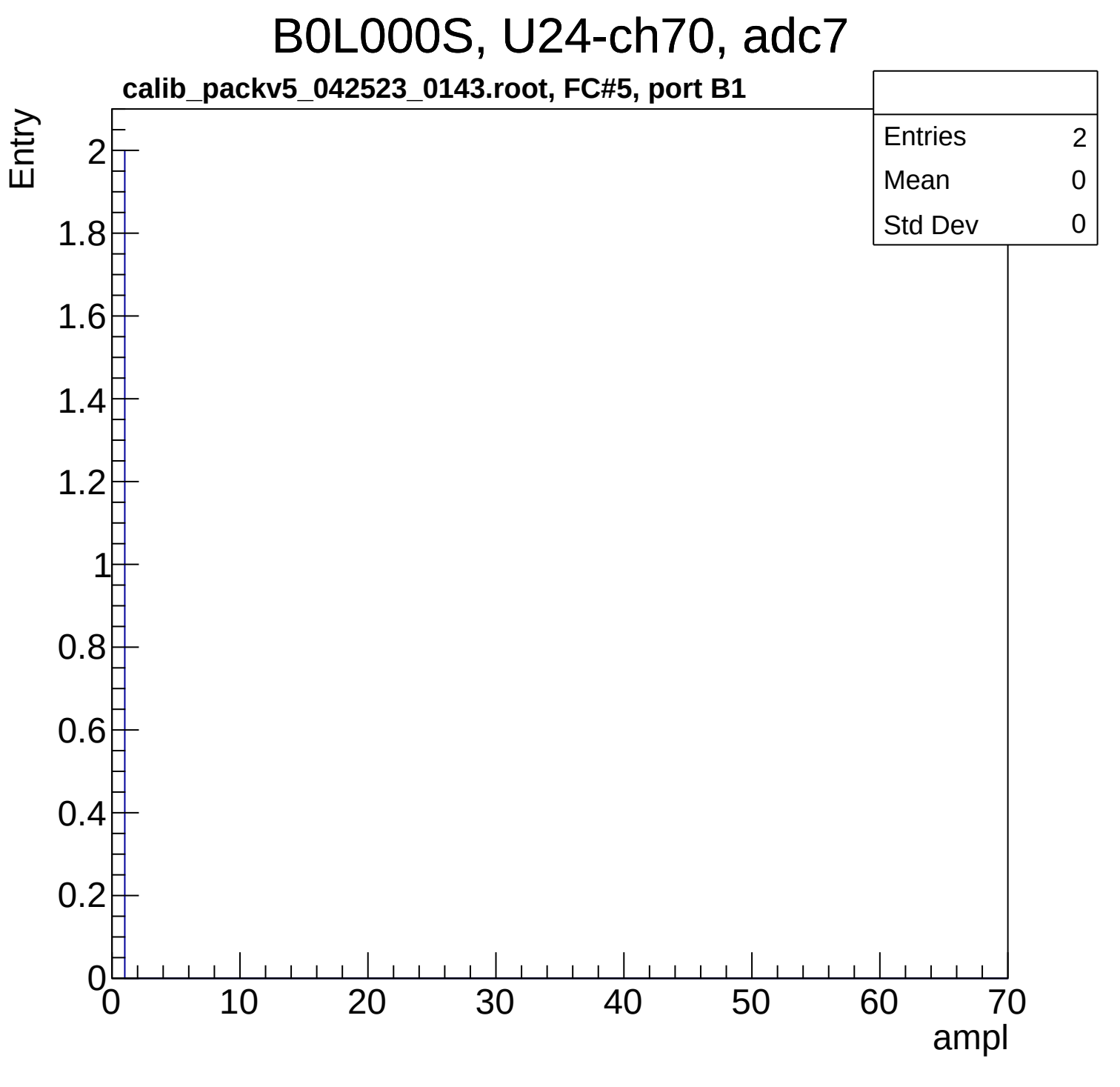

B0L000S, U24-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.92
Std Dev 3.568
Gaus mean : 44.3144
Gaus Width: 3.7744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

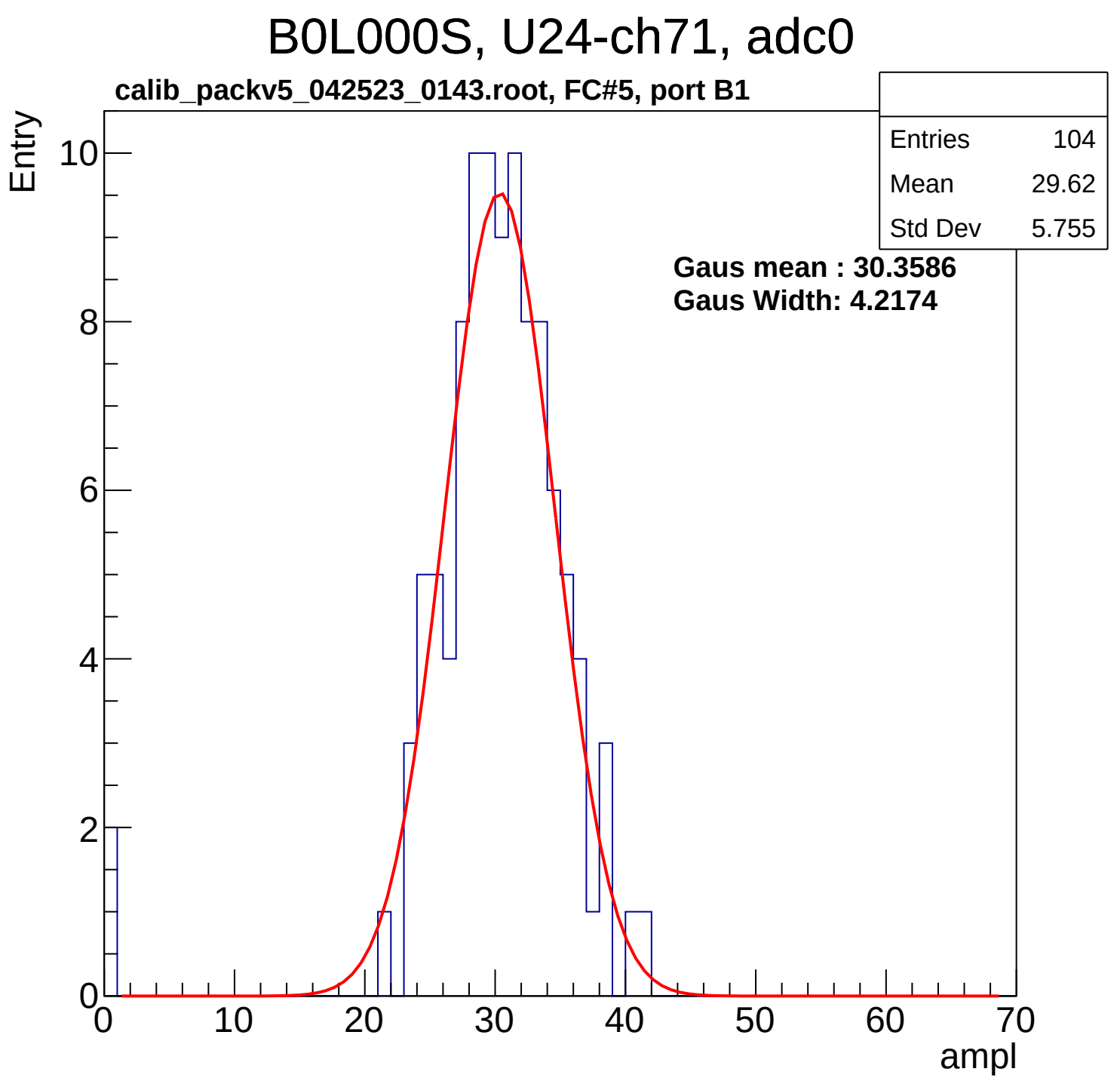

B0L000S, U24-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.15
Std Dev 3.664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

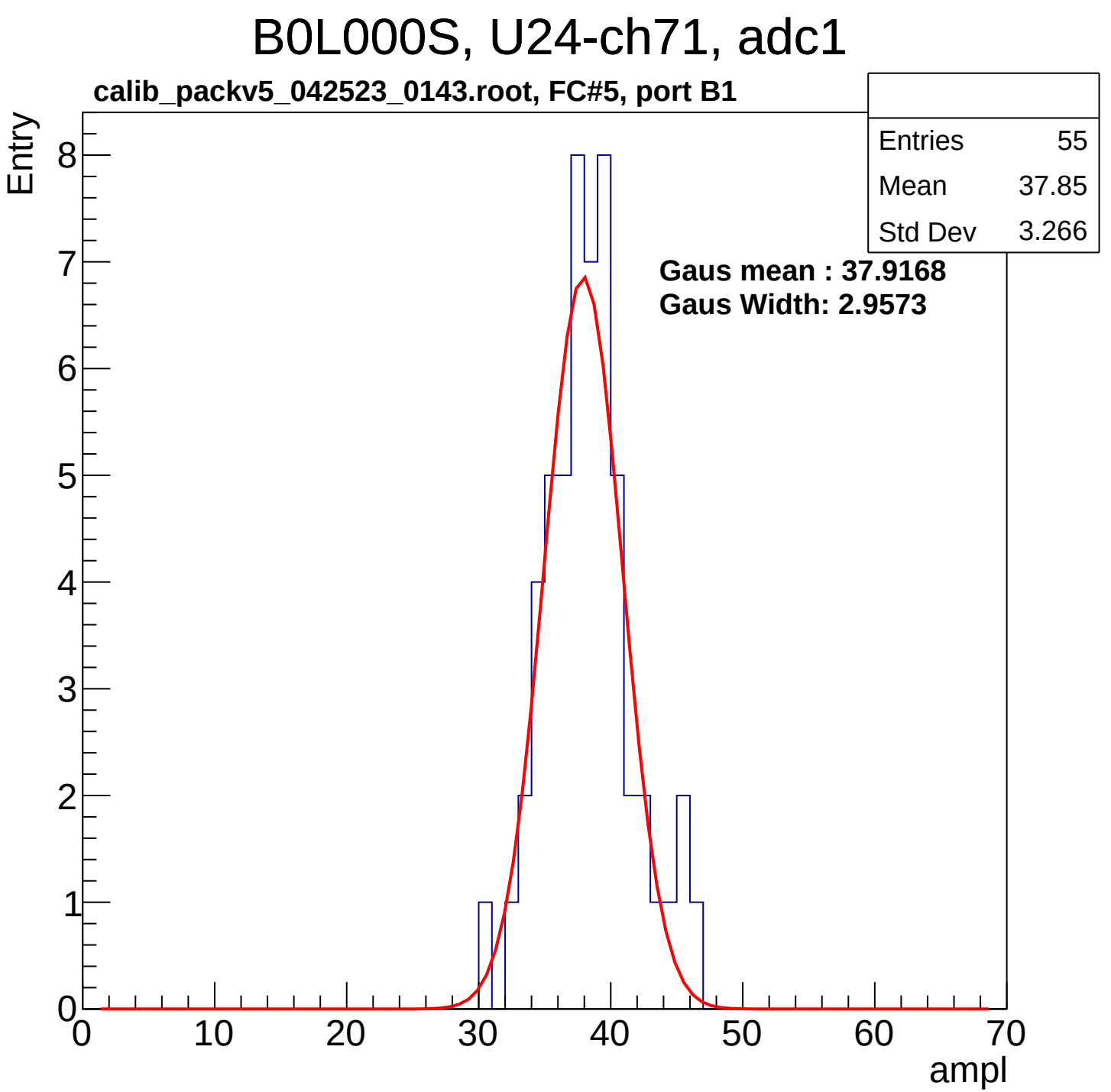

B0L000S, U24-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.19
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.31
Std Dev 10.78
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.25
Std Dev 1.588
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 104
Mean 29.62
Std Dev 5.755
Gaus mean : 30.3586
Gaus Width: 4.2174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 37.85
Std Dev 3.266
Gaus mean : 37.9168
Gaus Width: 2.9573
Entry
ampl
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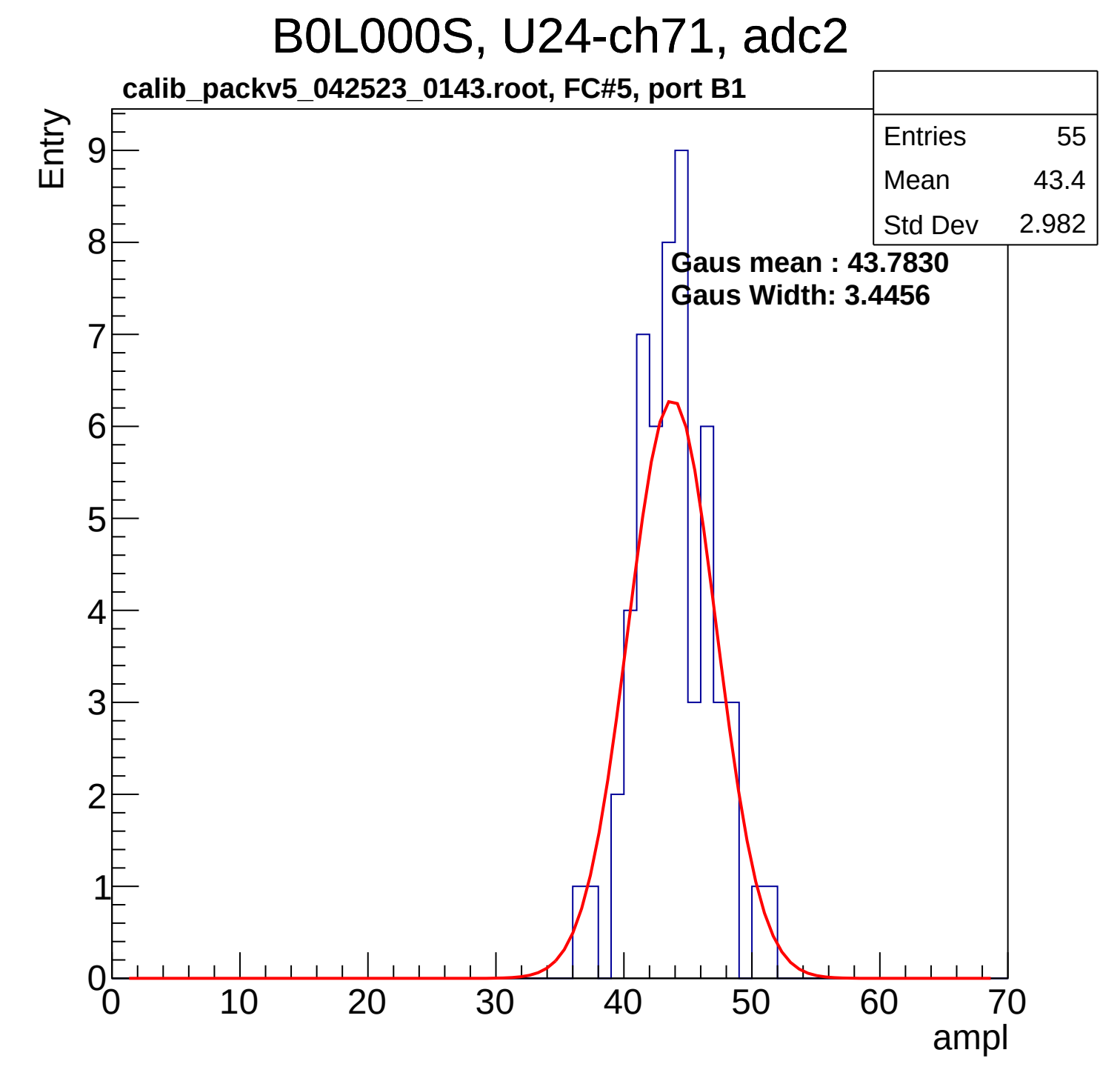
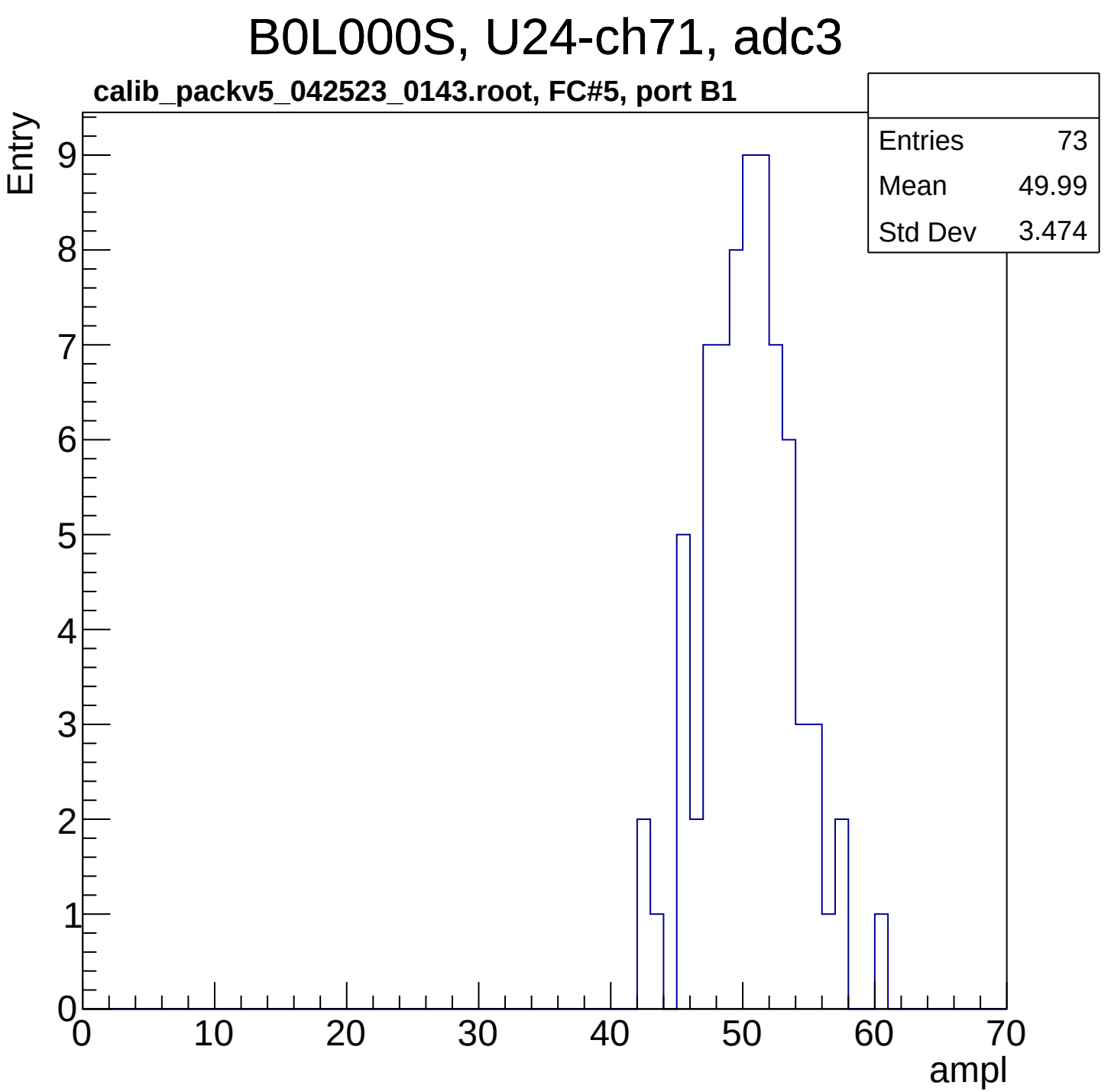
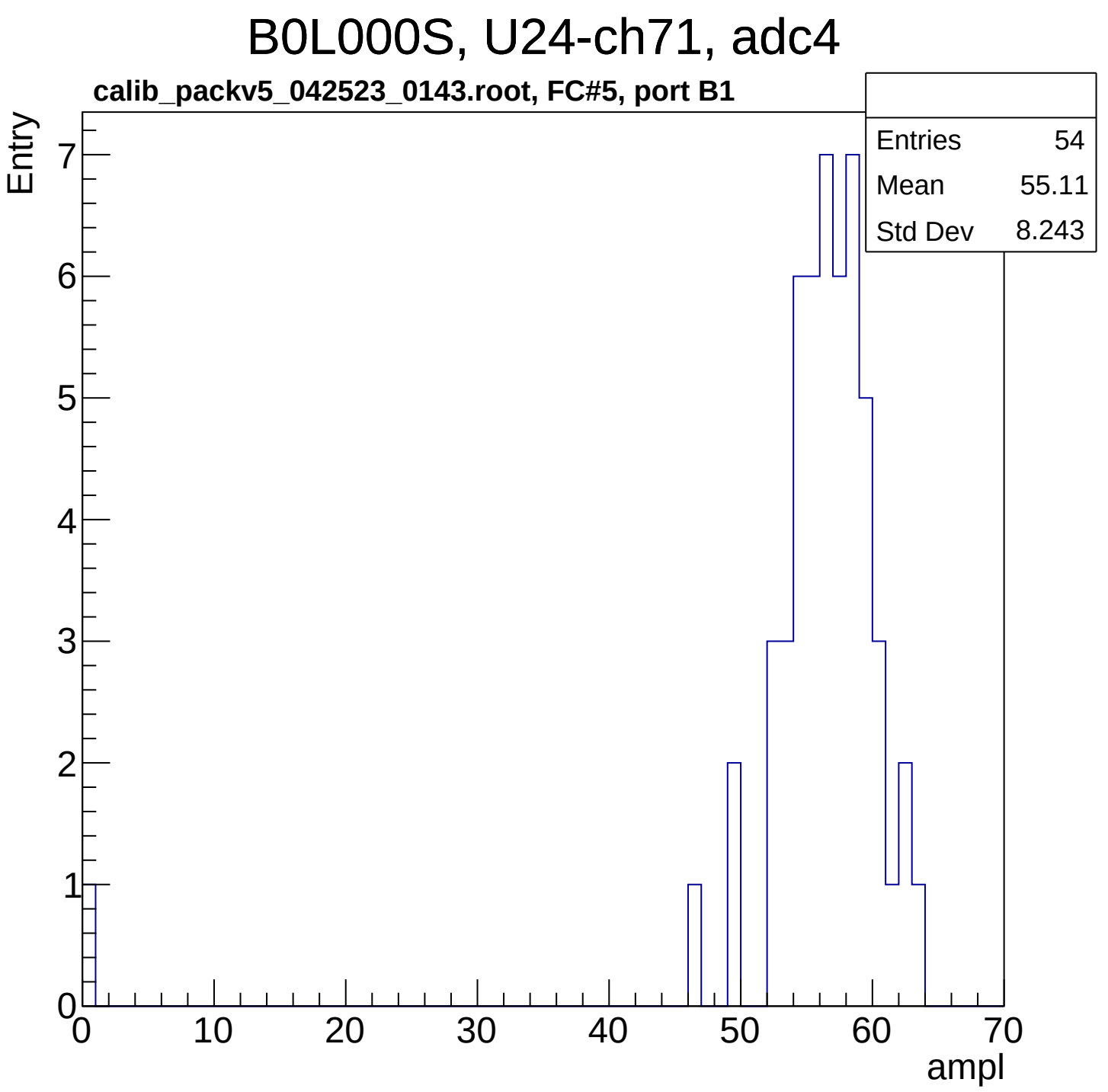
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.4
Std Dev 2.982
Gaus mean : 43.7830
Gaus Width: 3.4456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.99
Std Dev 3.474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.11
Std Dev 8.243
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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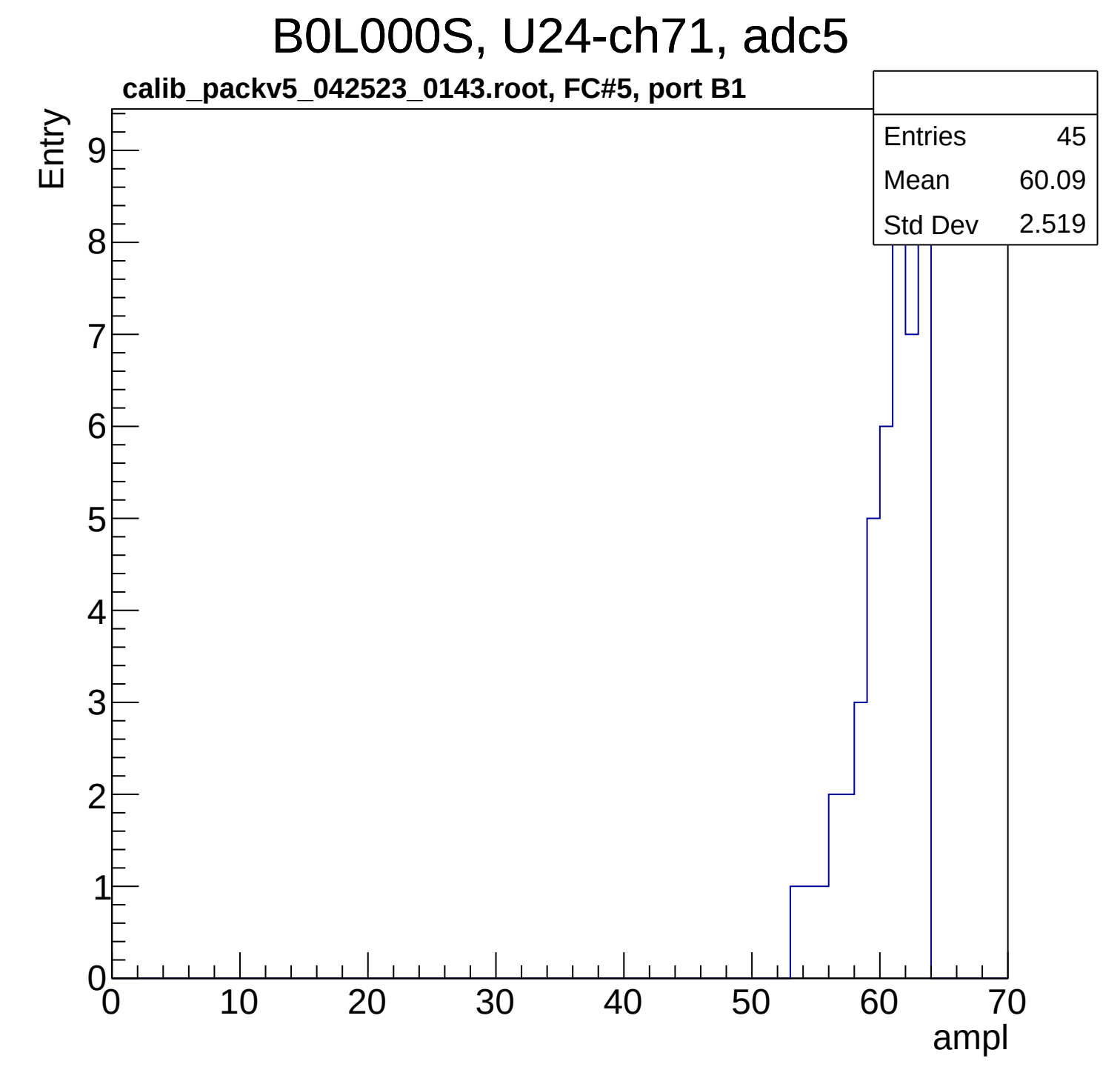
40
50
60
70


B0L000S, U24-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.09
Std Dev 2.519
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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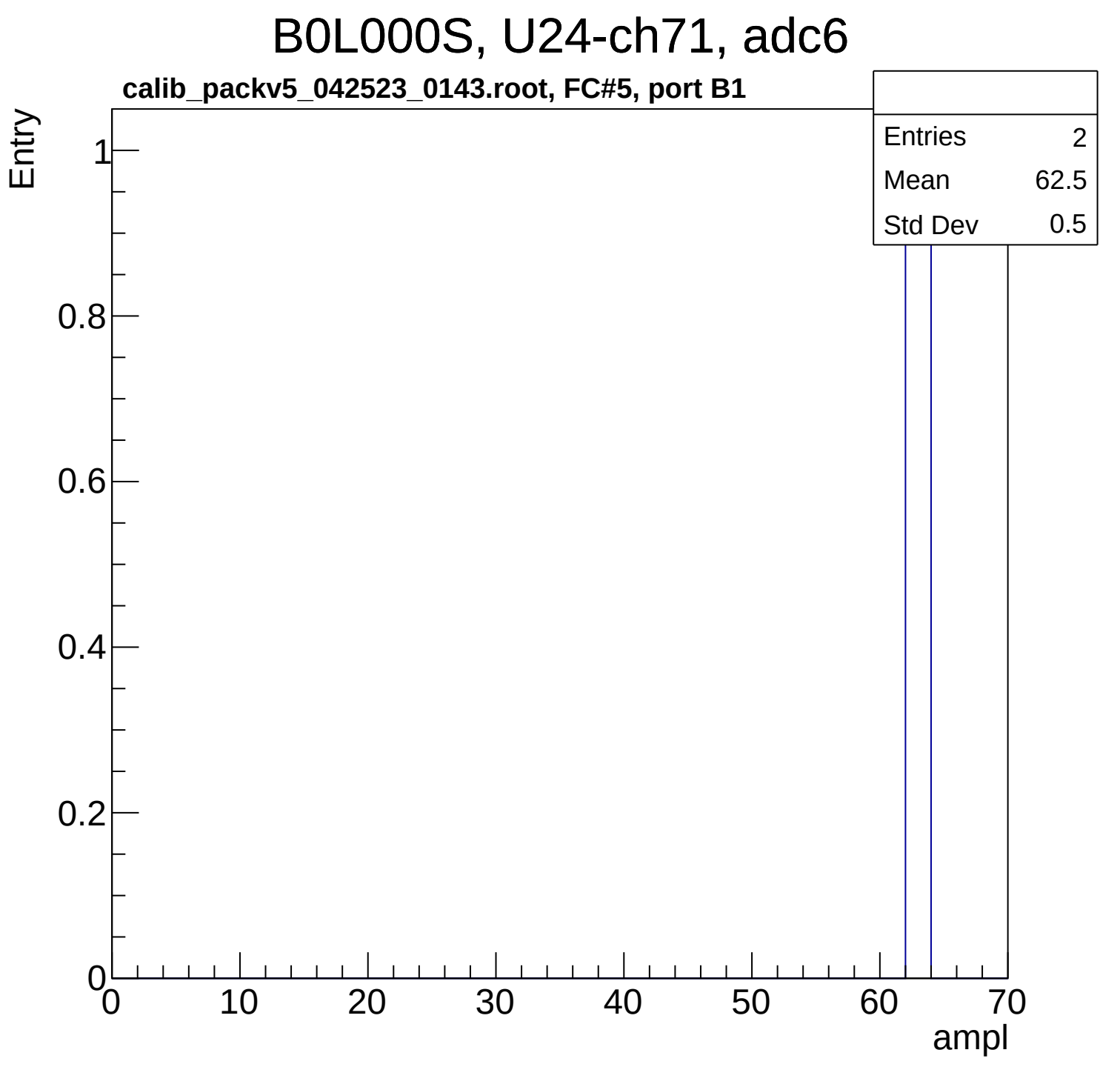
60
70


B0L000S, U24-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.6
0.4
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0
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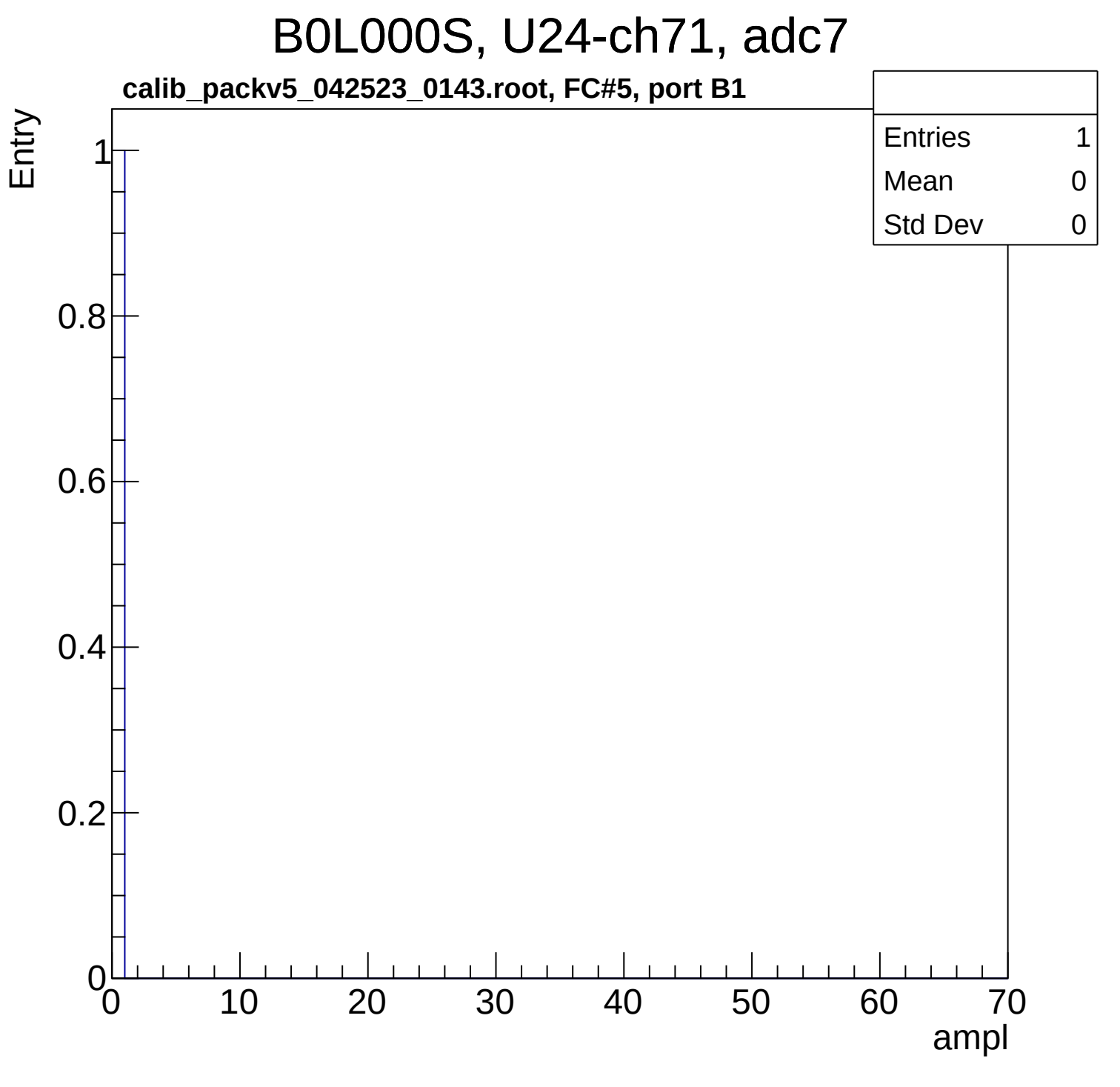
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
20
30
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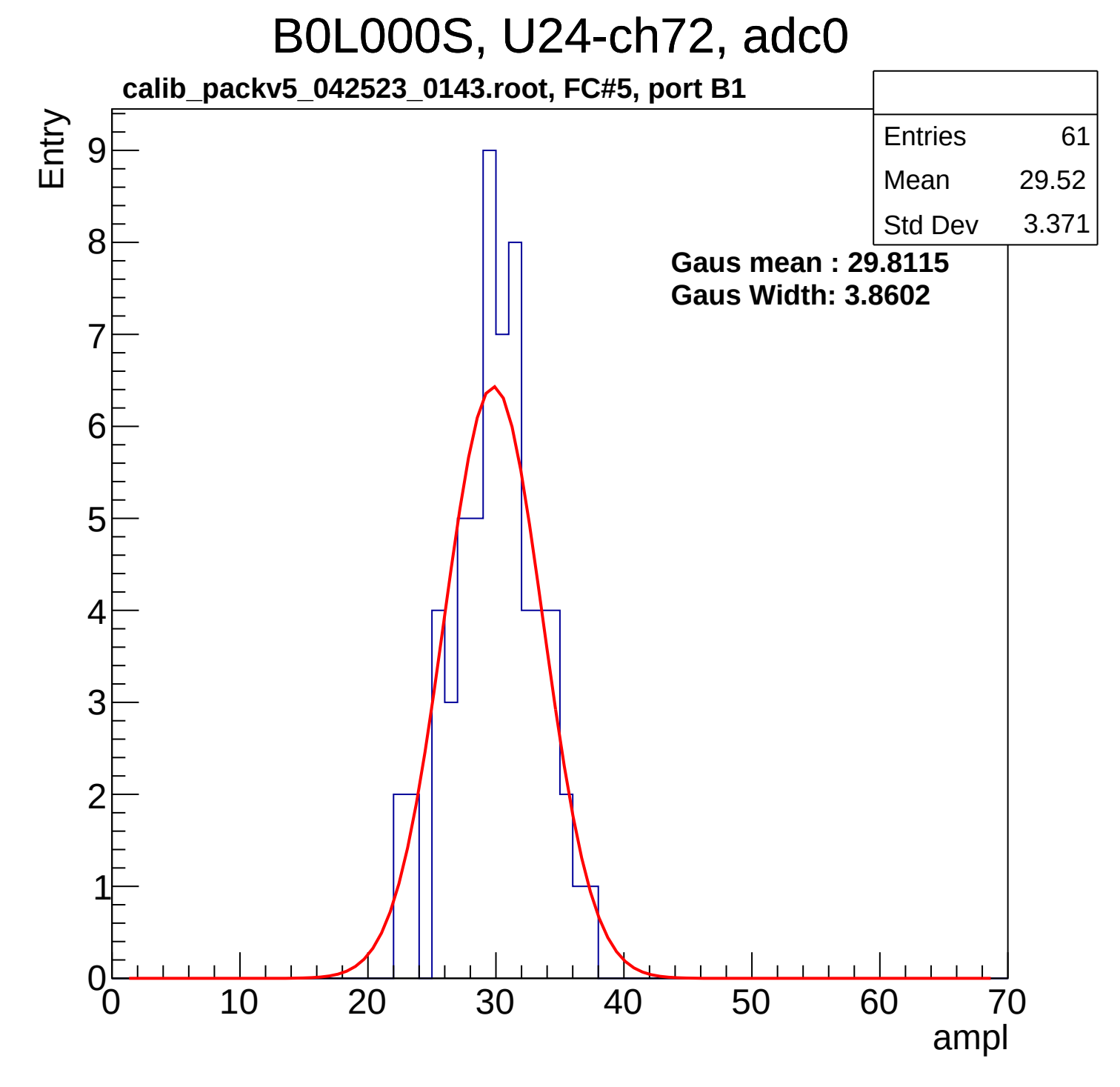
50
60
70
ampl


B0L000S, U24-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.52
Std Dev 3.371
Gaus mean : 29.8115
Gaus Width: 3.8602
Entry
ampl
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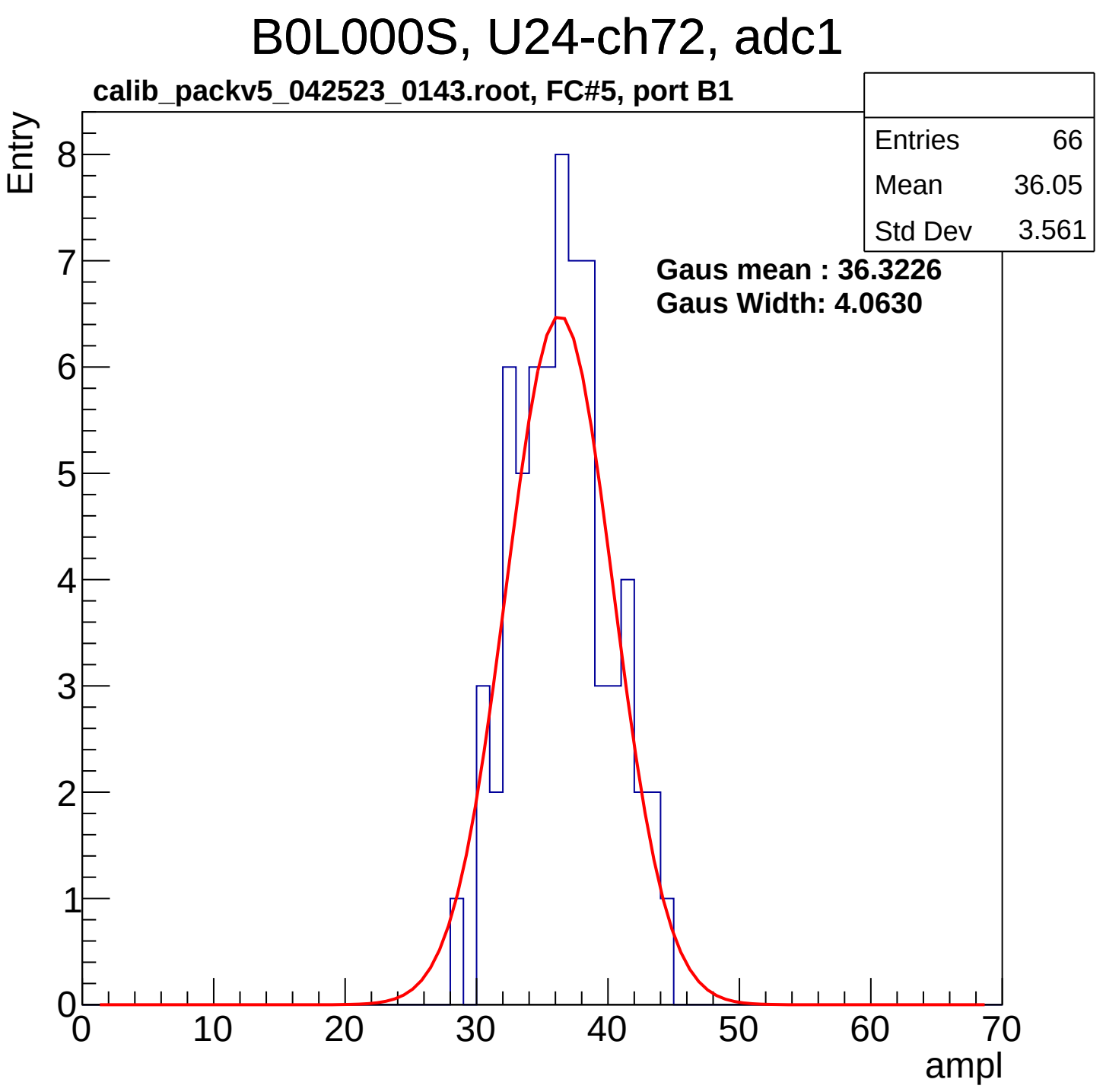
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.05
Std Dev 3.561
Gaus mean : 36.3226
Gaus Width: 4.0630
Entry
ampl
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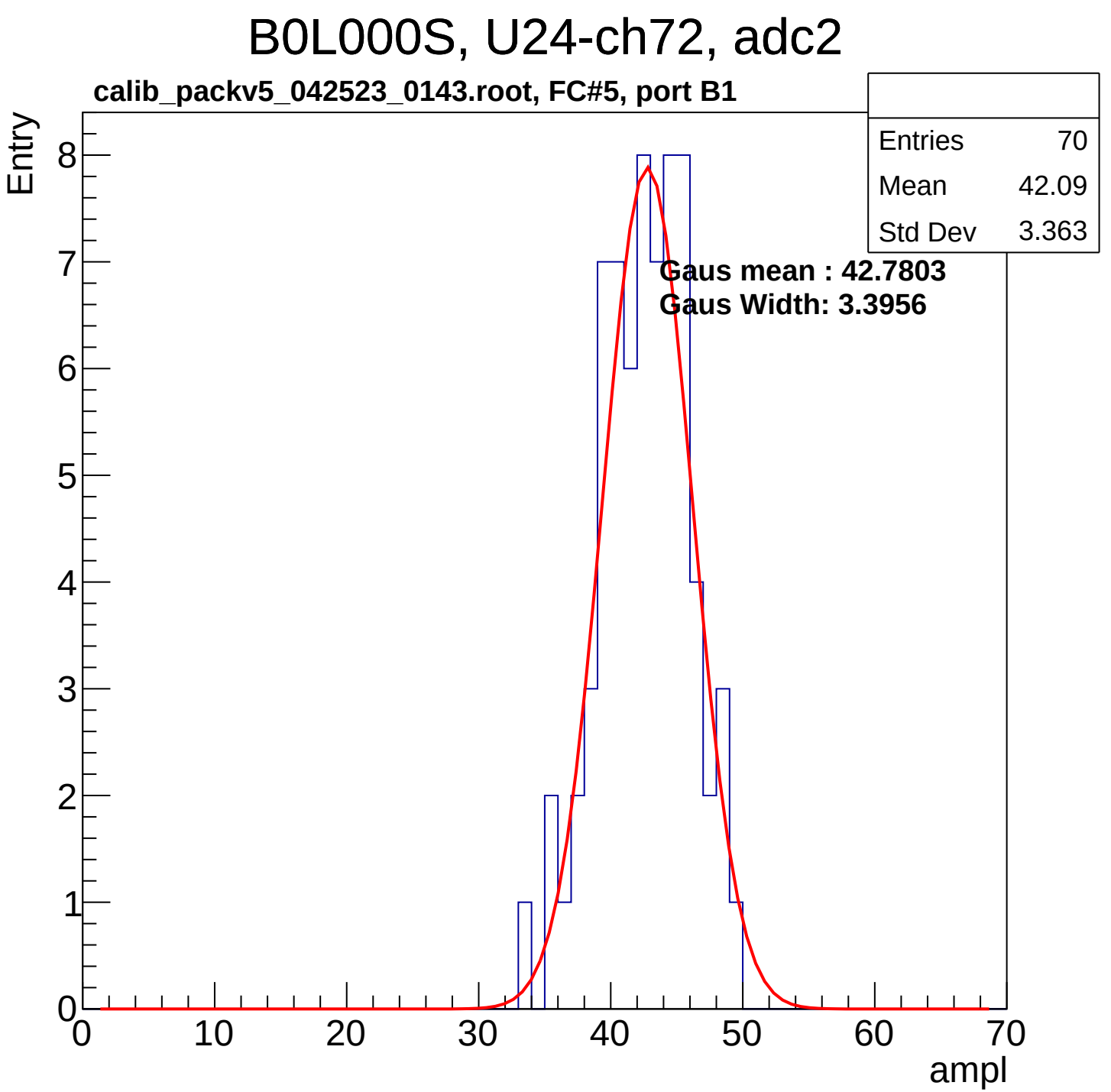
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.09
Std Dev 3.363
Gaus mean : 42.7803
Gaus Width: 3.3956
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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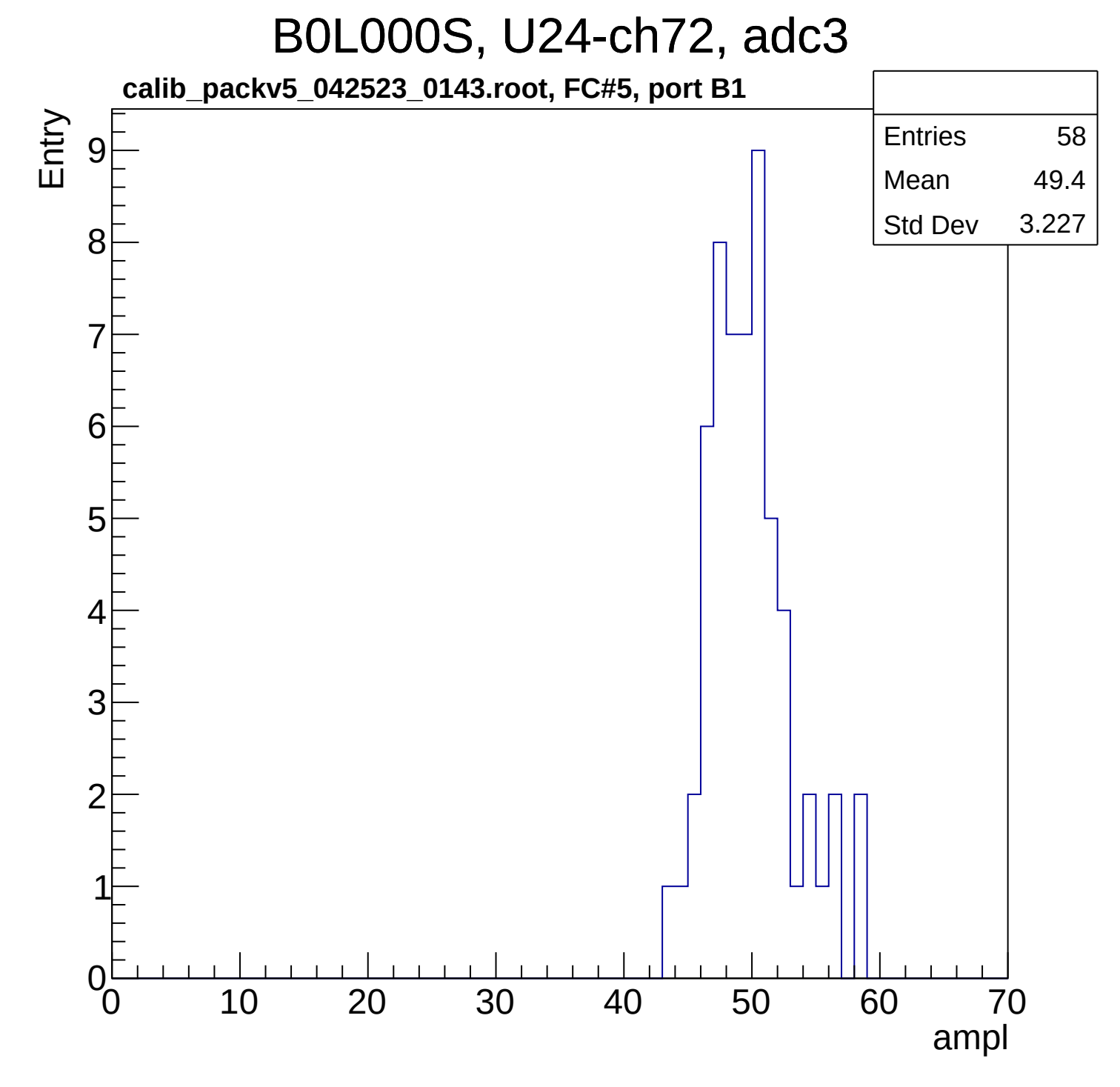
40
50
60
70


B0L000S, U24-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.4
Std Dev 3.227
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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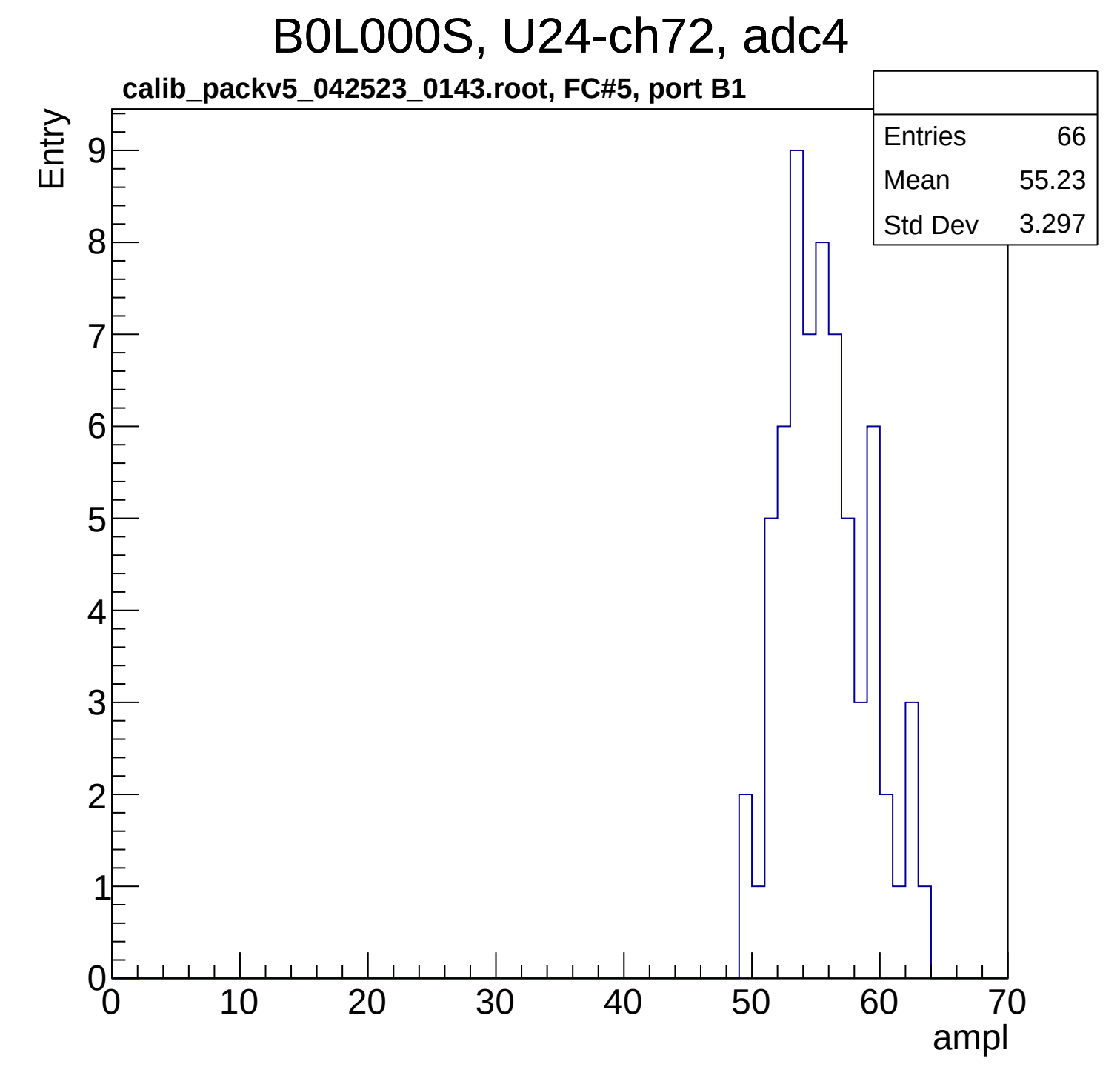
B0L000S, U24-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 55.23
Std Dev 3.297
Entry
ampl
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2
3
4
5
6
7
8
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0
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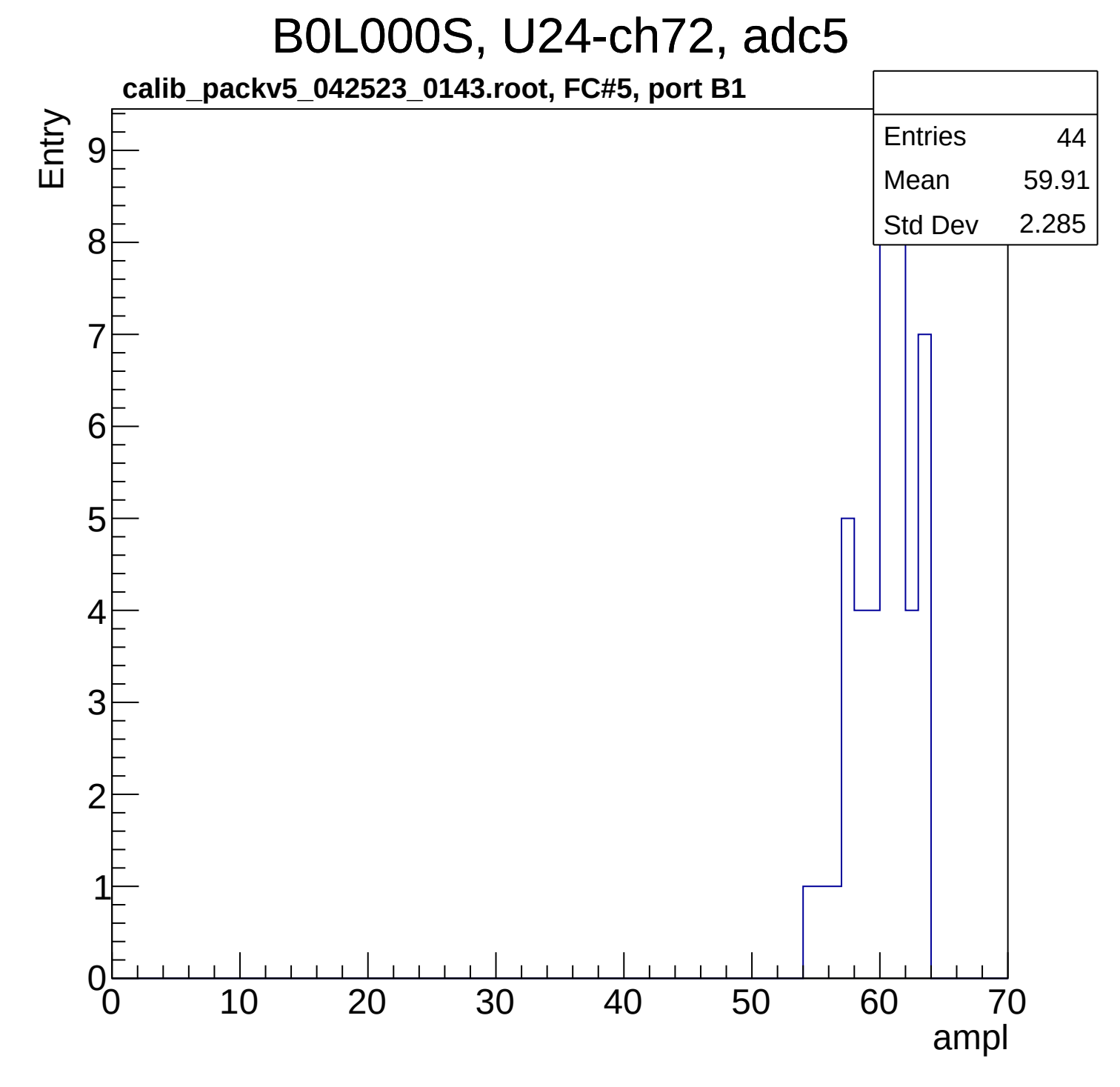
B0L000S, U24-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.91
Std Dev 2.285
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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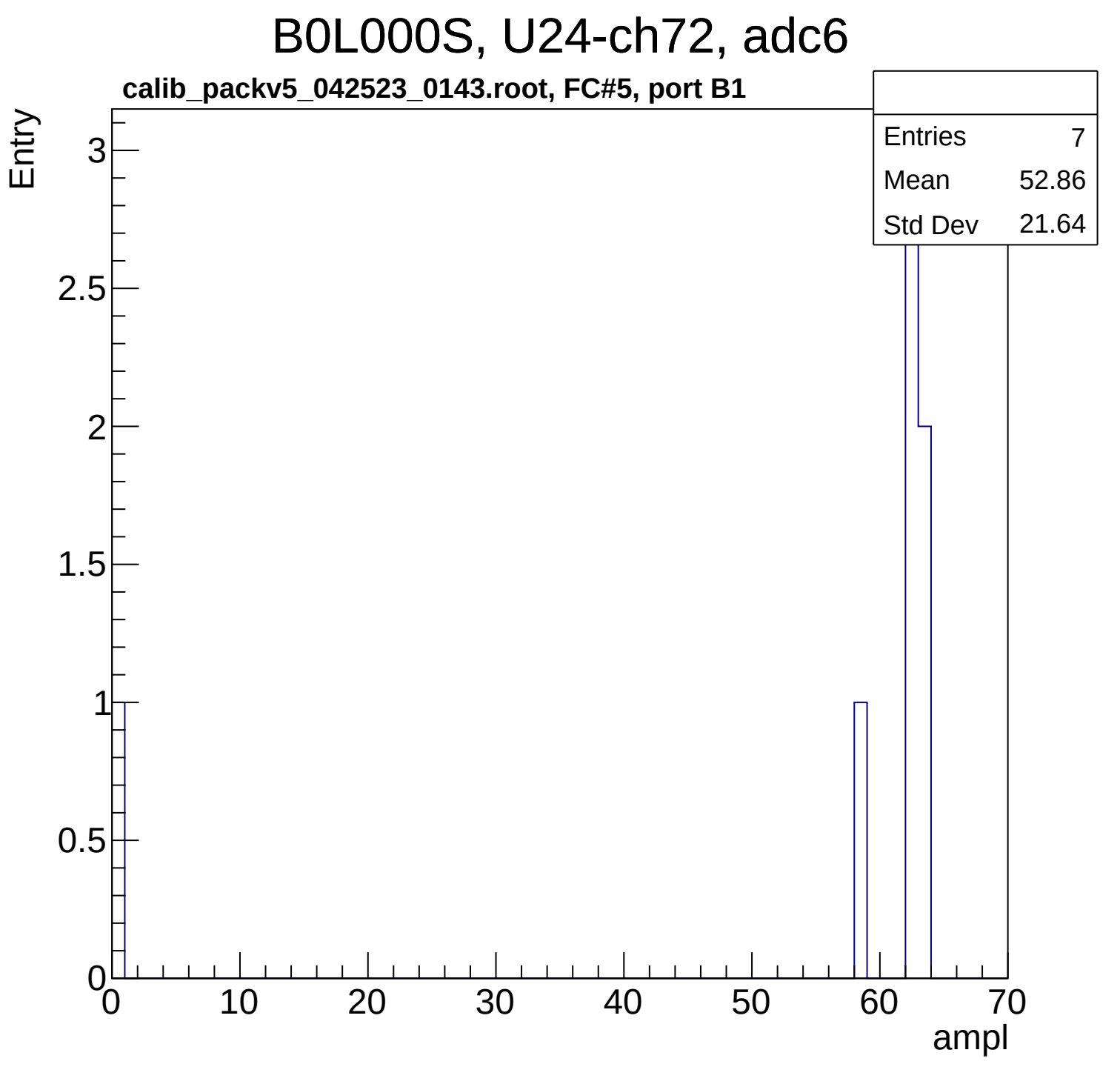
B0L000S, U24-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 52.86
Std Dev 21.64
Entry
ampl
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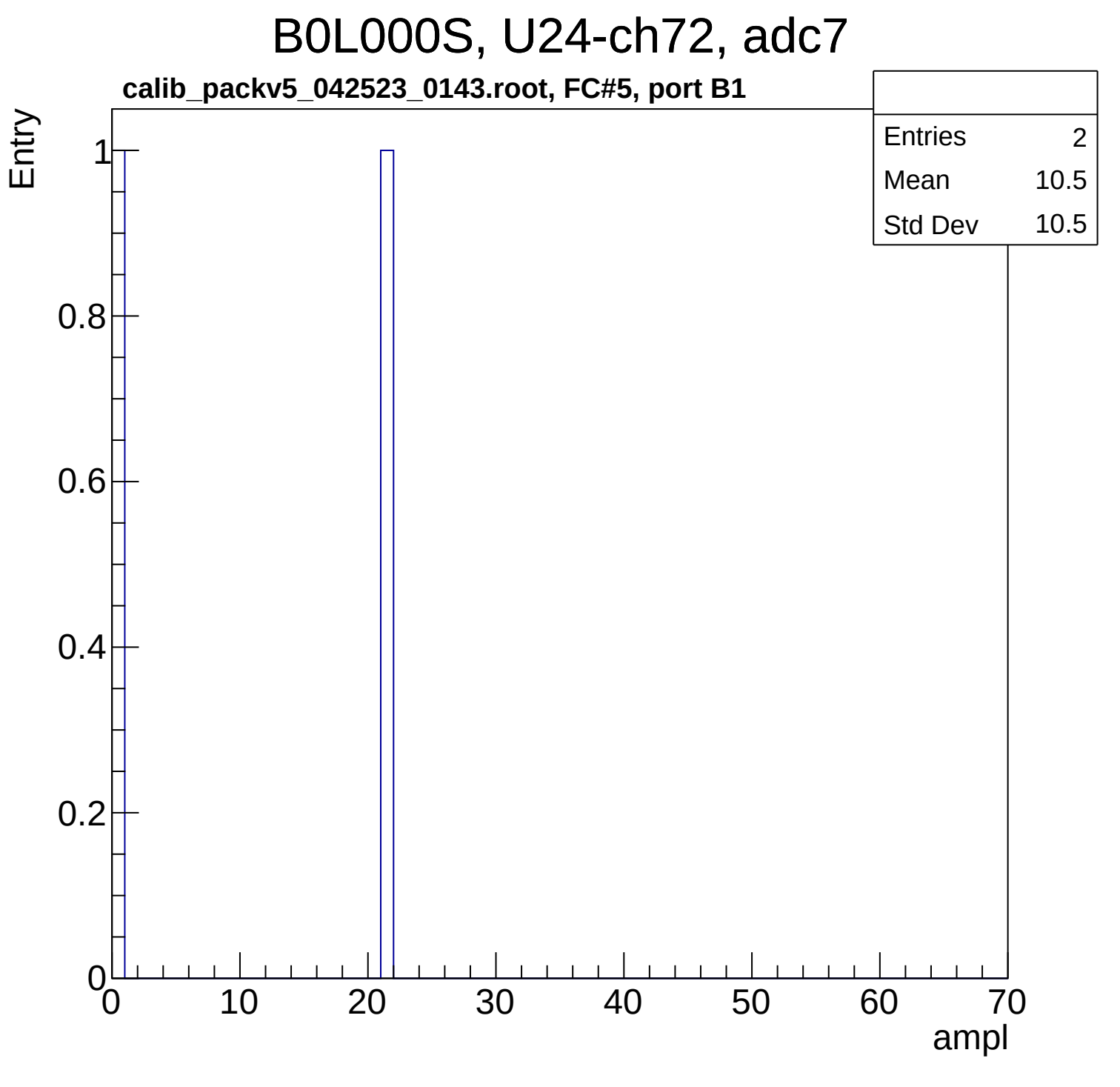
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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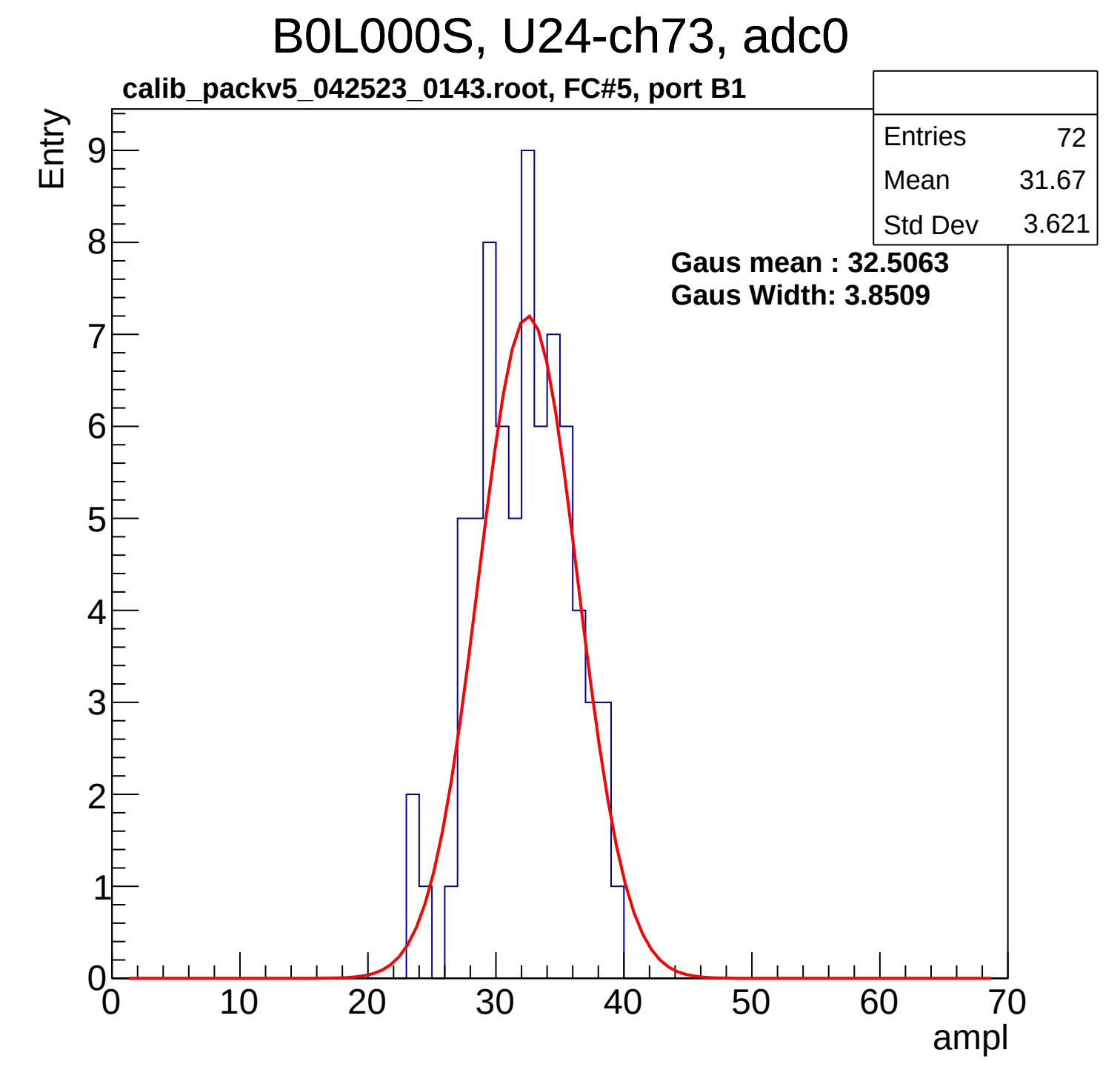
B0L000S, U24-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 31.67
Std Dev 3.621
Gaus mean : 32.5063
Gaus Width: 3.8509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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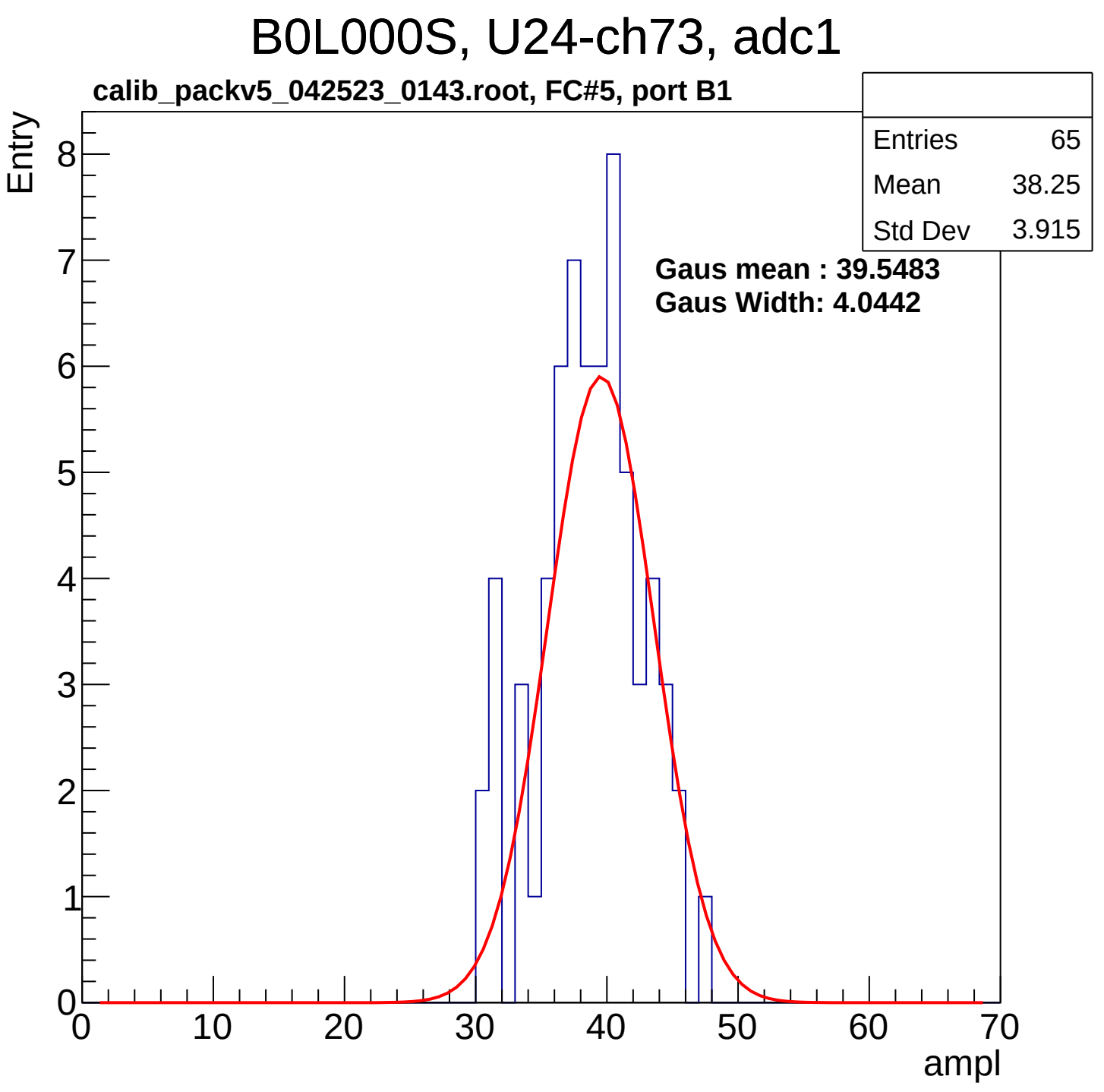
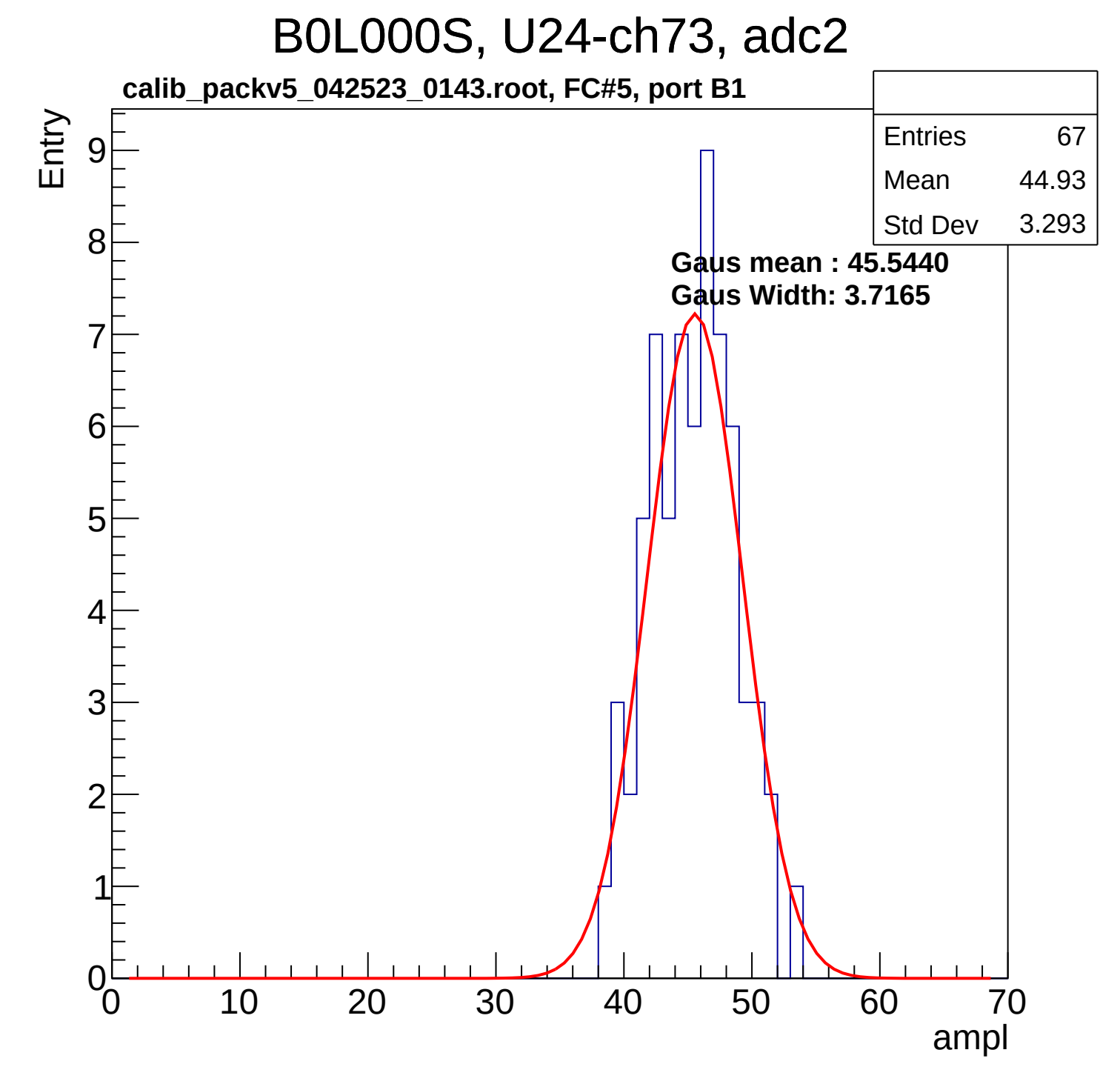
60
70


B0L000S, U24-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.25
Std Dev 3.915
Gaus mean : 39.5483
Gaus Width: 4.0442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.93
Std Dev 3.293
Gaus mean : 45.5440
Gaus Width: 3.7165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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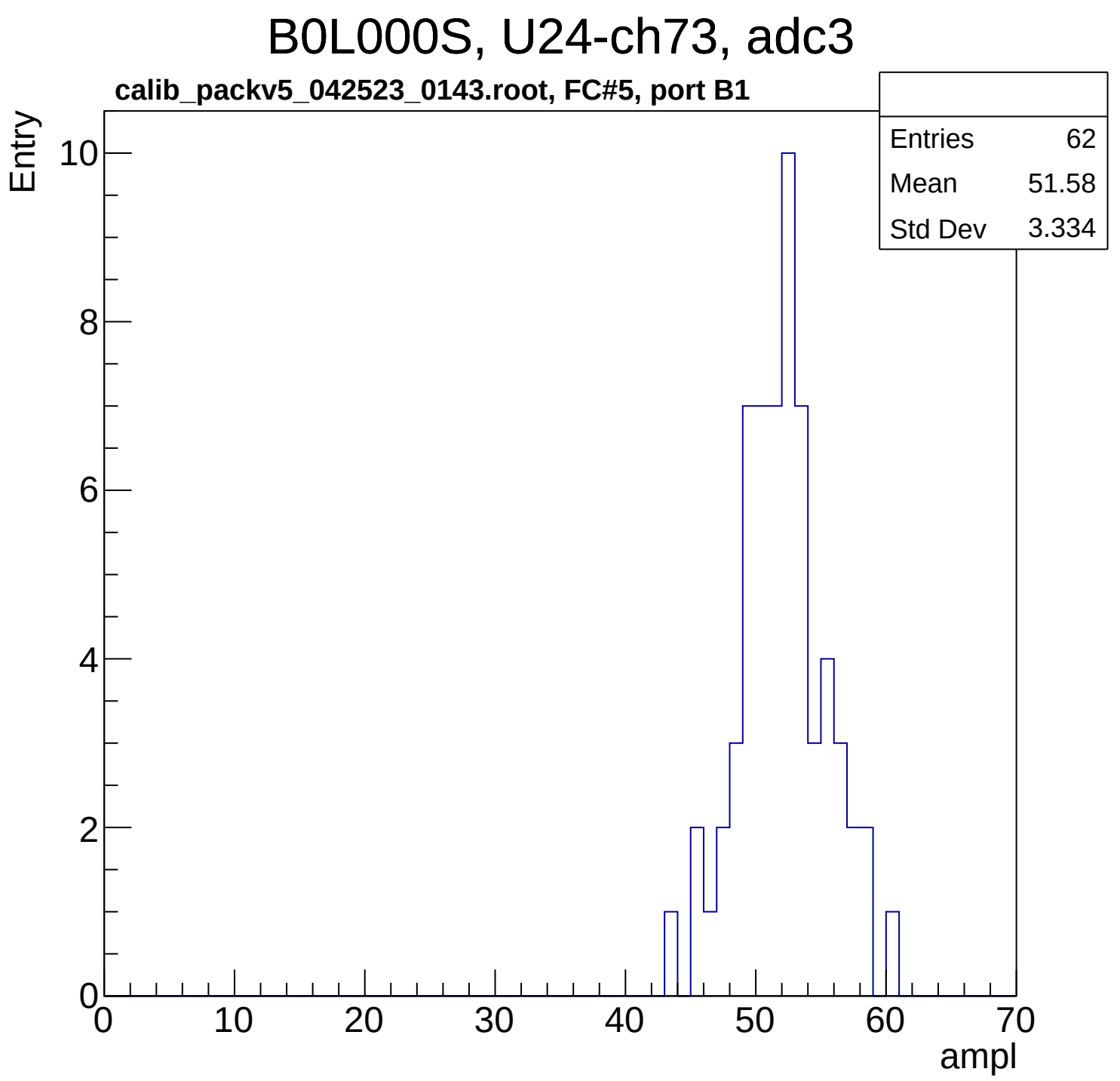
B0L000S, U24-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.58
Std Dev 3.334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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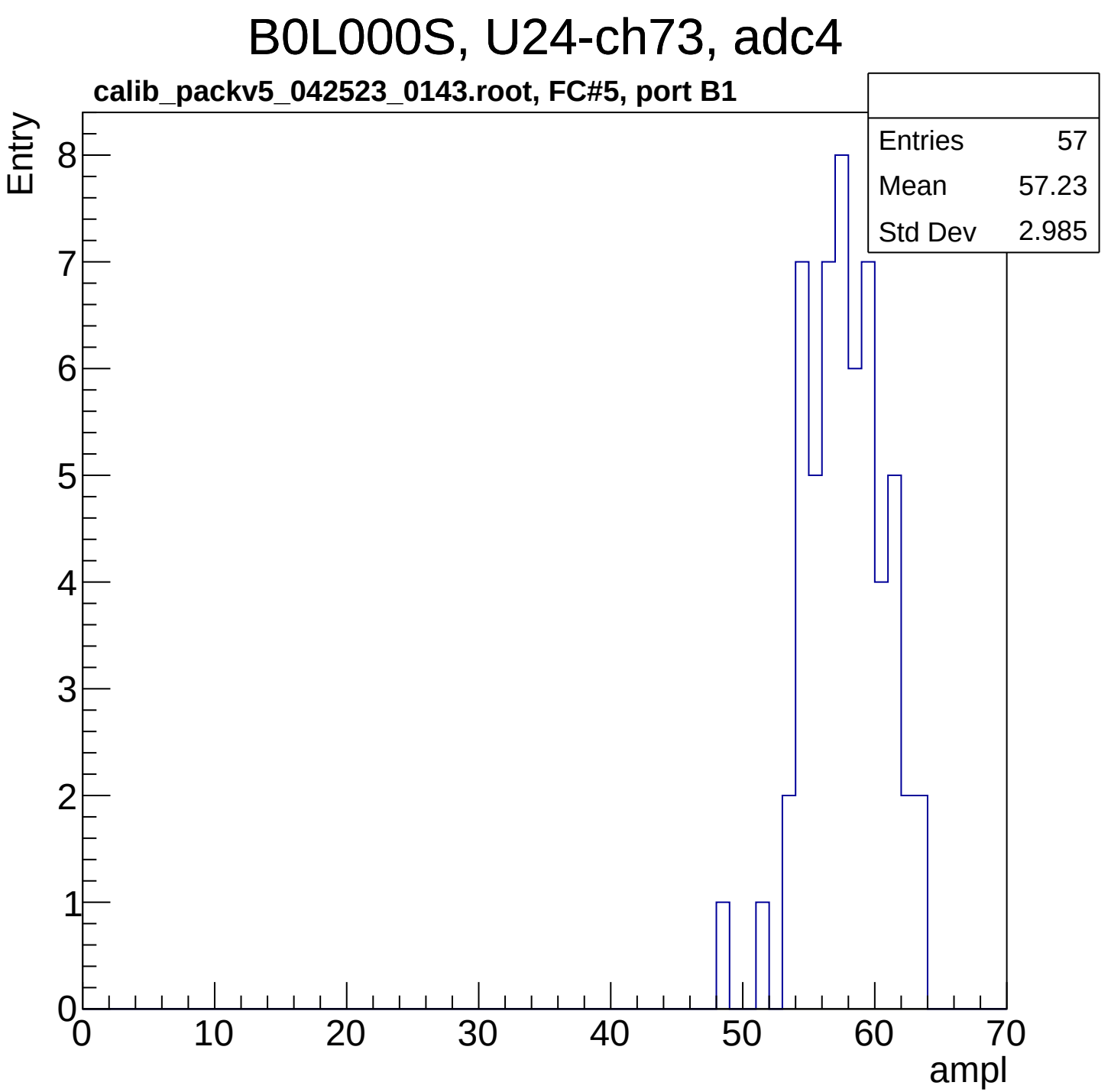
B0L000S, U24-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.23
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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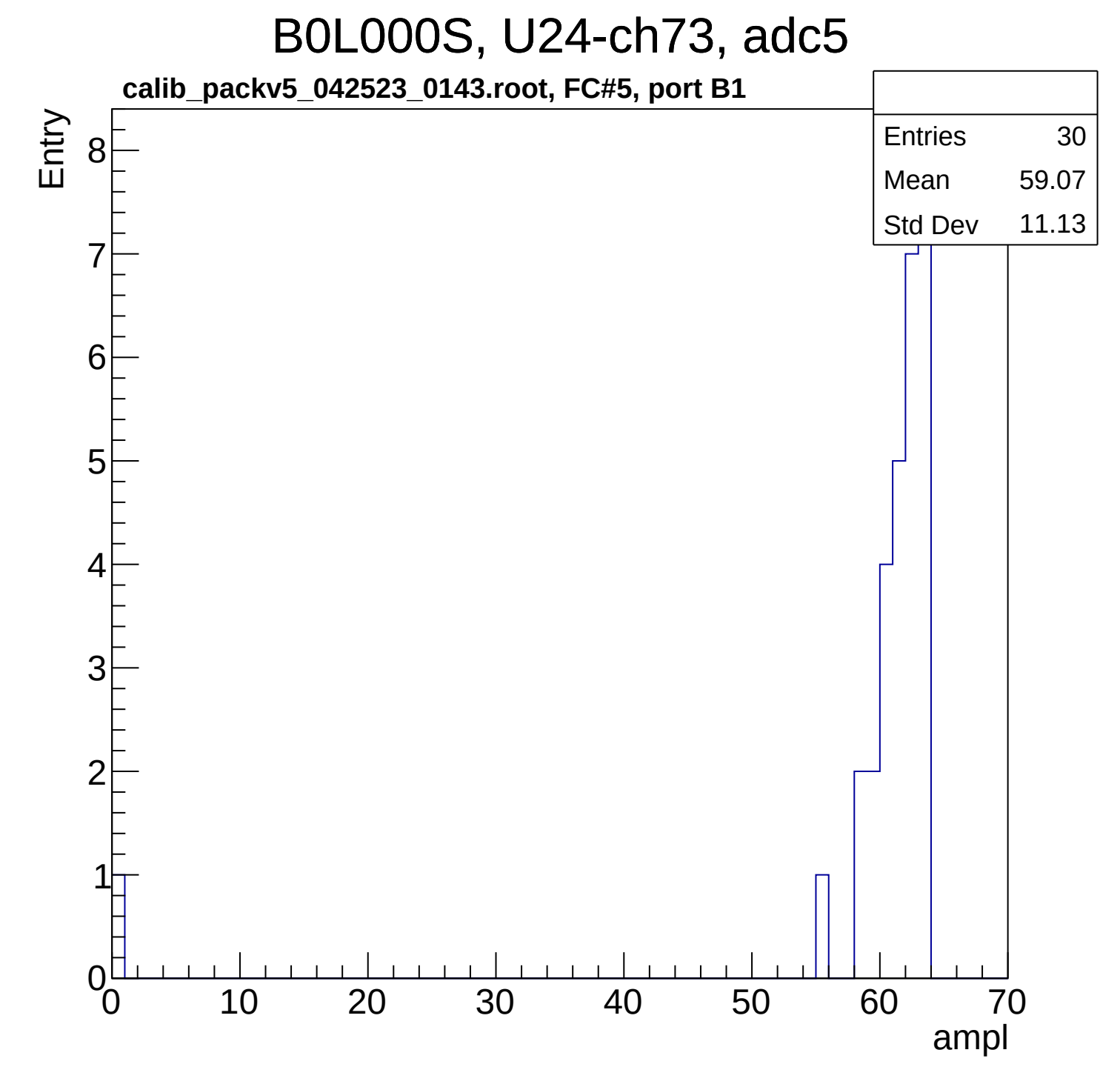
B0L000S, U24-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 59.07
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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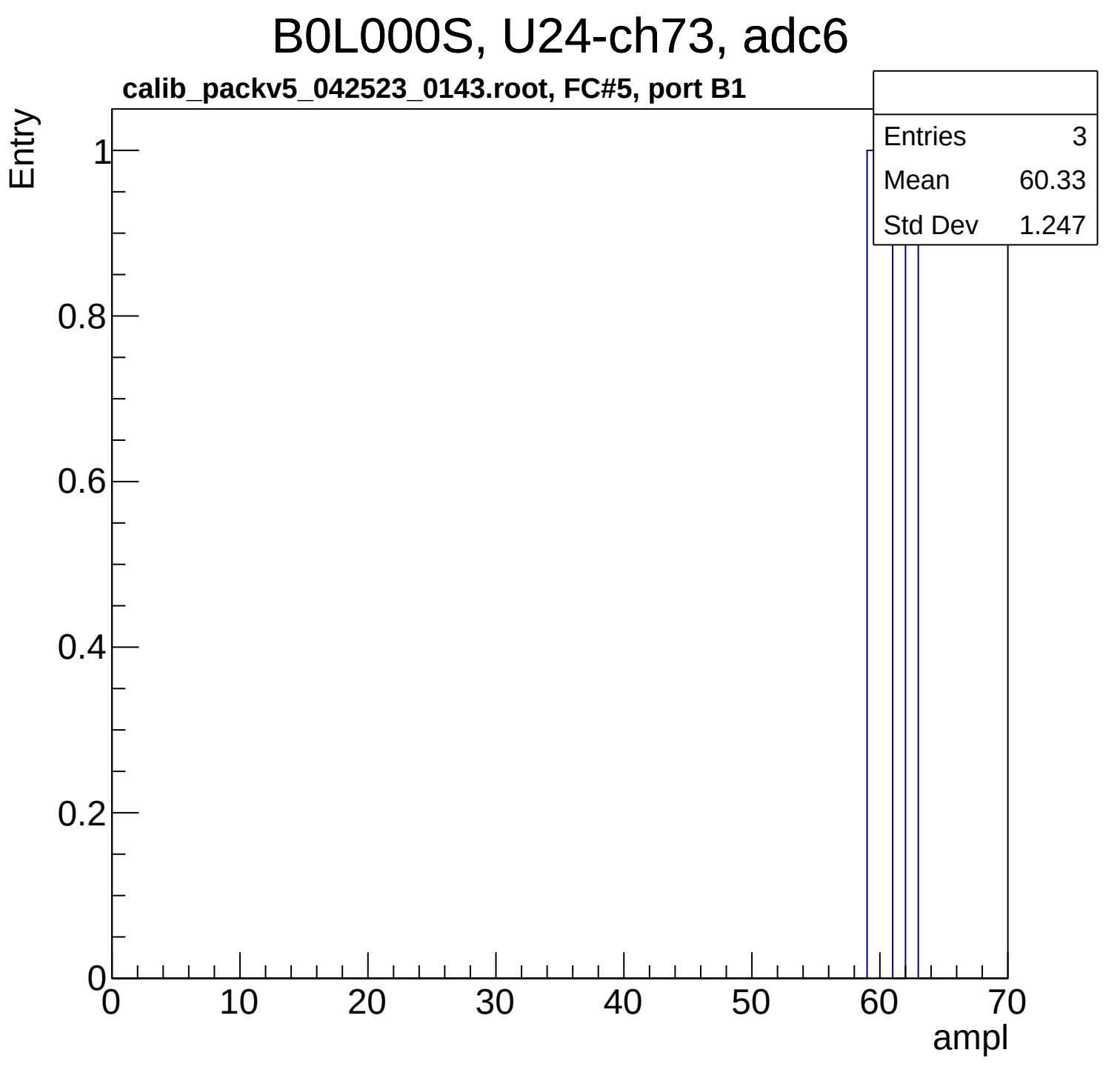
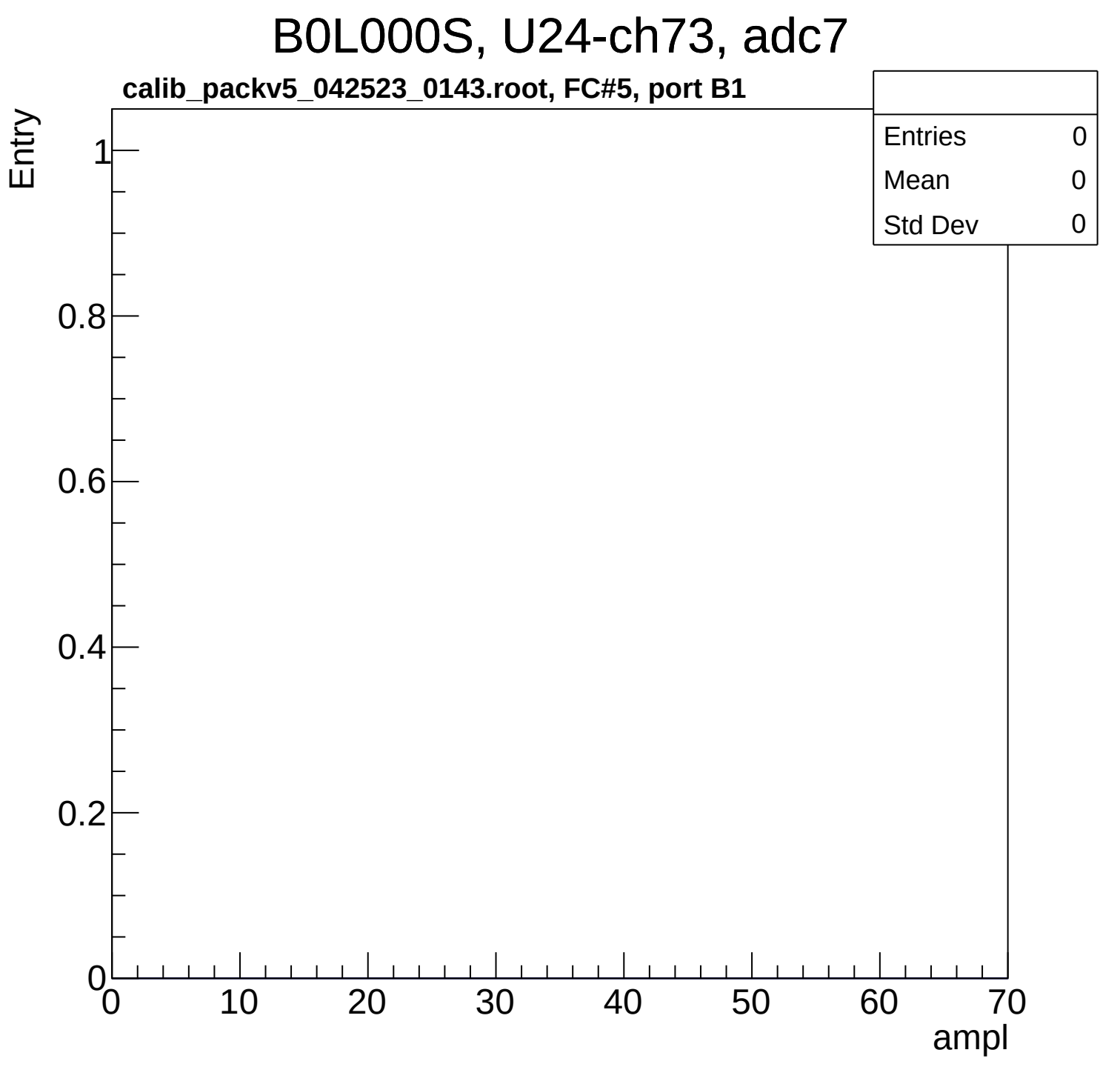
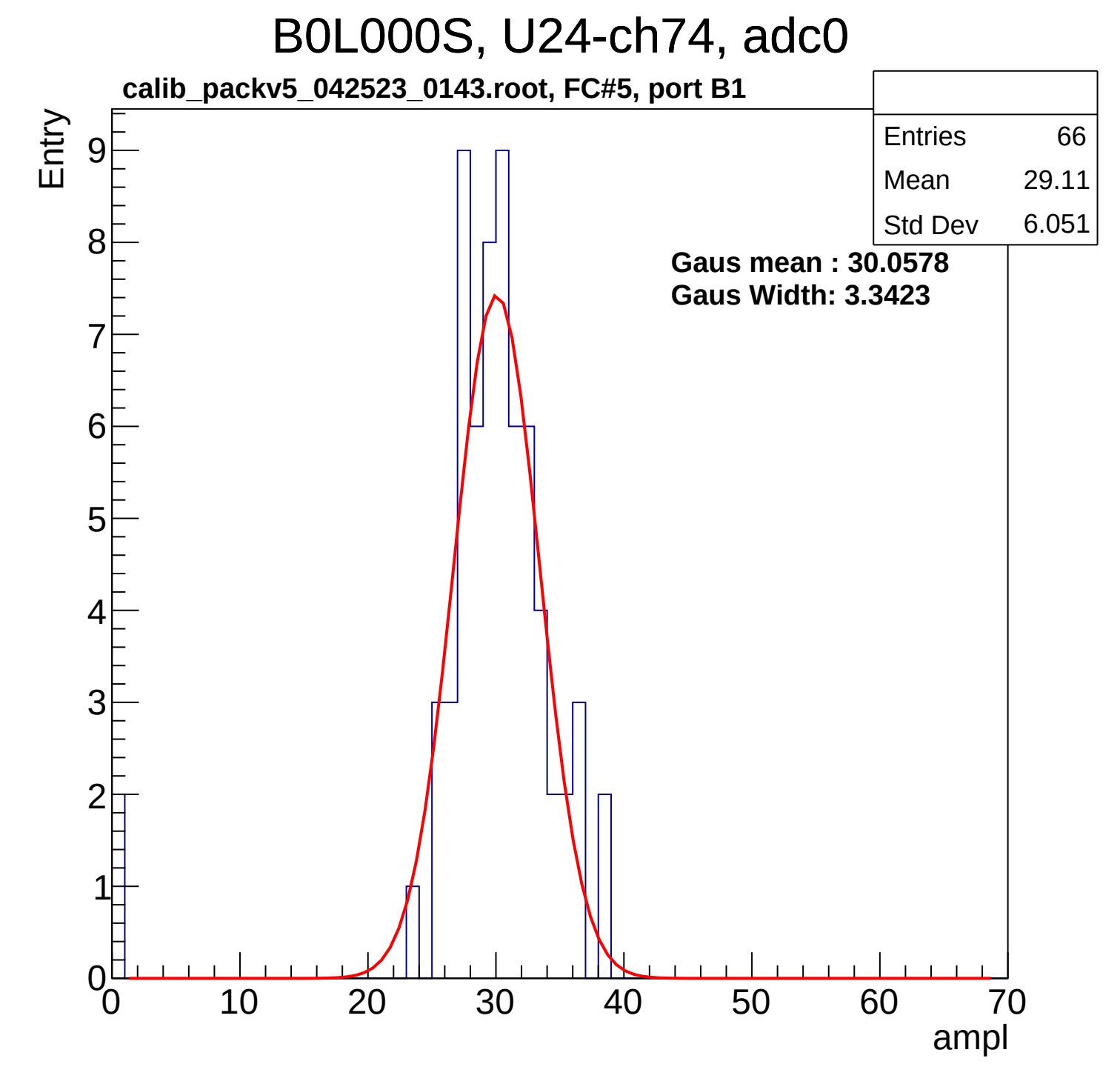
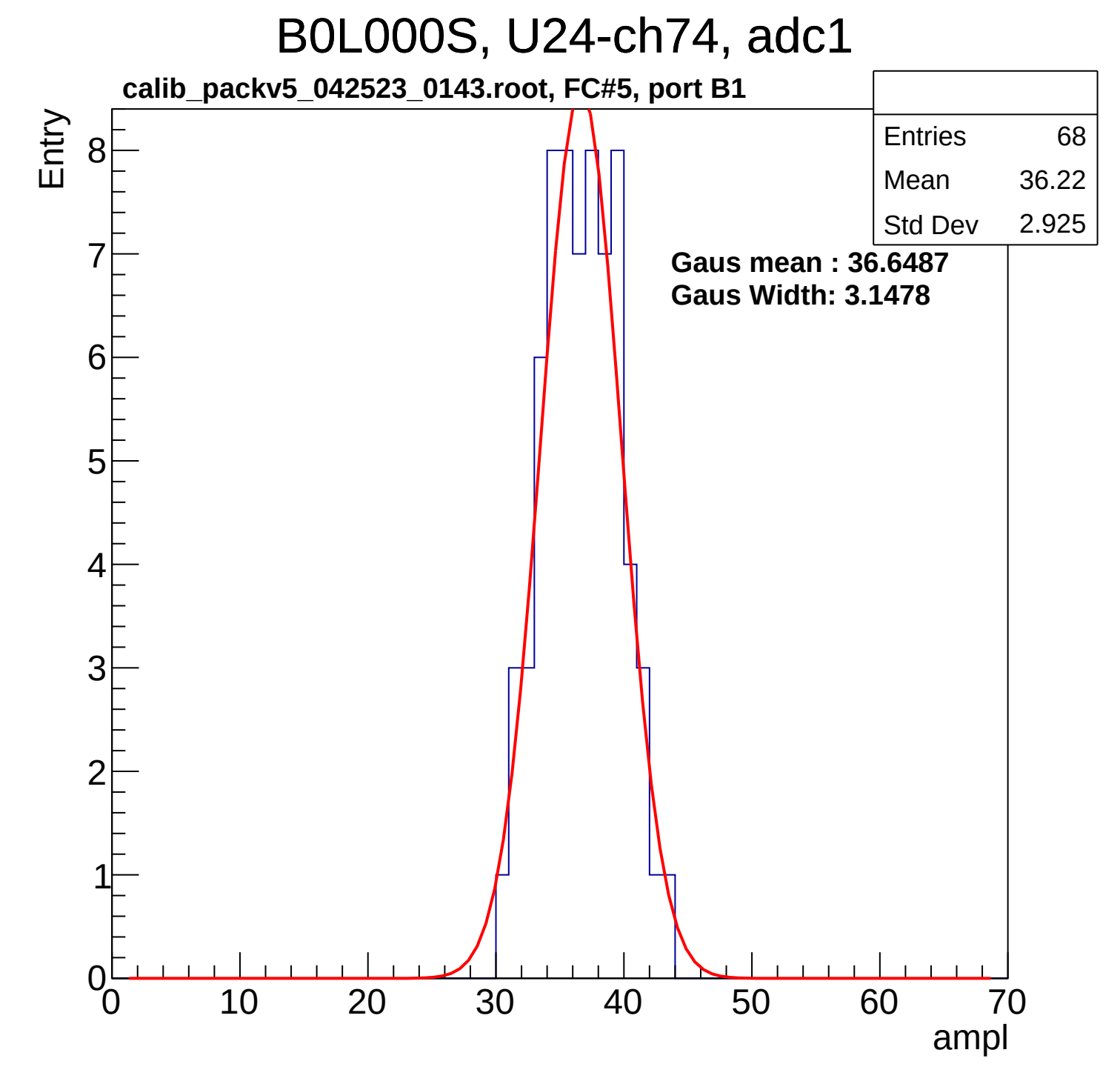
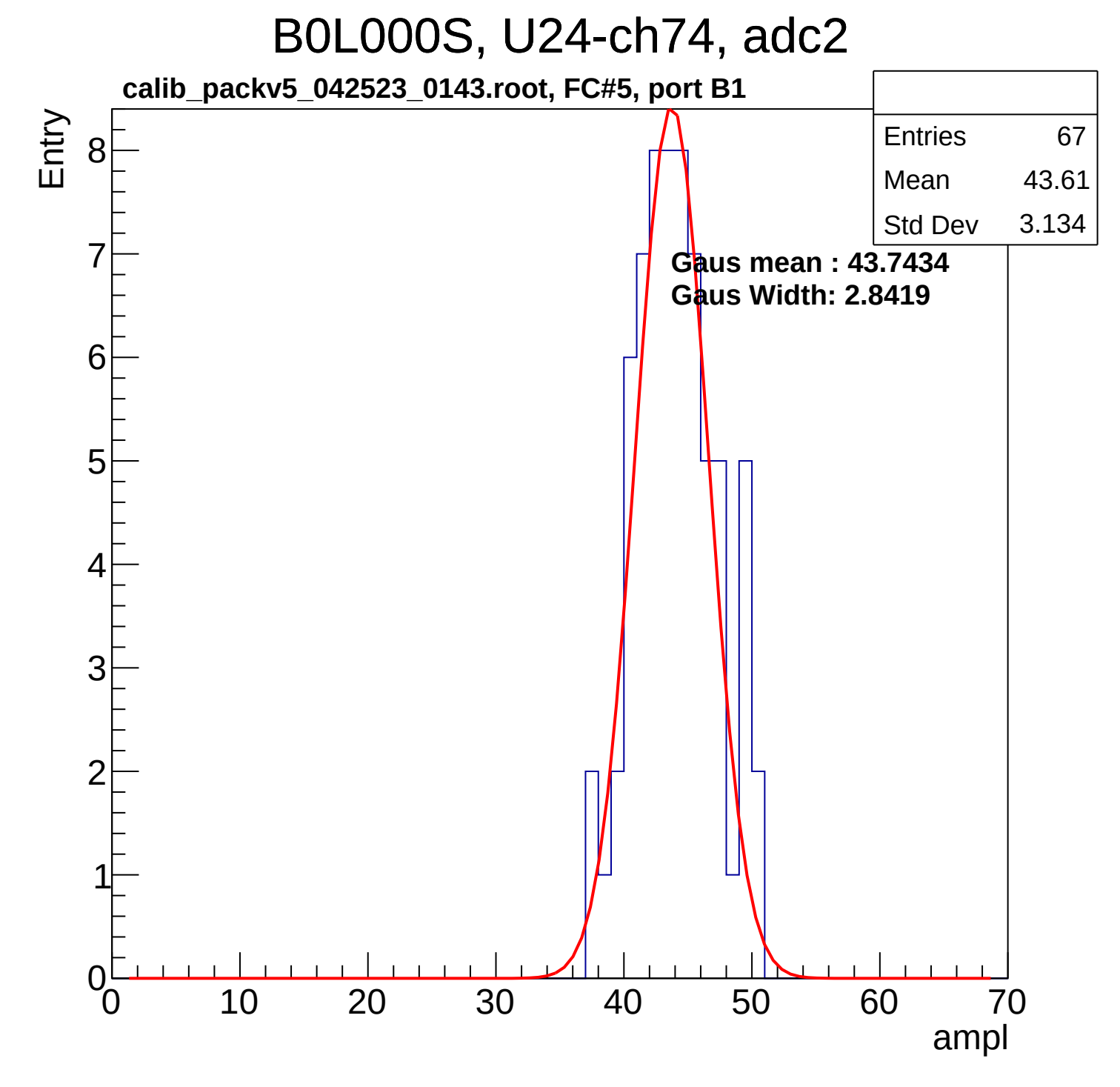
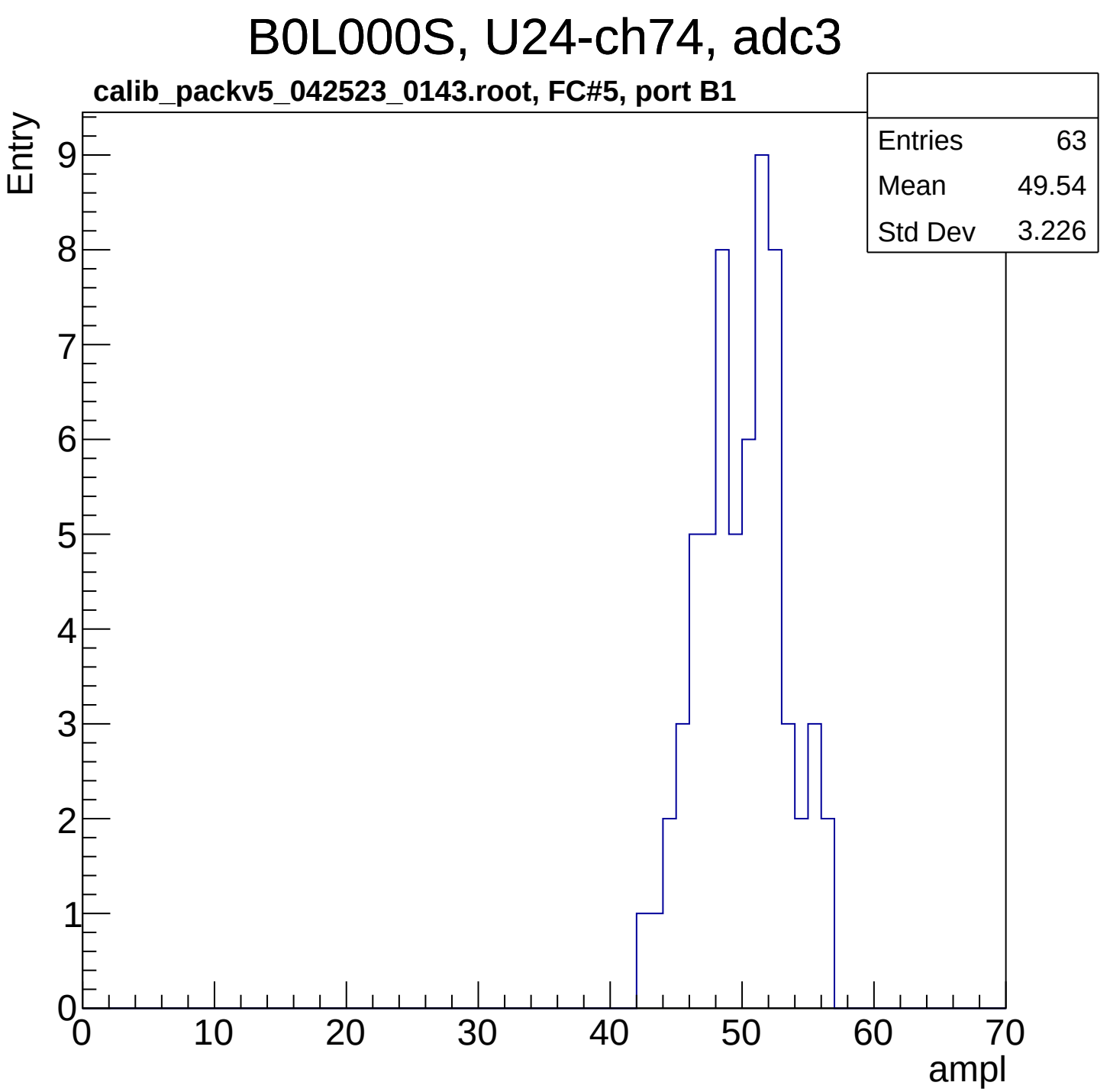

B0L000S, U24-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

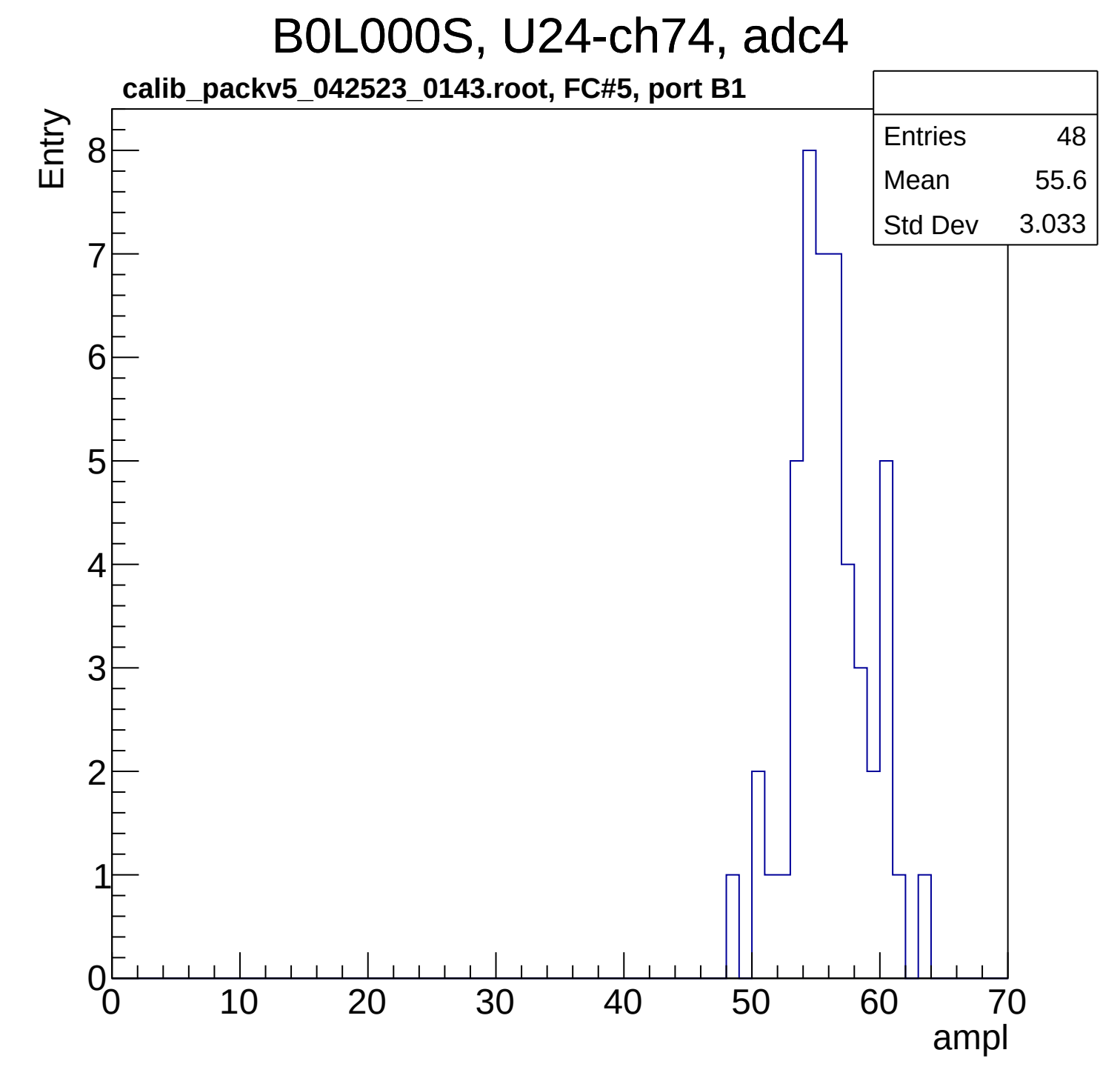

B0L000S, U24-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

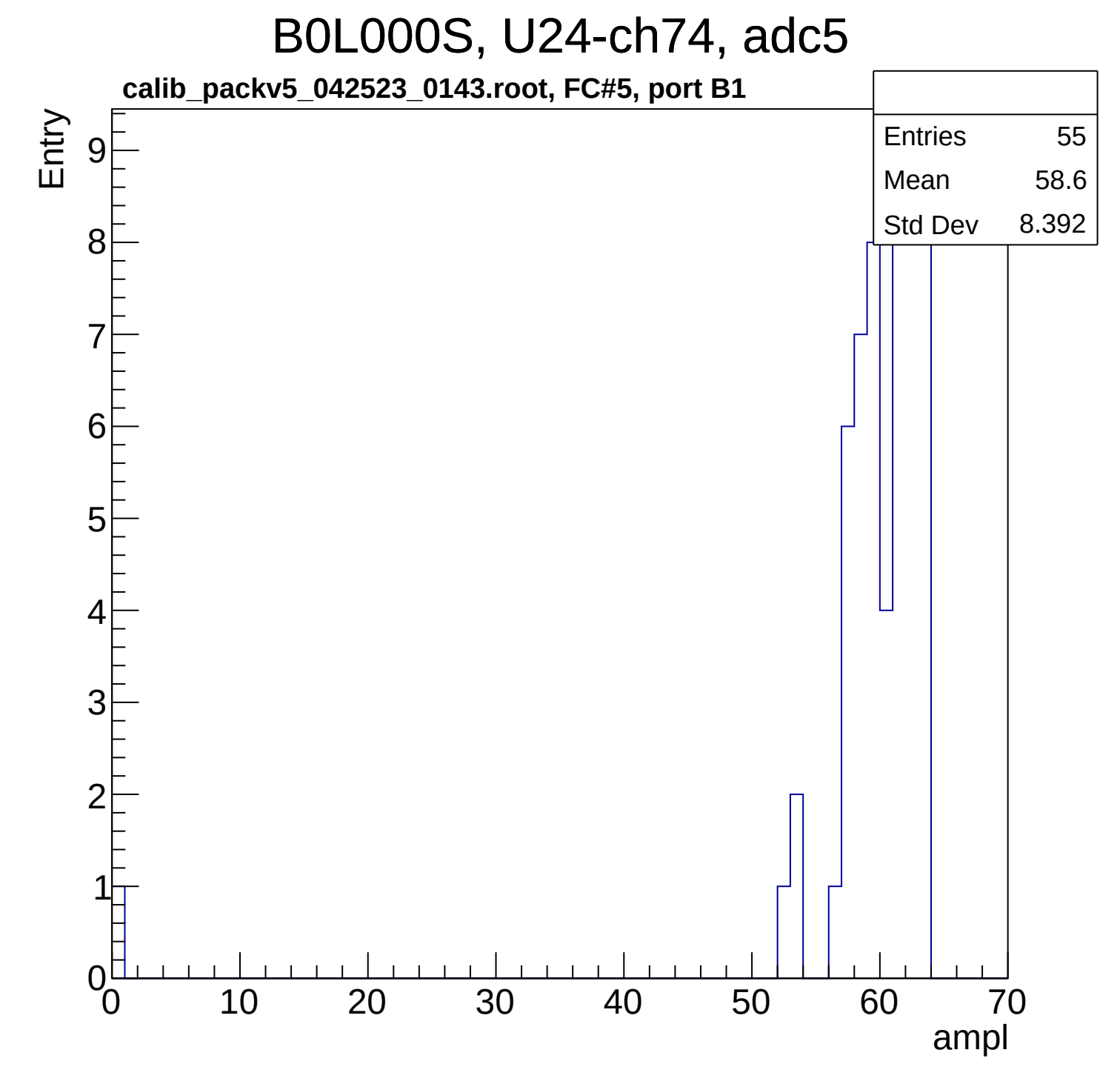

B0L000S, U24-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.11
Std Dev 6.051
Gaus mean : 30.0578
Gaus Width: 3.3423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.22
Std Dev 2.925
Gaus mean : 36.6487
Gaus Width: 3.1478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.61
Std Dev 3.134
Gaus mean : 43.7434
Gaus Width: 2.8419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.54
Std Dev 3.226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.6
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.6
Std Dev 8.392
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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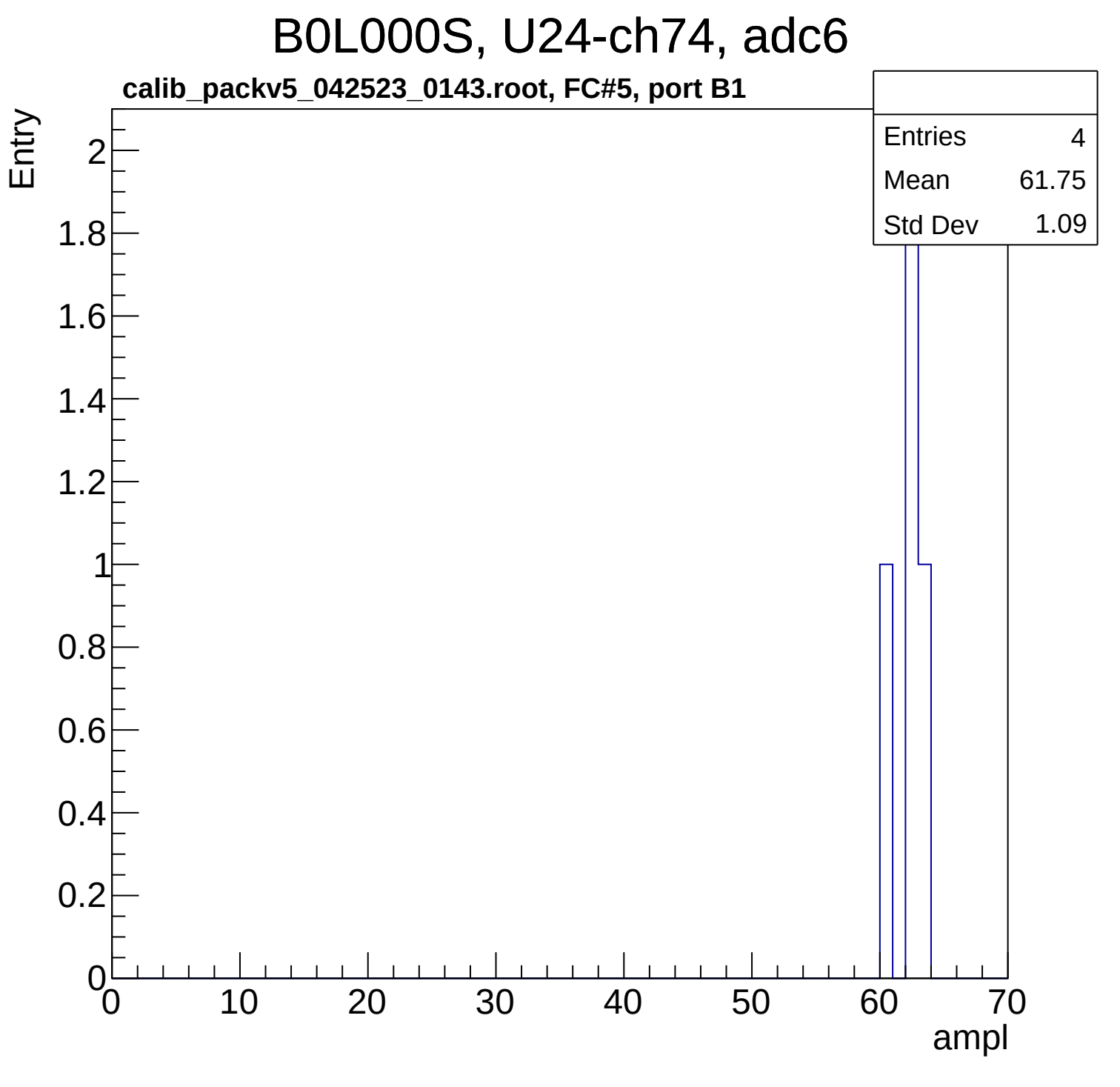
B0L000S, U24-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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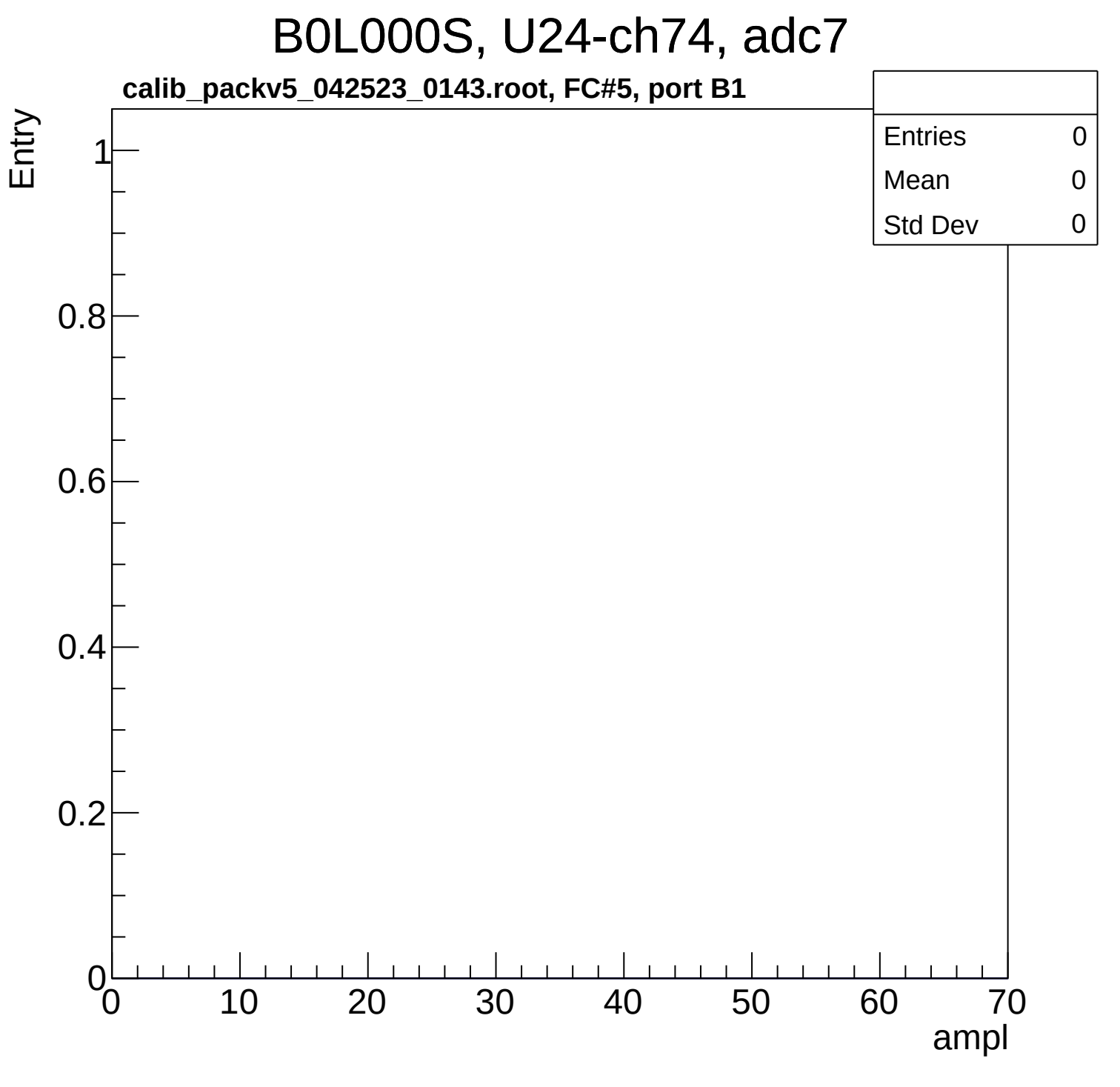
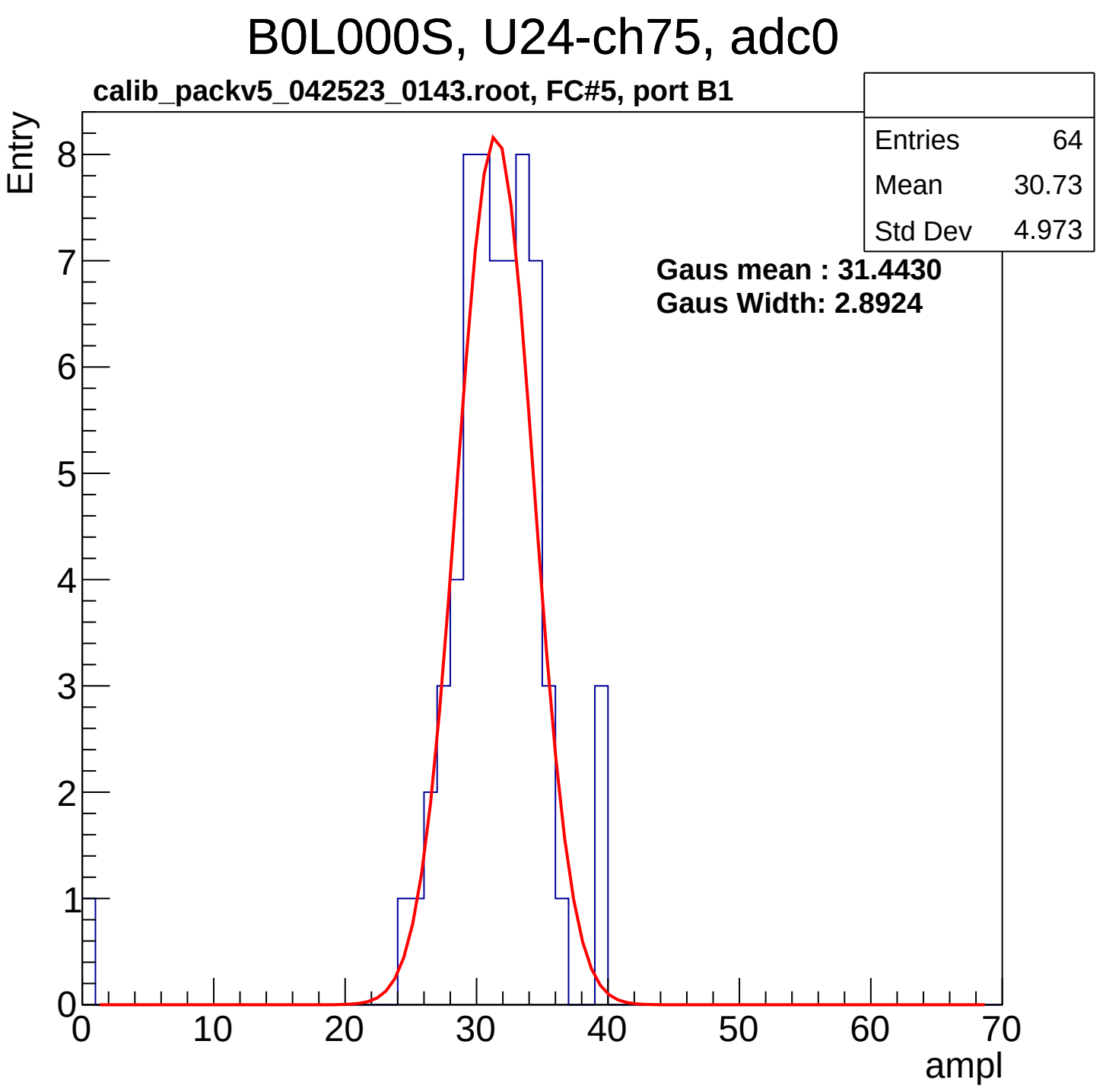
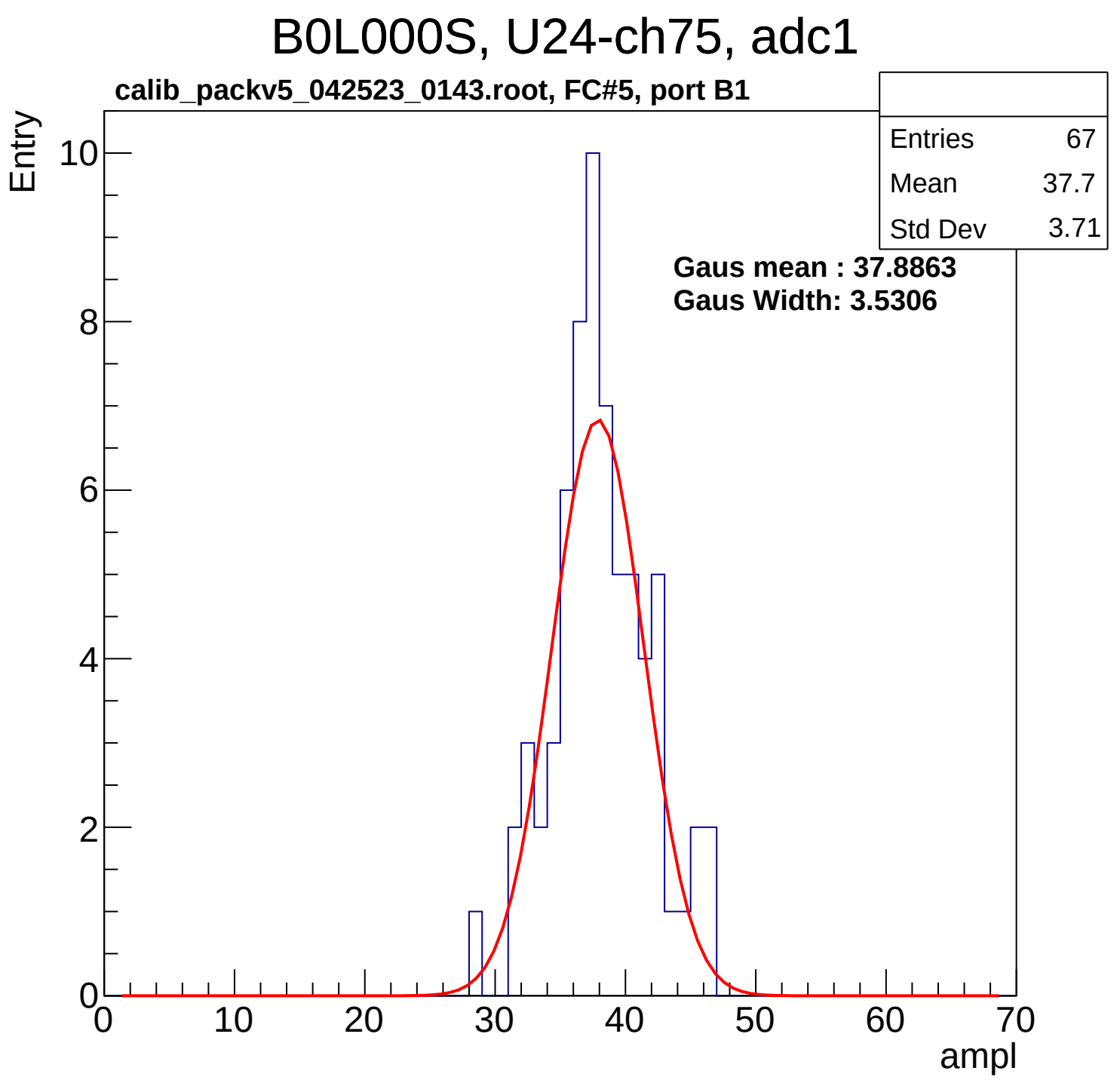
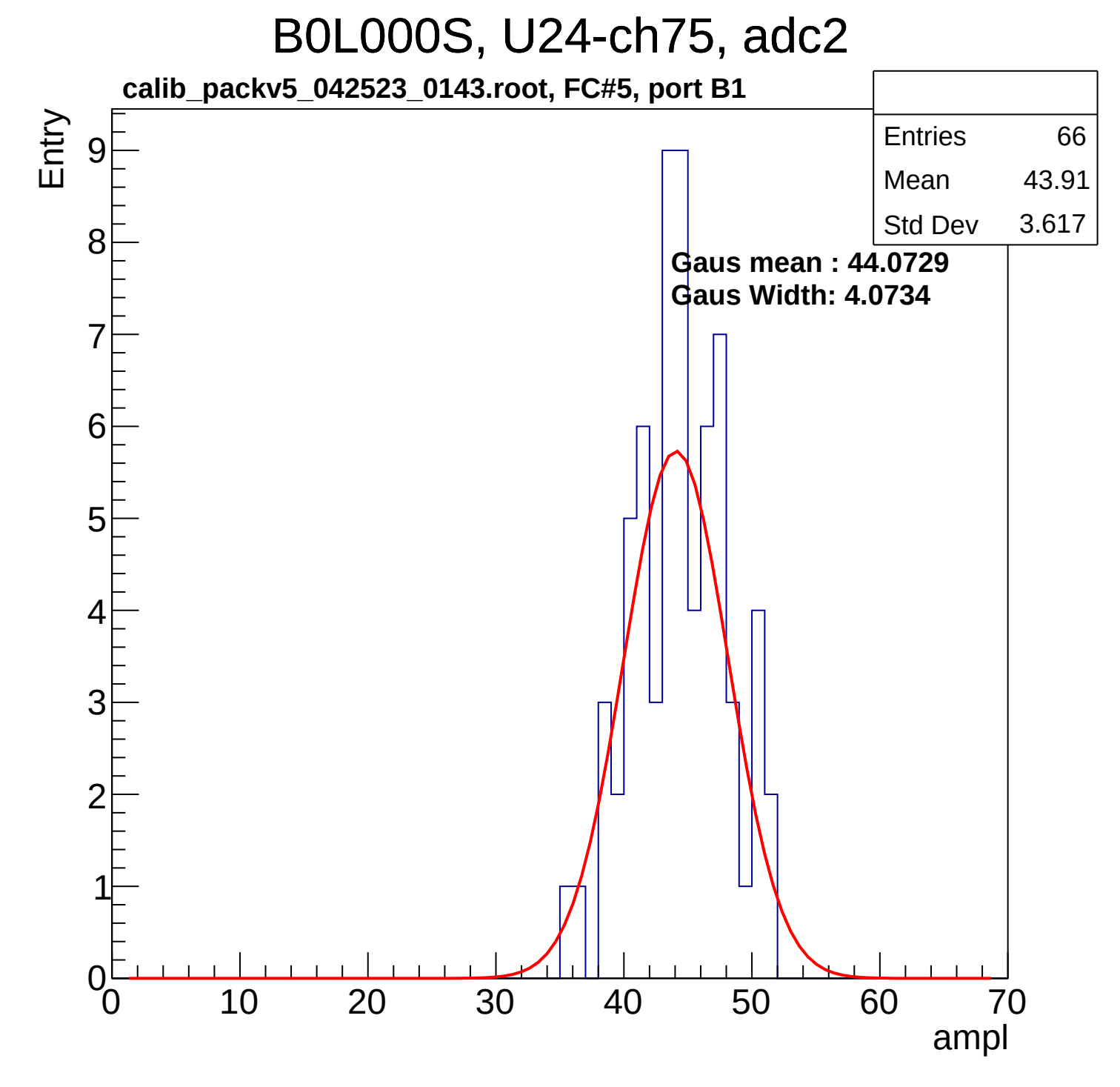

B0L000S, U24-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.73
Std Dev 4.973
Gaus mean : 31.4430
Gaus Width: 2.8924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.7
Std Dev 3.71
Gaus mean : 37.8863
Gaus Width: 3.5306
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.91
Std Dev 3.617
Gaus mean : 44.0729
Gaus Width: 4.0734
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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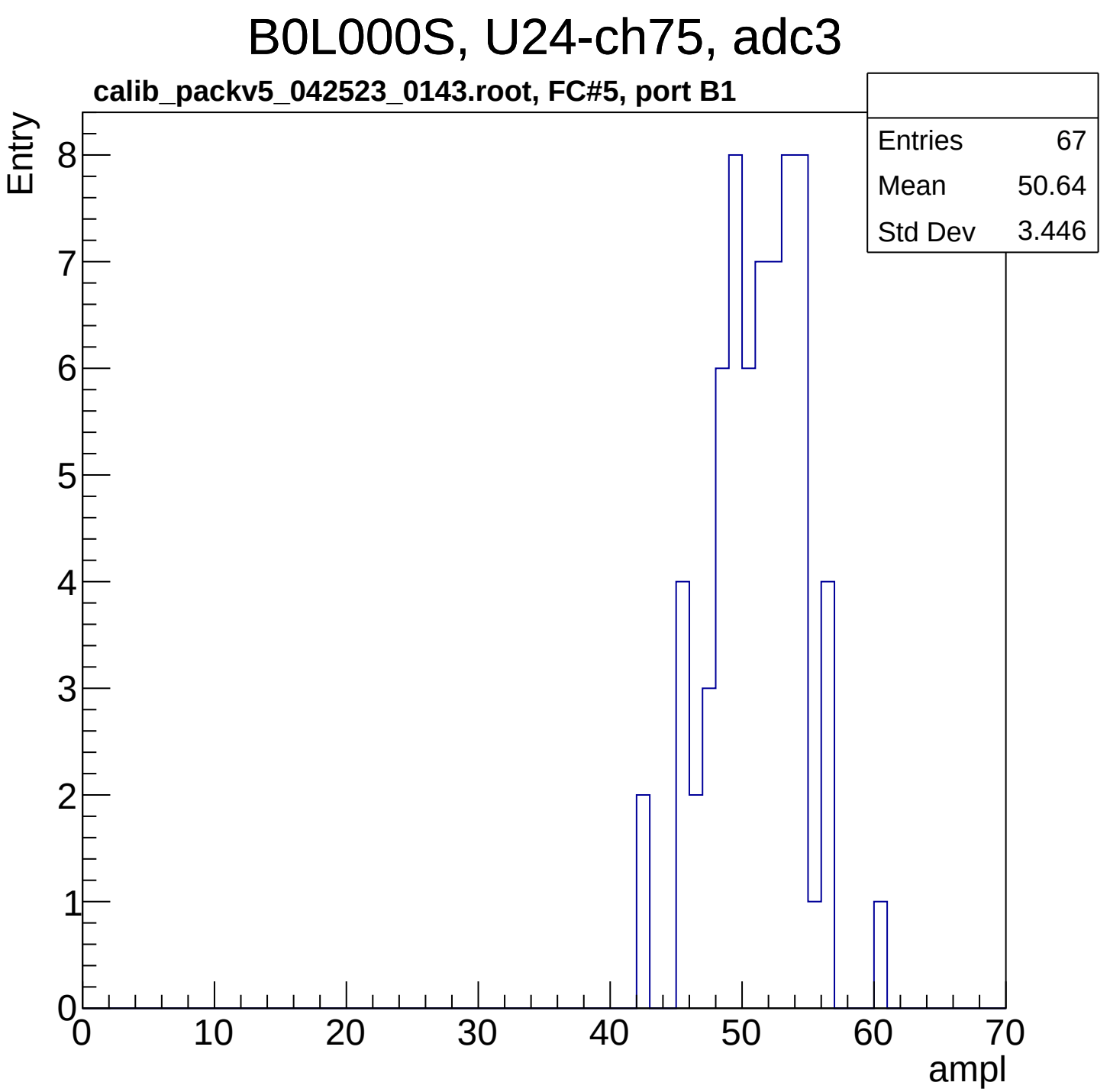
B0L000S, U24-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.64
Std Dev 3.446
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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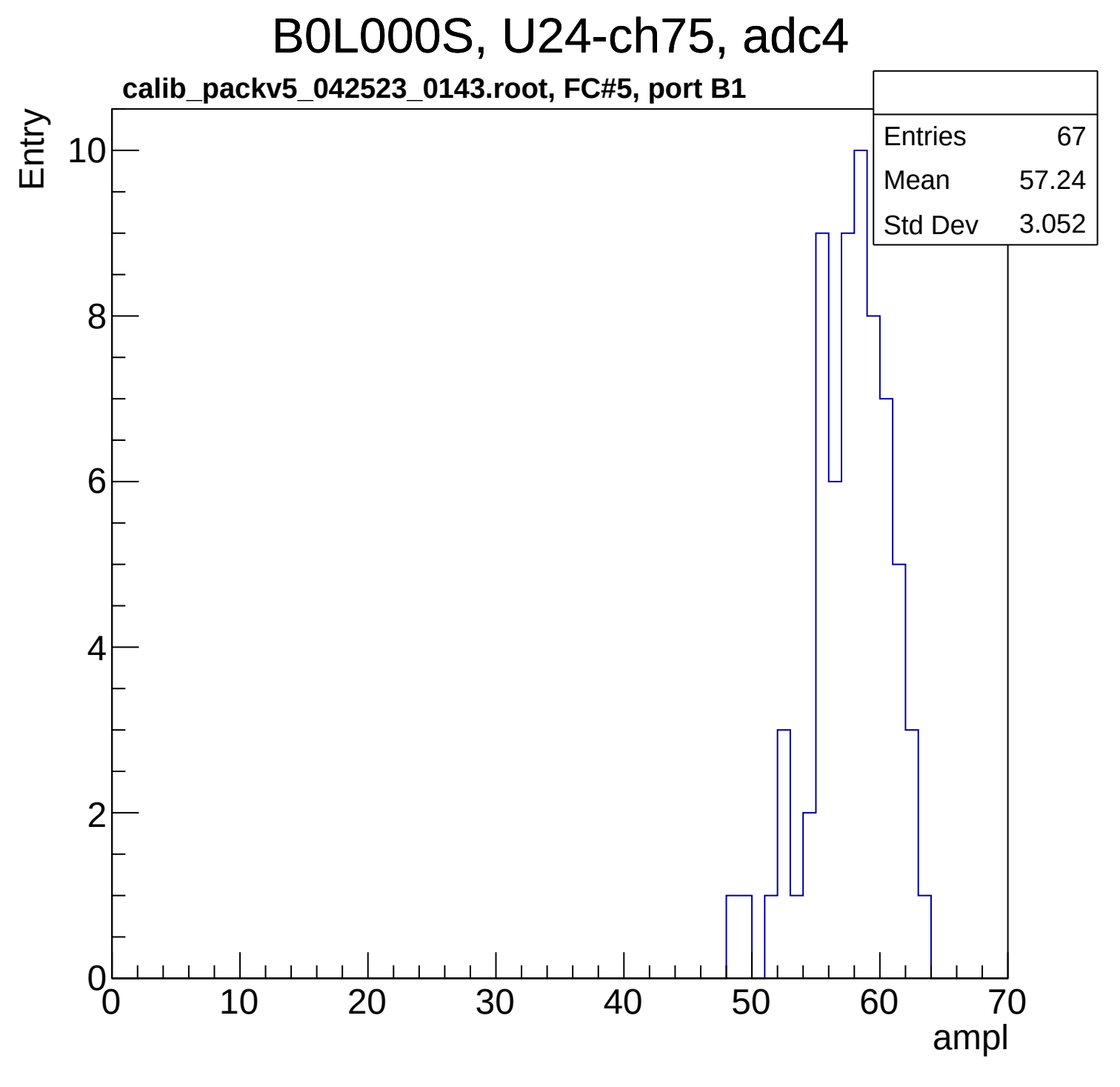
40
50
60
70


B0L000S, U24-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 57.24
Std Dev 3.052
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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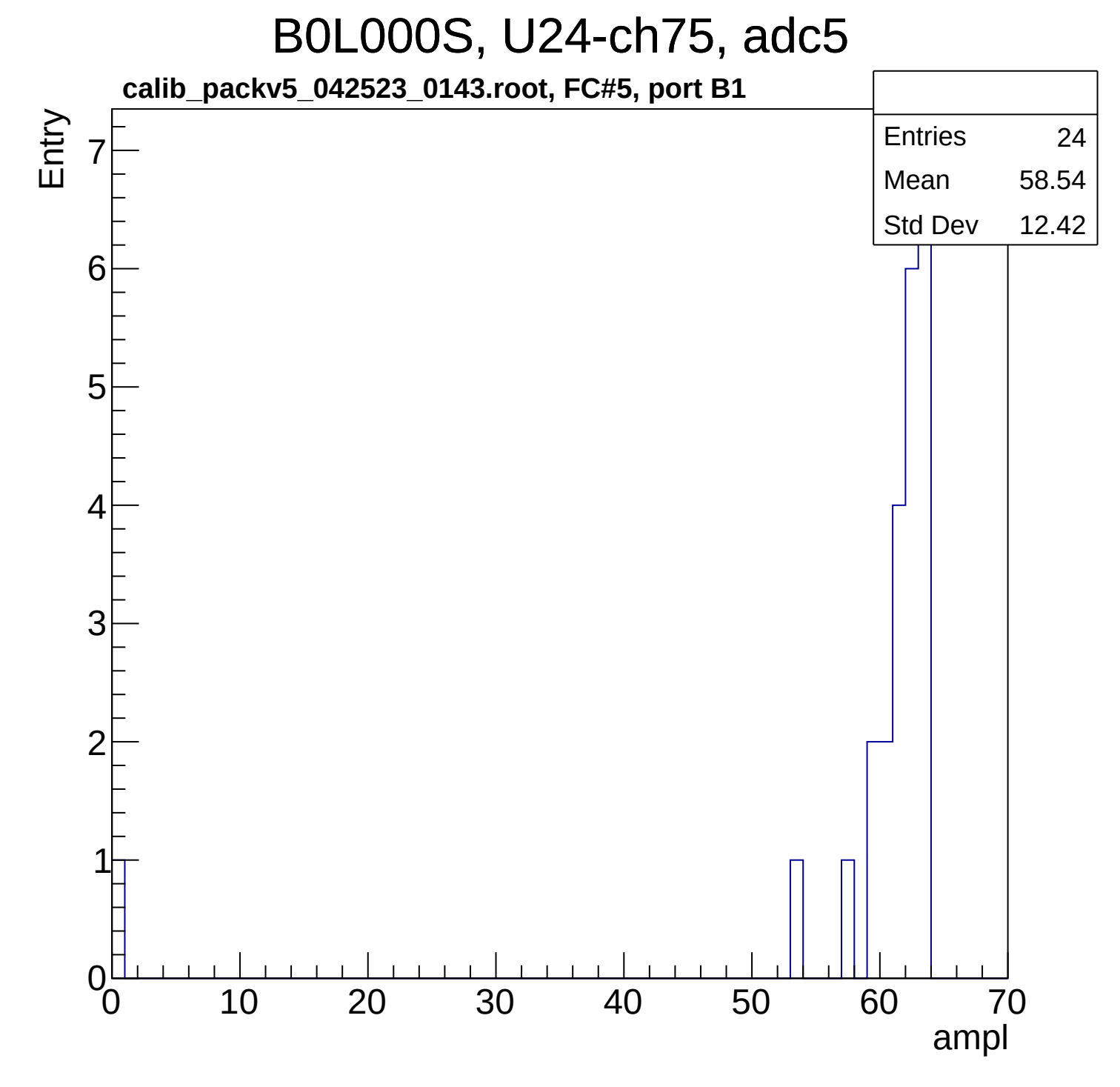
B0L000S, U24-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.54
Std Dev 12.42
Entry
ampl
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1
2
3
4
5
6
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0
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20
30
40
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60
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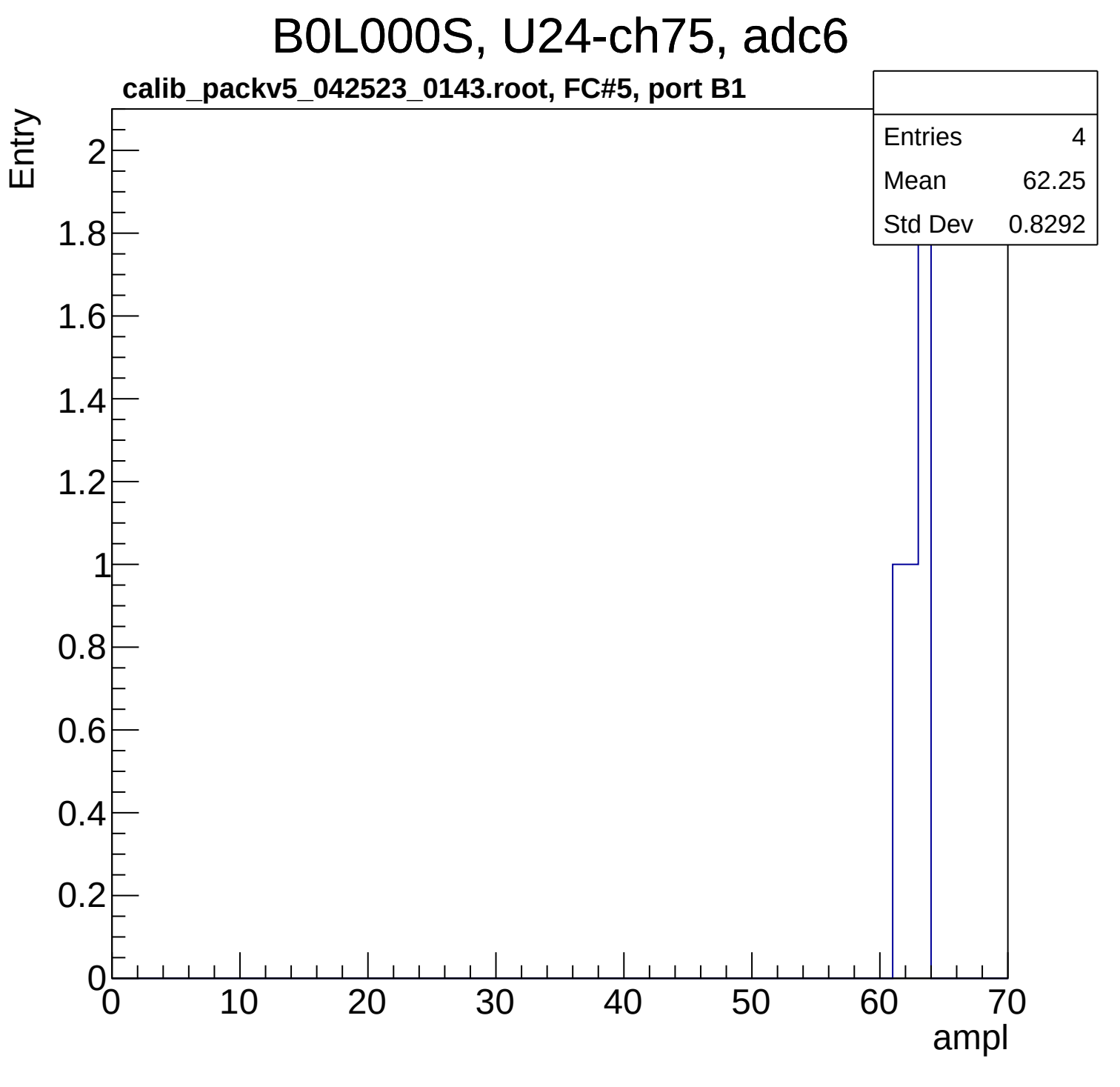
B0L000S, U24-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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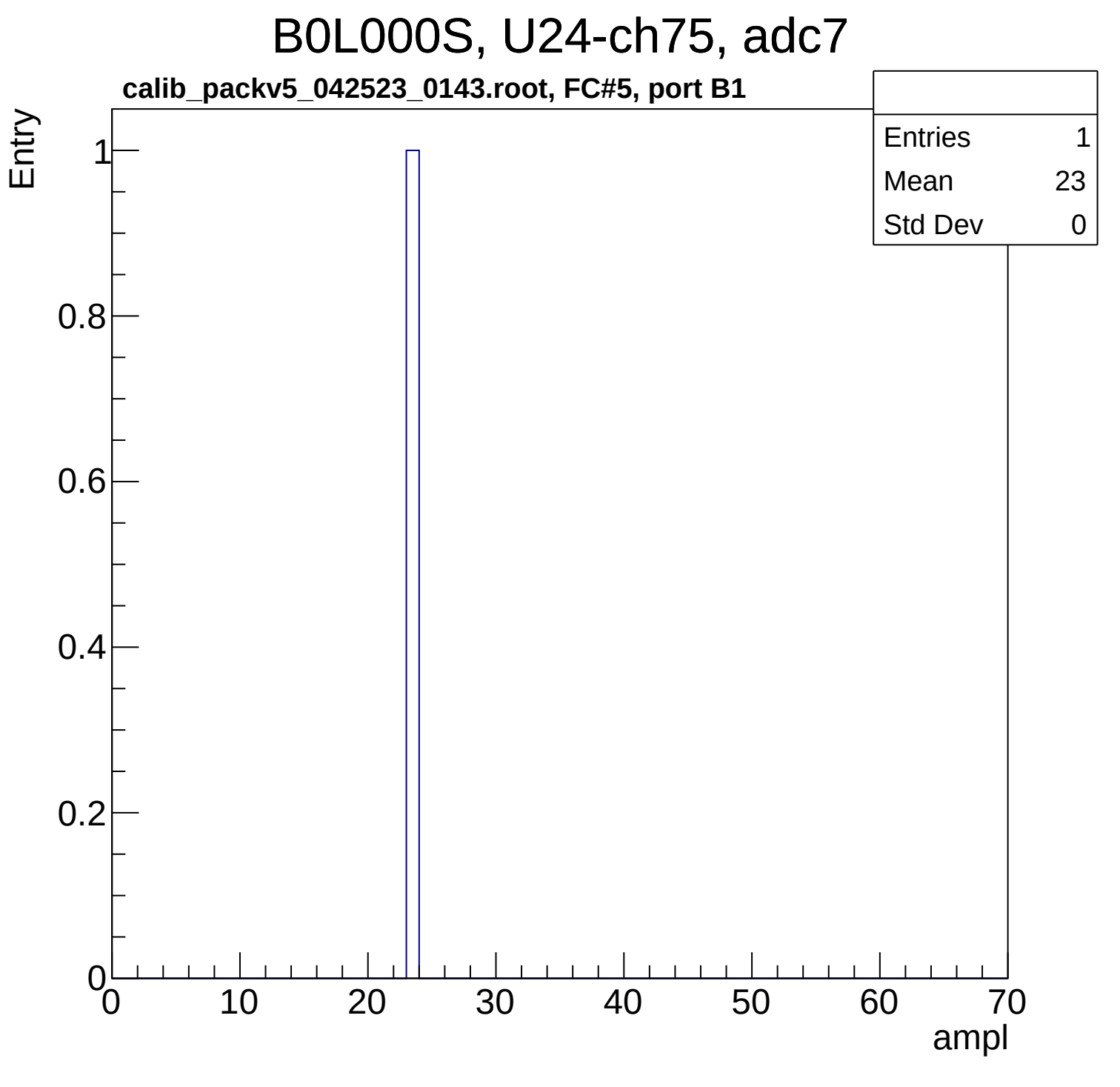
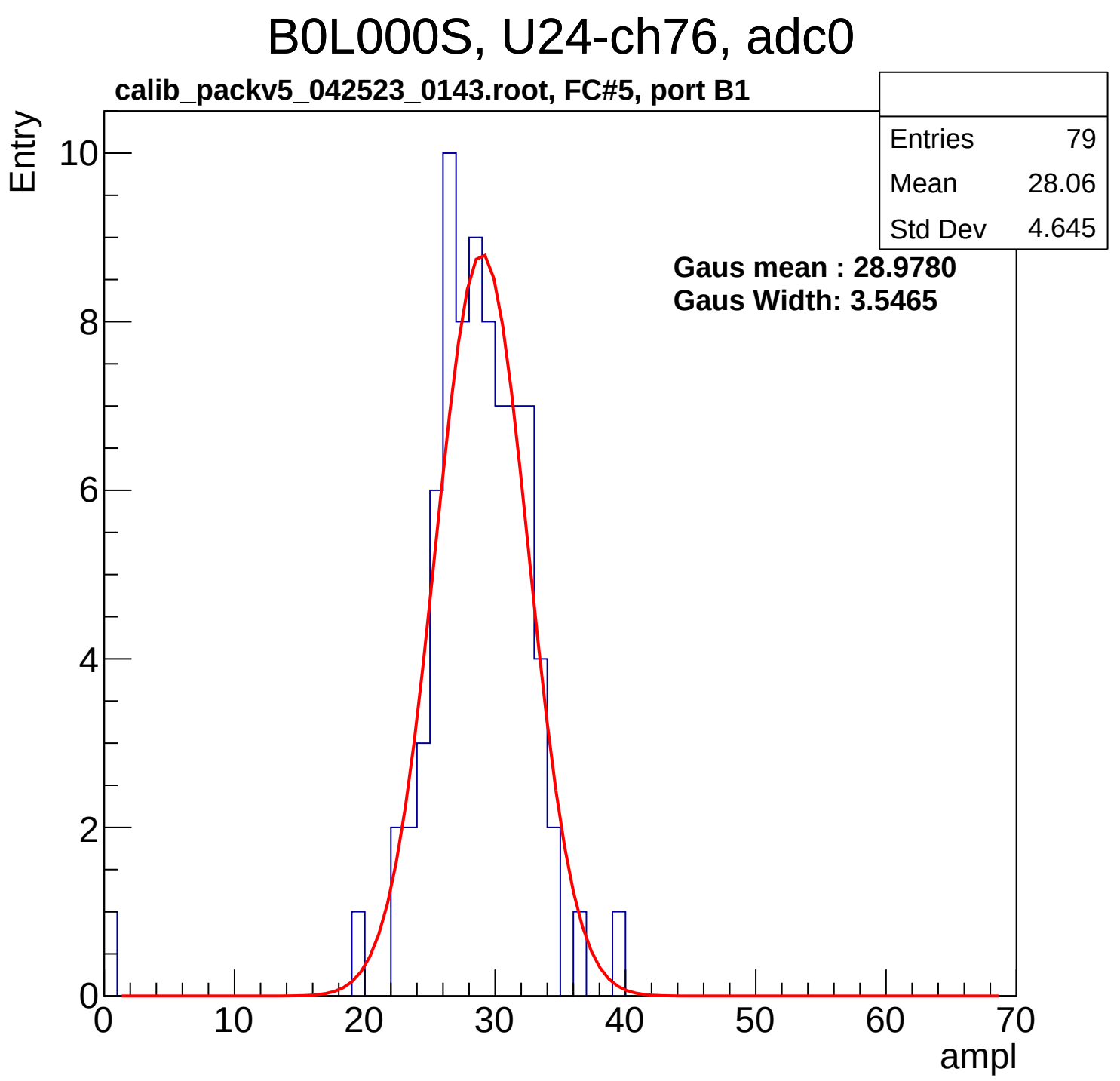
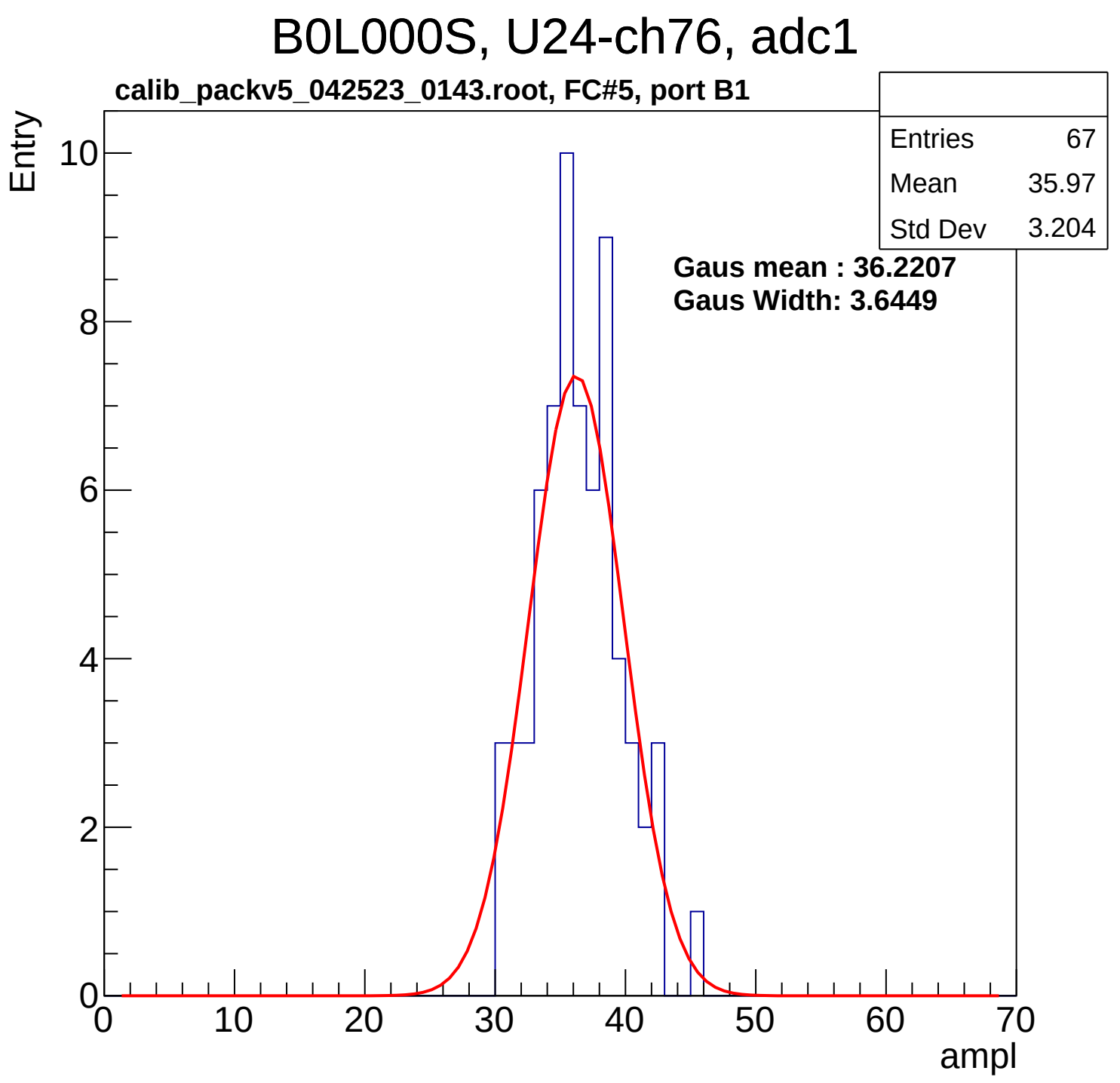
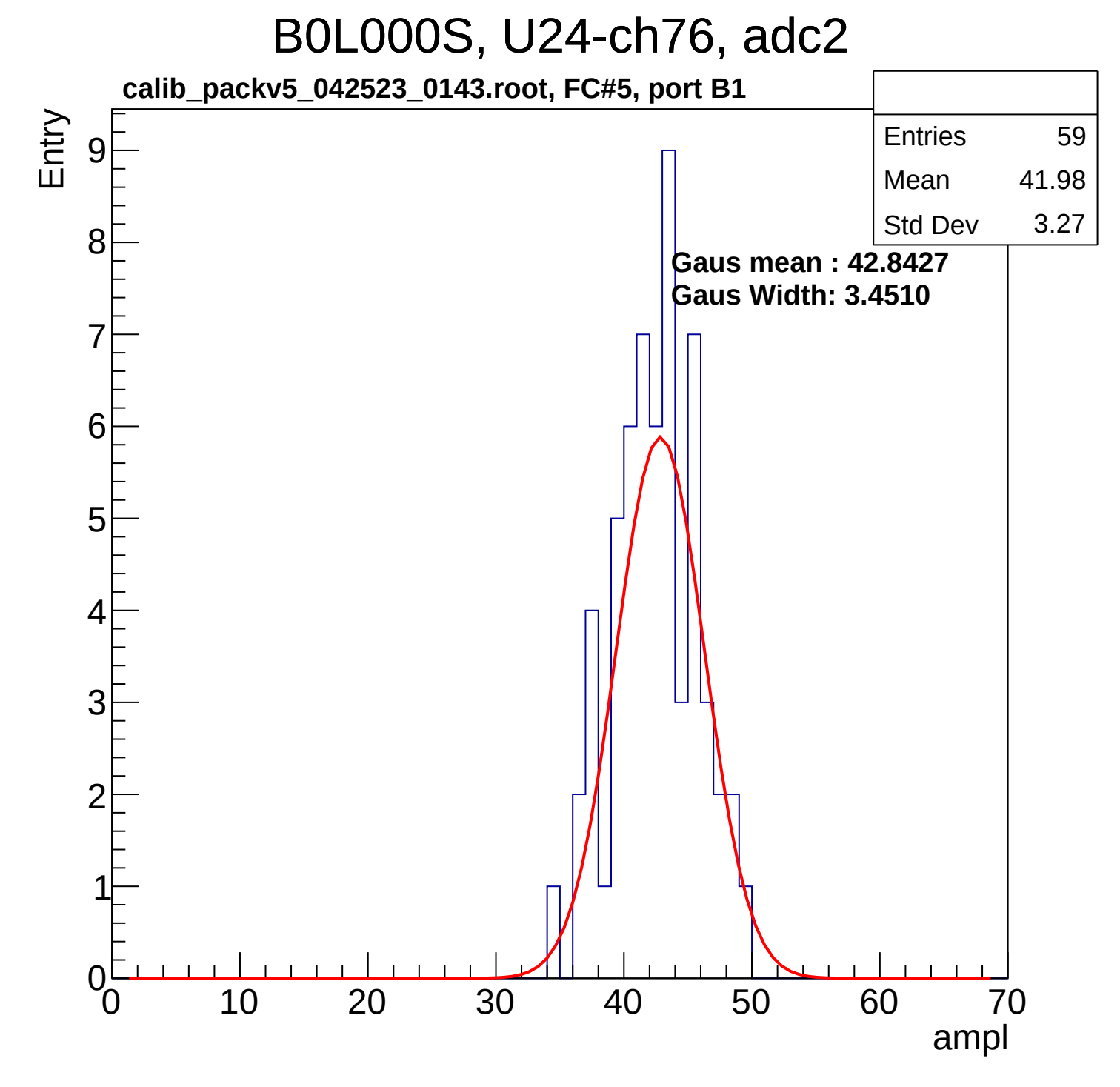
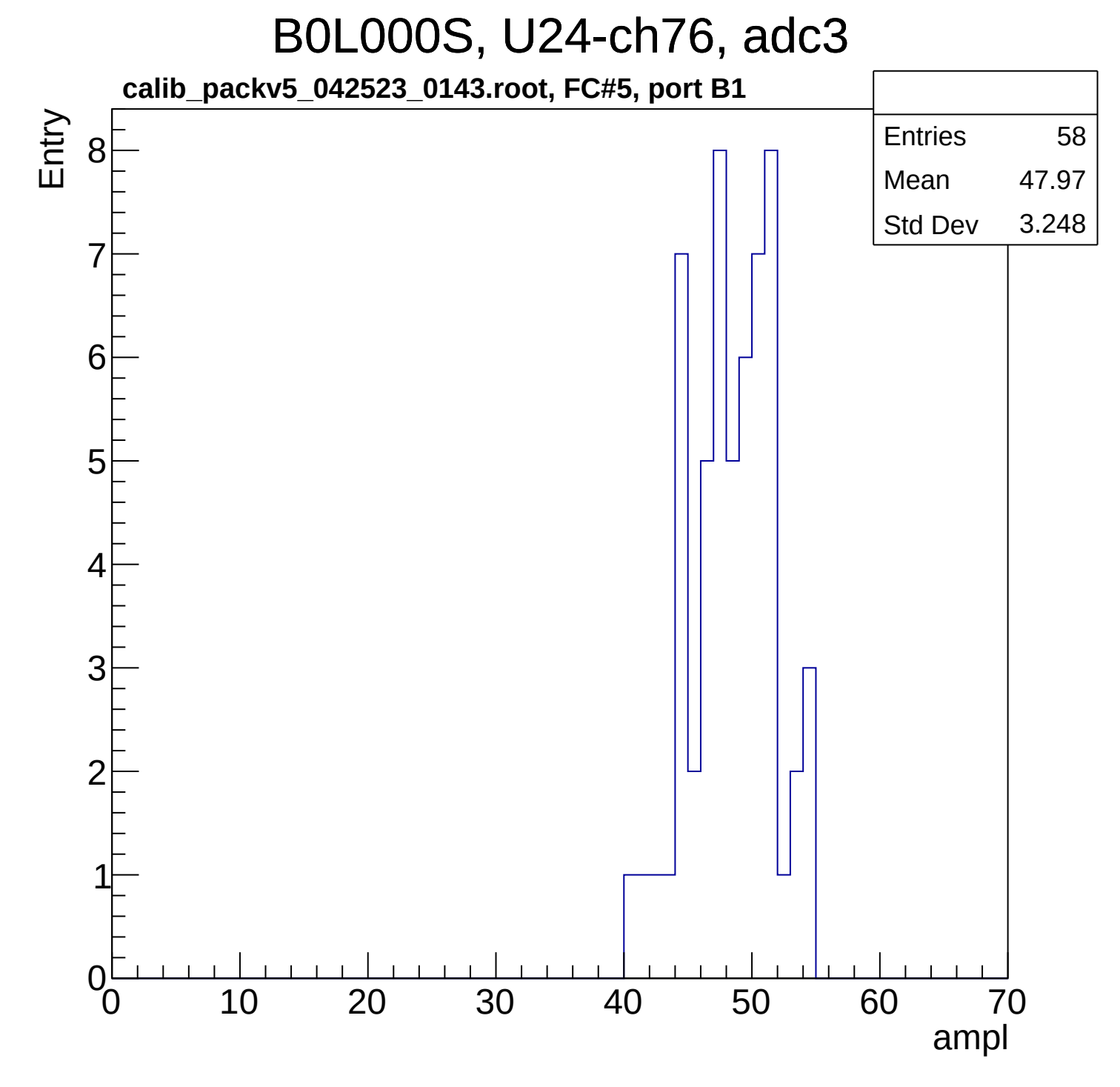
60
70


B0L000S, U24-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 28.06
Std Dev 4.645
Gaus mean : 28.9780
Gaus Width: 3.5465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.97
Std Dev 3.204
Gaus mean : 36.2207
Gaus Width: 3.6449
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 41.98
Std Dev 3.27
Gaus mean : 42.8427
Gaus Width: 3.4510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.97
Std Dev 3.248
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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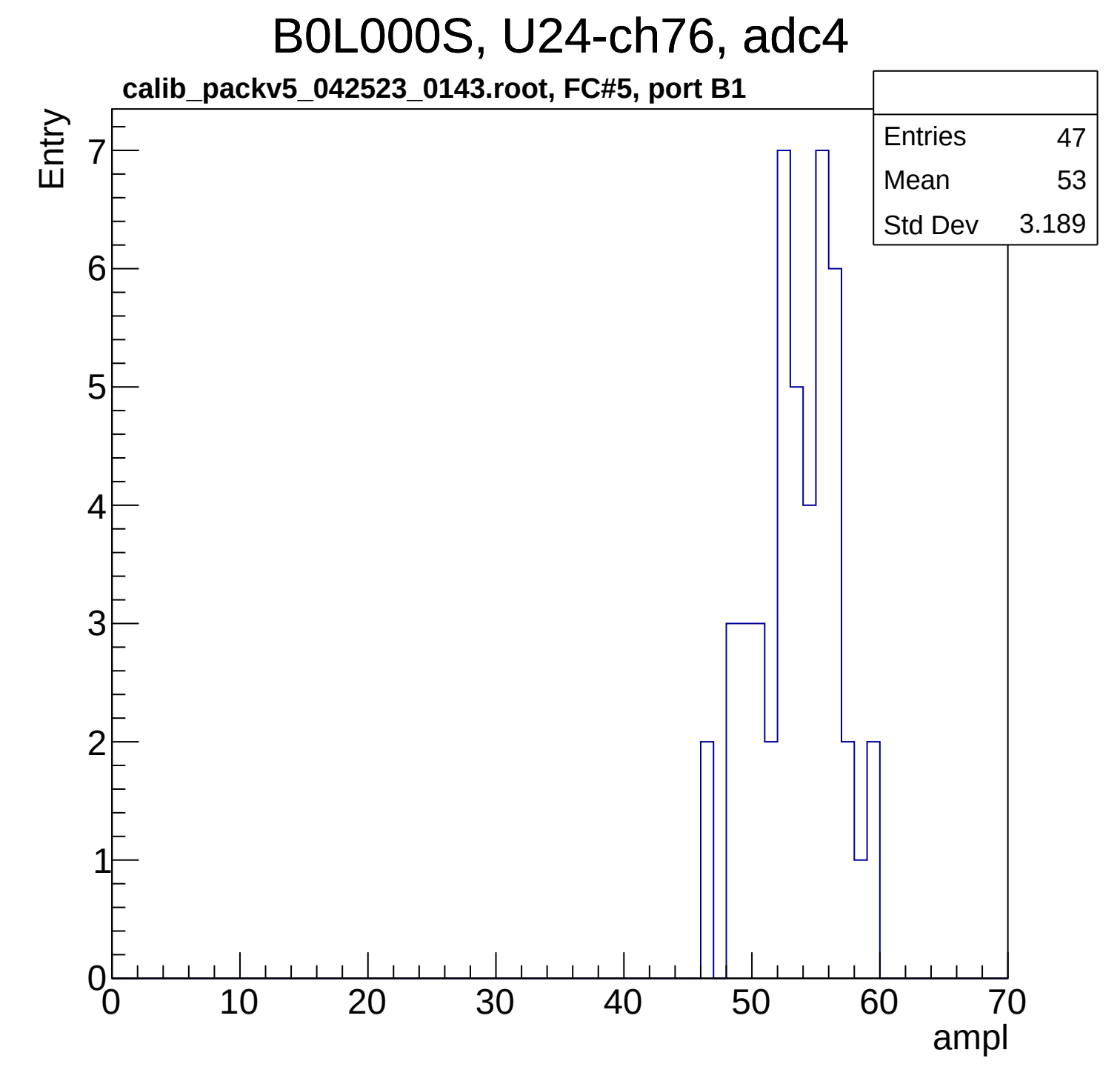
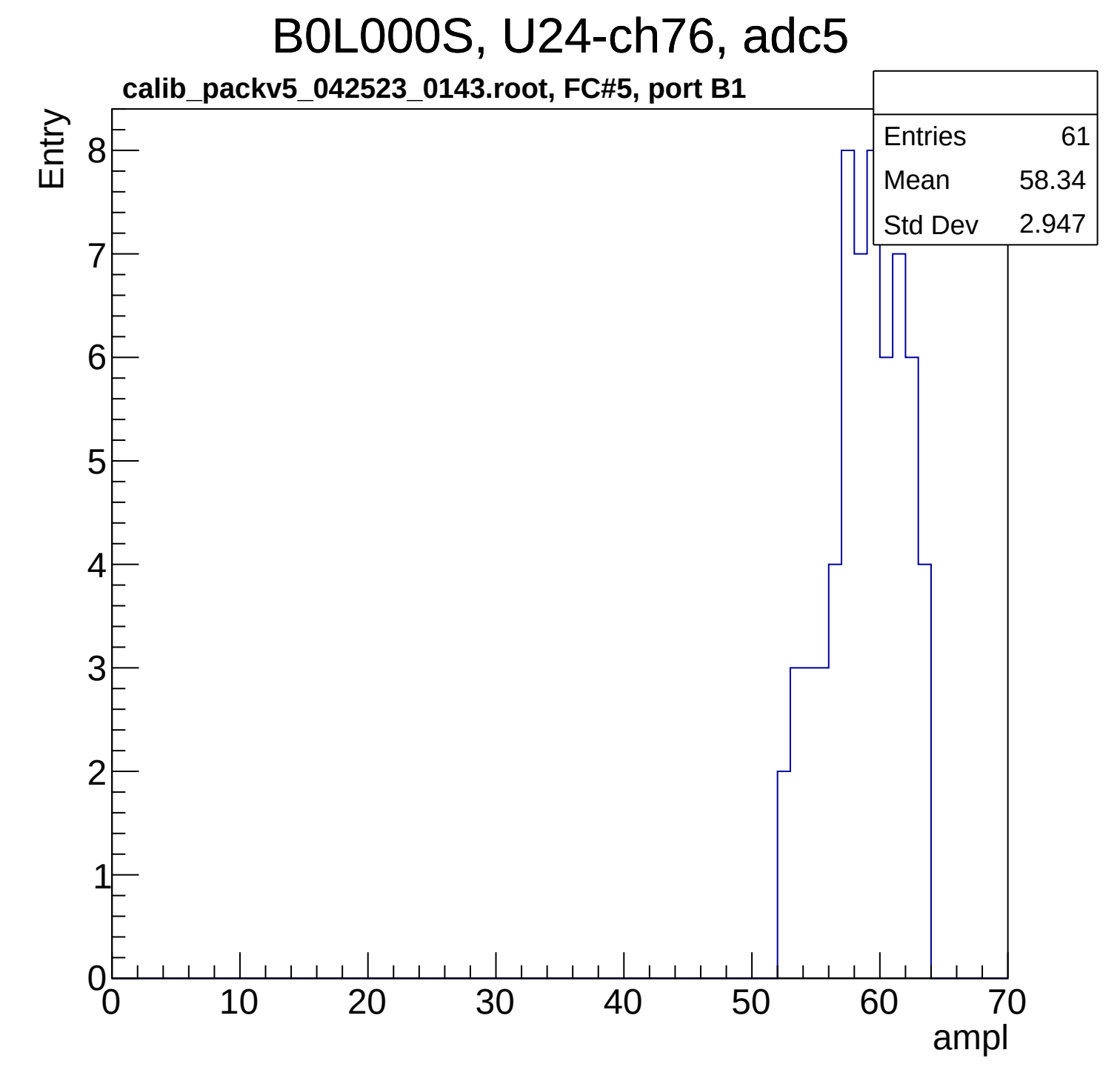

B0L000S, U24-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 53
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 58.34
Std Dev 2.947
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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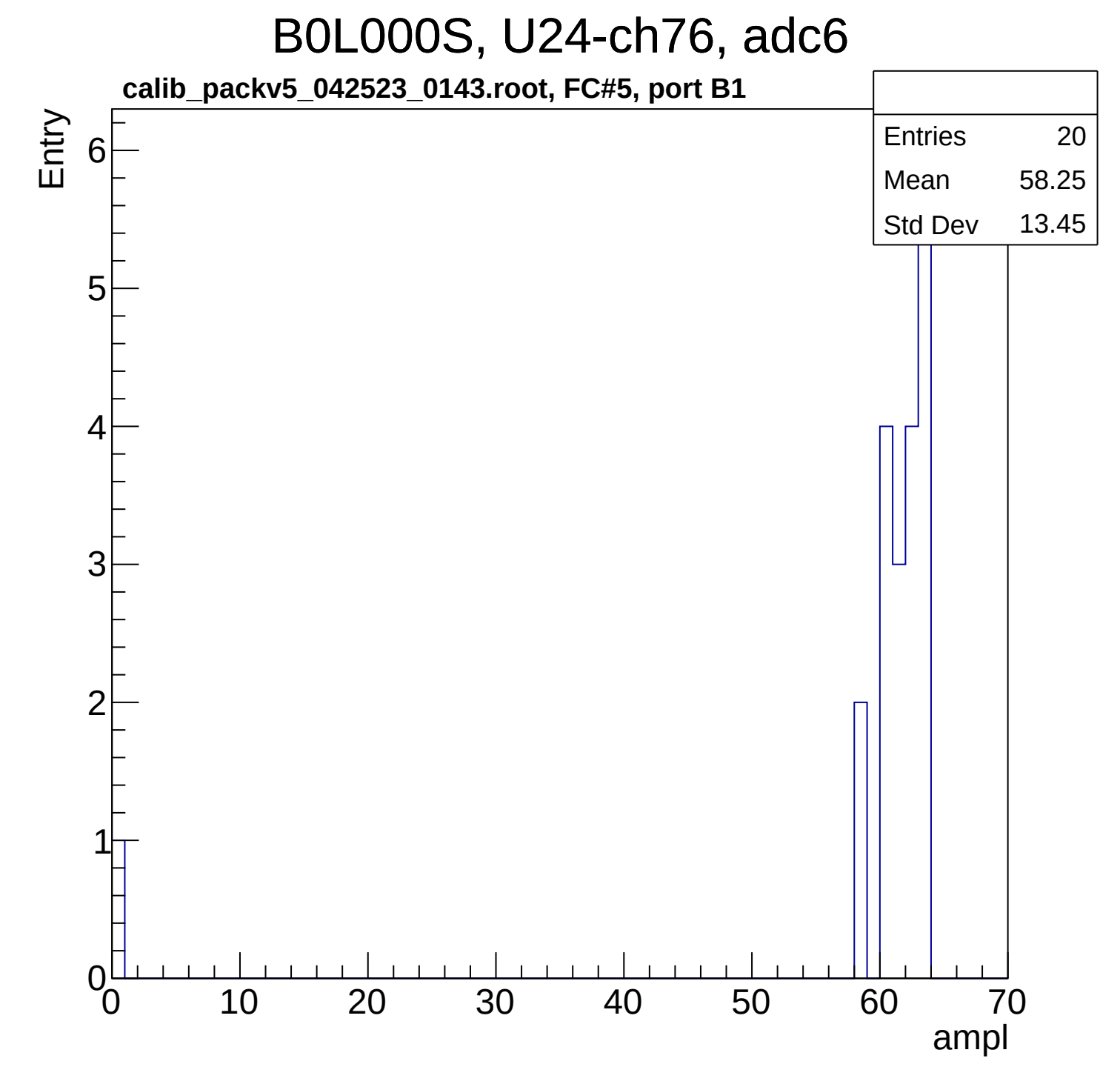
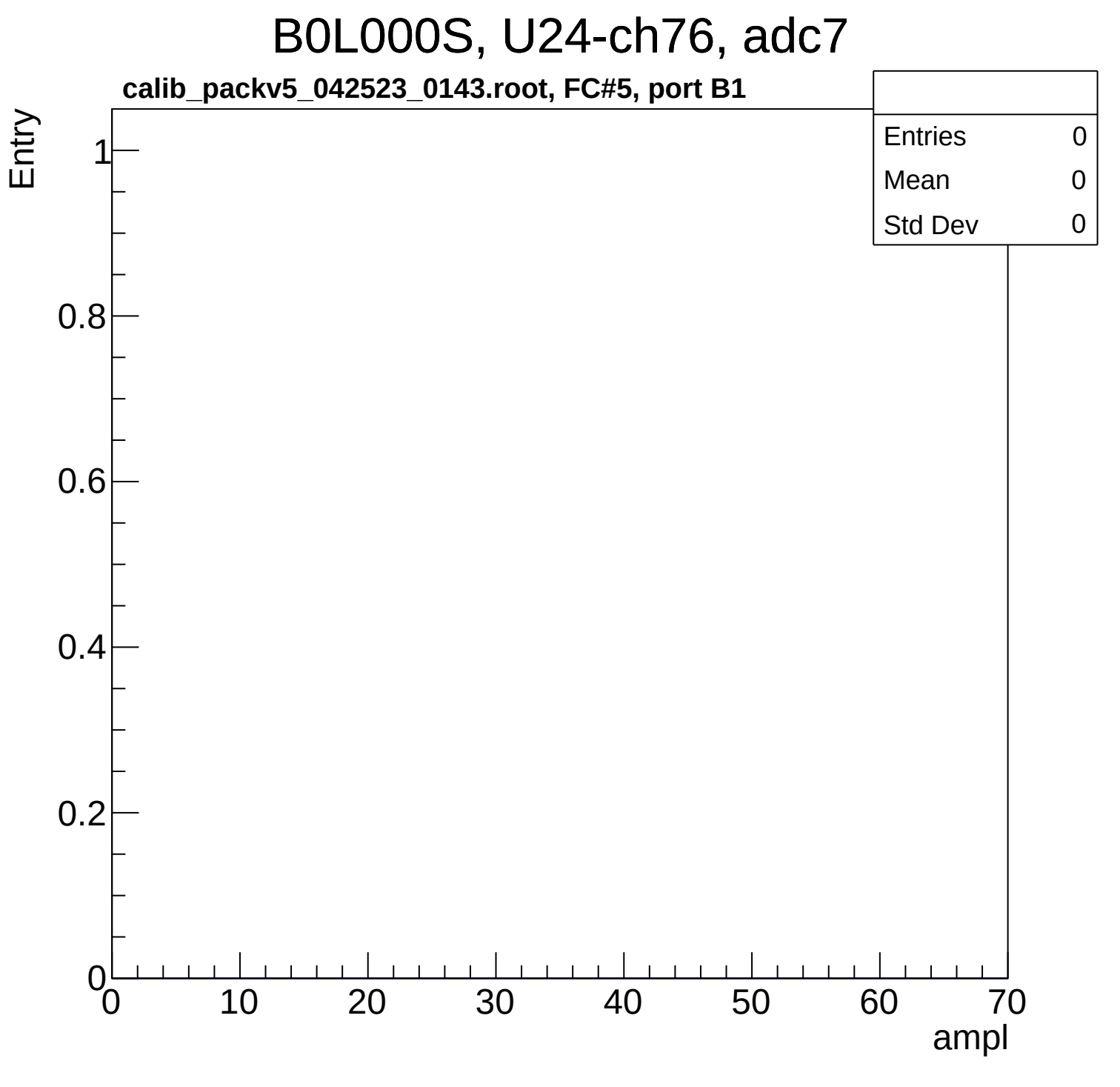
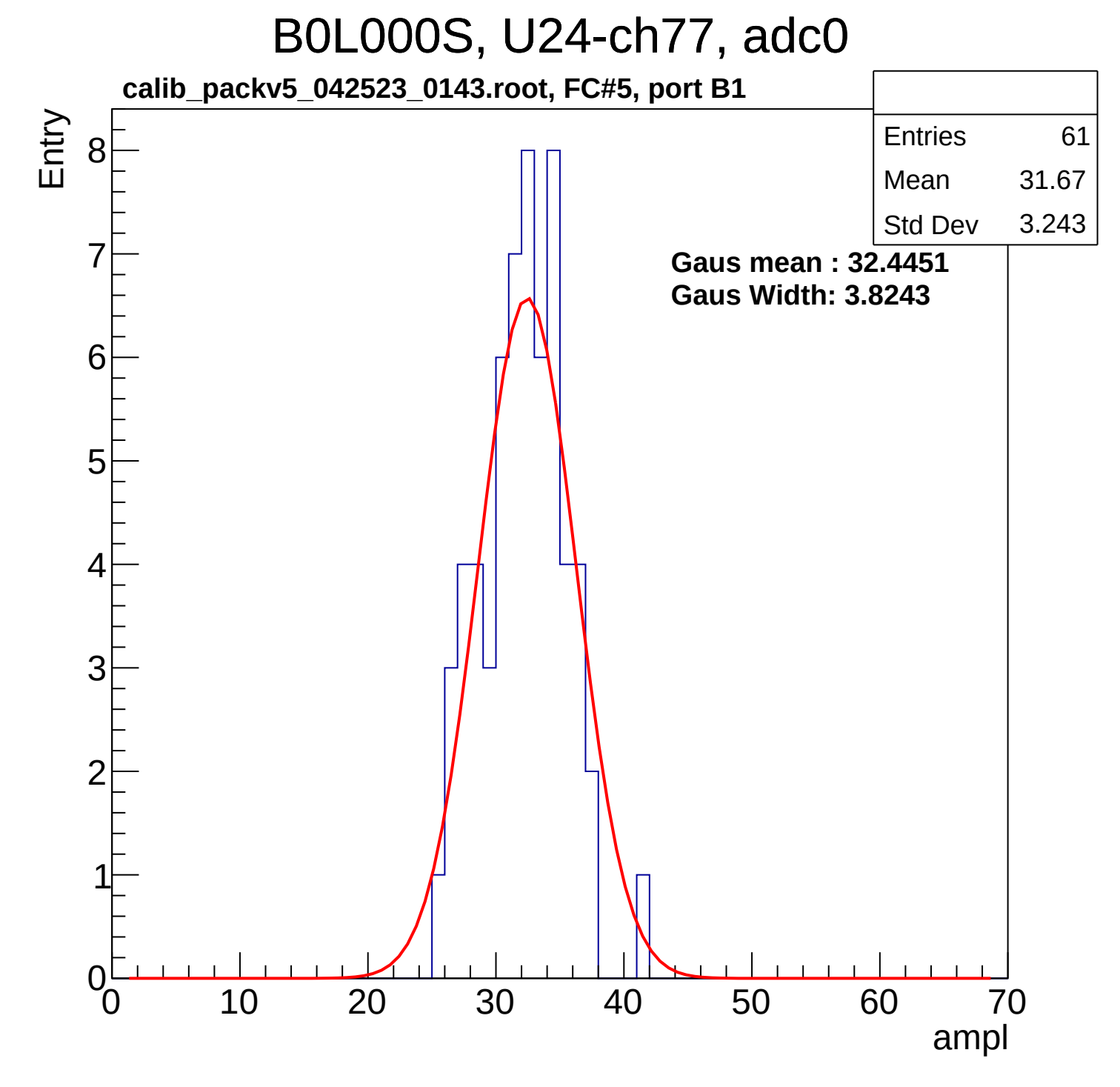

B0L000S, U24-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.25
Std Dev 13.45
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 31.67
Std Dev 3.243
Gaus mean : 32.4451
Gaus Width: 3.8243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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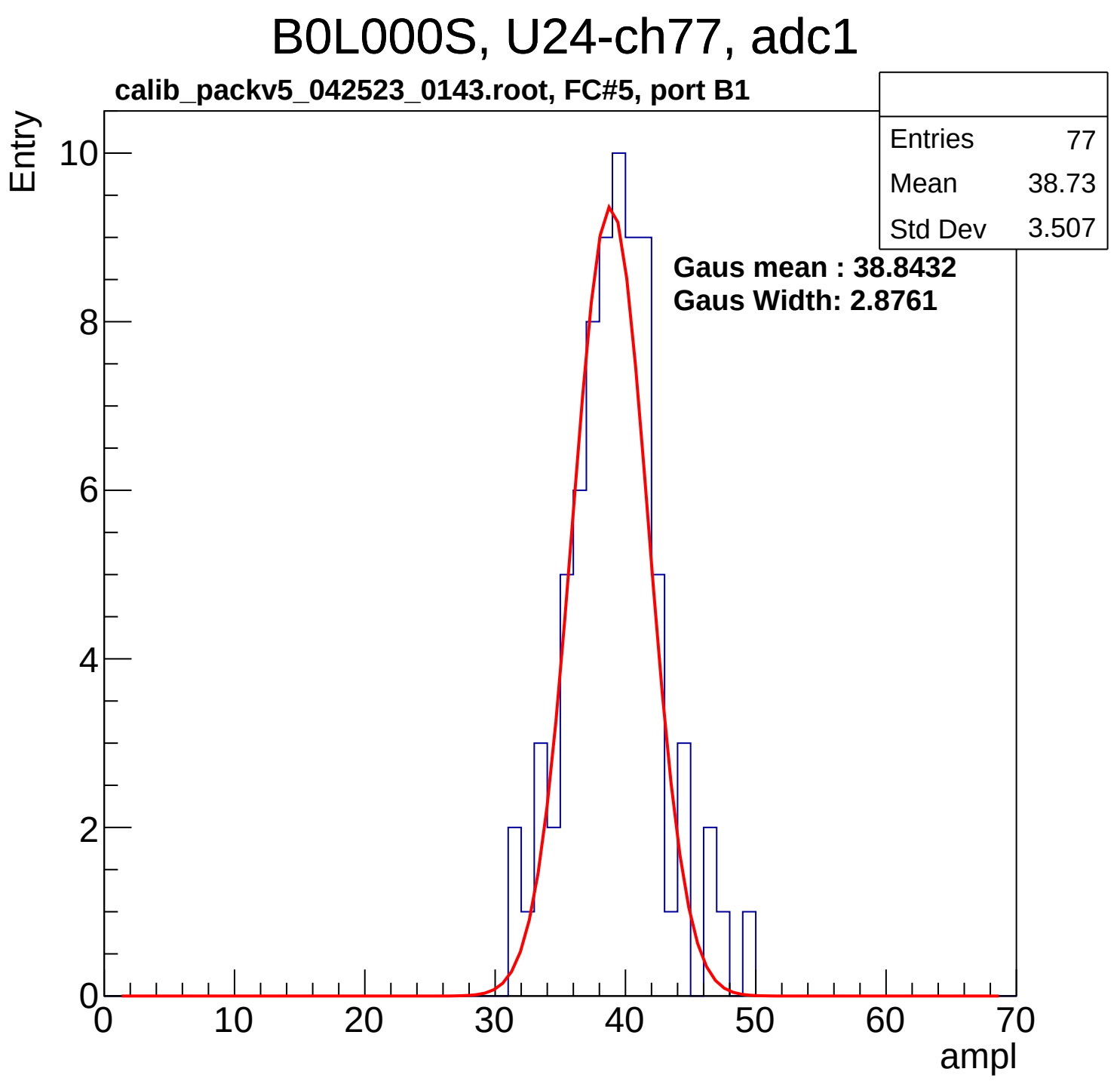
B0L000S, U24-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 38.73
Std Dev 3.507
Gaus mean : 38.8432
Gaus Width: 2.8761
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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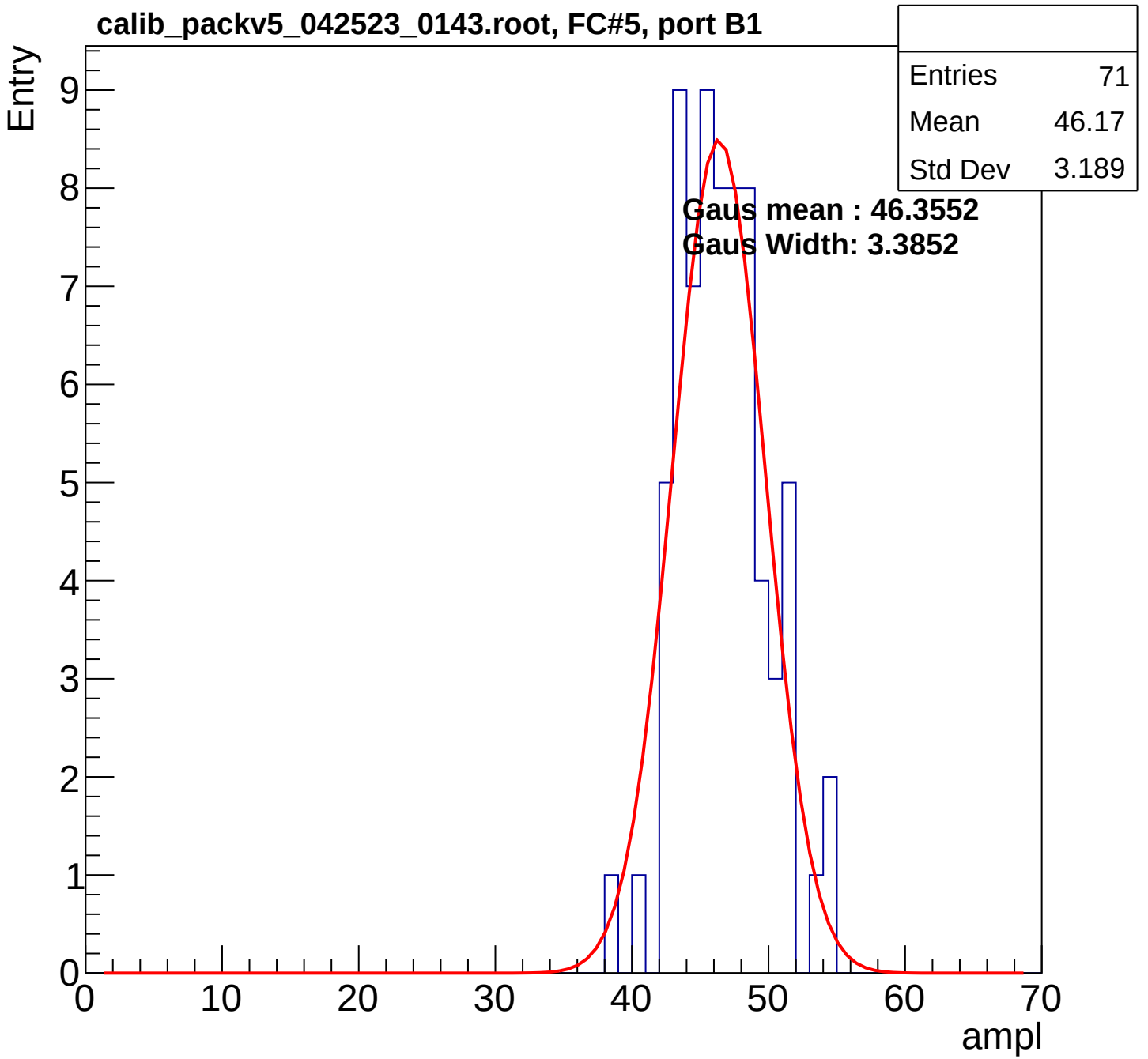
B0L000S, U24-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 46.17
Std Dev 3.189
Gaus mean : 46.3552
Gaus Width: 3.3852
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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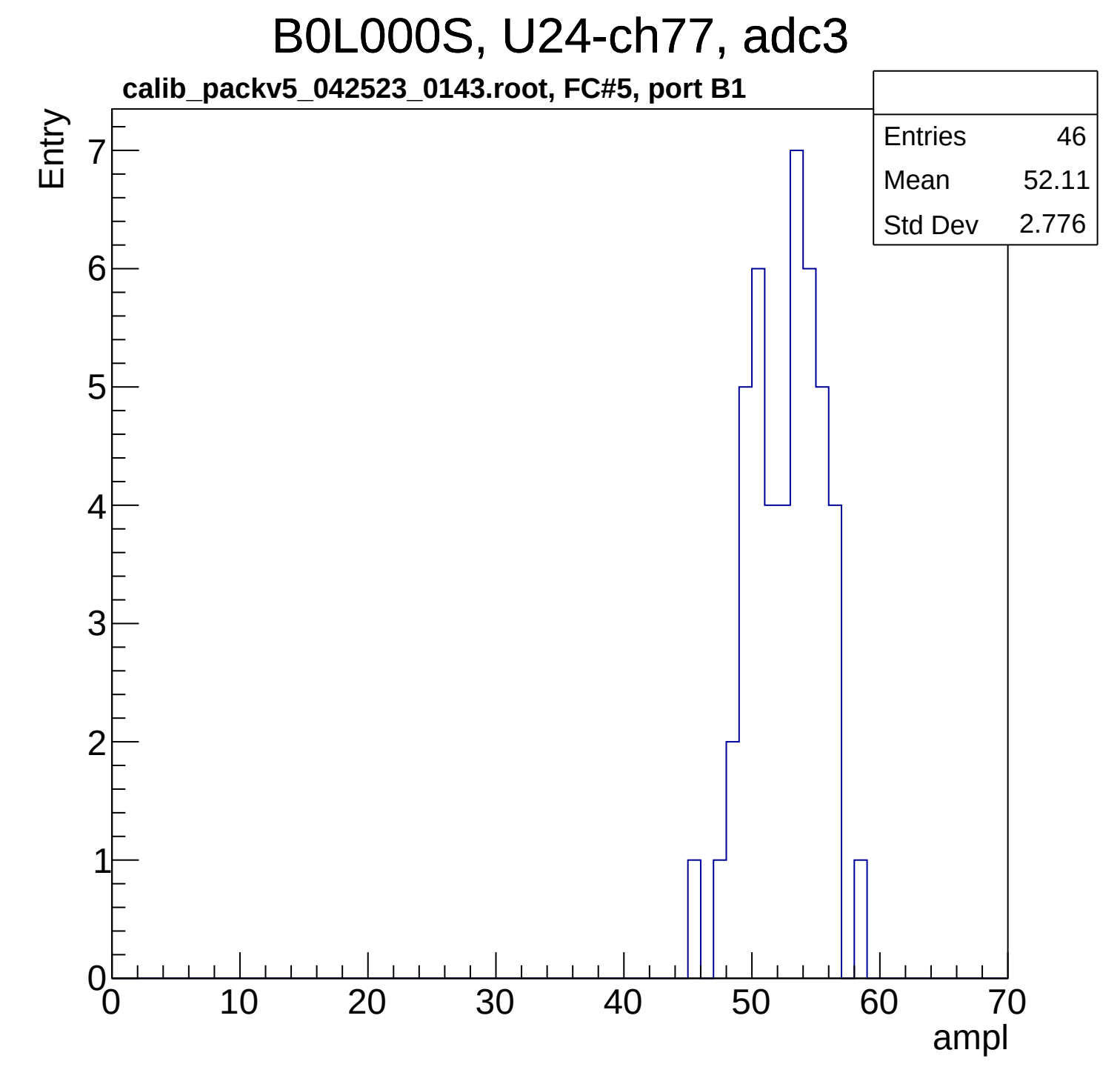
B0L000S, U24-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 52.11
Std Dev 2.776
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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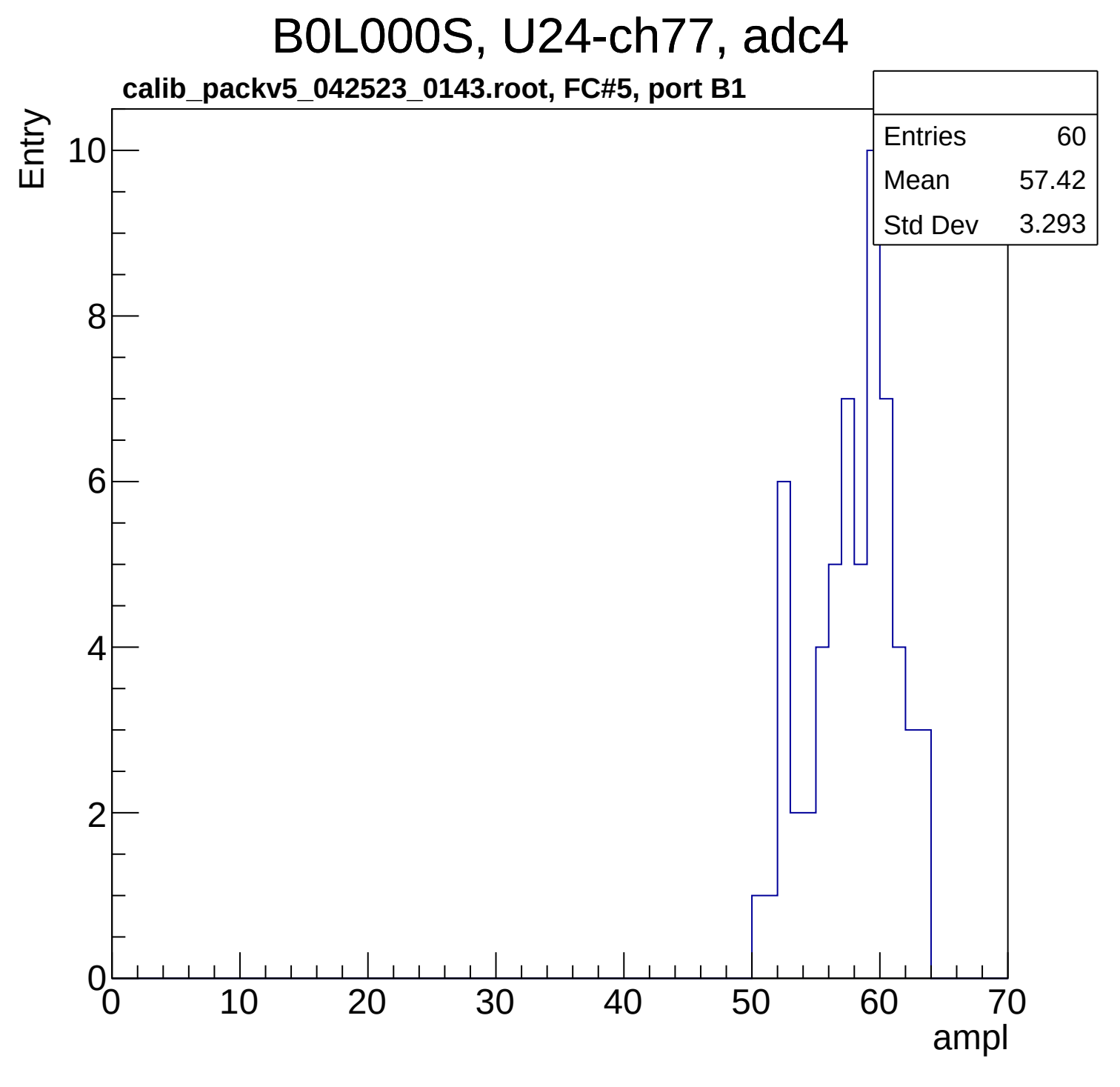
B0L000S, U24-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.42
Std Dev 3.293
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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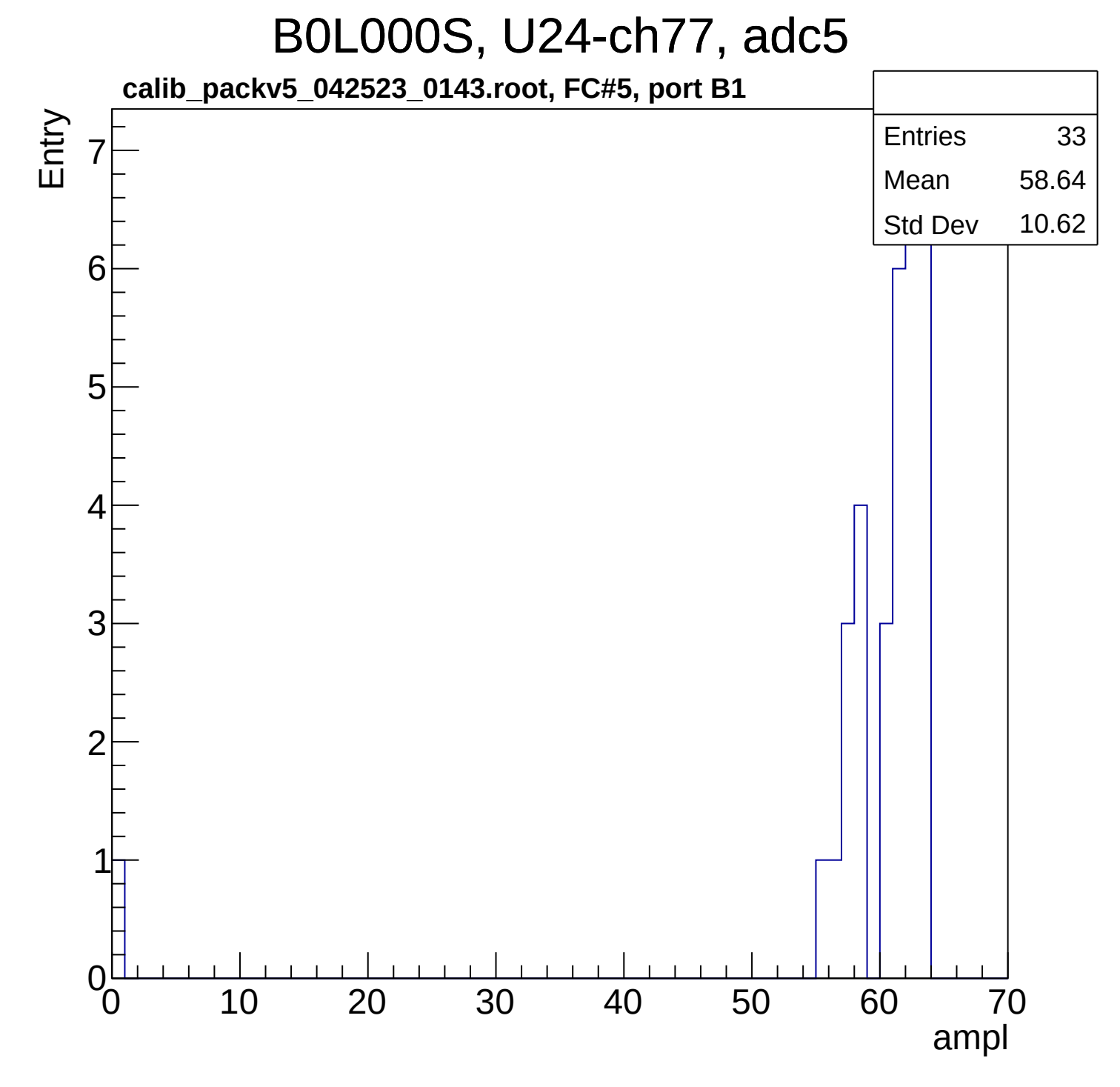
B0L000S, U24-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.64
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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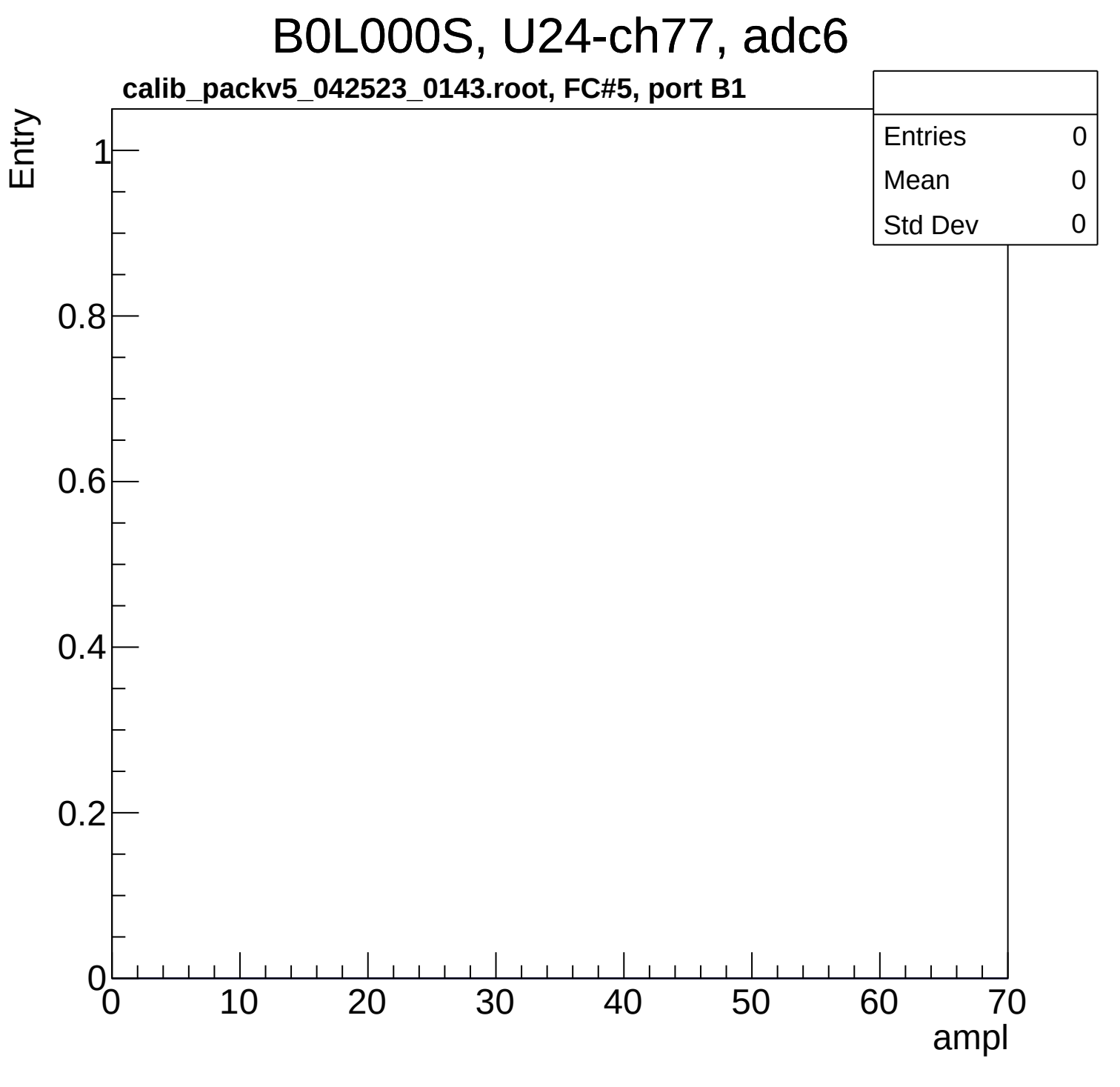
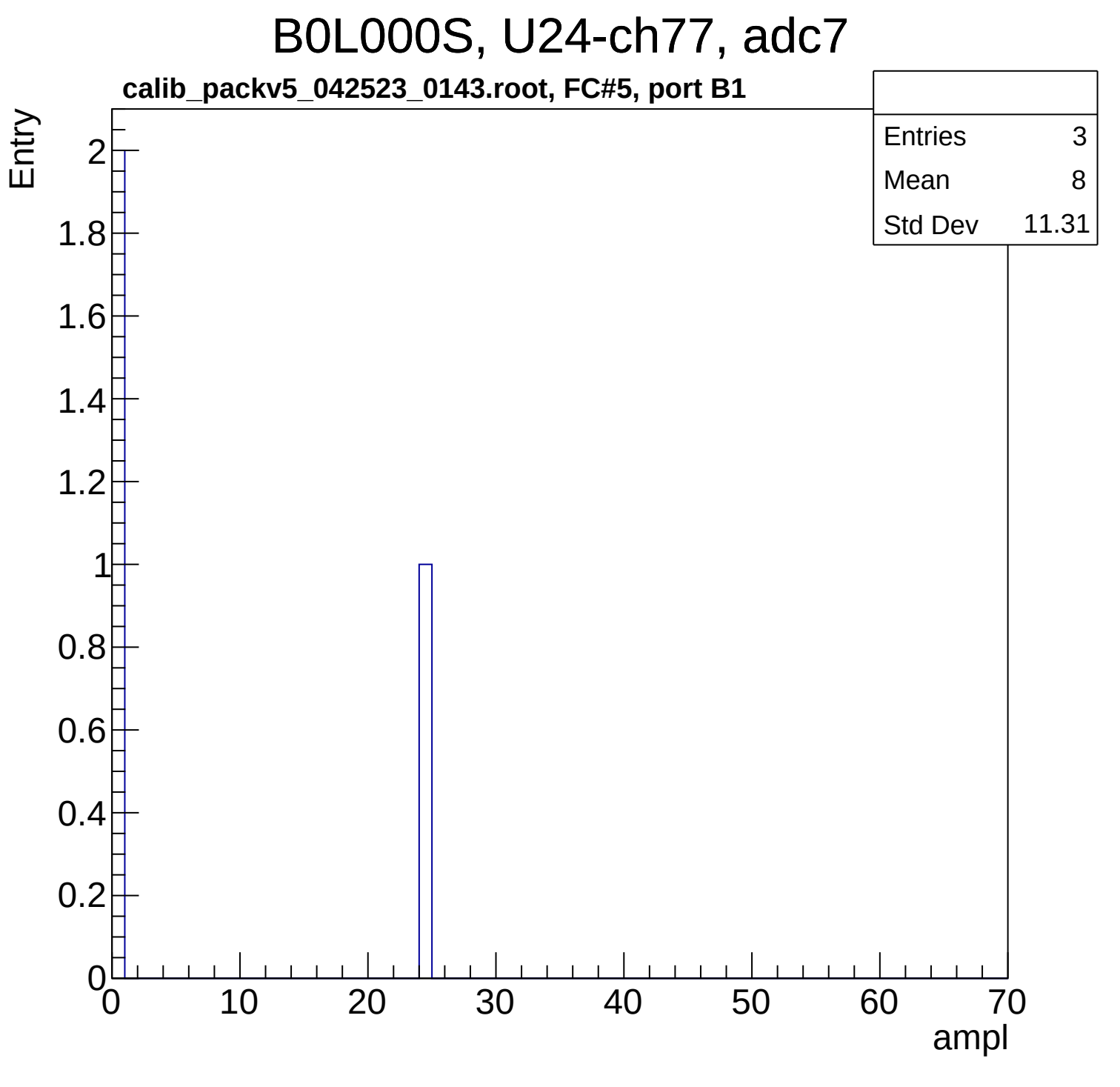

B0L000S, U24-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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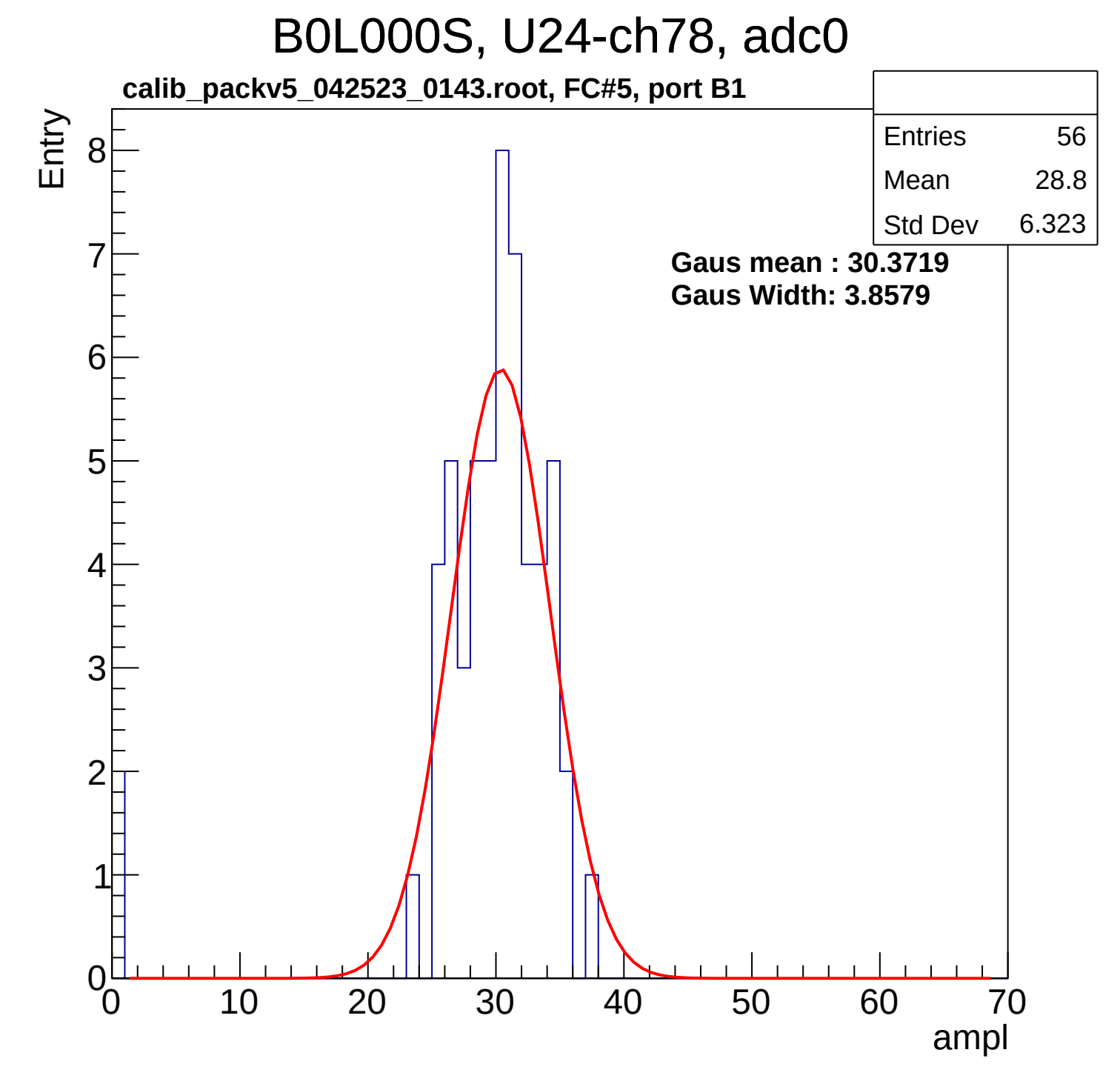
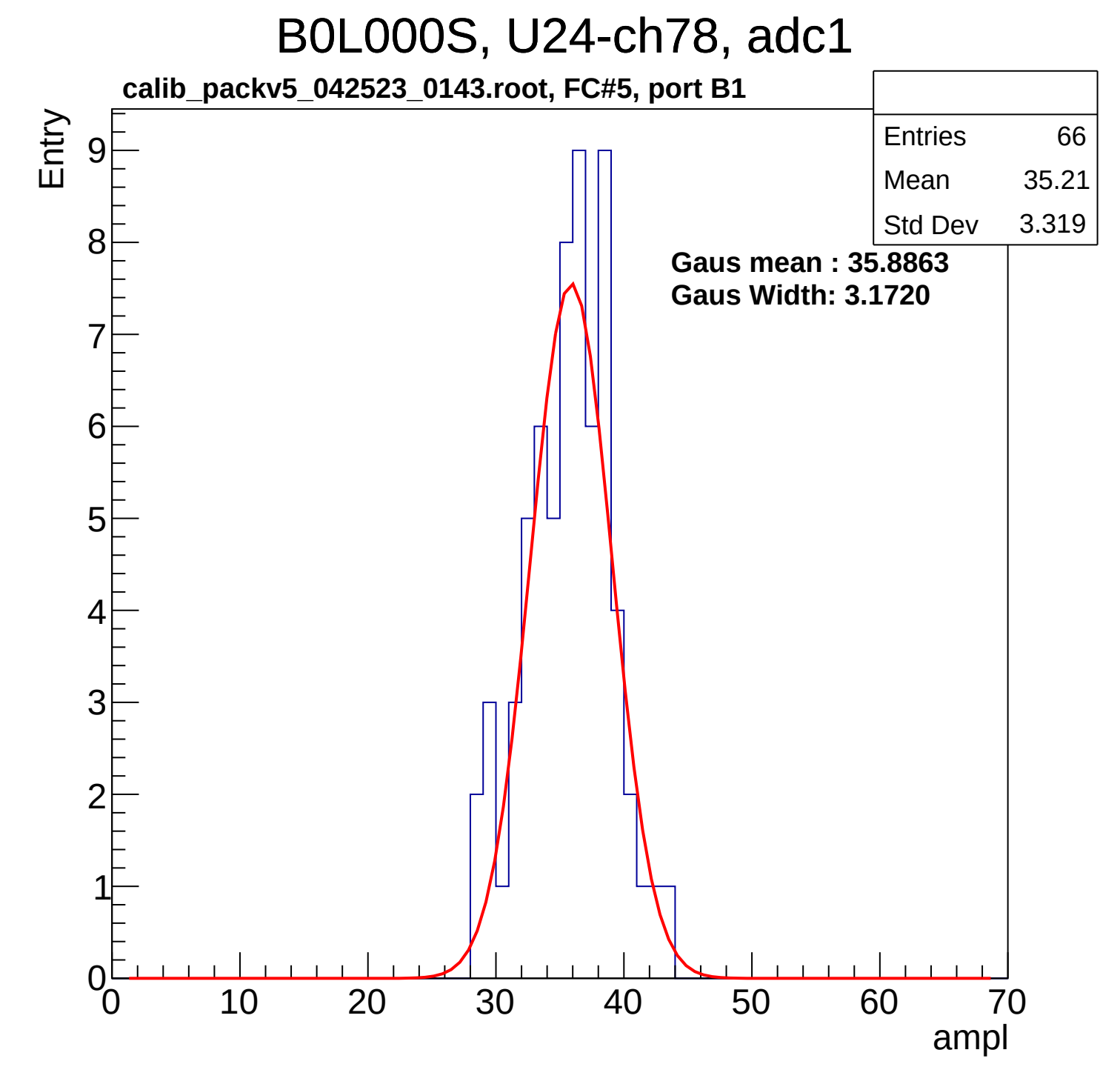
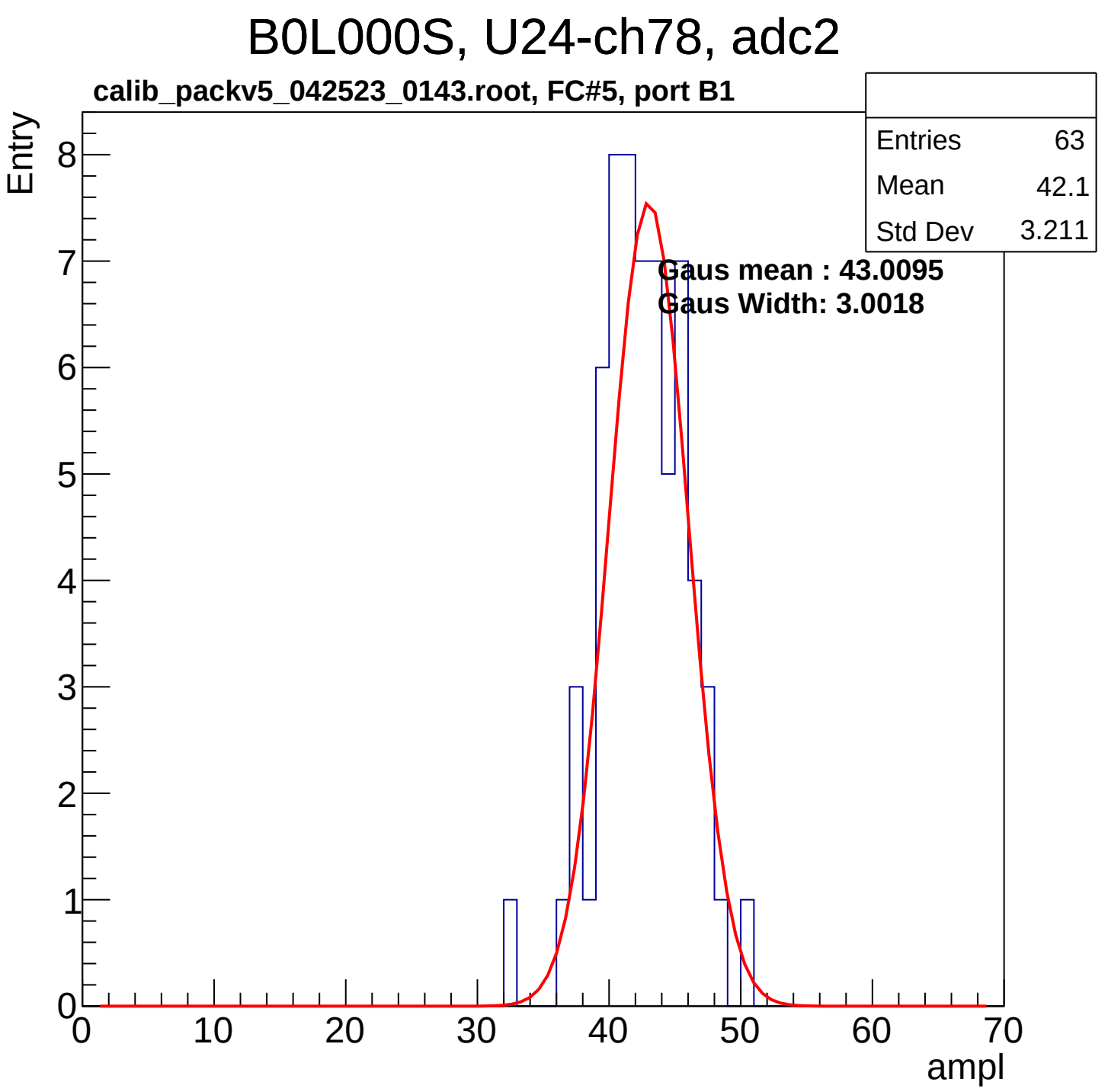

B0L000S, U24-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 28.8
Std Dev 6.323
Gaus mean : 30.3719
Gaus Width: 3.8579
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.21
Std Dev 3.319
Gaus mean : 35.8863
Gaus Width: 3.1720
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.1
Std Dev 3.211
Gaus mean : 43.0095
Gaus Width: 3.0018
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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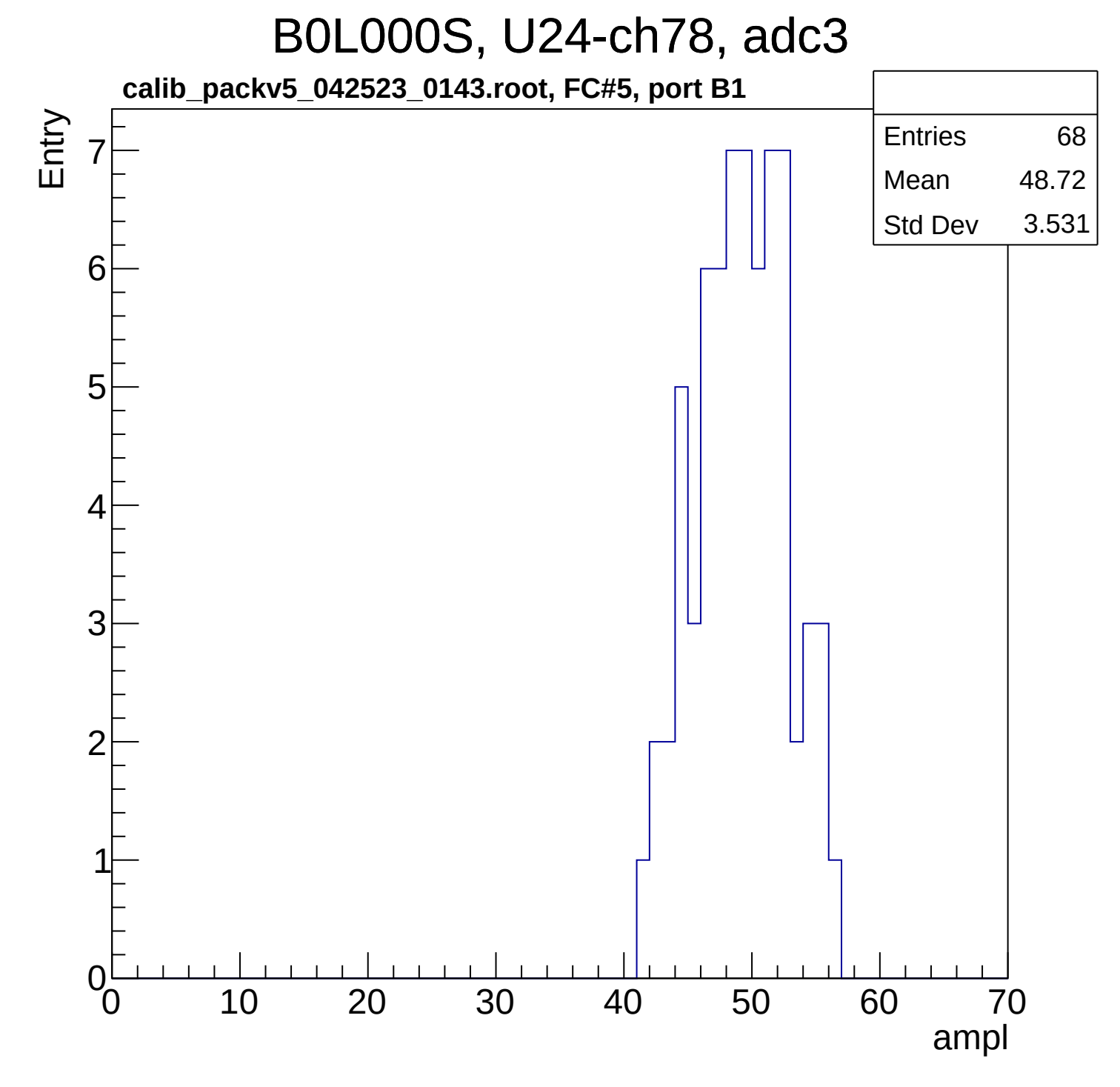
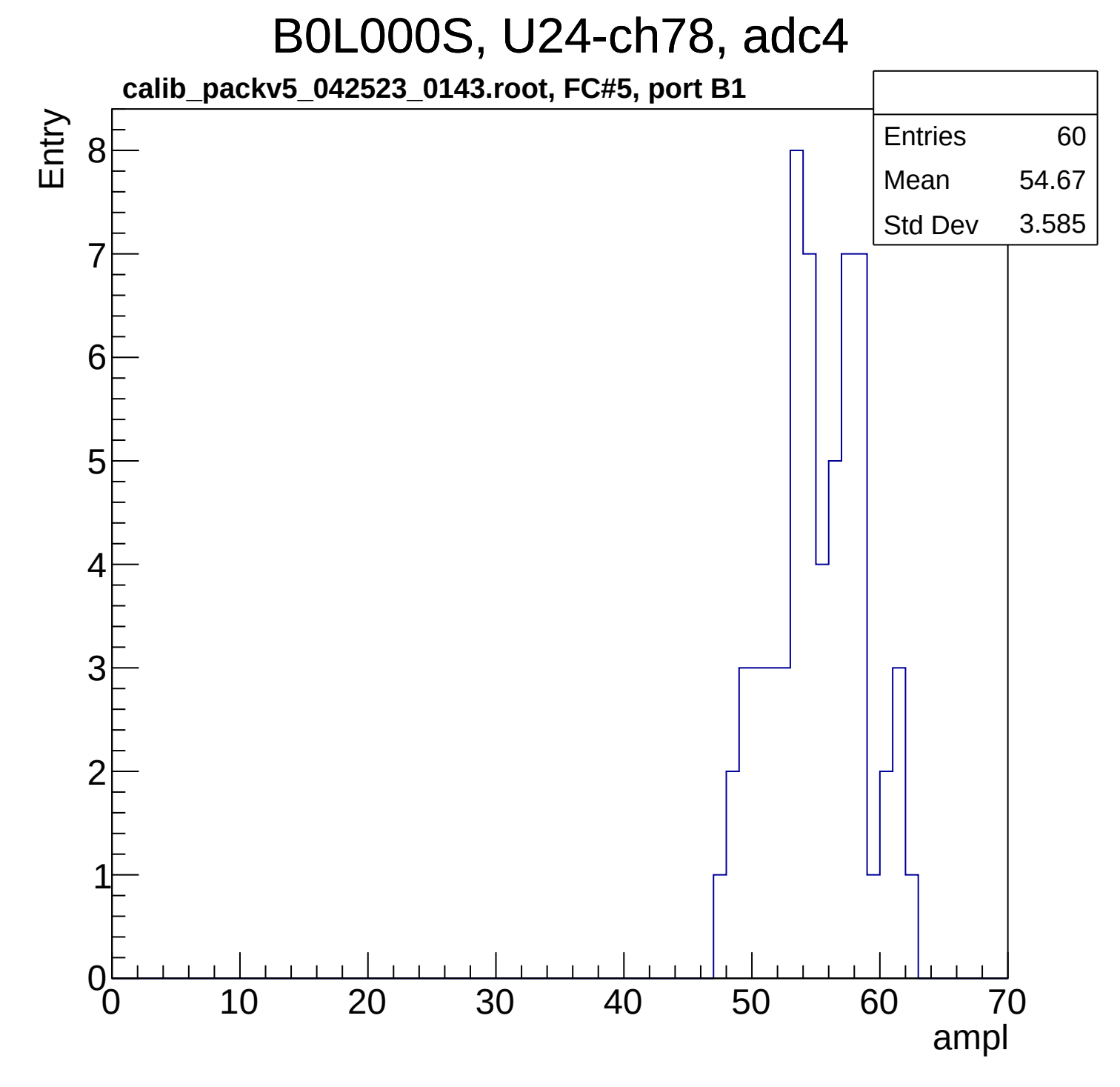

B0L000S, U24-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.72
Std Dev 3.531
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 54.67
Std Dev 3.585
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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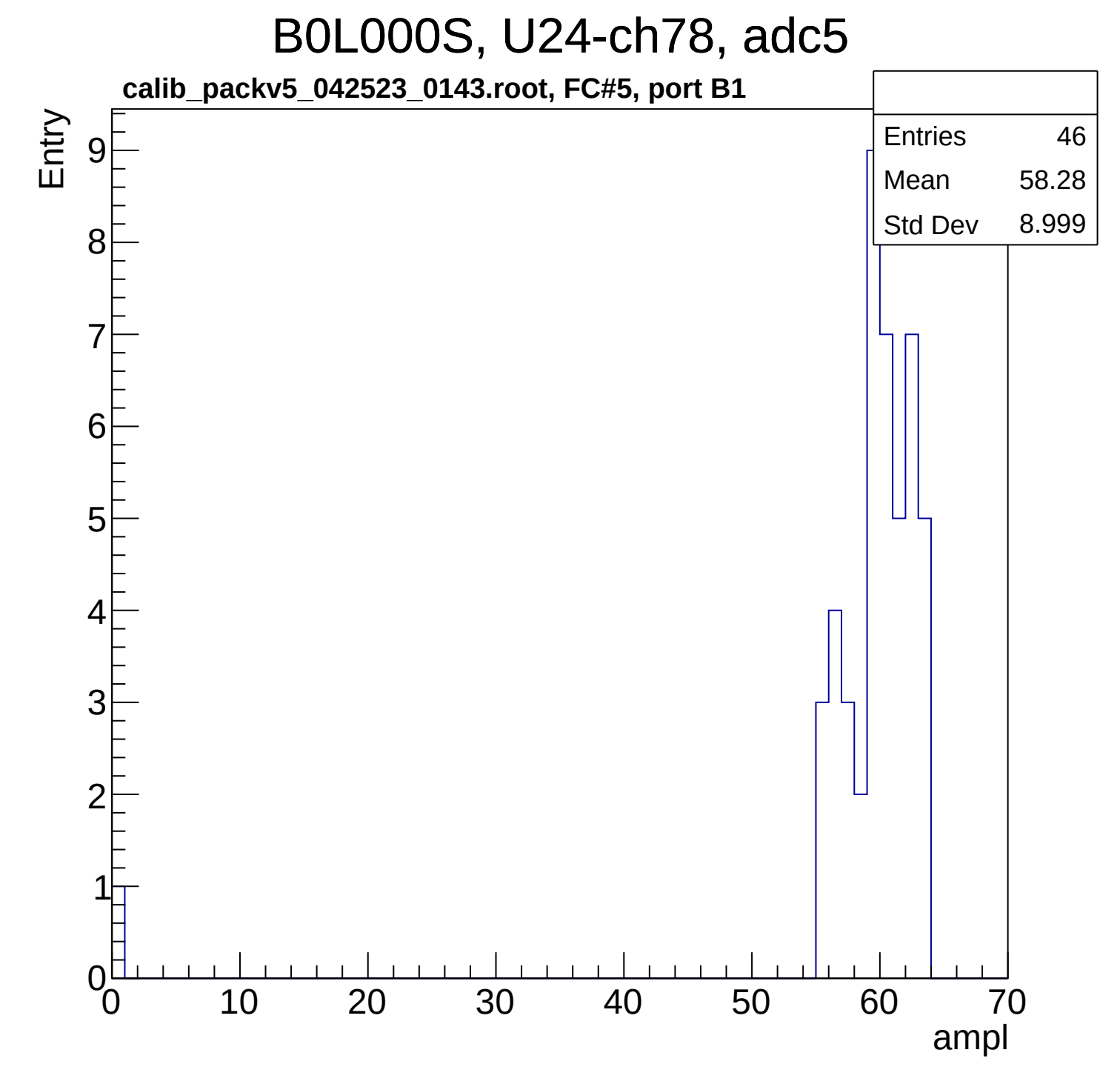
B0L000S, U24-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.28
Std Dev 8.999
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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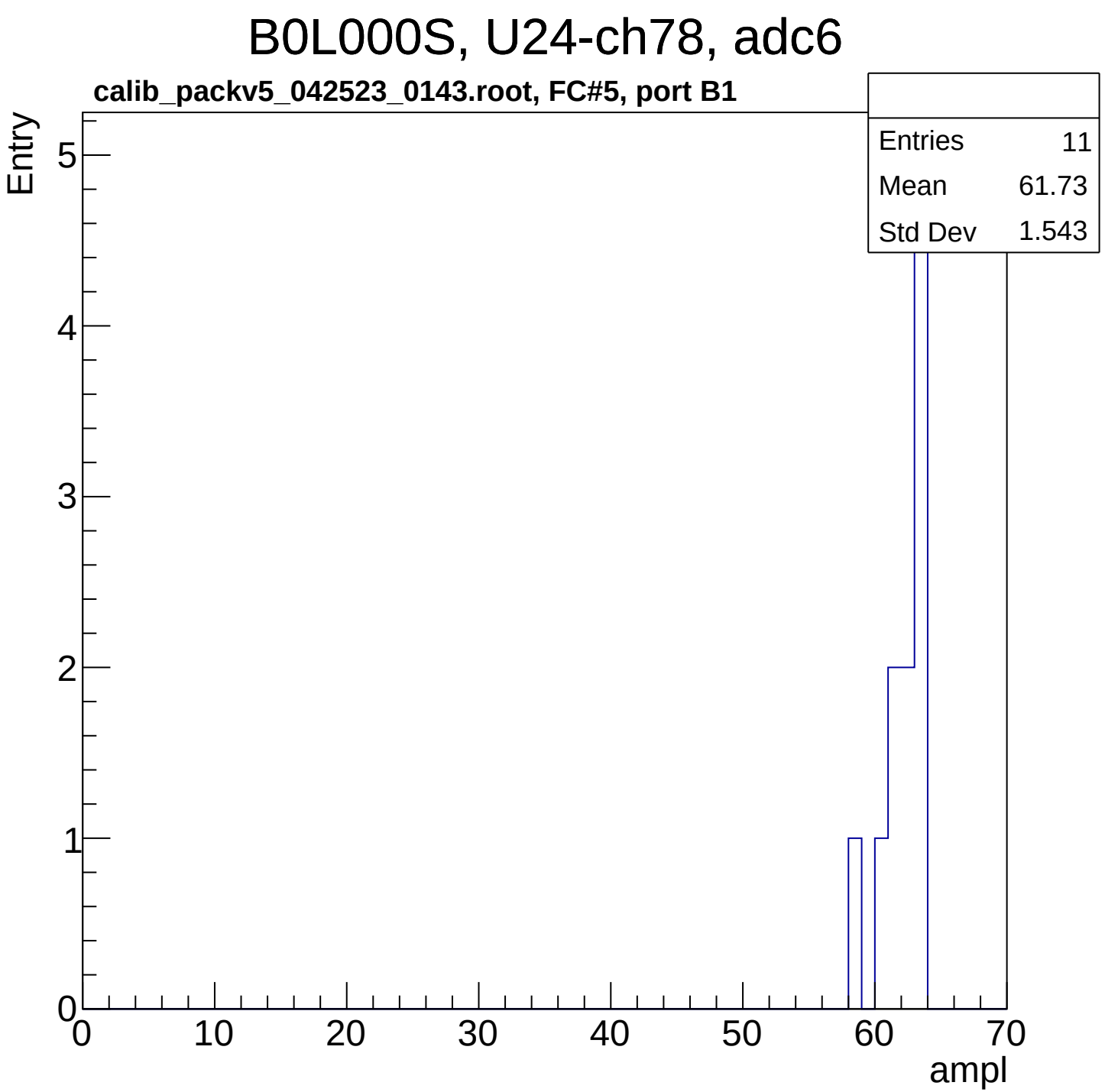
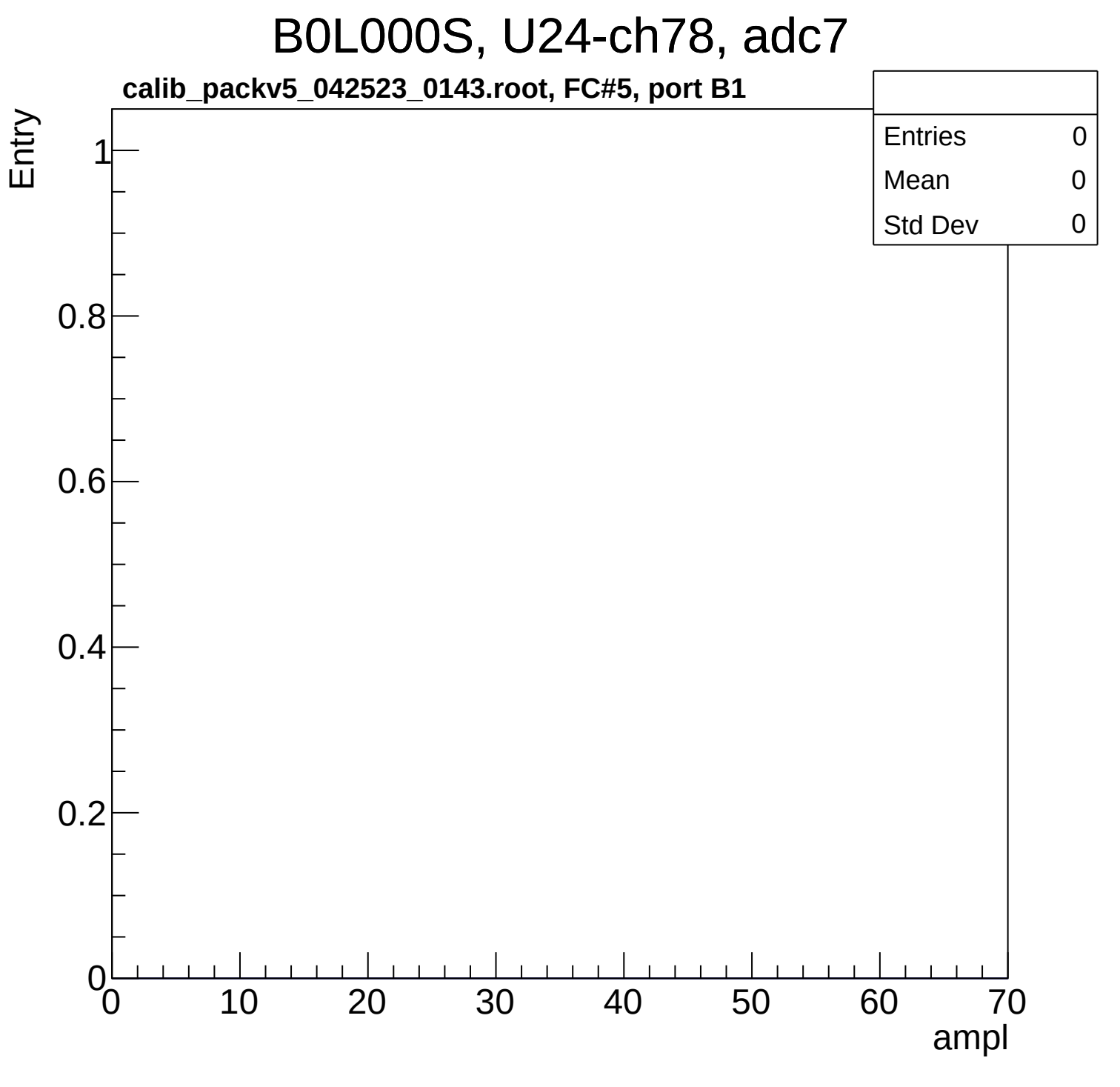
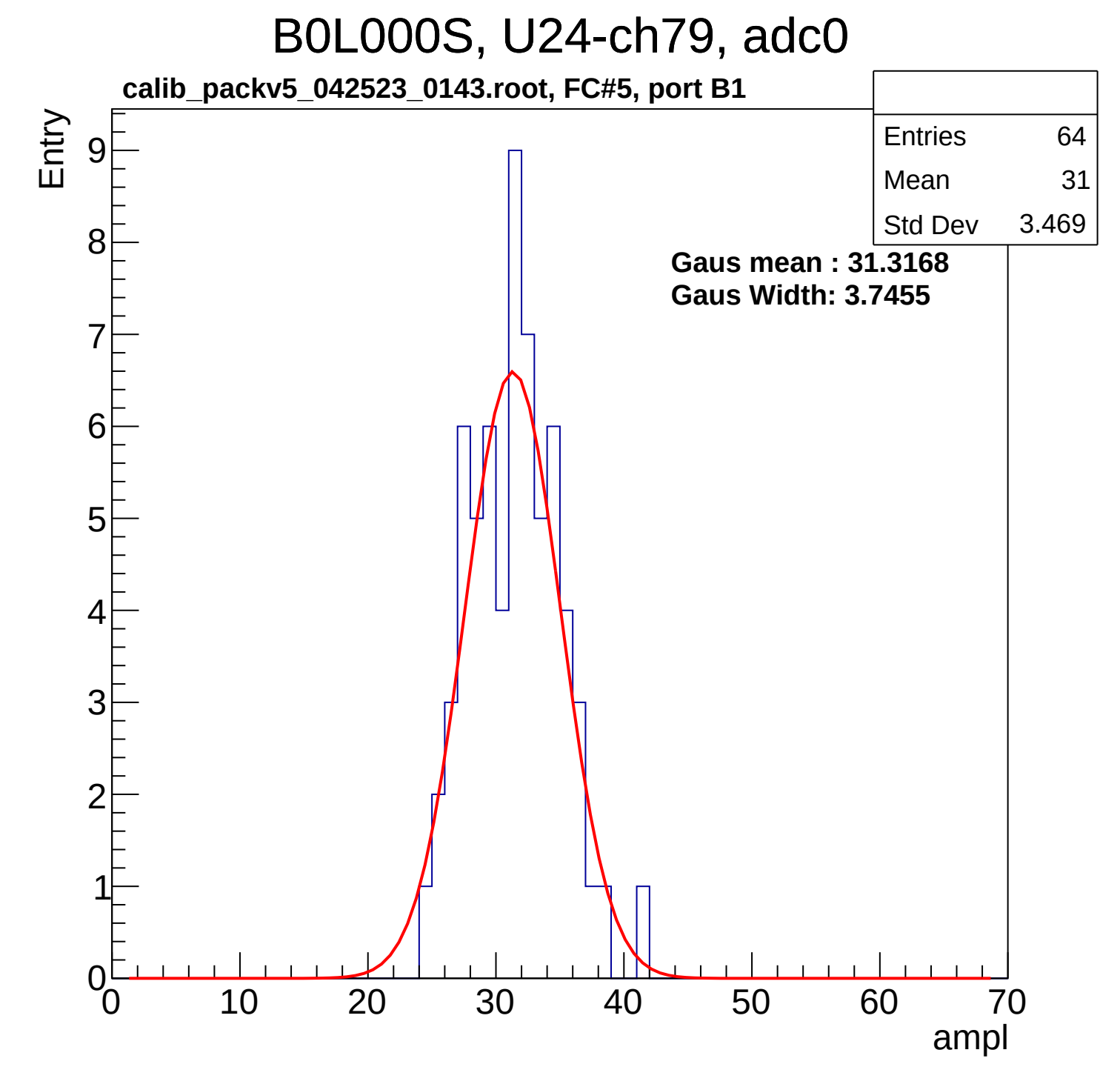

B0L000S, U24-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.73
Std Dev 1.543
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 31
Std Dev 3.469
Gaus mean : 31.3168
Gaus Width: 3.7455
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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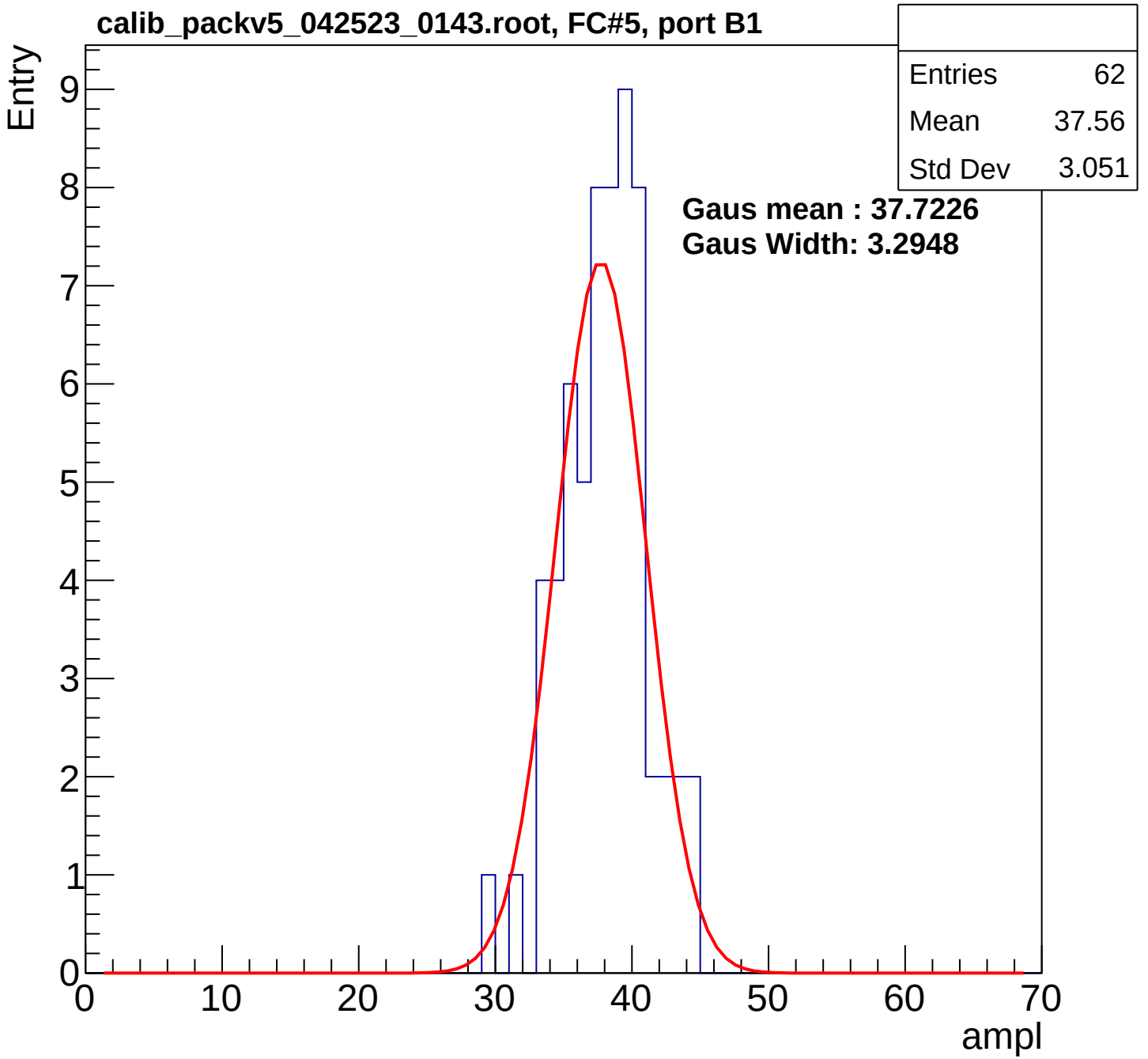
60
70


B0L000S, U24-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.56
Std Dev 3.051
Gaus mean : 37.7226
Gaus Width: 3.2948
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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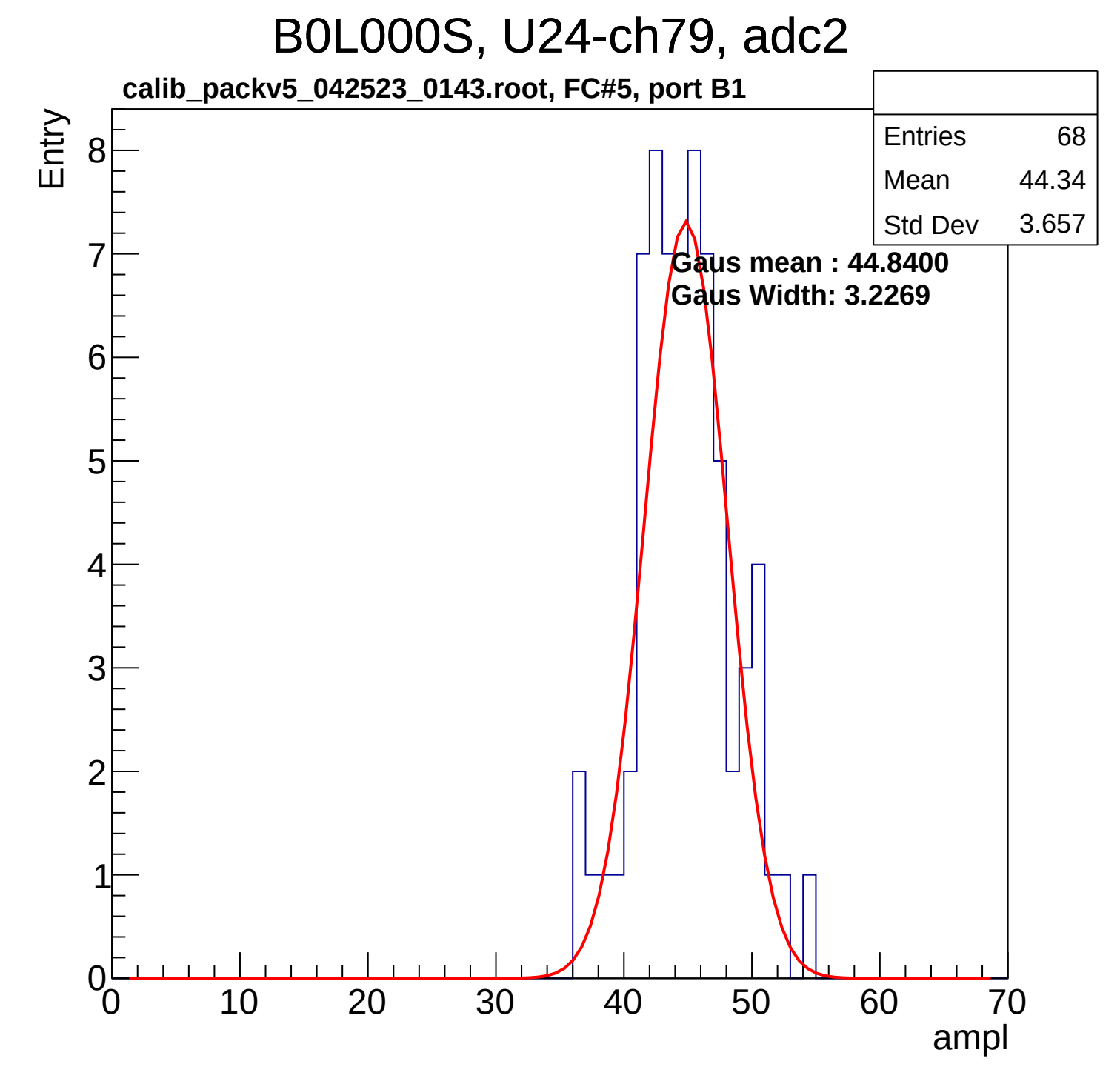
B0L000S, U24-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.34
Std Dev 3.657
Gaus mean : 44.8400
Gaus Width: 3.2269
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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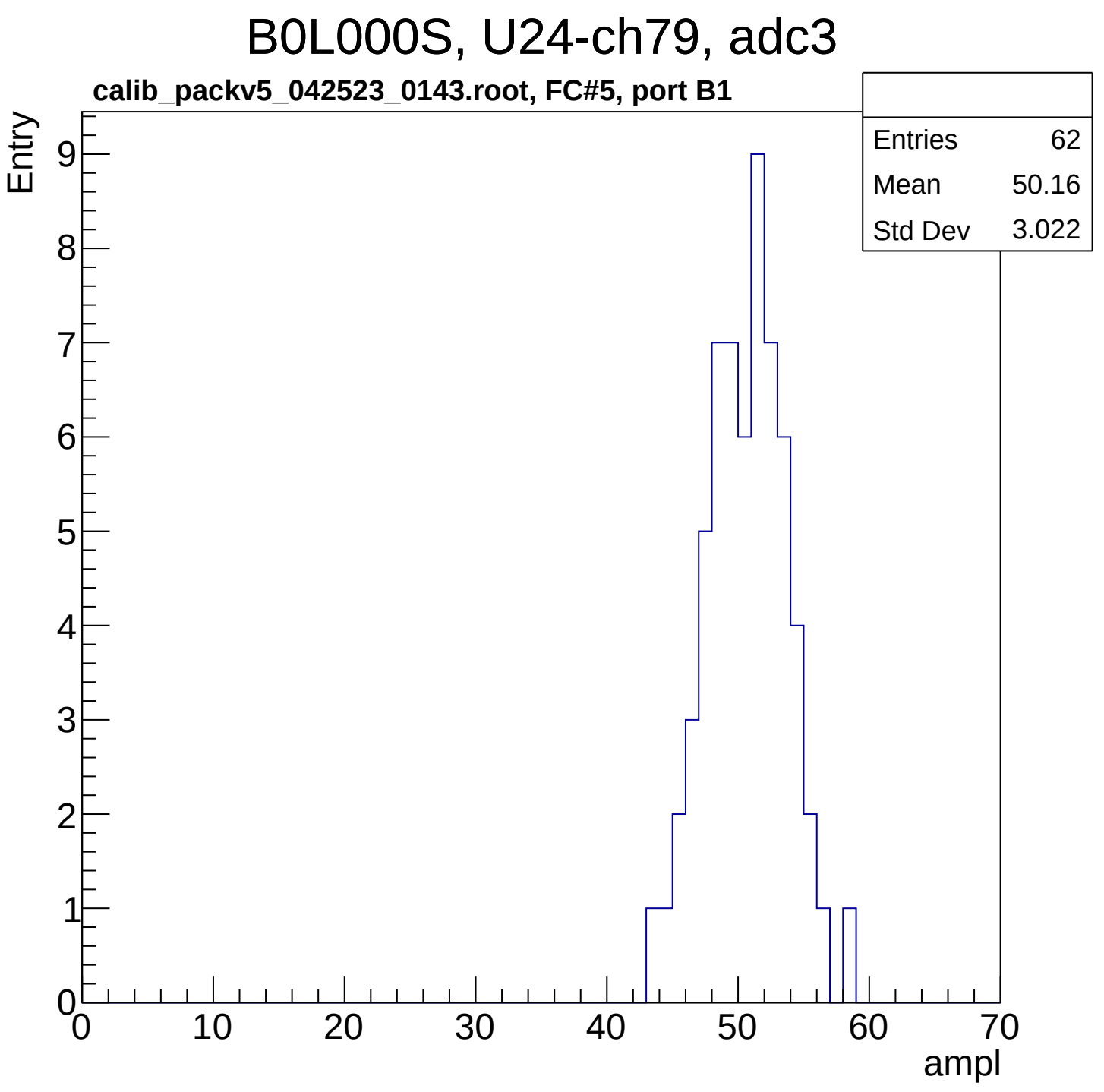
B0L000S, U24-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.16
Std Dev 3.022
Entry
ampl
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3
4
5
6
7
8
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0
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60
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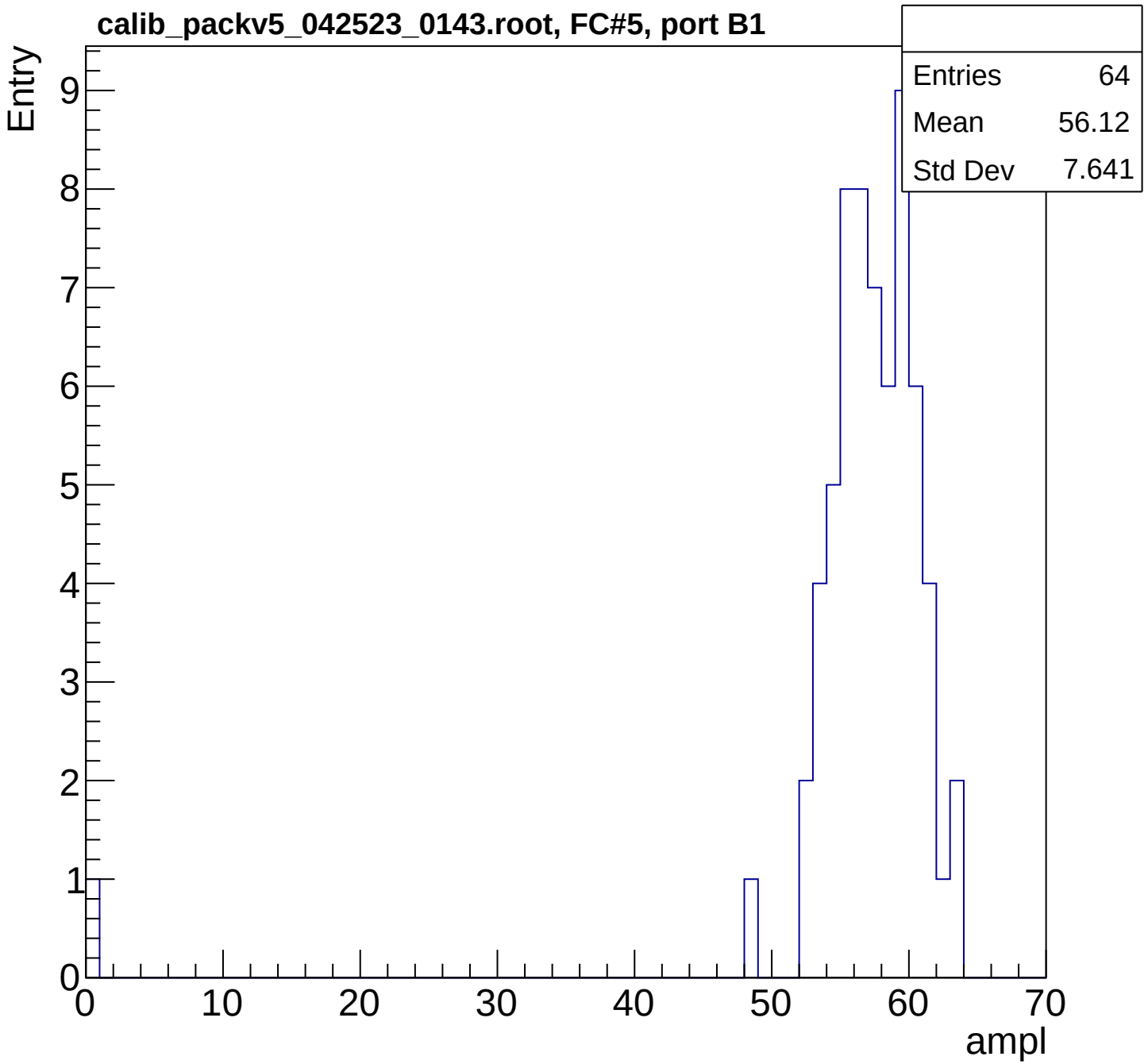
B0L000S, U24-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 56.12
Std Dev 7.641
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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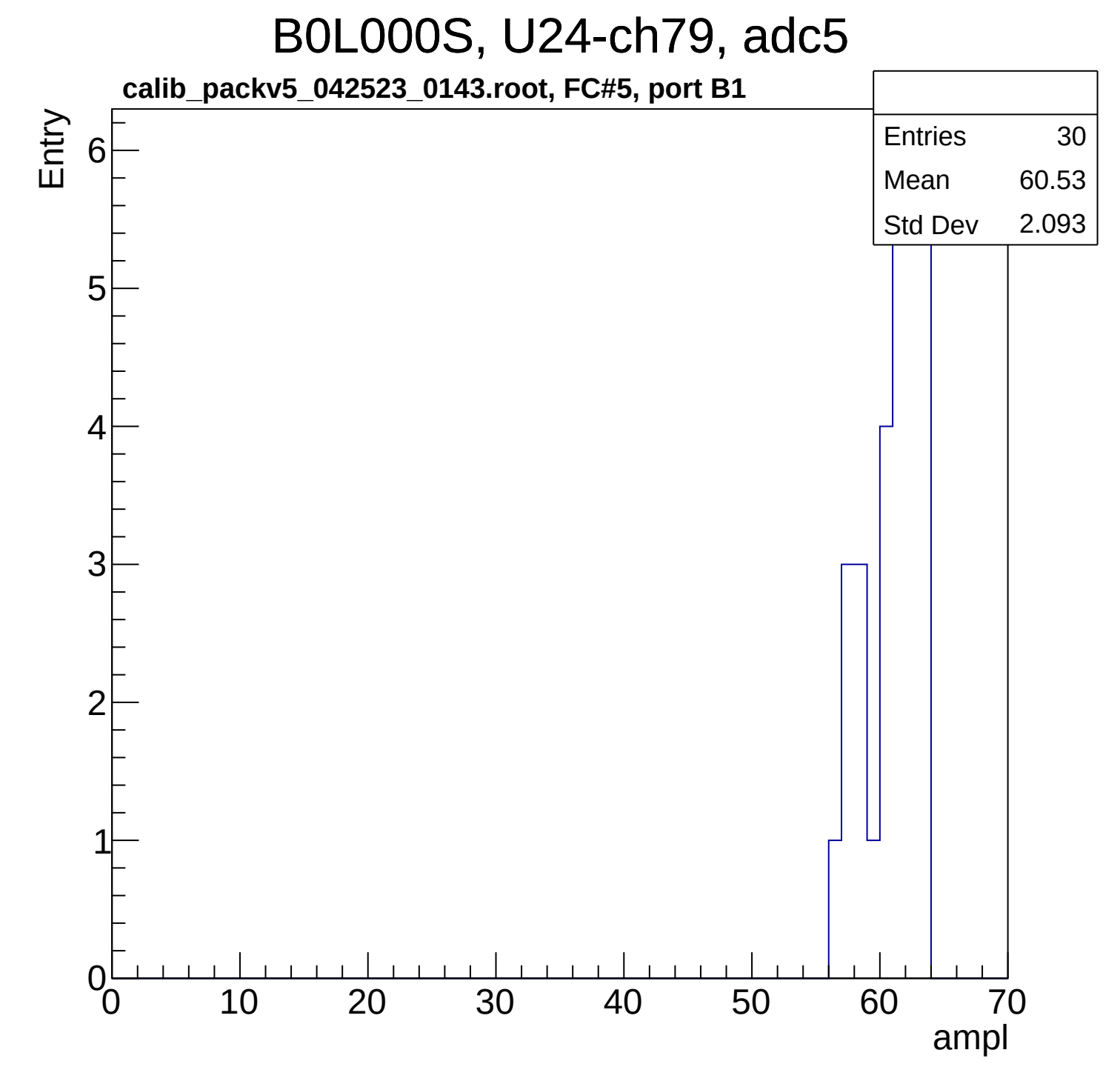
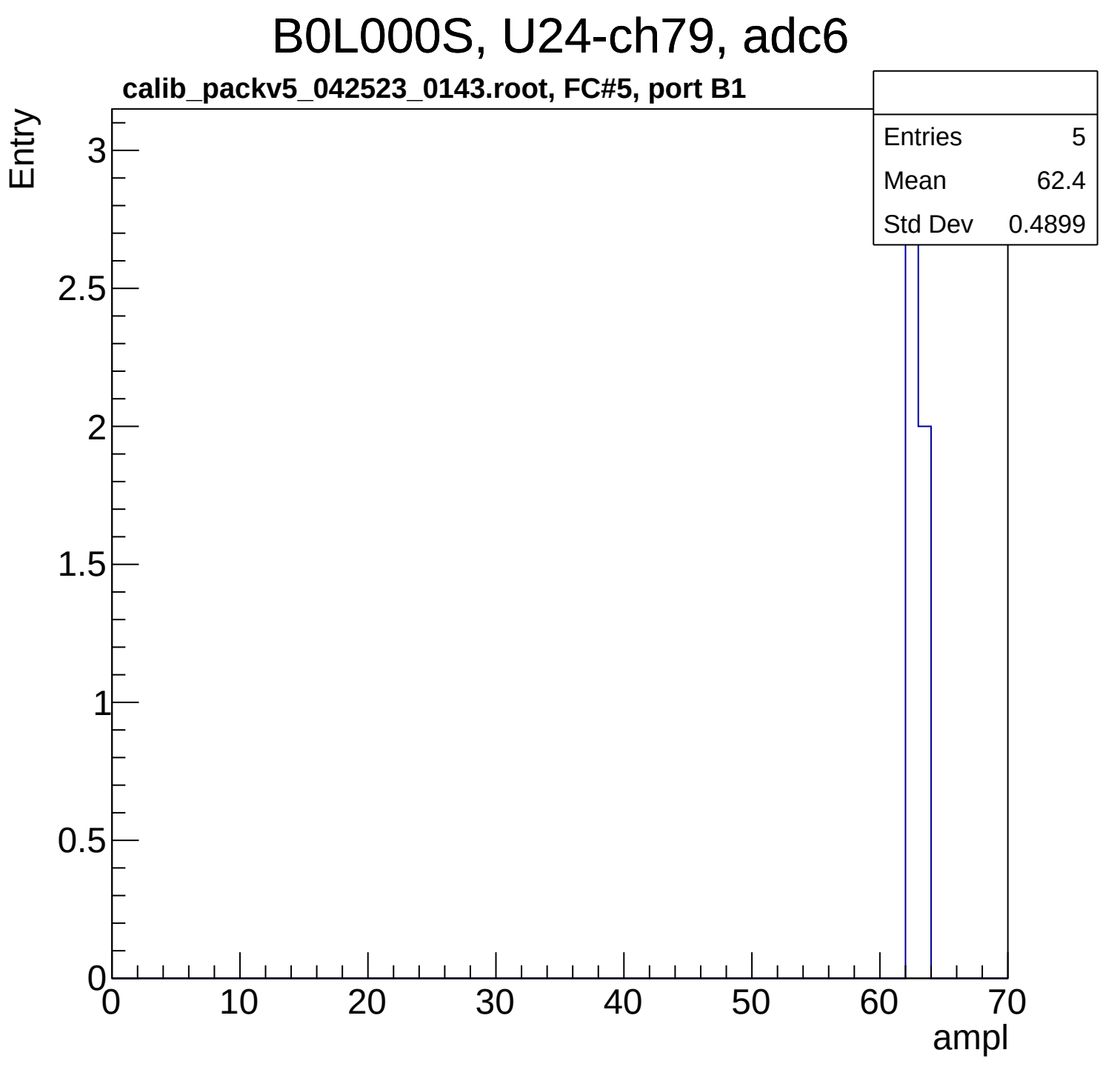

B0L000S, U24-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.53
Std Dev 2.093
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
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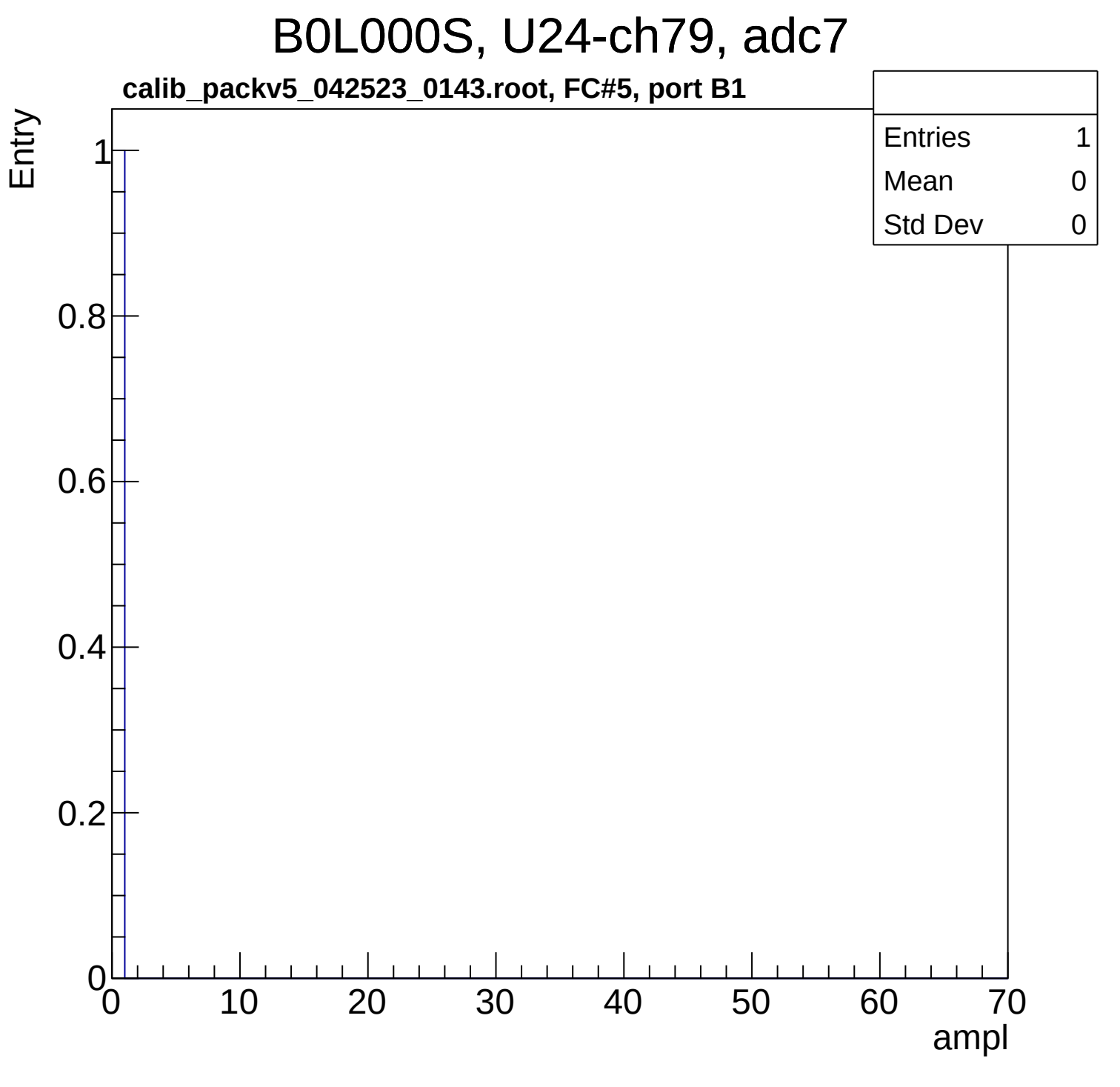
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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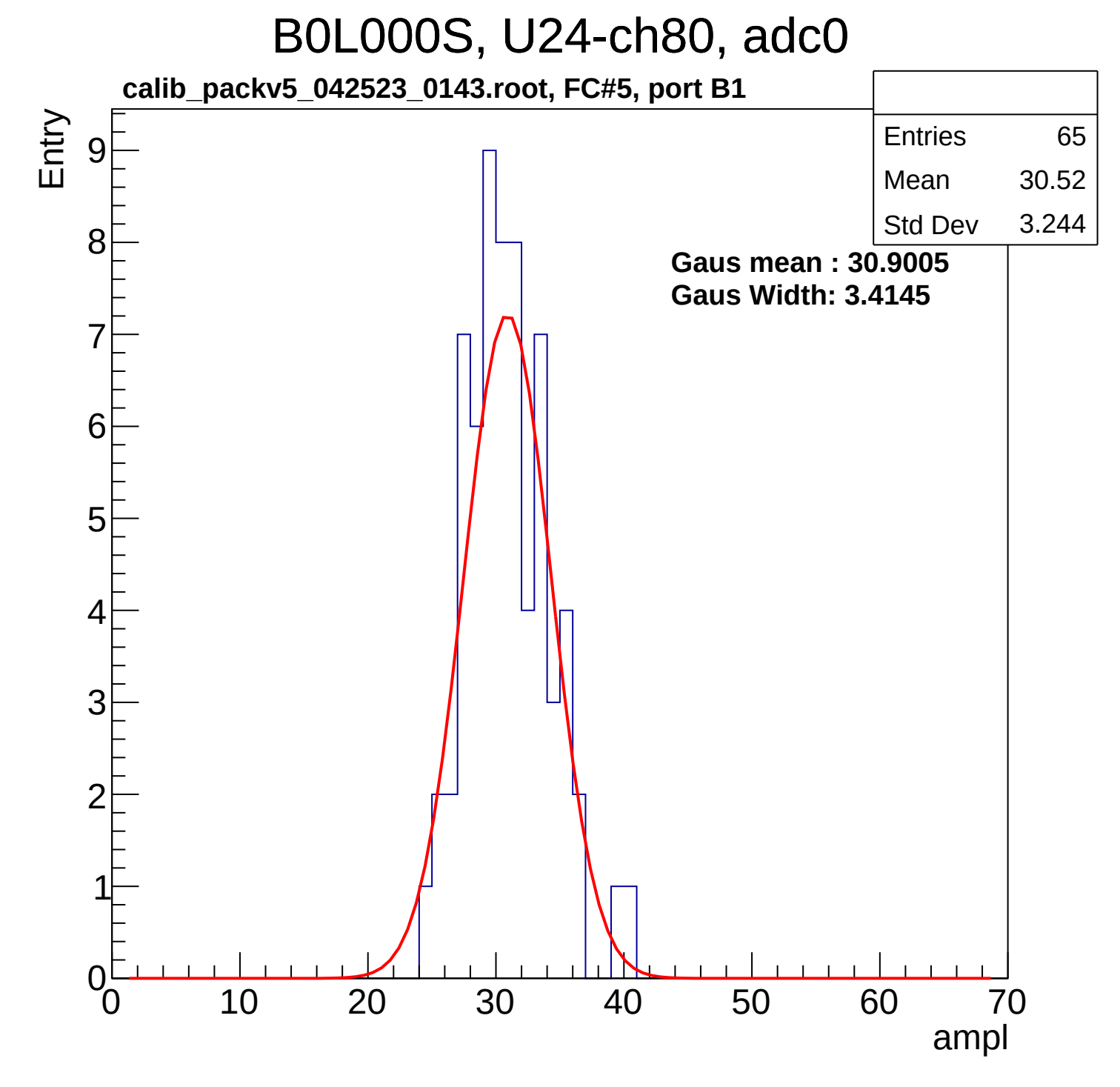
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.52
Std Dev 3.244
Gaus mean : 30.9005
Gaus Width: 3.4145
Entry
ampl
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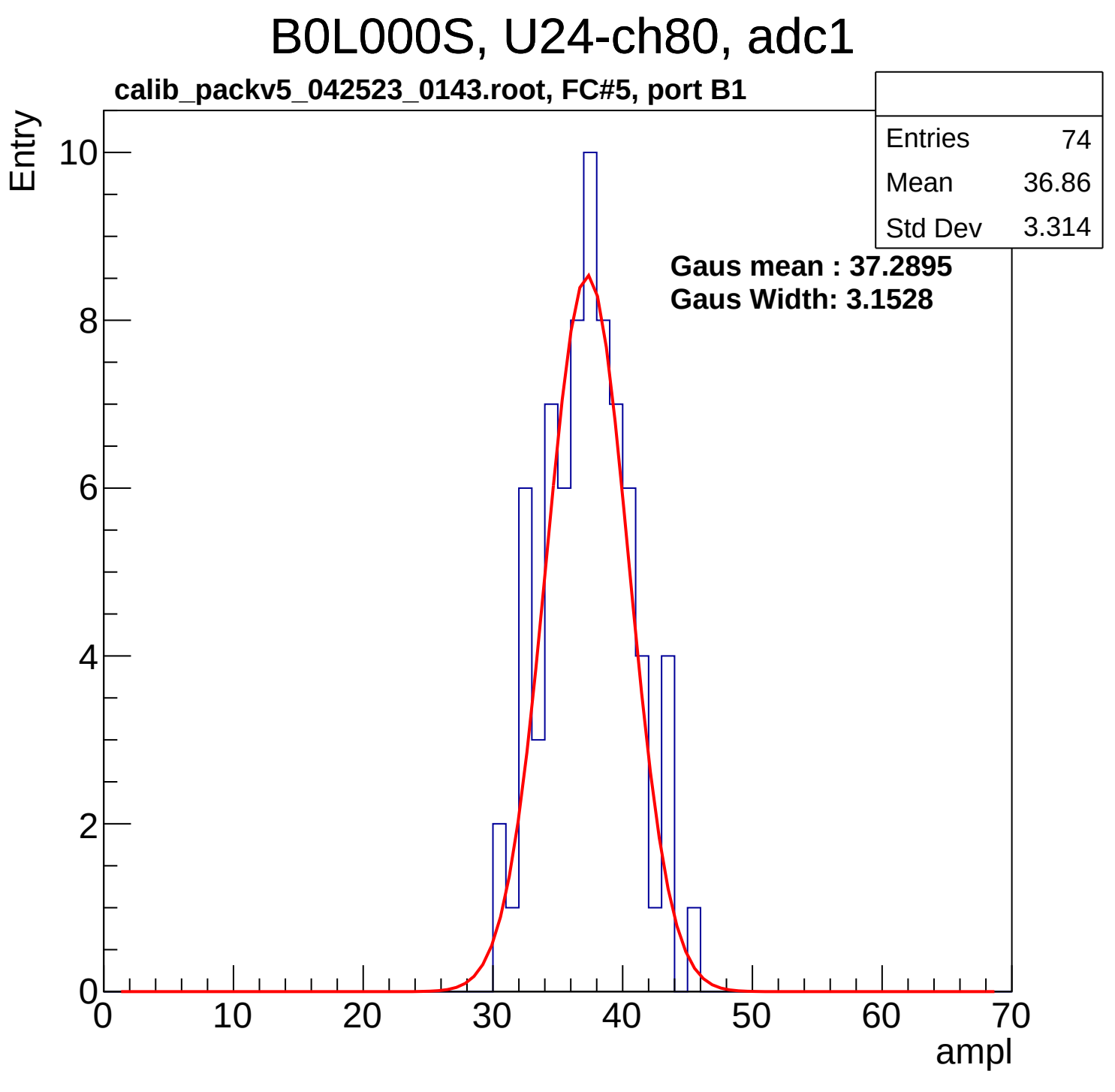
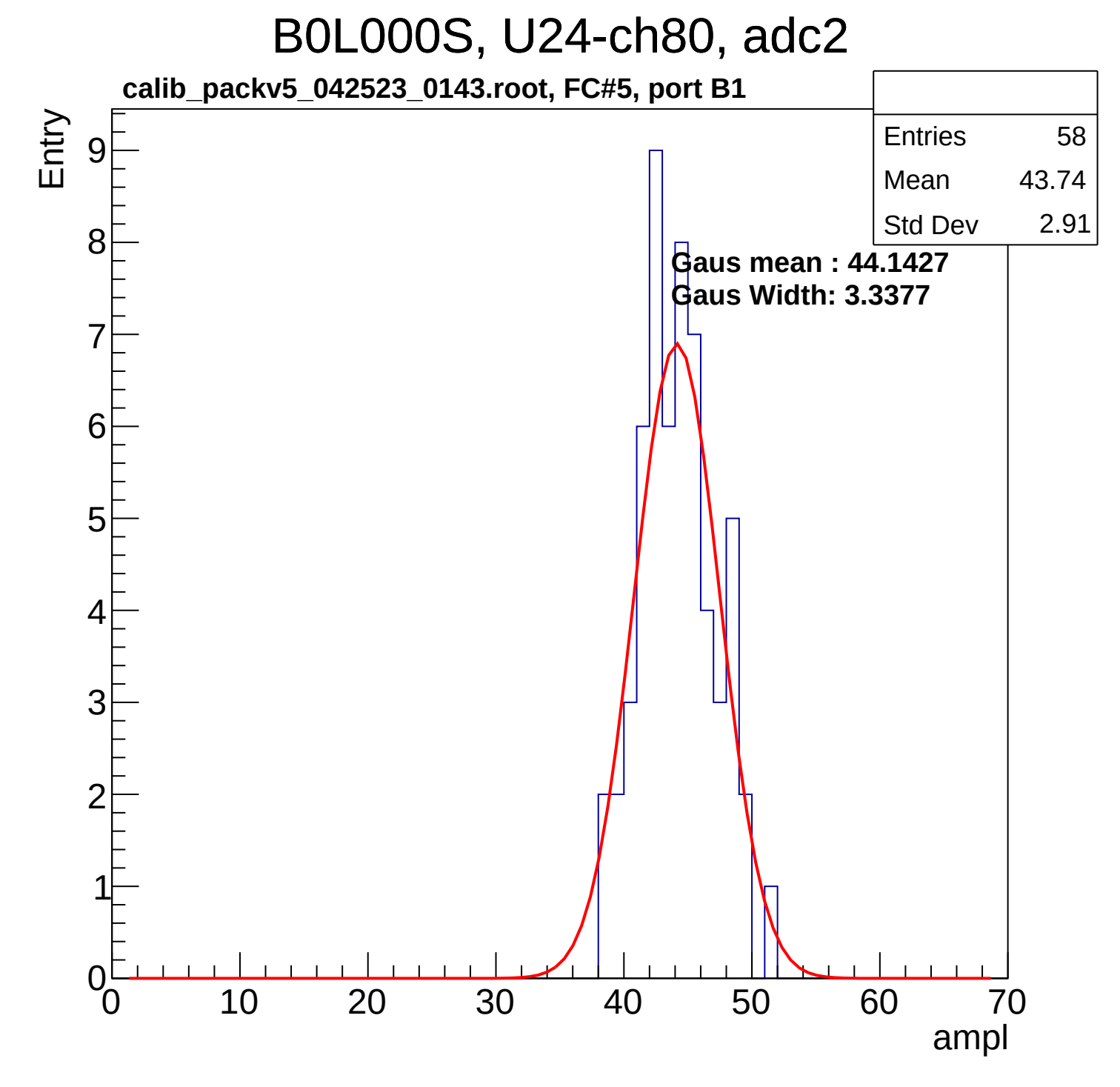
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.86
Std Dev 3.314
Gaus mean : 37.2895
Gaus Width: 3.1528
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.74
Std Dev 2.91
Gaus mean : 44.1427
Gaus Width: 3.3377
Entry
ampl
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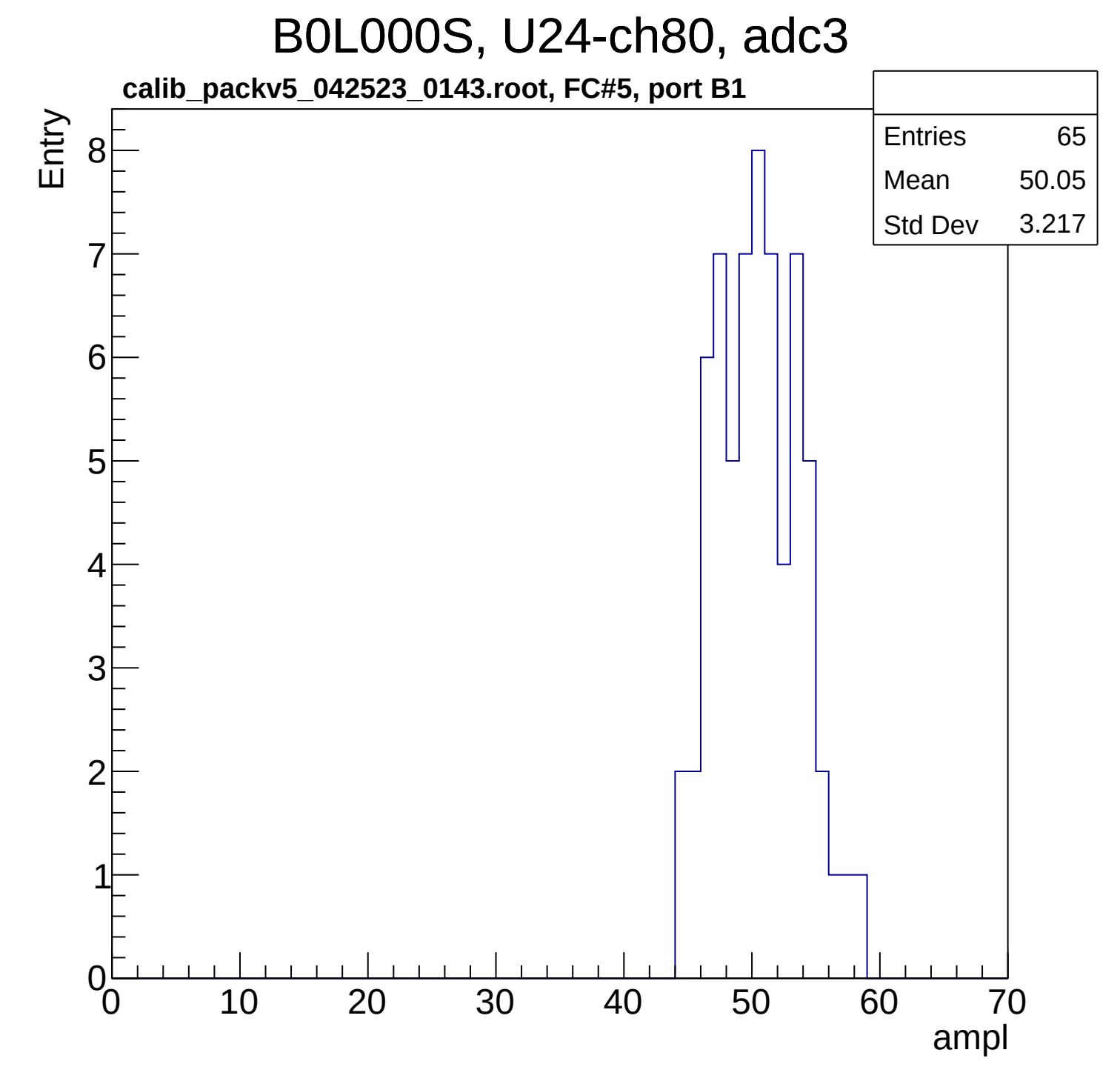
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.05
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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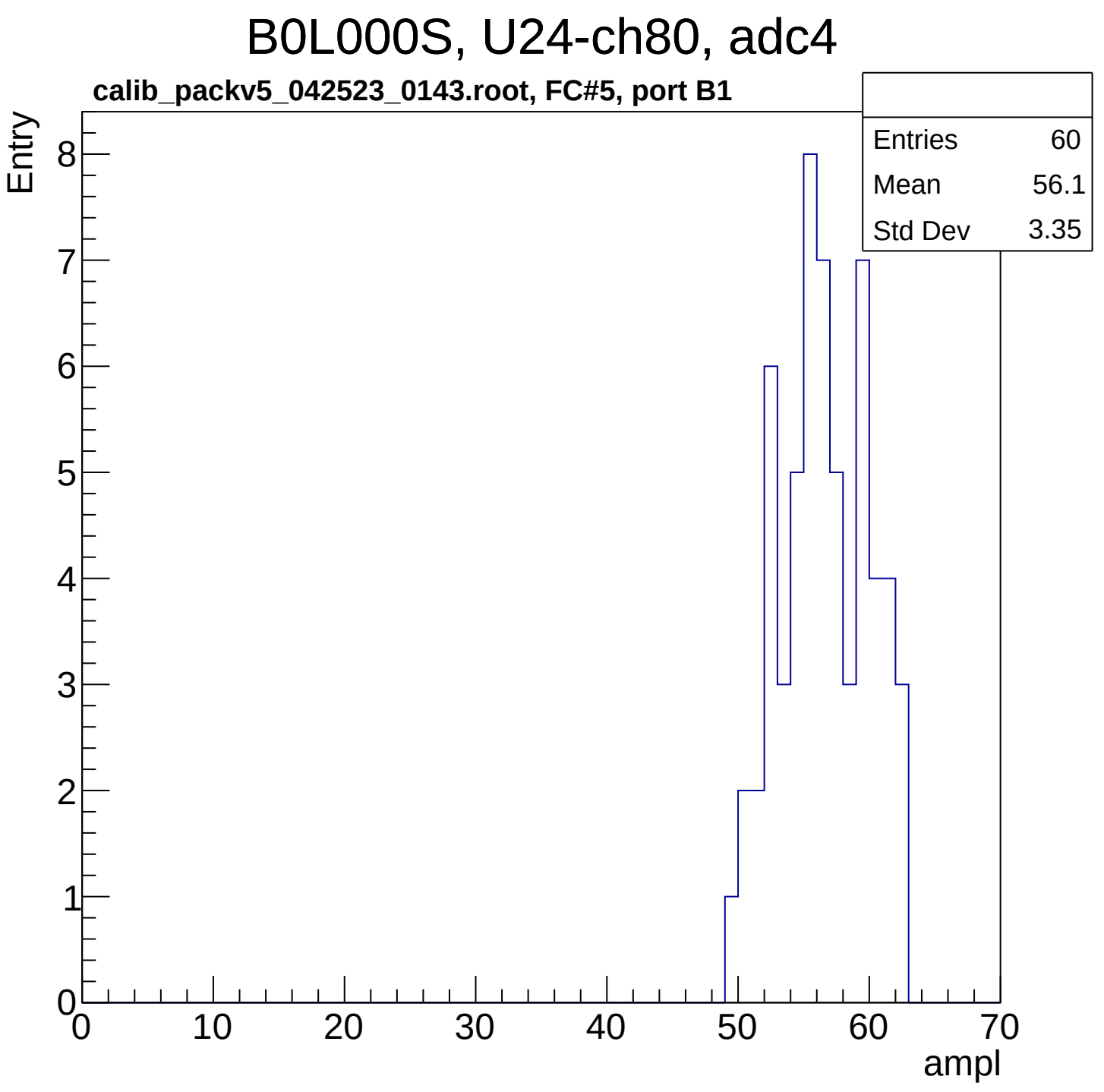
40
50
60
70


B0L000S, U24-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.1
Std Dev 3.35
Entry
ampl
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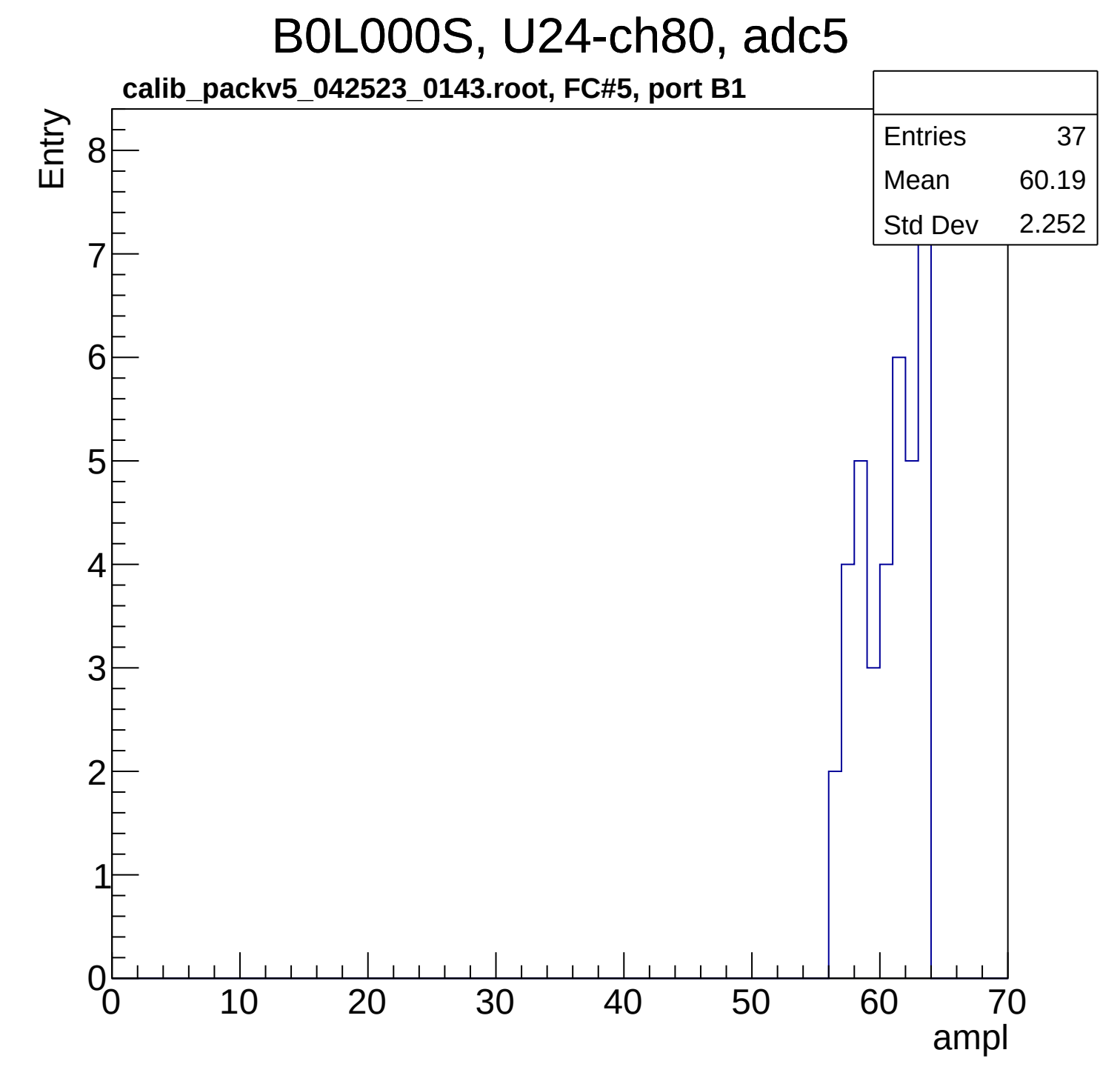
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.19
Std Dev 2.252
Entry
ampl
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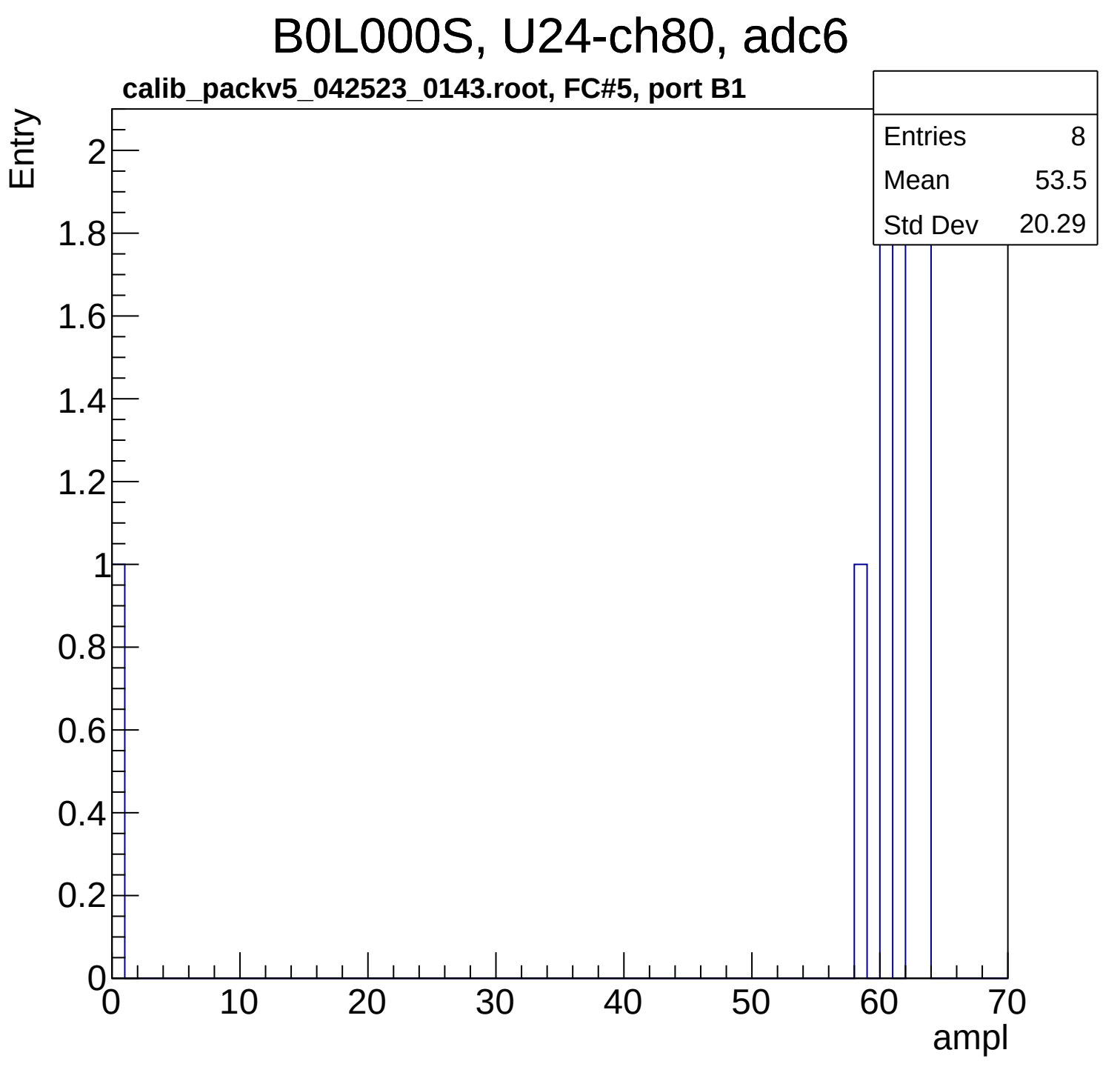
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.5
Std Dev 20.29
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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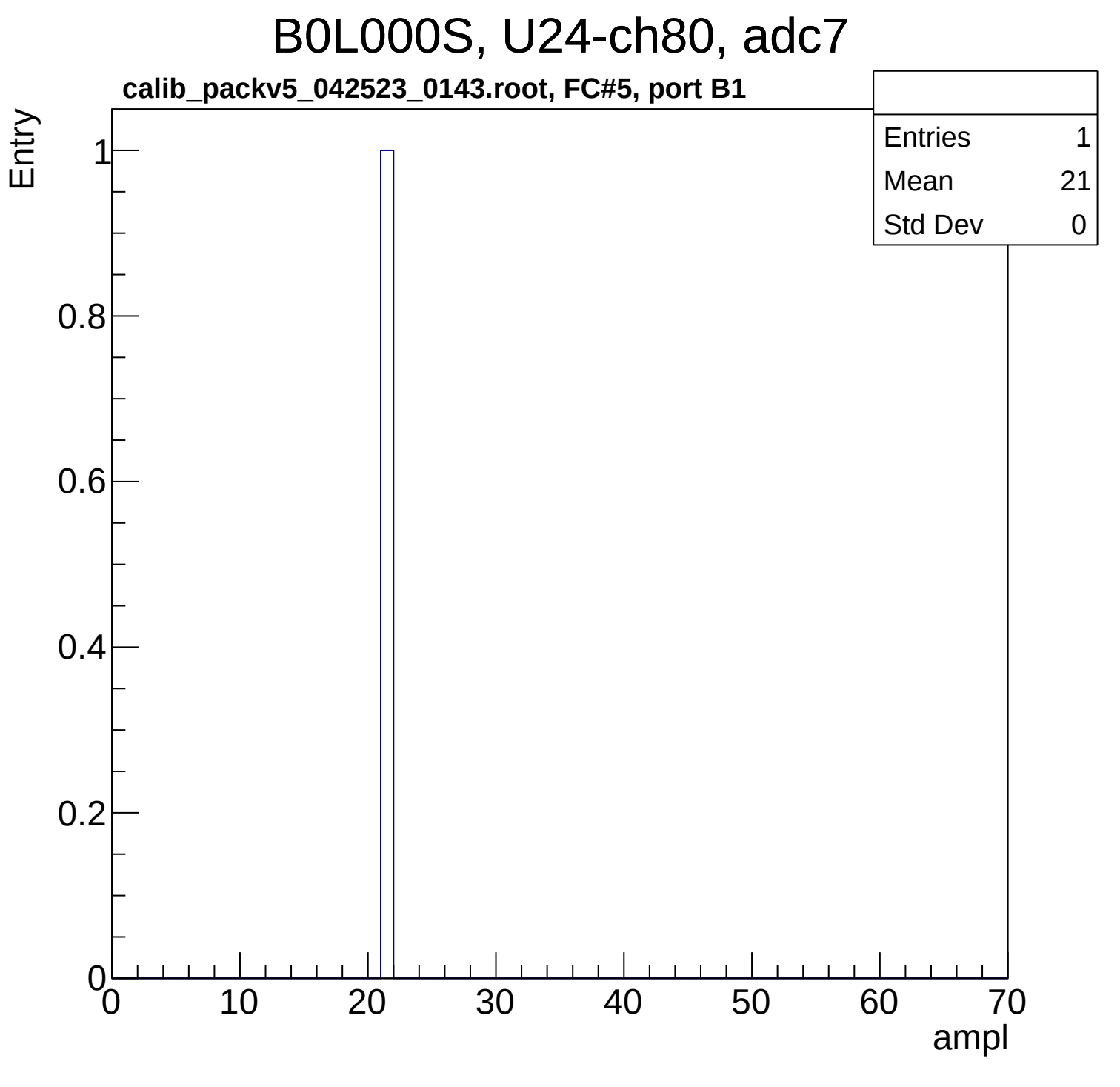
B0L000S, U24-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
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10
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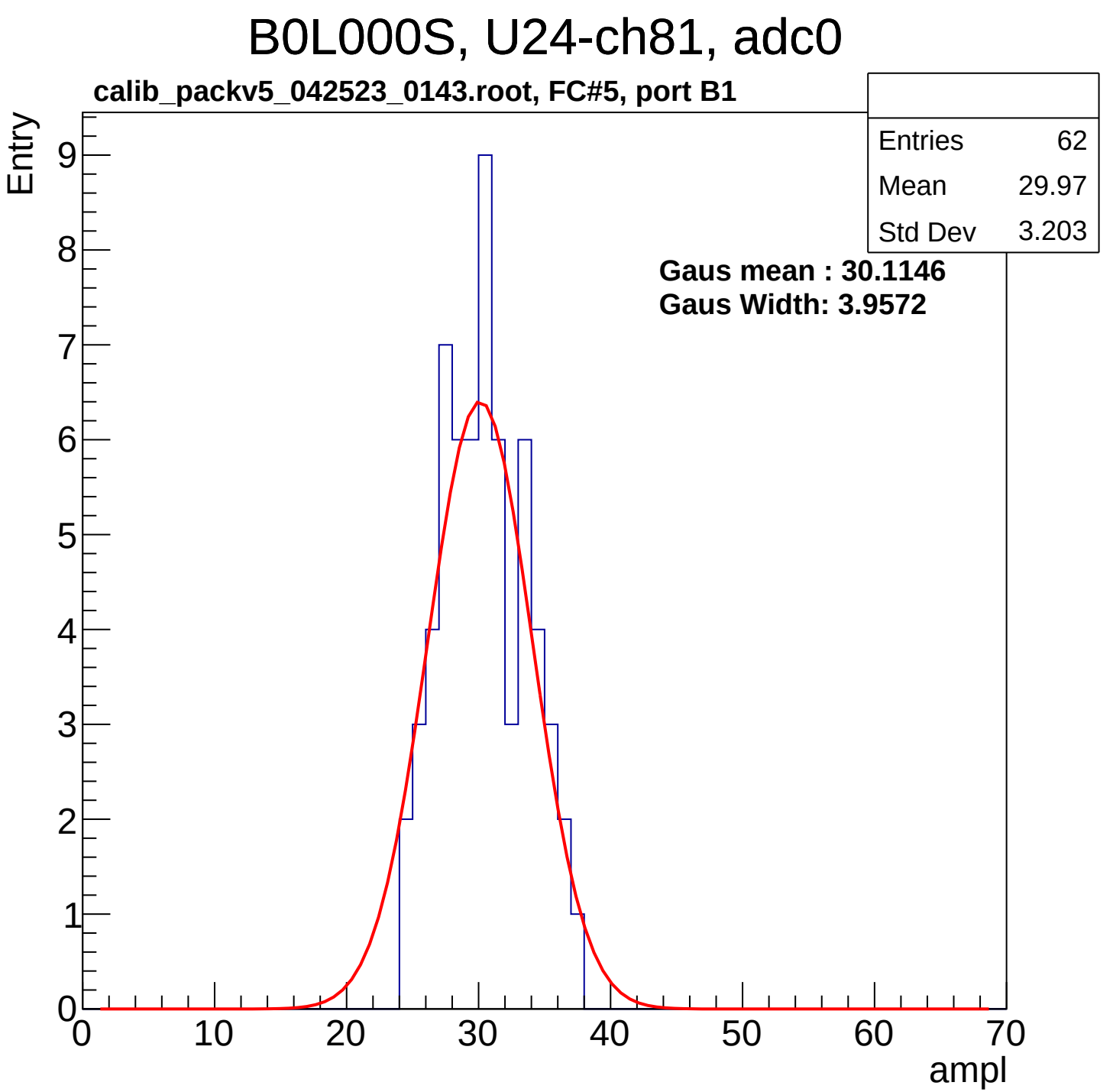
ampl


B0L000S, U24-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.97
Std Dev 3.203
Gaus mean : 30.1146
Gaus Width: 3.9572
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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40
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60
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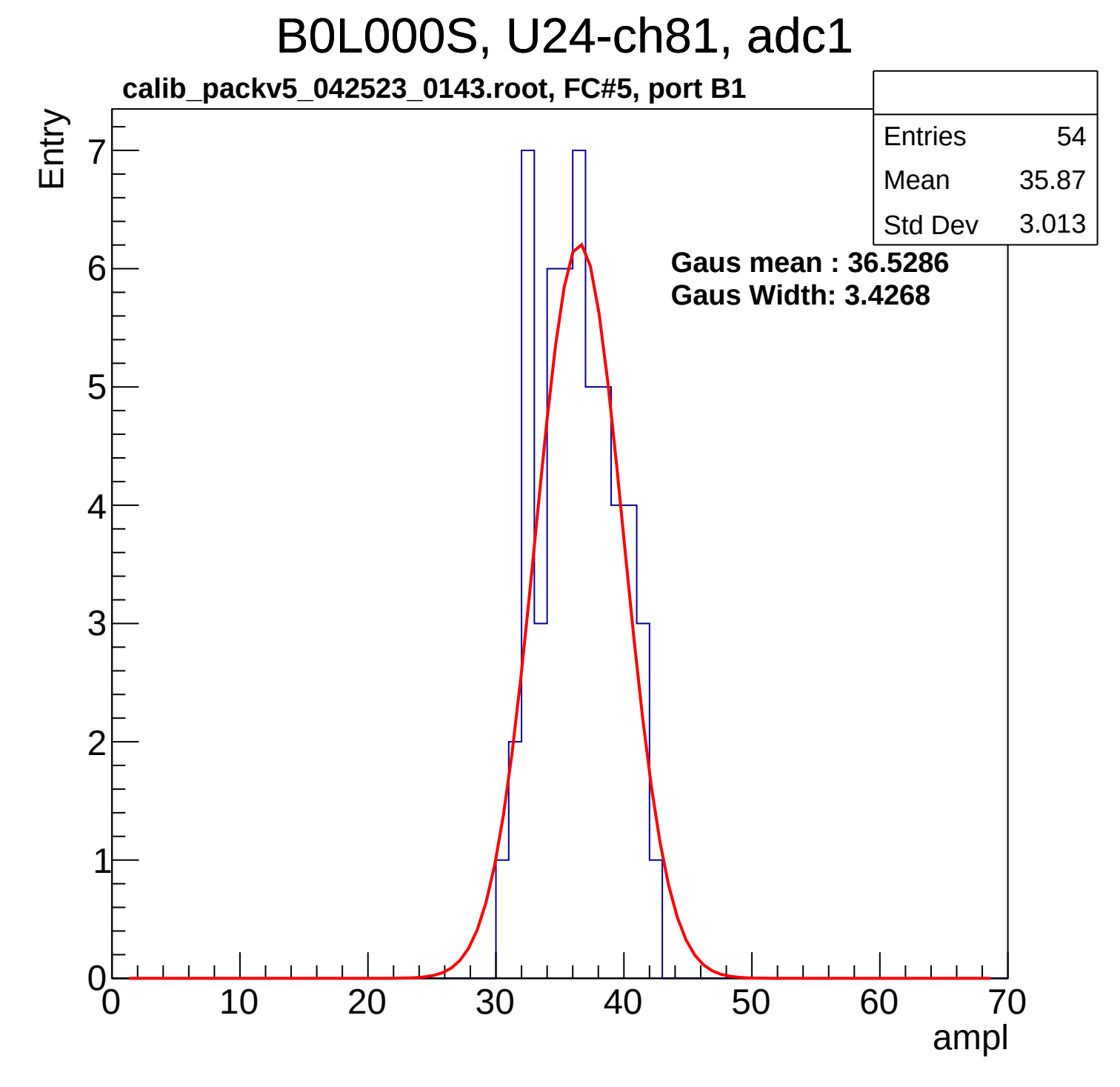
B0L000S, U24-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 35.87
Std Dev 3.013
Gaus mean : 36.5286
Gaus Width: 3.4268
Entry
ampl
0
1
2
3
4
5
6
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0
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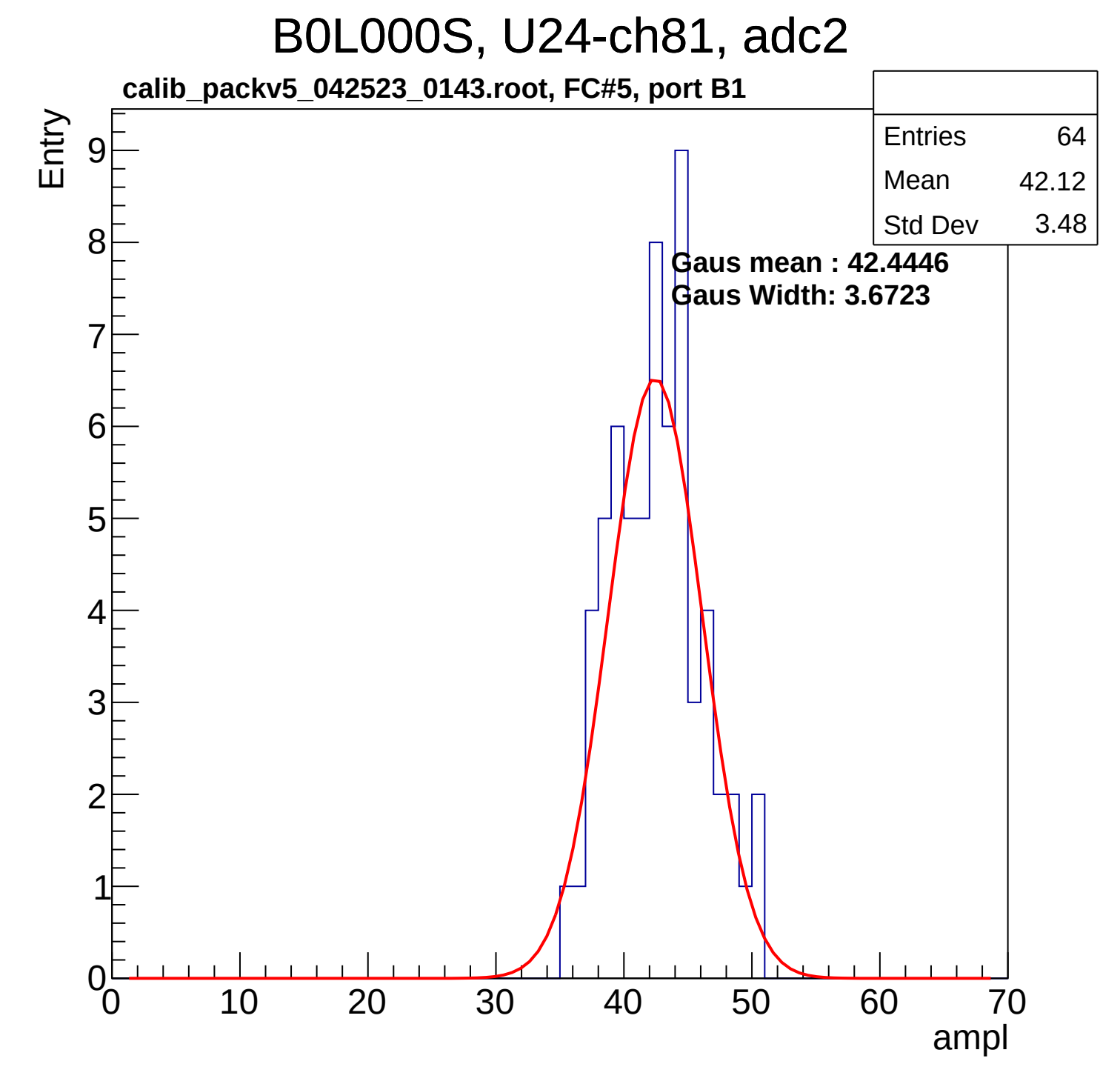
B0L000S, U24-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.12
Std Dev 3.48
Gaus mean : 42.4446
Gaus Width: 3.6723
Entry
ampl
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2
3
4
5
6
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0
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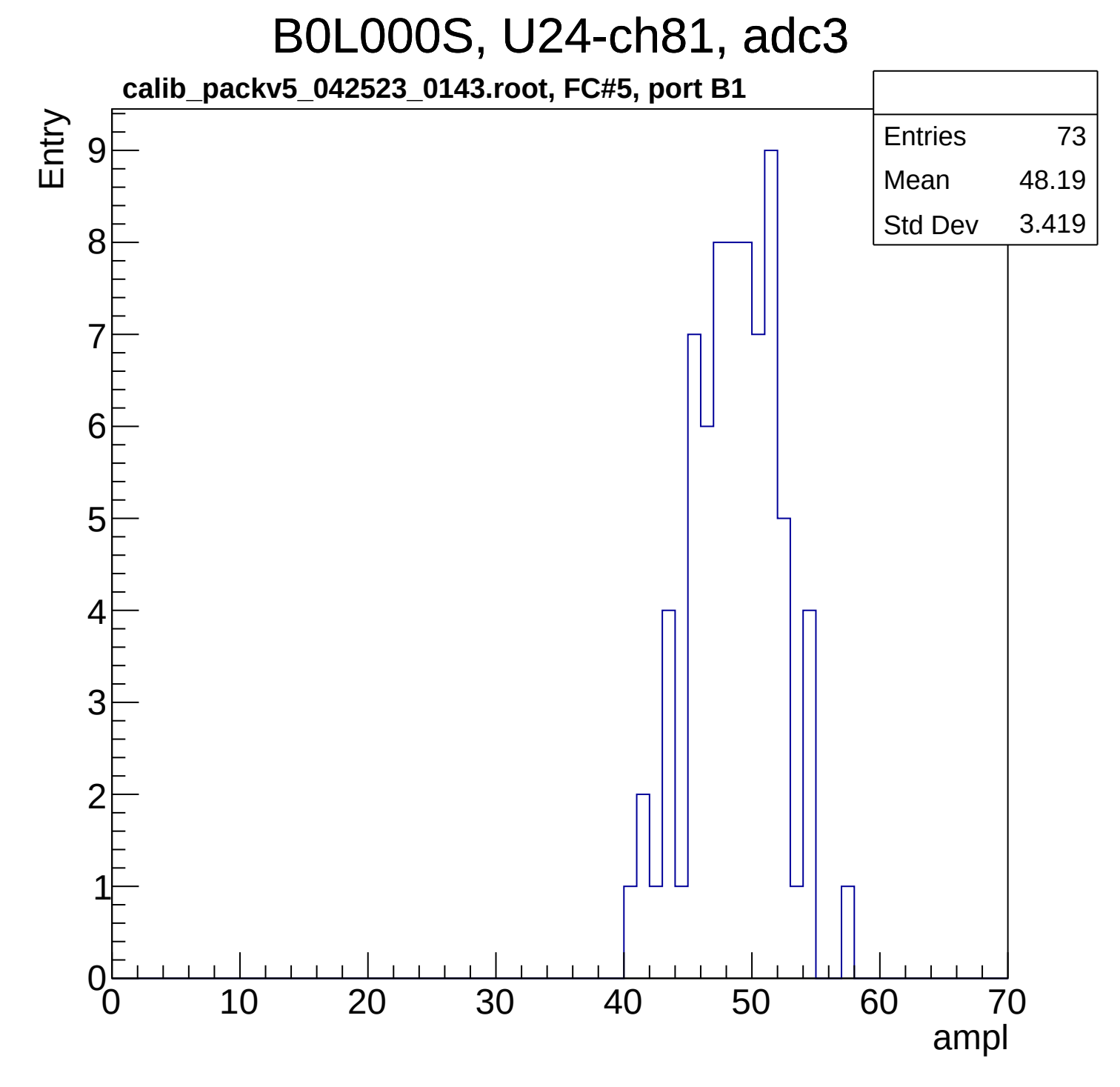
B0L000S, U24-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.19
Std Dev 3.419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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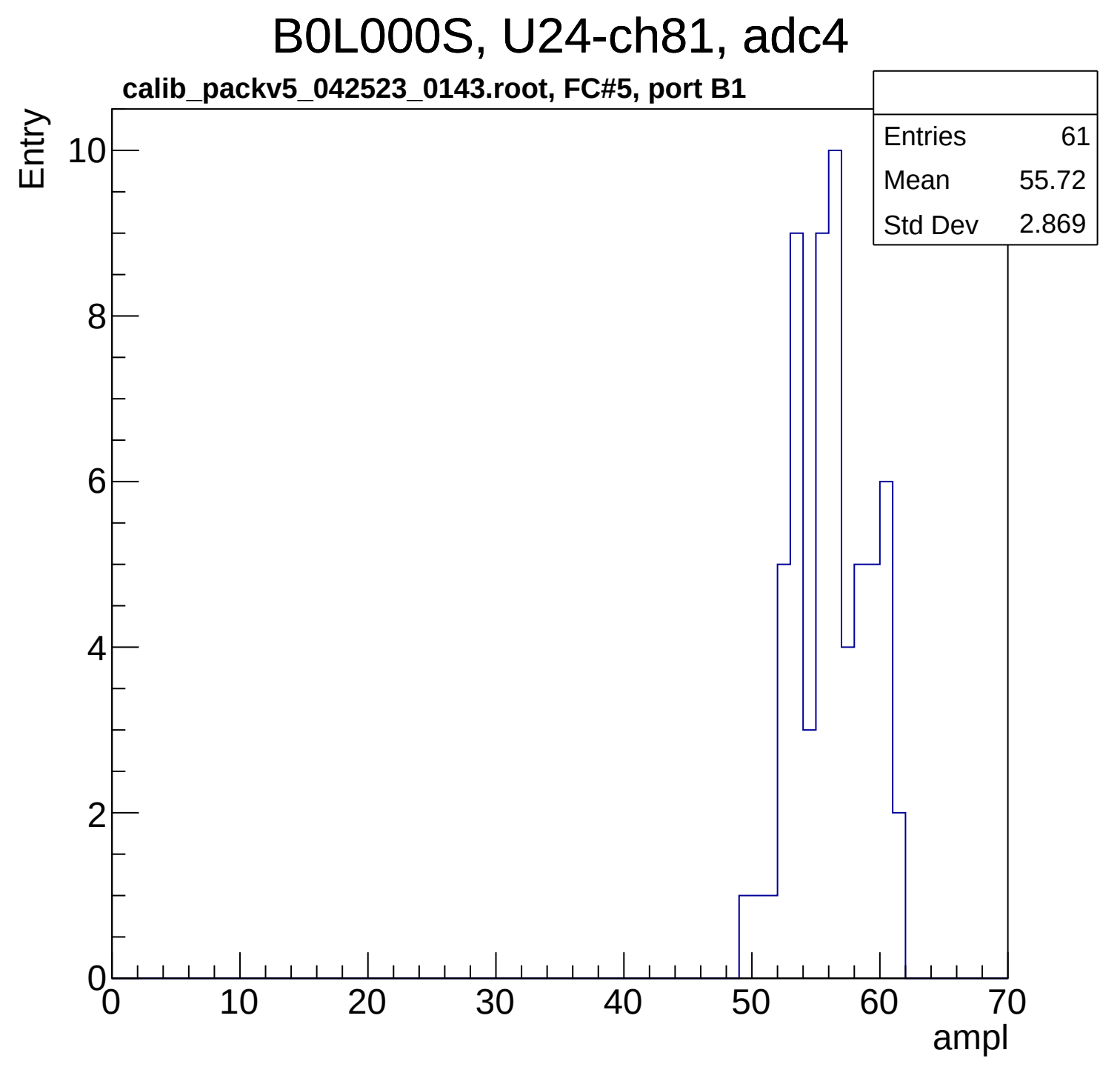
B0L000S, U24-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.72
Std Dev 2.869
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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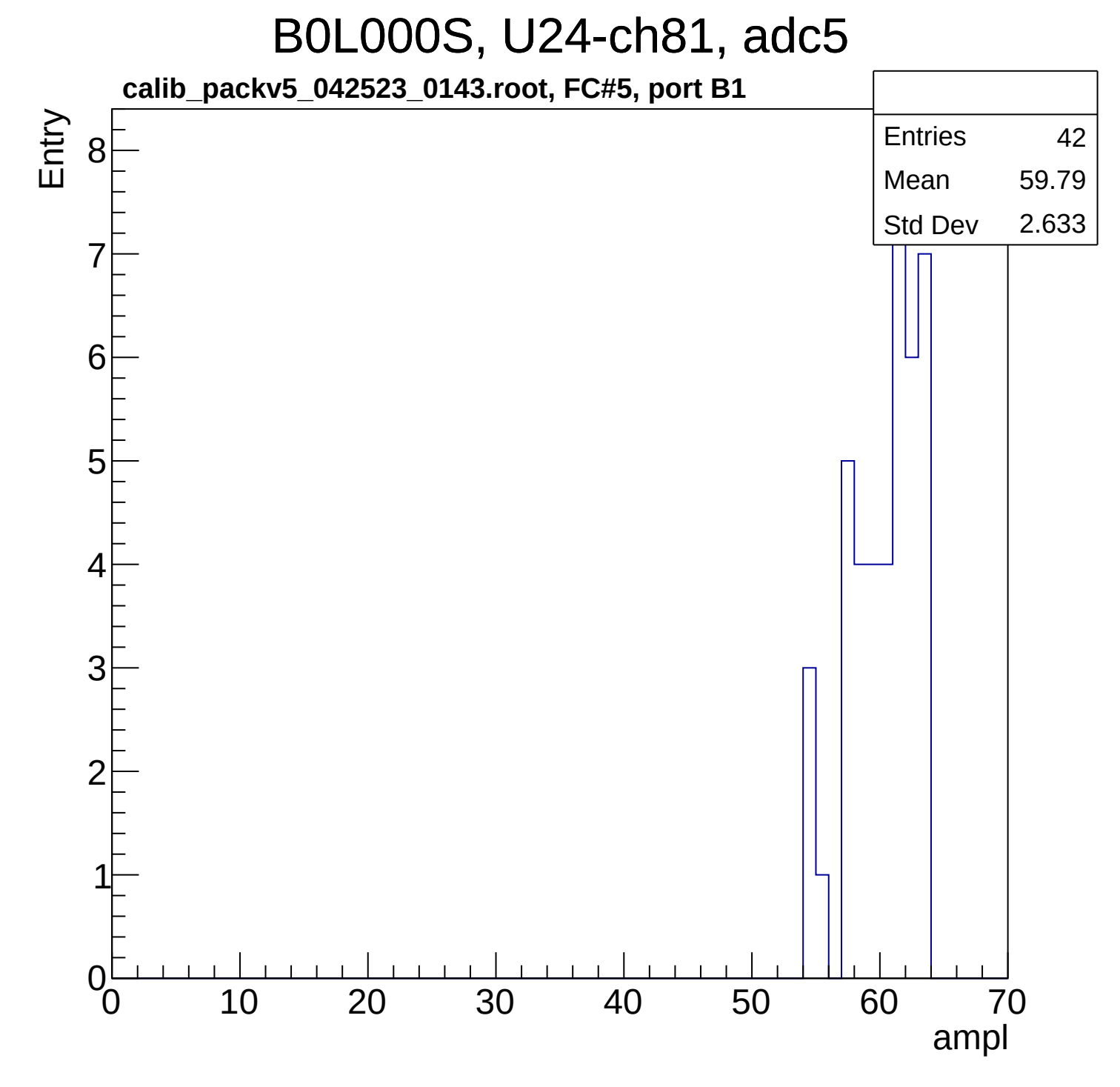
B0L000S, U24-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.79
Std Dev 2.633
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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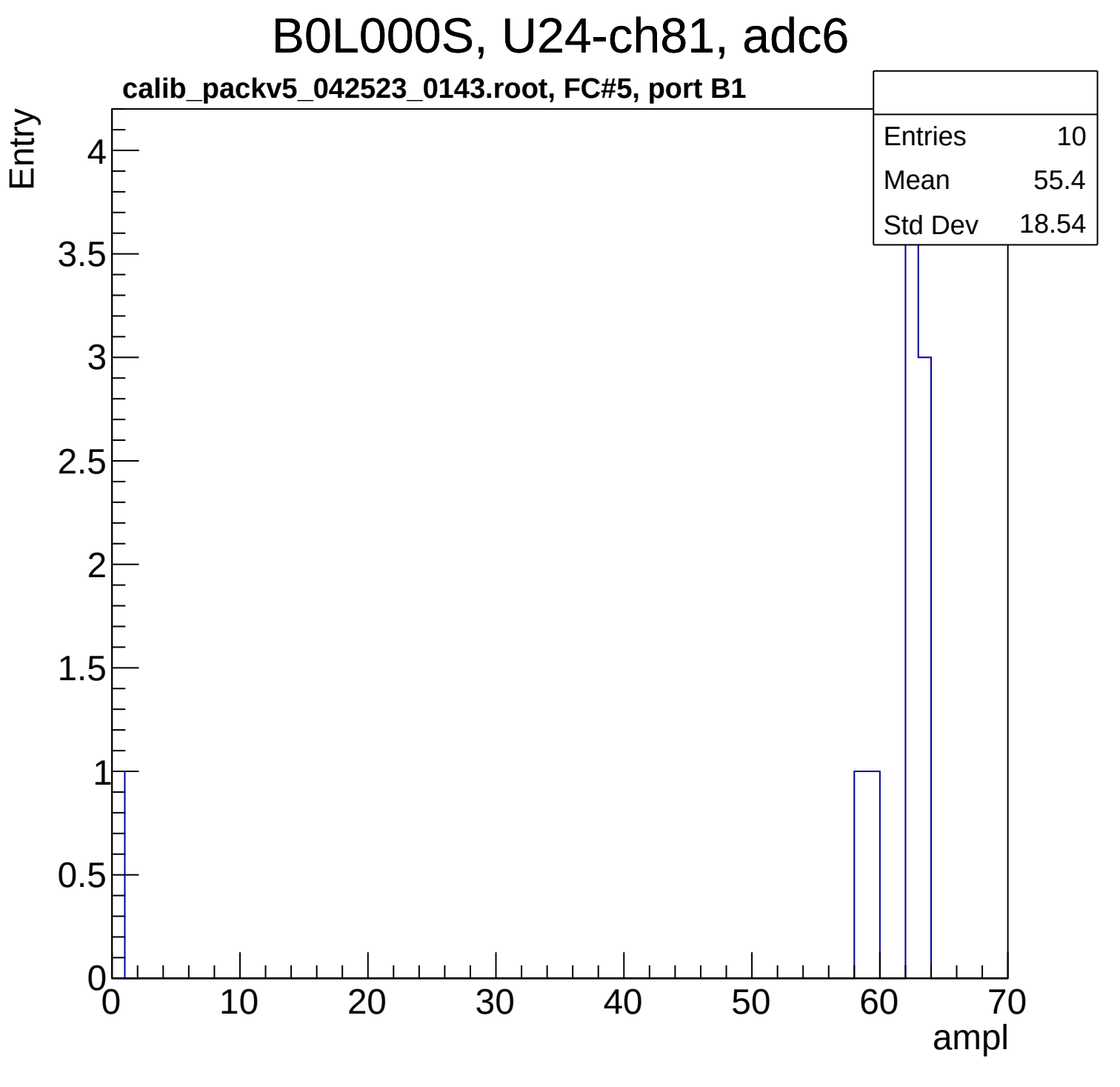
B0L000S, U24-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.4
Std Dev 18.54
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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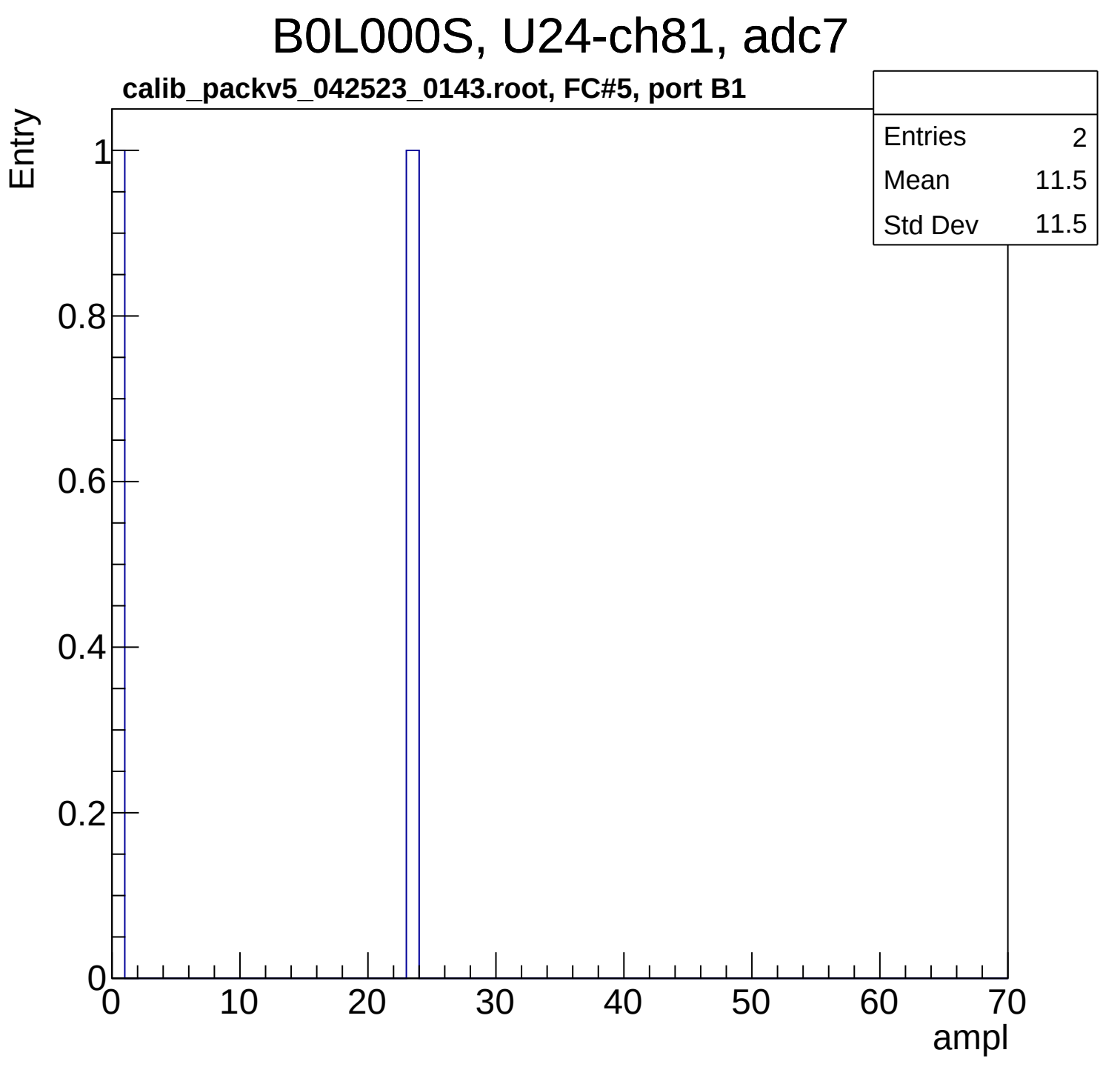
B0L000S, U24-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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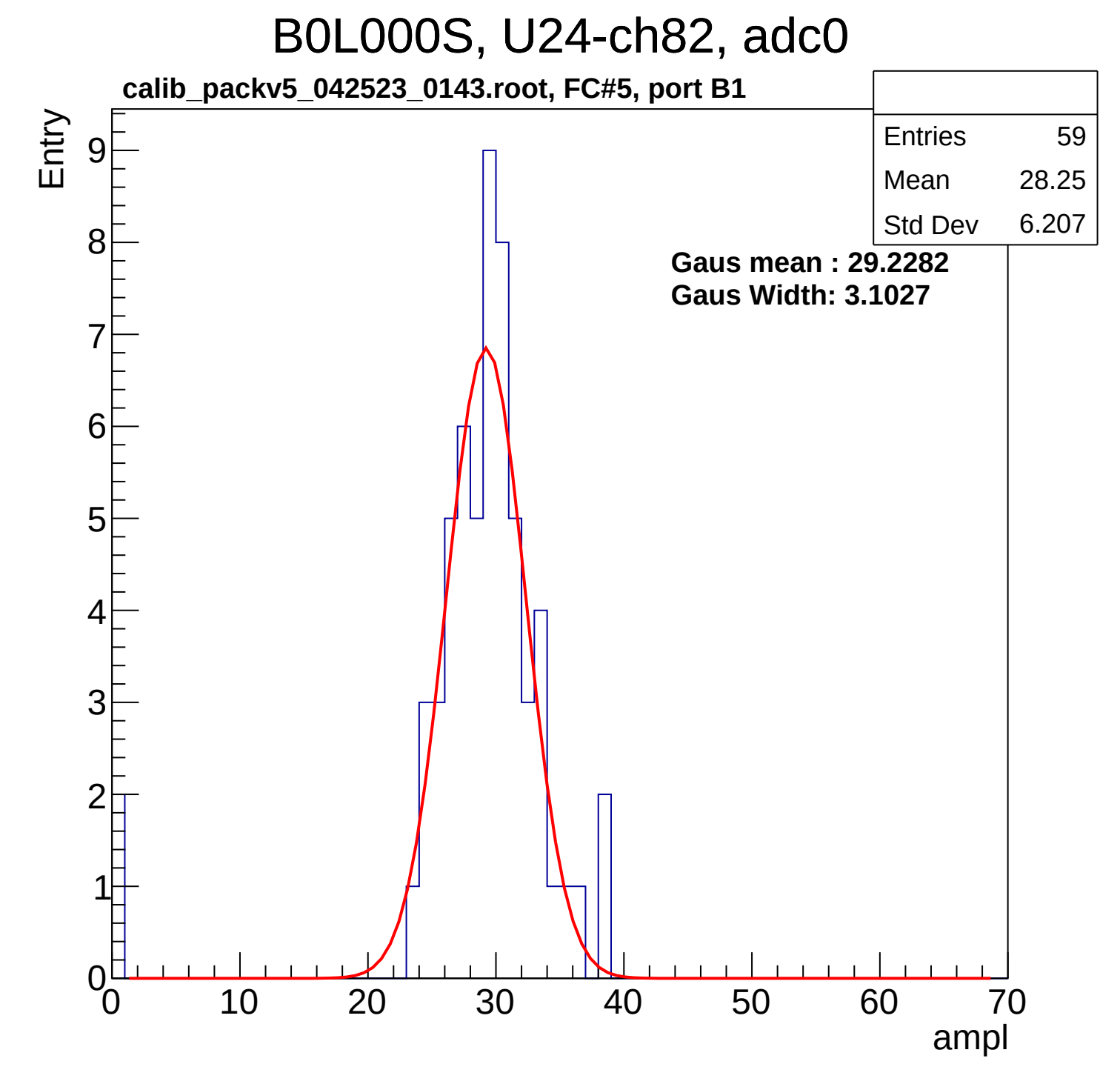
B0L000S, U24-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 28.25
Std Dev 6.207
Gaus mean : 29.2282
Gaus Width: 3.1027
Entry
ampl
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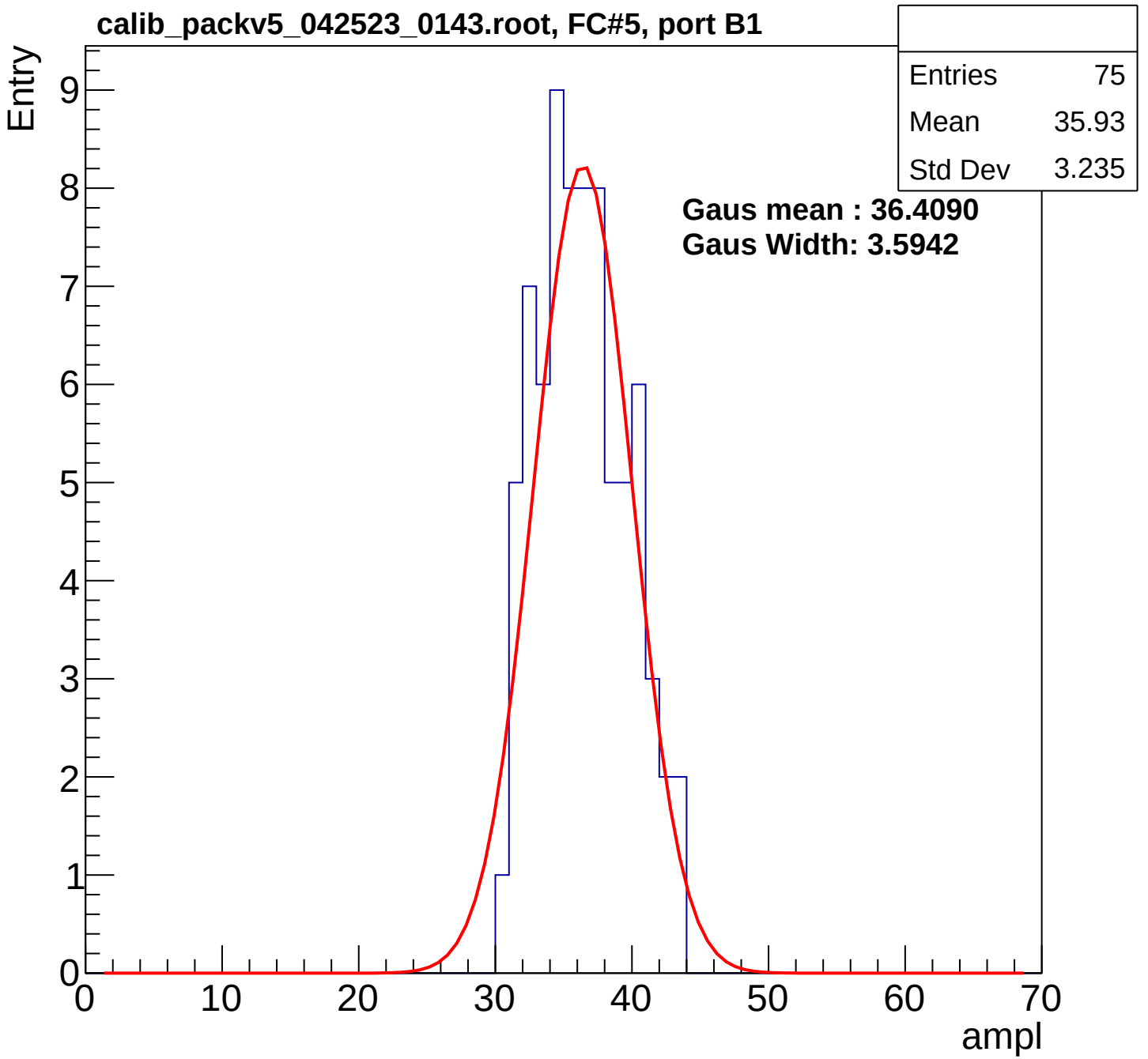
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.93
Std Dev 3.235
Gaus mean : 36.4090
Gaus Width: 3.5942
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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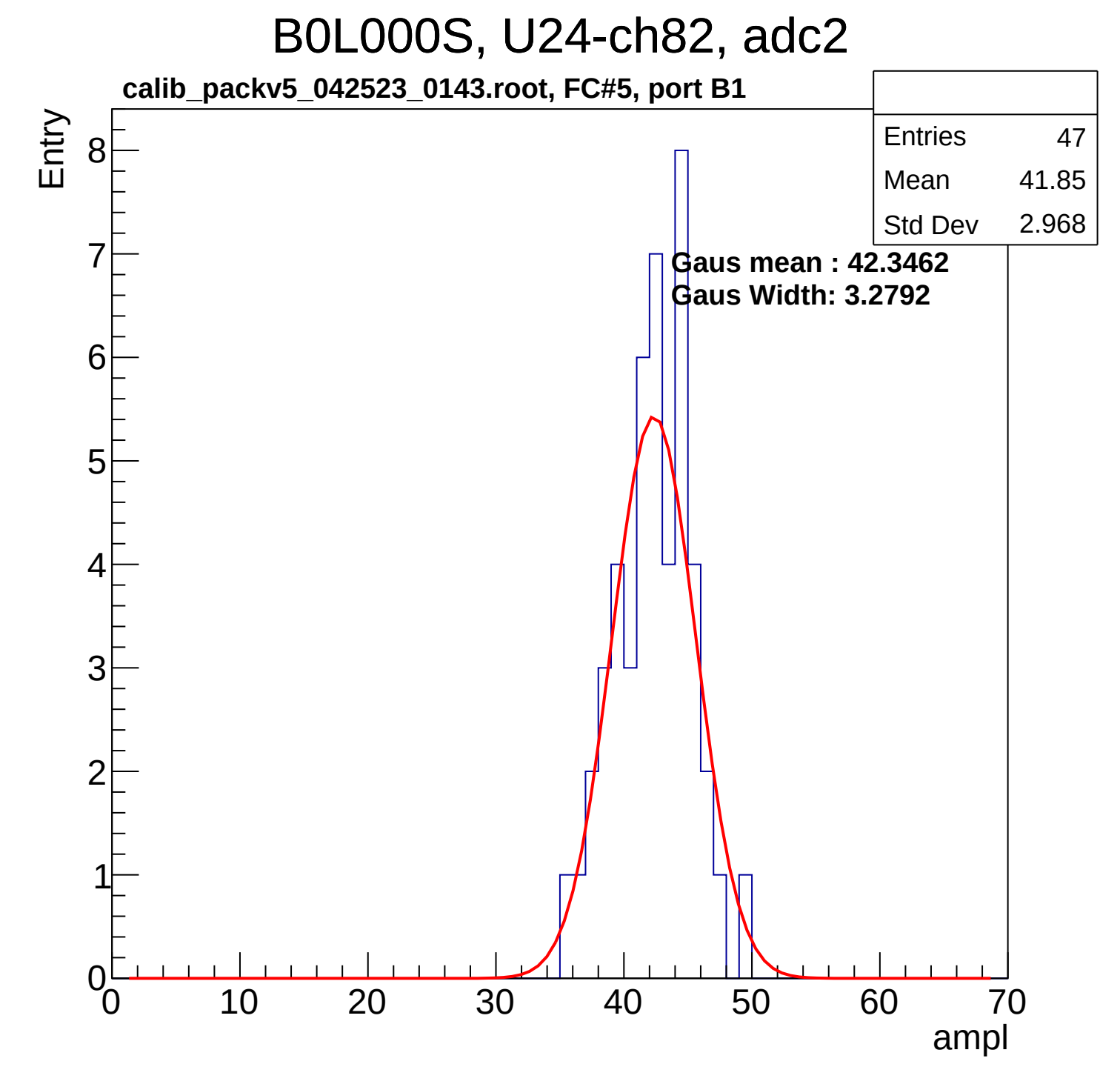
B0L000S, U24-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 41.85
Std Dev 2.968
Gaus mean : 42.3462
Gaus Width: 3.2792
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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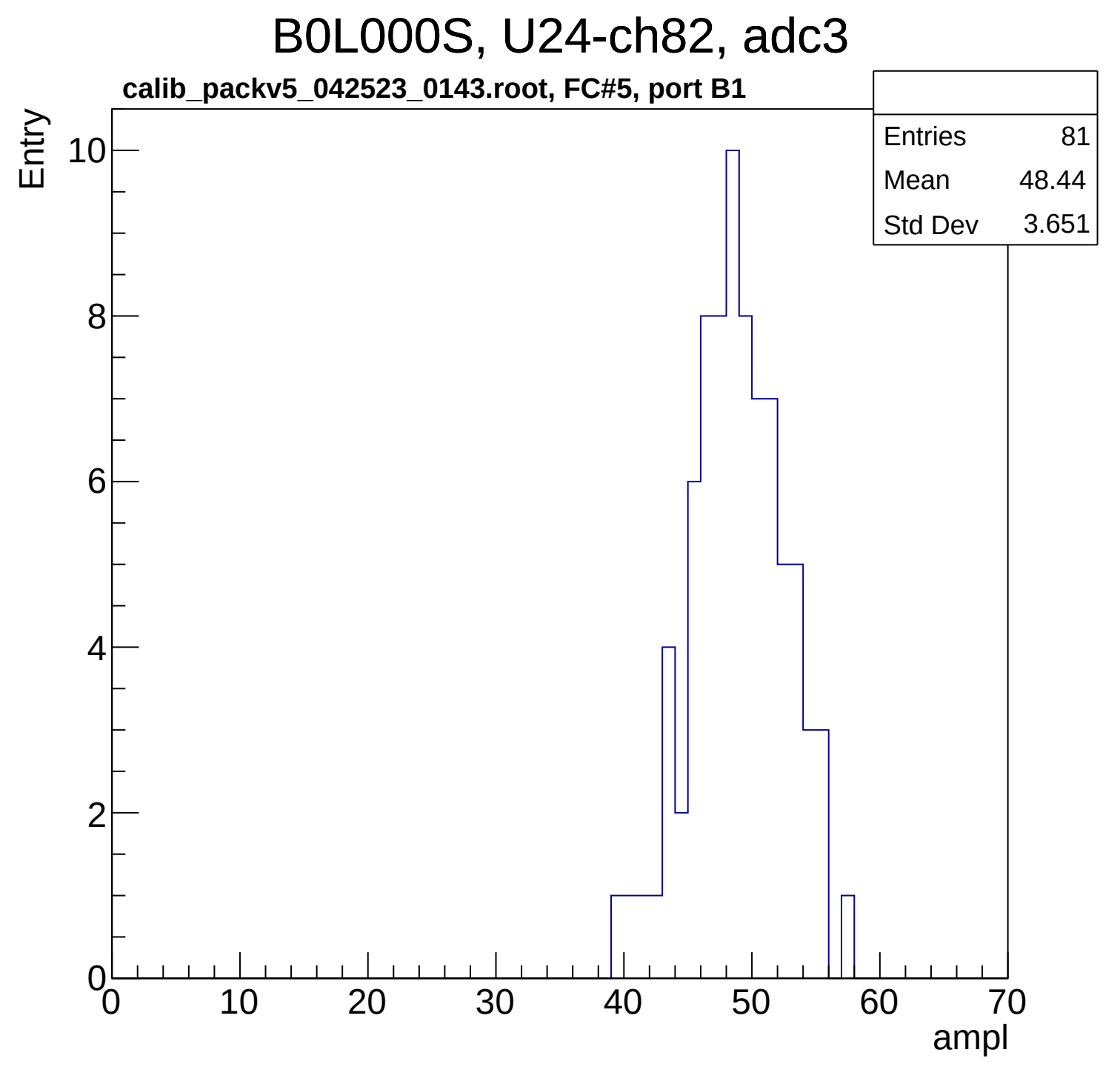
B0L000S, U24-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 48.44
Std Dev 3.651
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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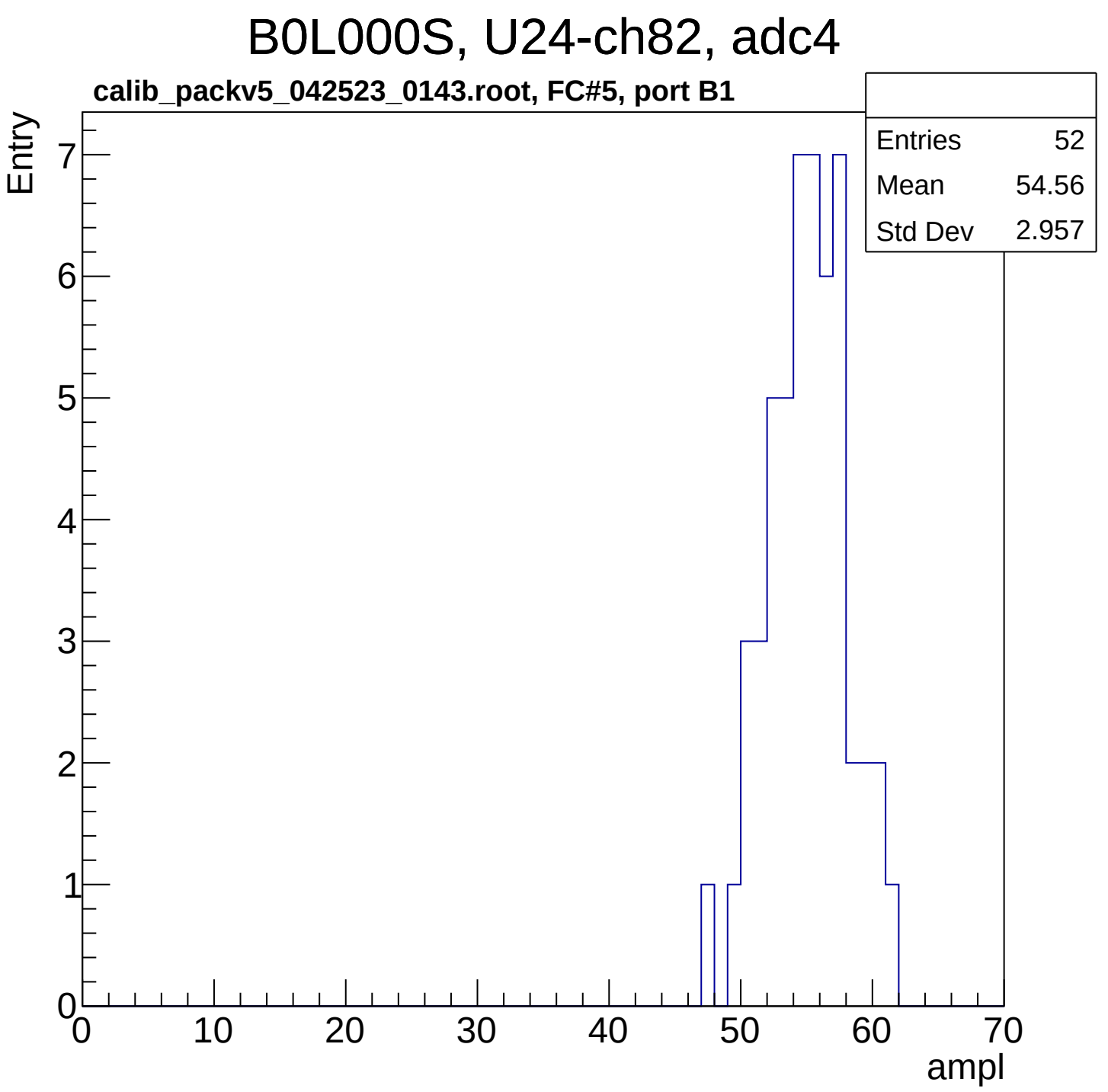
B0L000S, U24-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.56
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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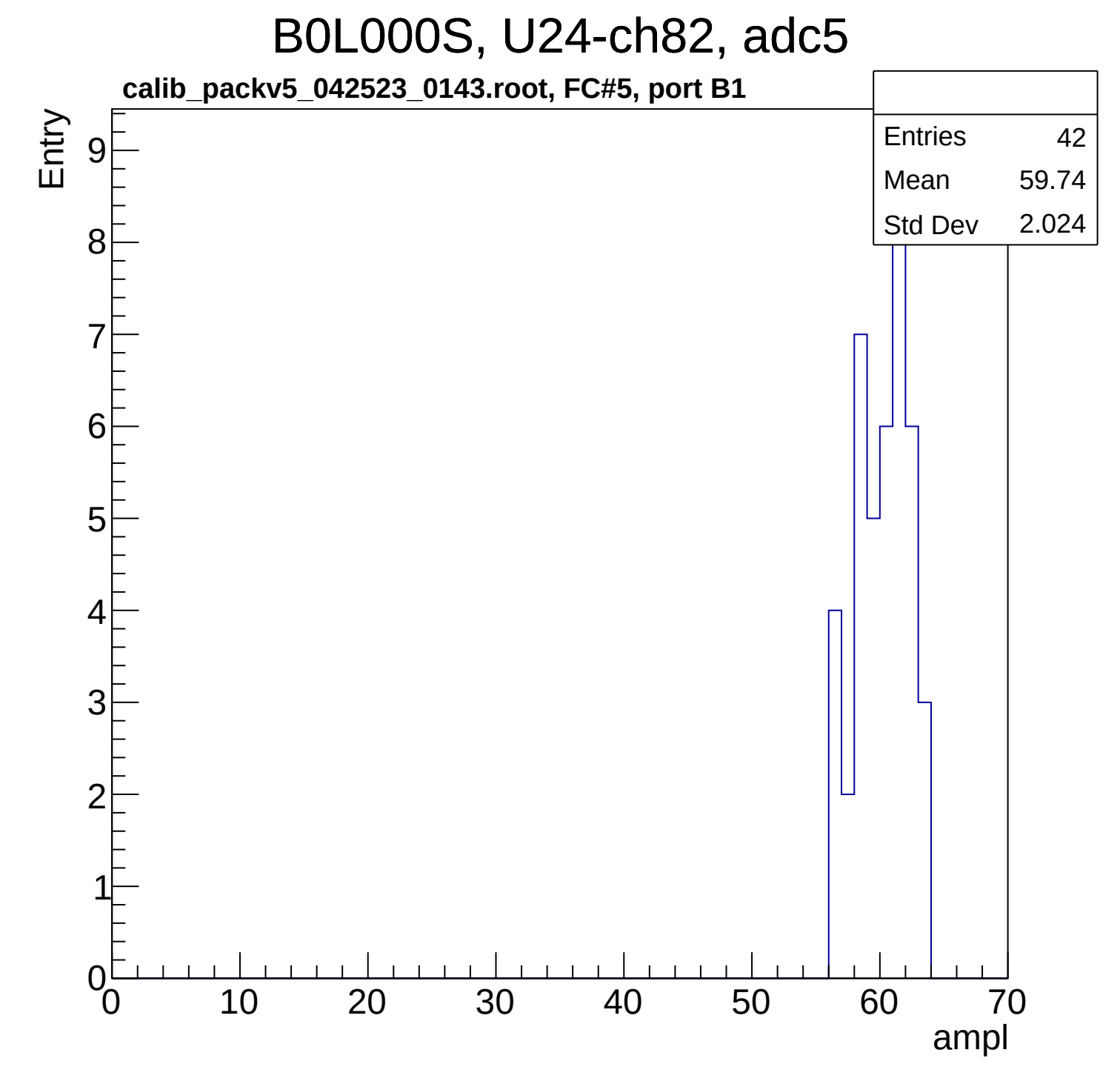
40
50
60
70


B0L000S, U24-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.74
Std Dev 2.024
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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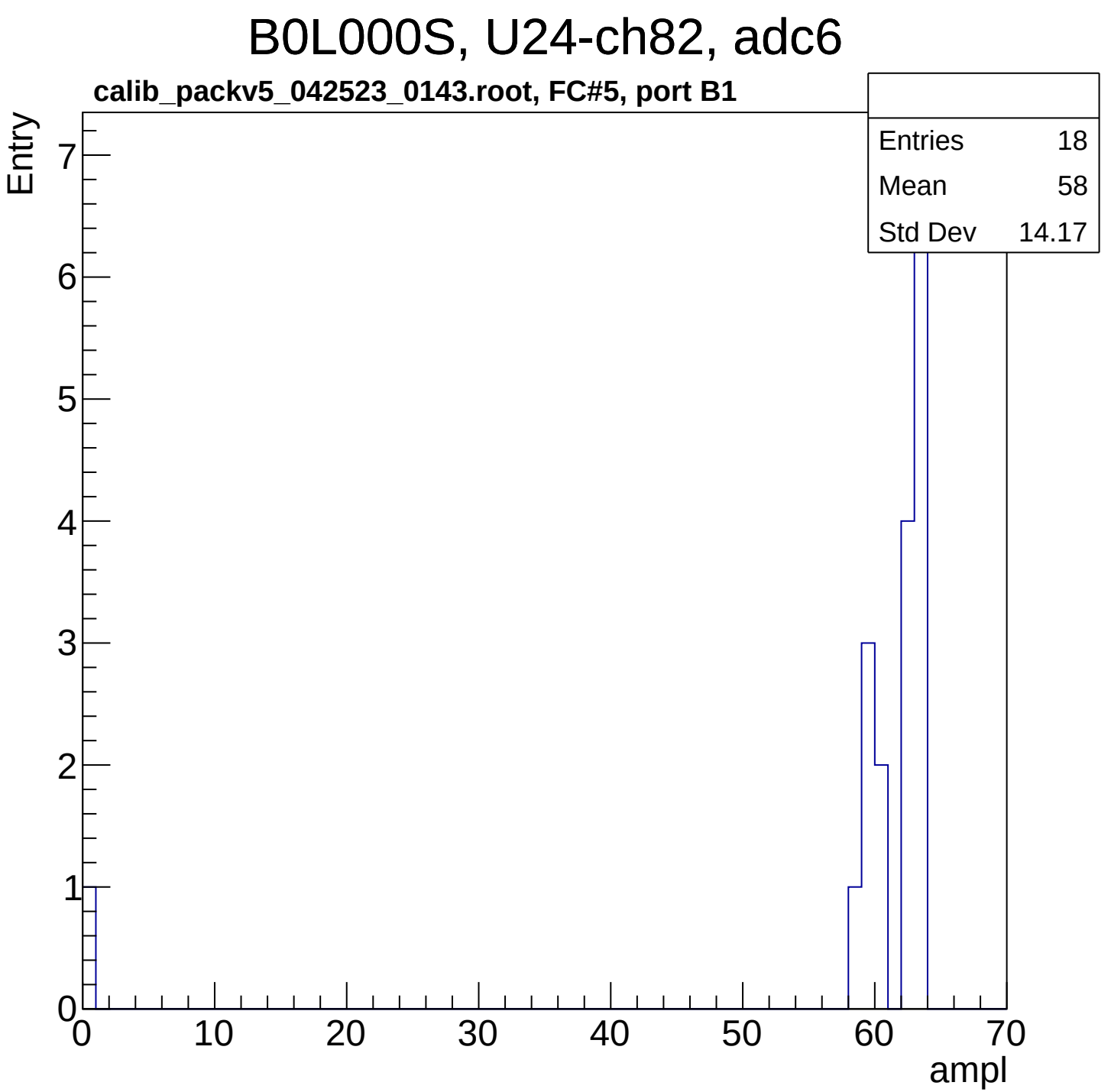
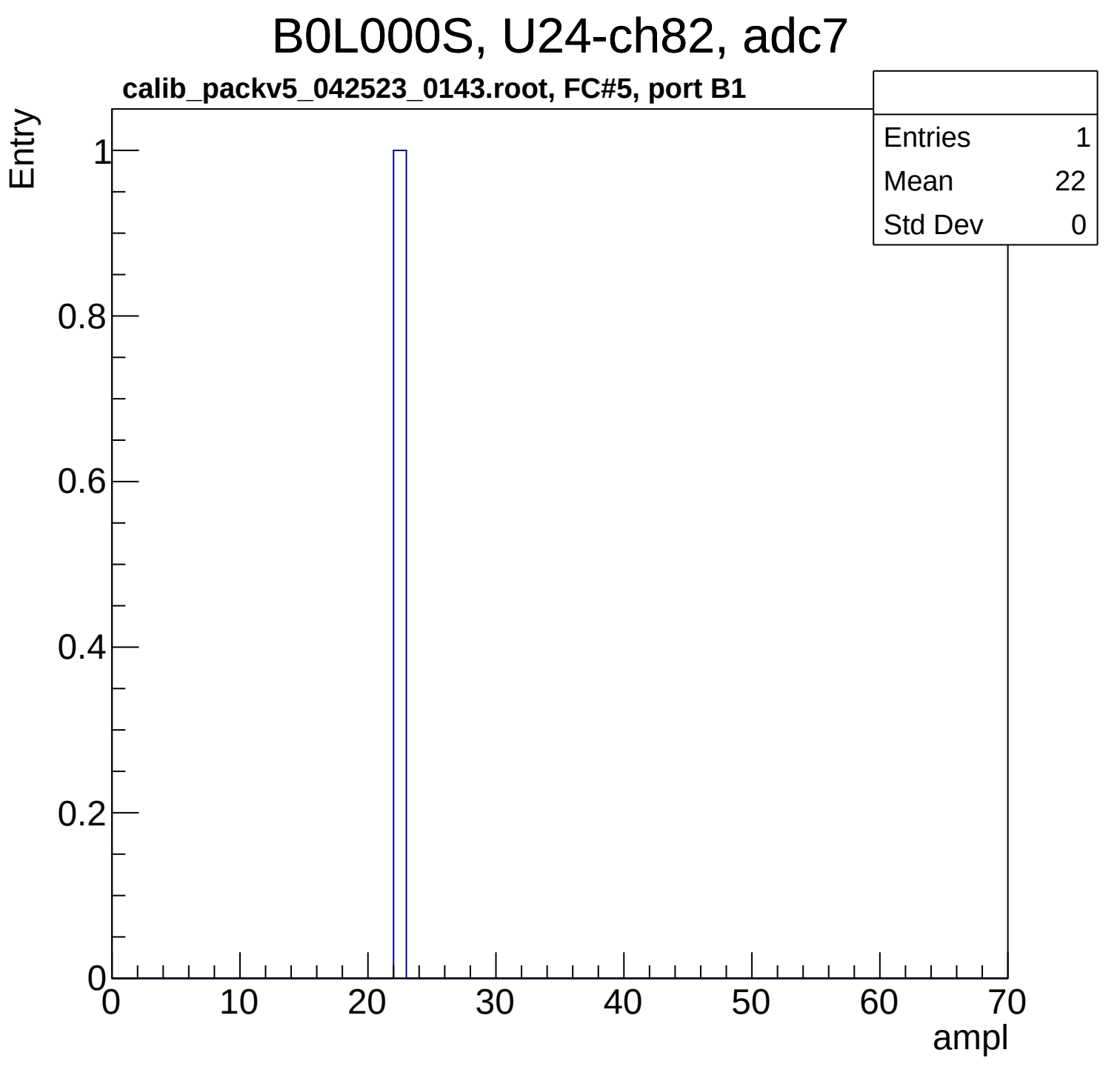

B0L000S, U24-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58
Std Dev 14.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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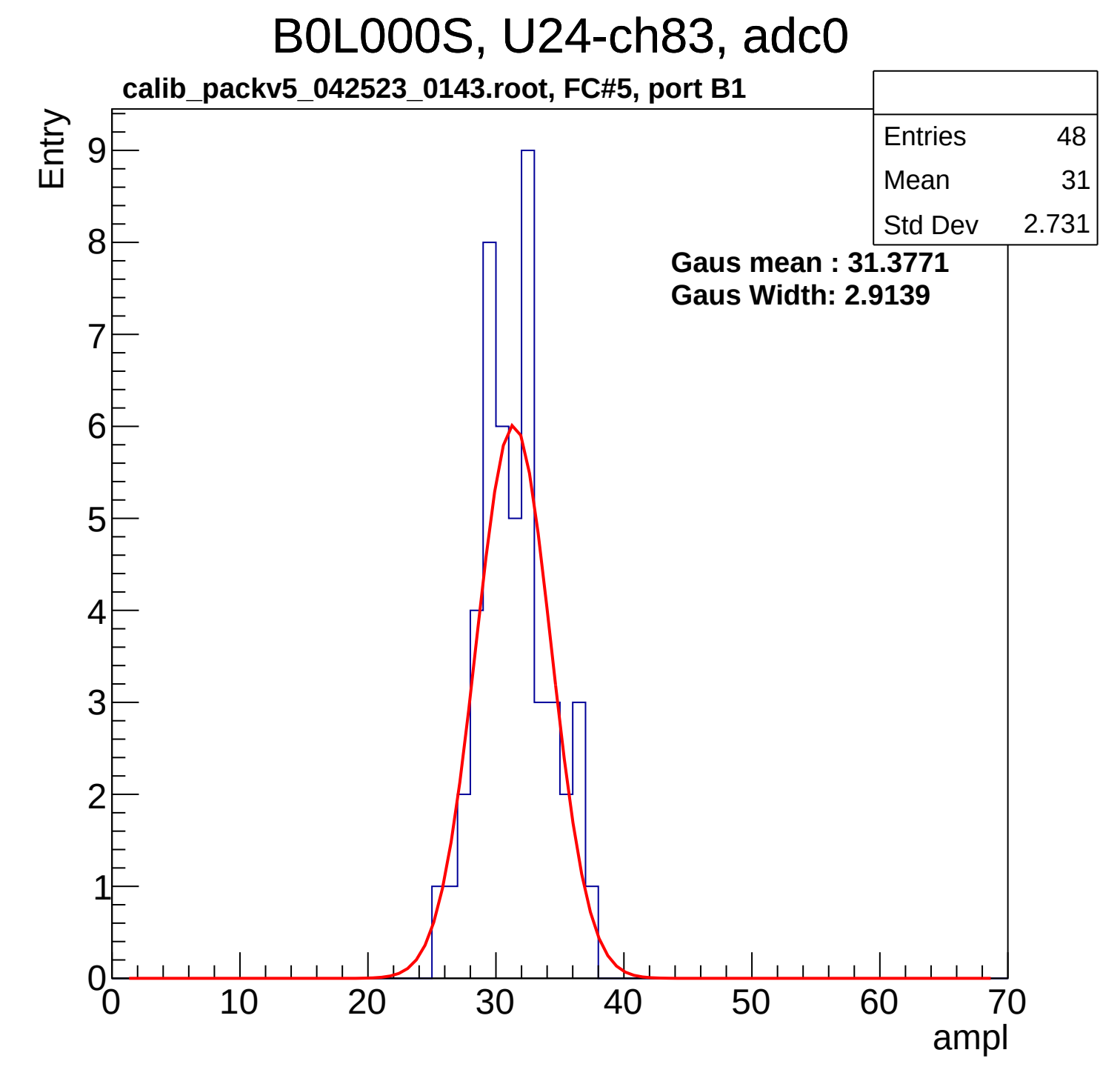
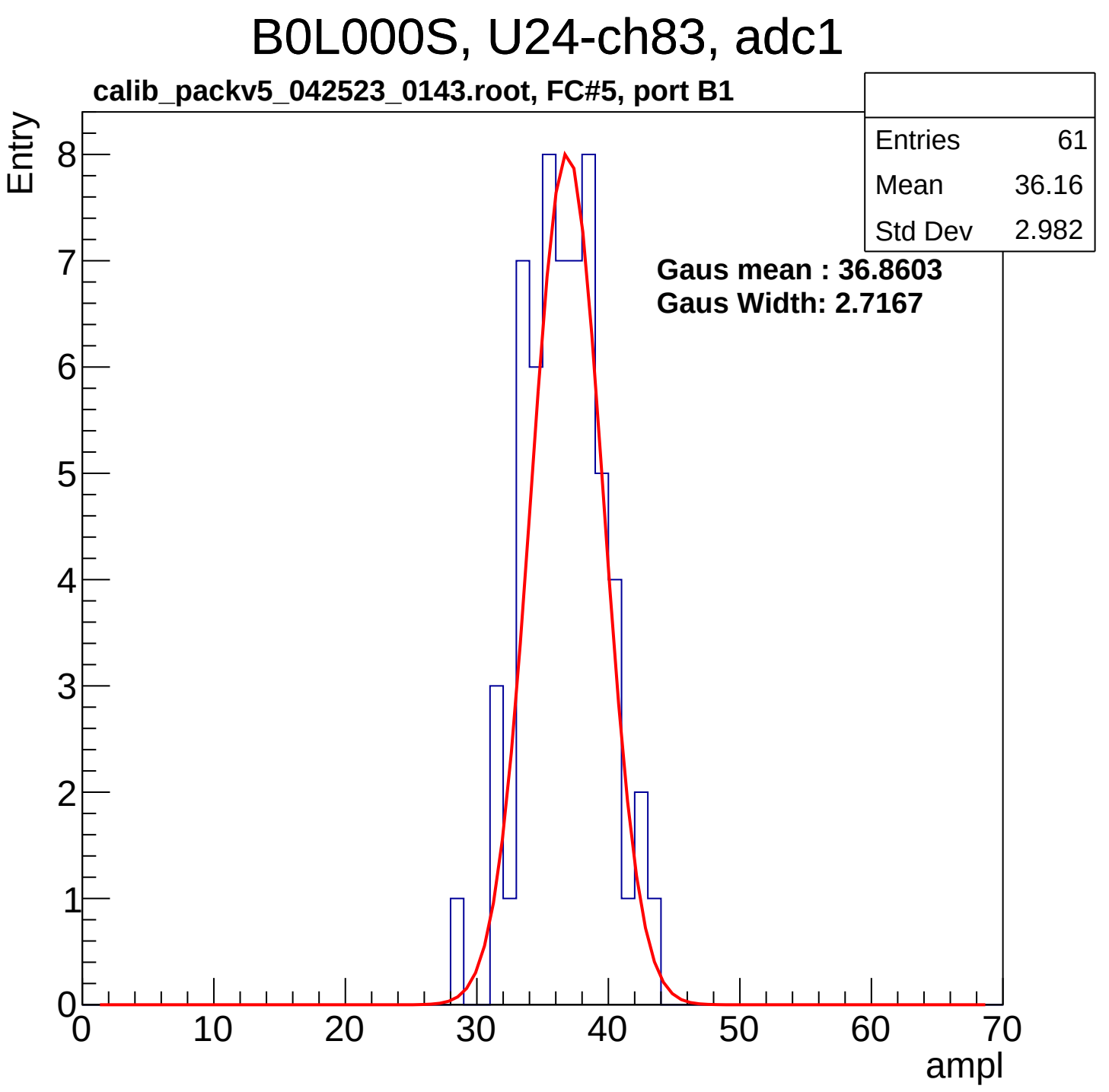
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 31
Std Dev 2.731
Gaus mean : 31.3771
Gaus Width: 2.9139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.16
Std Dev 2.982
Gaus mean : 36.8603
Gaus Width: 2.7167
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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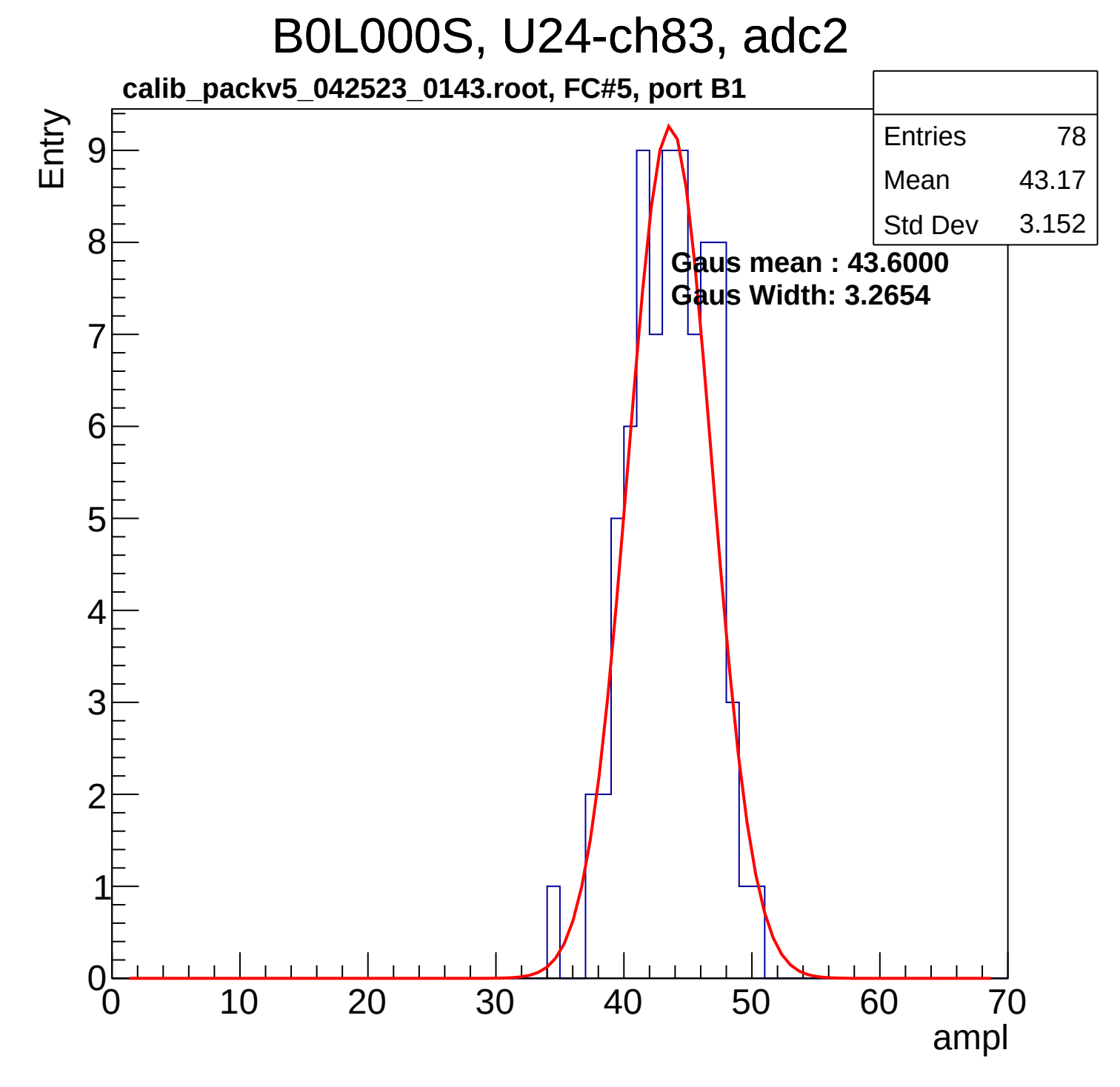
60
70


B0L000S, U24-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.17
Std Dev 3.152
Gaus mean : 43.6000
Gaus Width: 3.2654
Entry
ampl
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3
4
5
6
7
8
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0
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60
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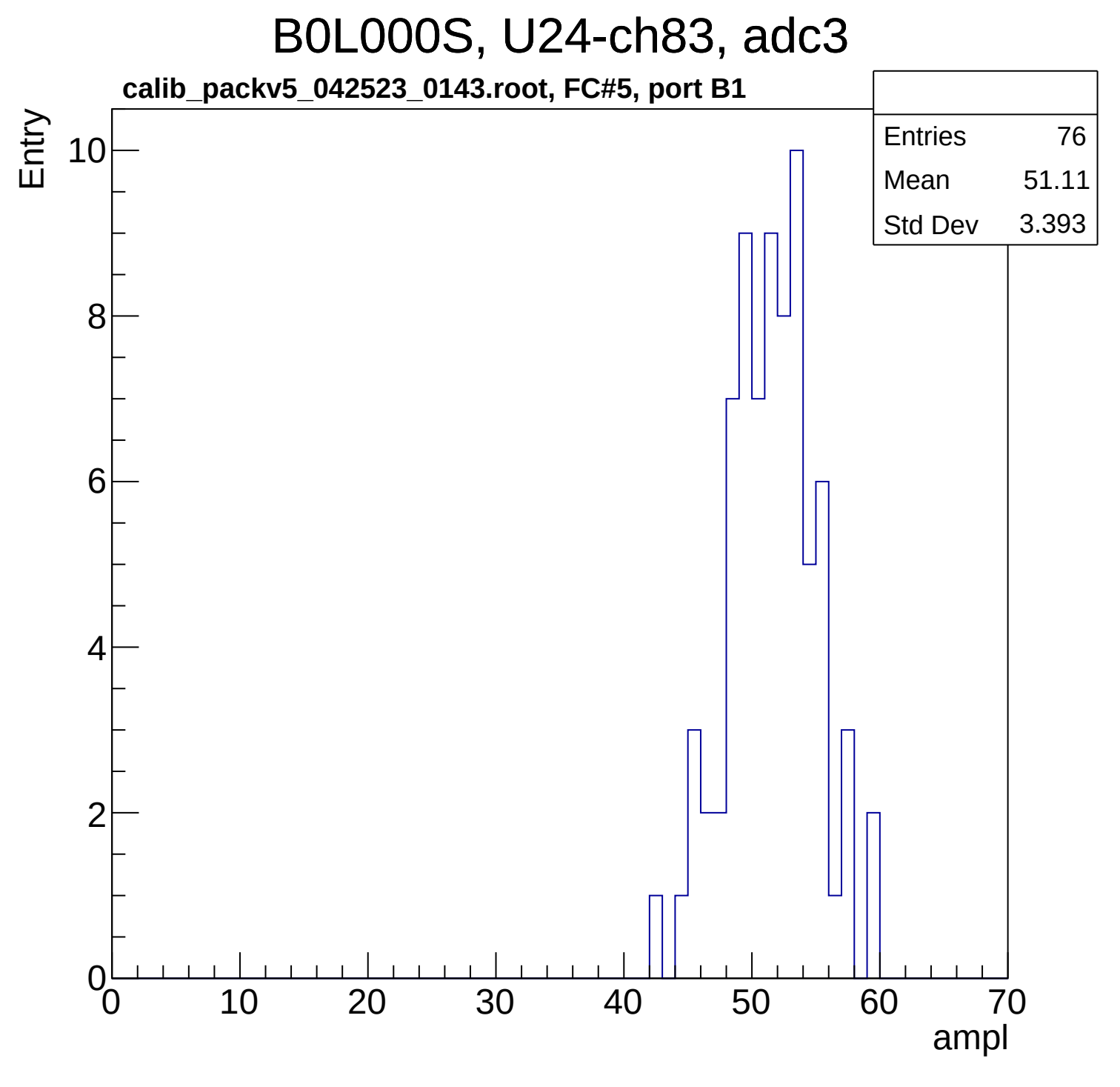
B0L000S, U24-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 51.11
Std Dev 3.393
Entry
ampl
0
2
4
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0
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20
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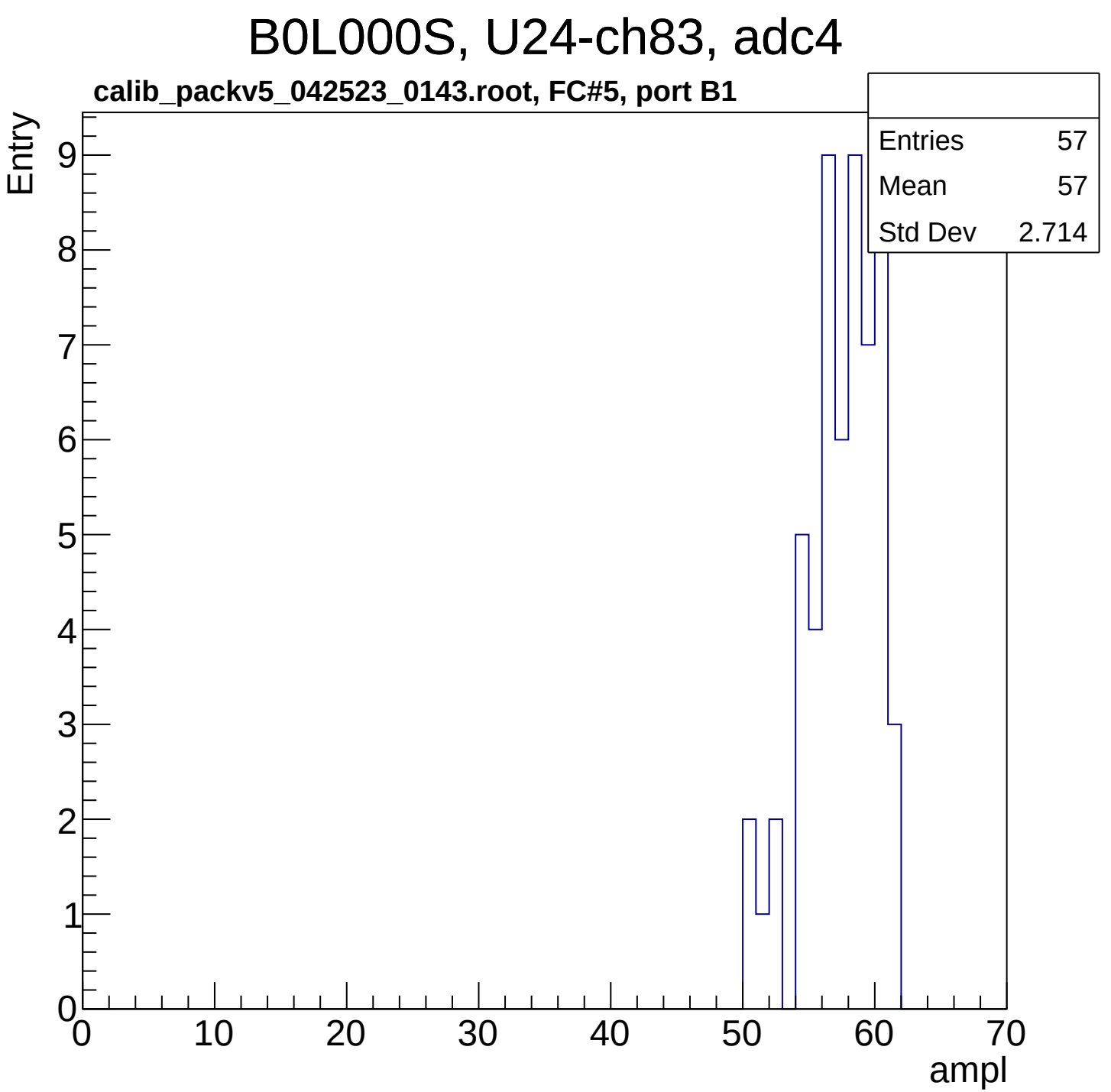
60
70


B0L000S, U24-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57
Std Dev 2.714
Entry
ampl
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3
4
5
6
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0
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20
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60
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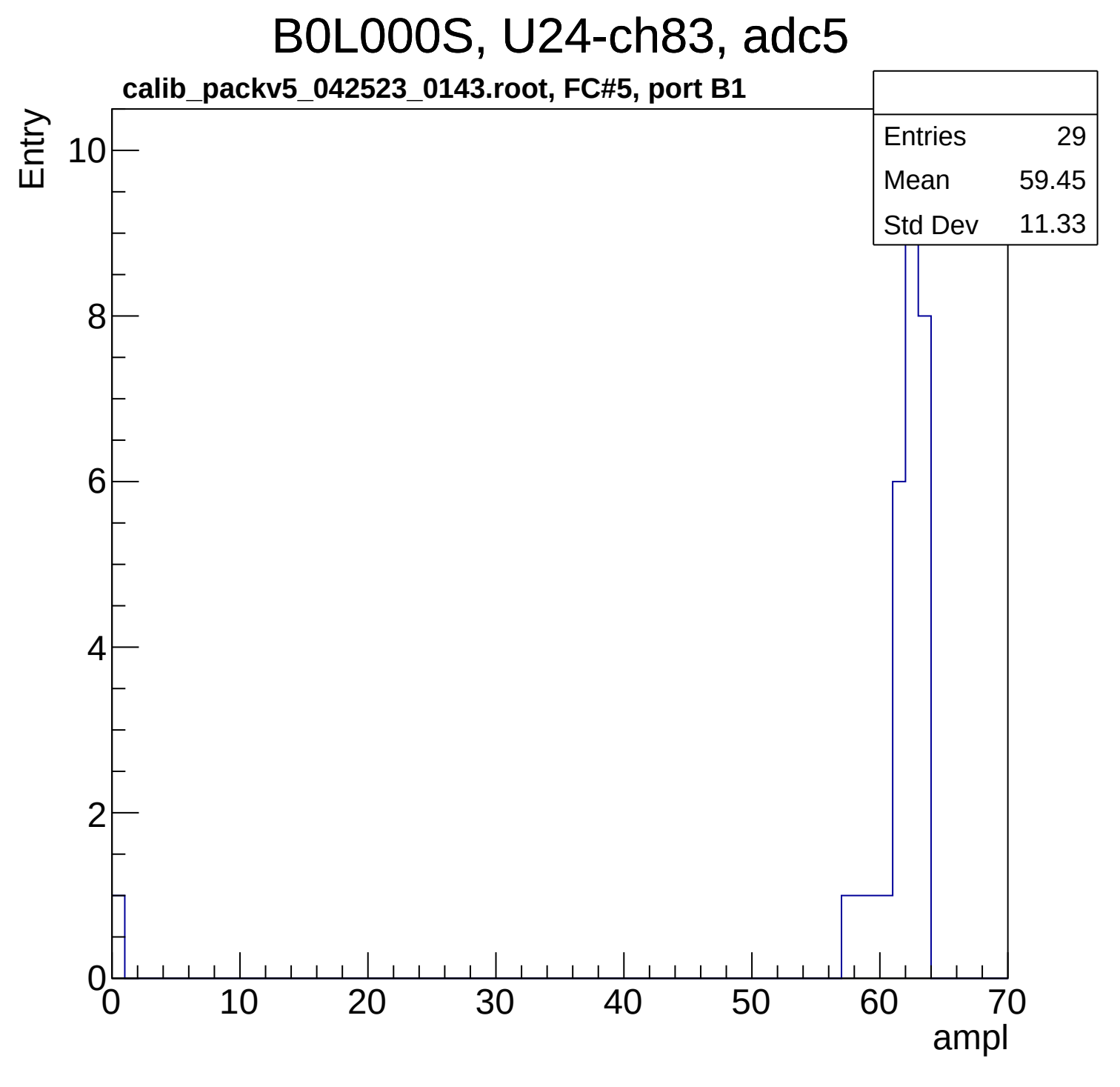
B0L000S, U24-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 59.45
Std Dev 11.33
Entry
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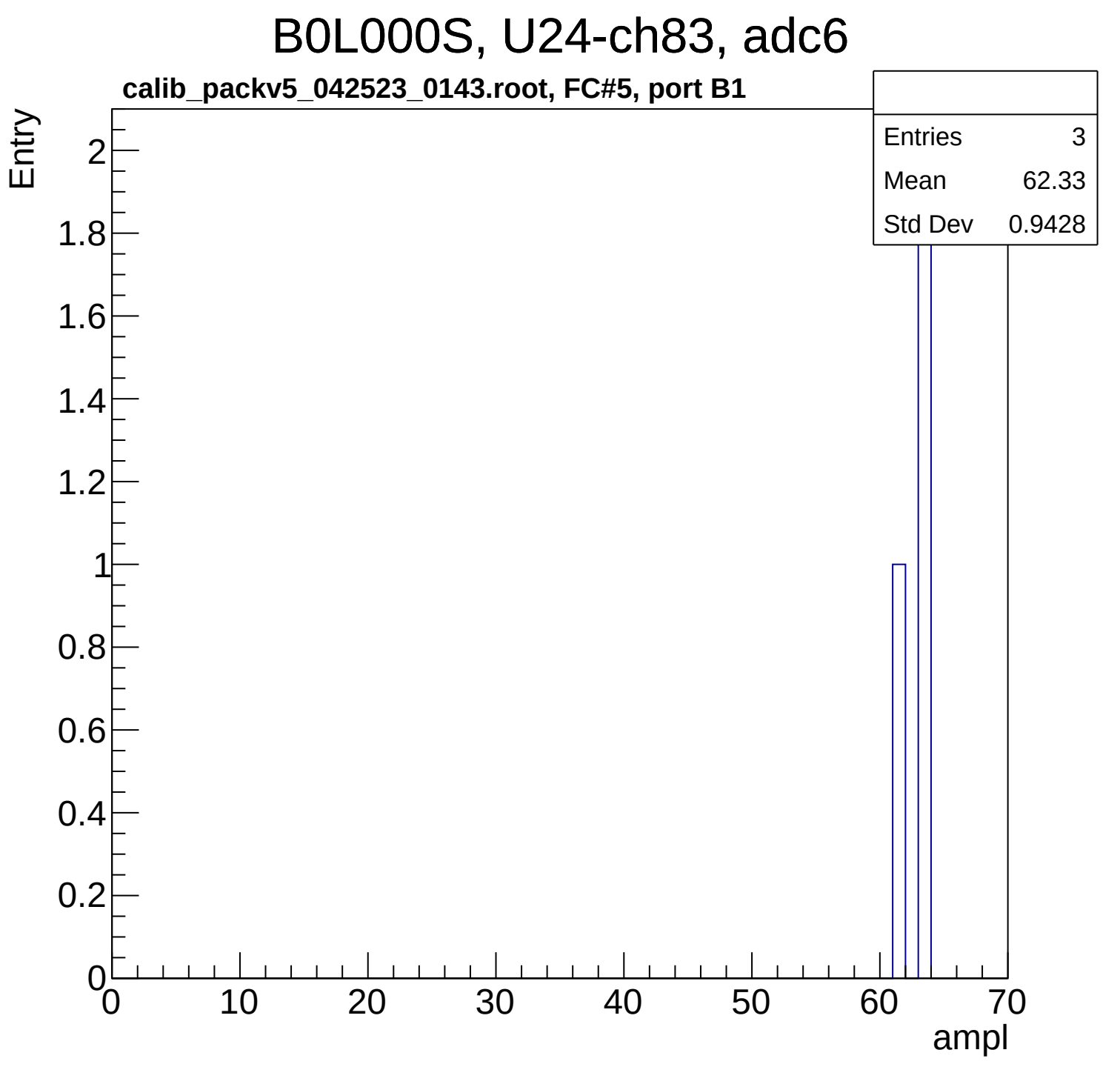
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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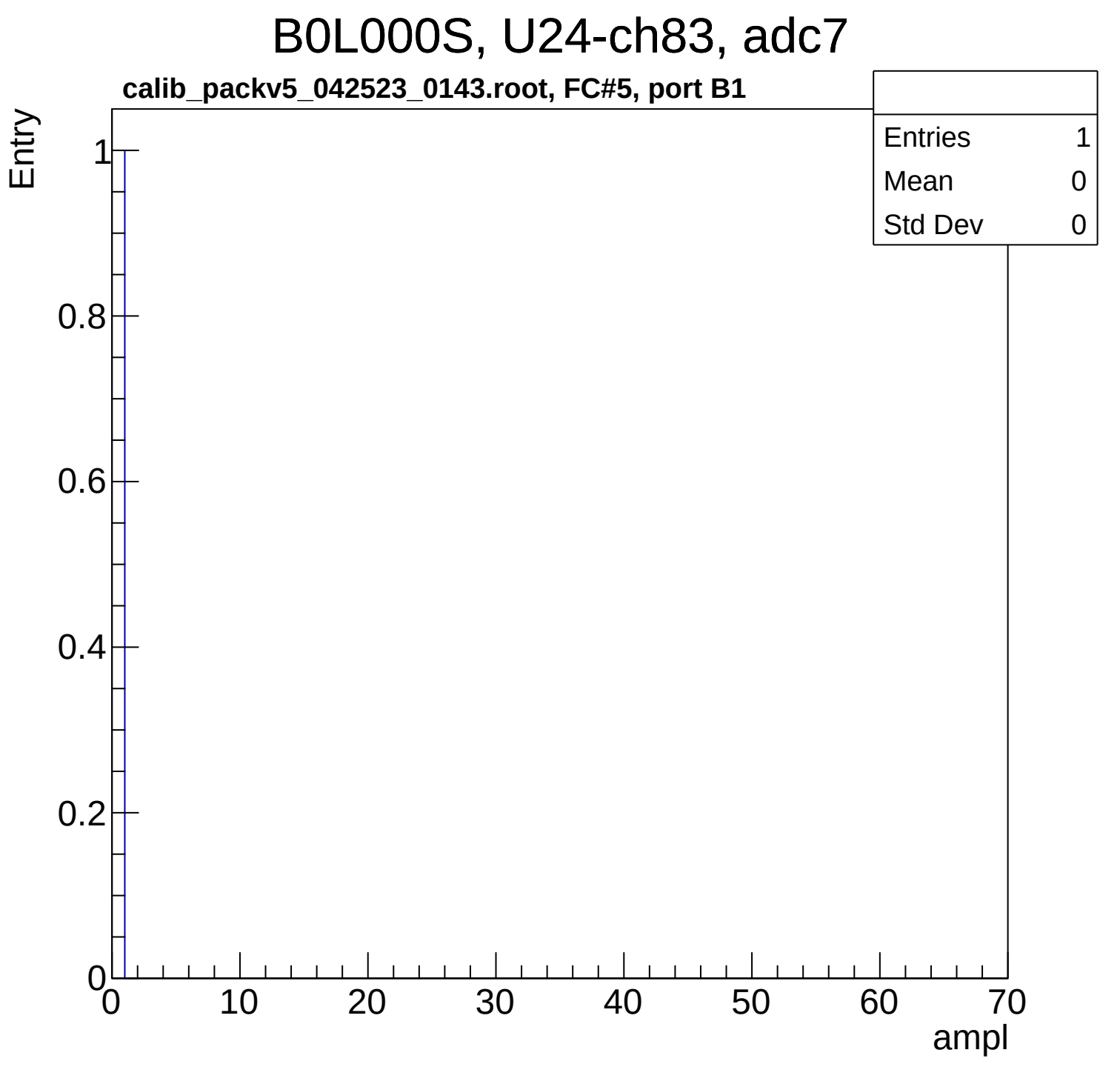
B0L000S, U24-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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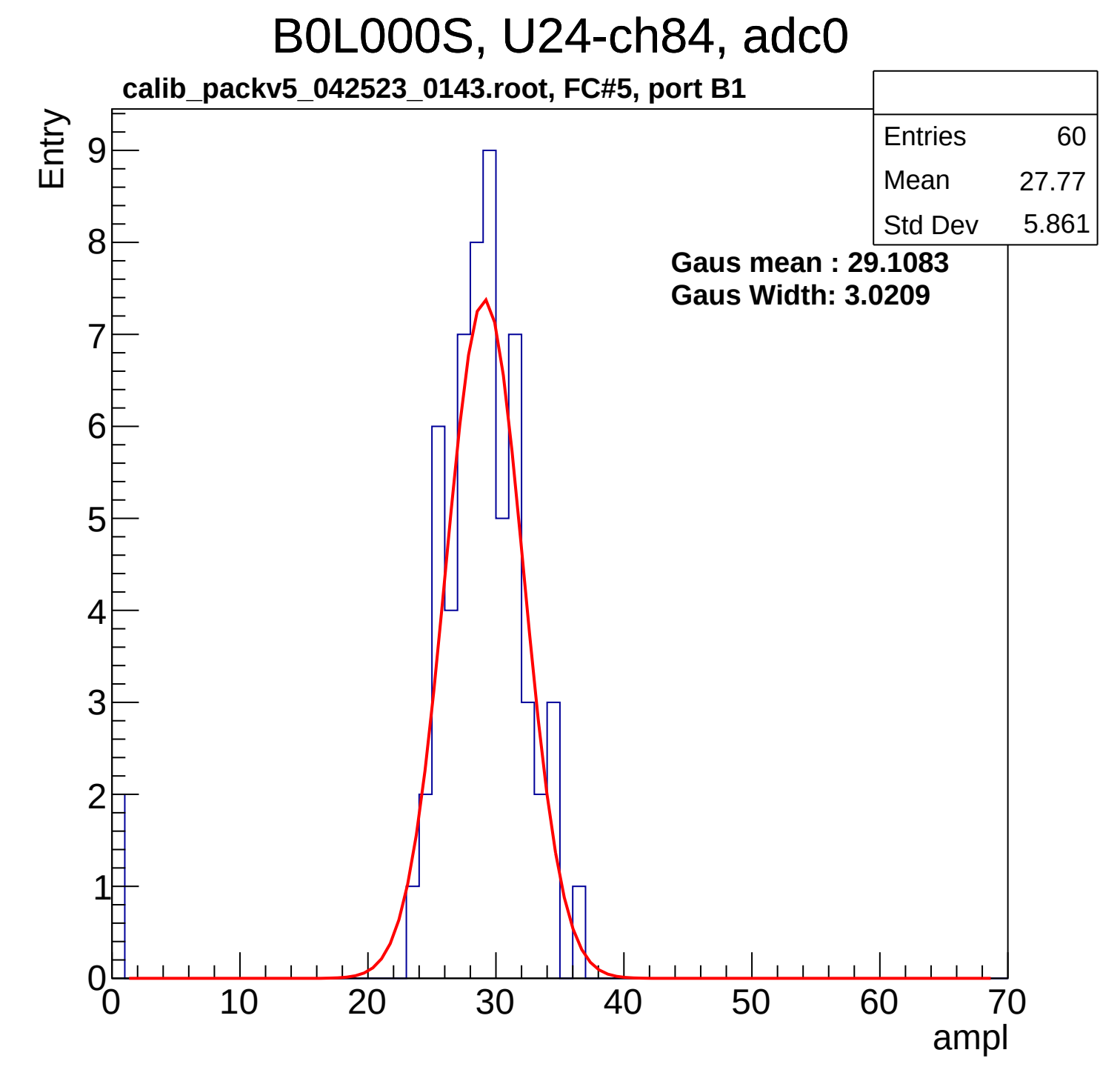
50
60
70
ampl


B0L000S, U24-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 27.77
Std Dev 5.861
Gaus mean : 29.1083
Gaus Width: 3.0209
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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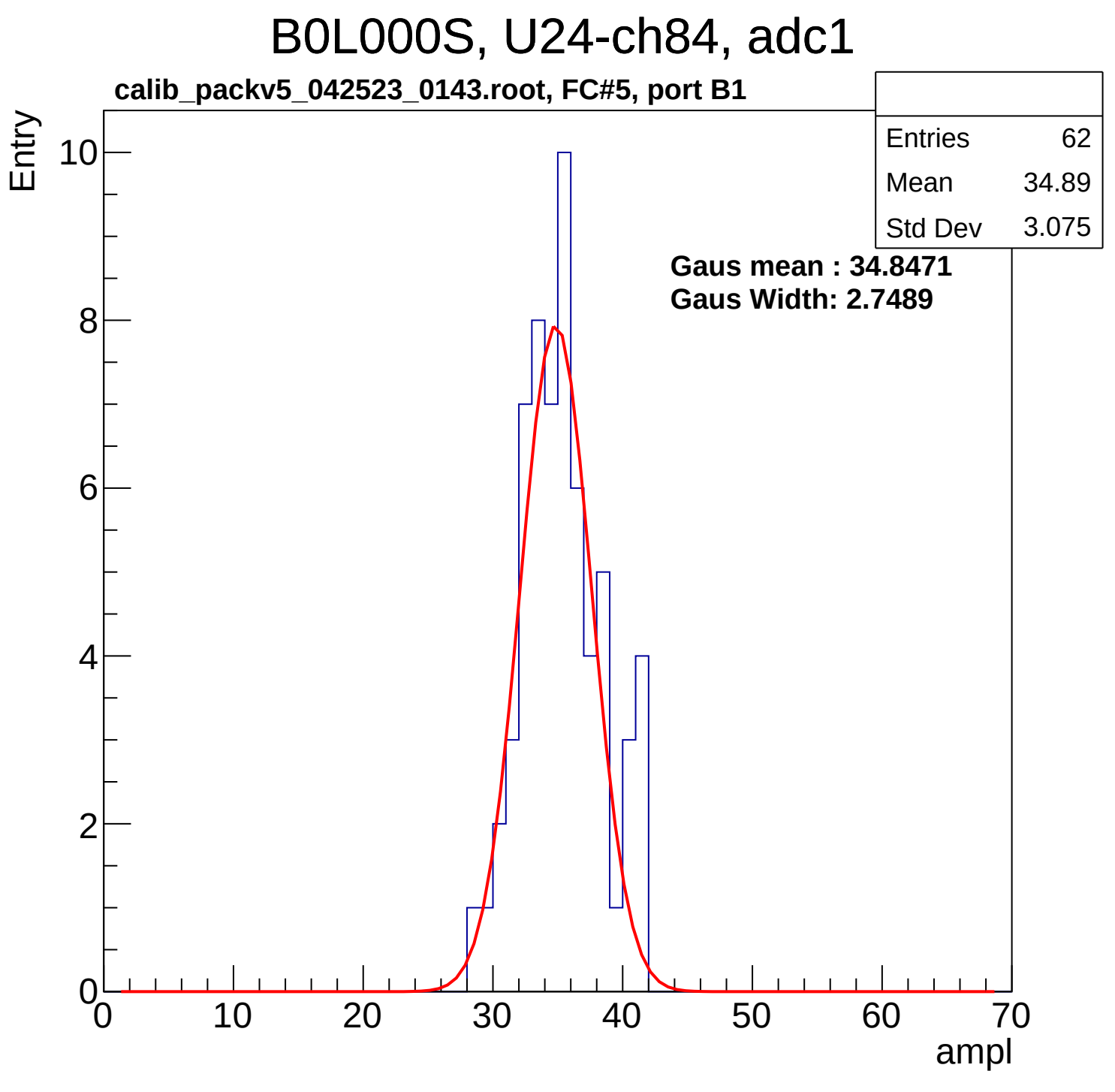
B0L000S, U24-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 34.89
Std Dev 3.075
Gaus mean : 34.8471
Gaus Width: 2.7489
Entry
ampl
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4
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8
10
0
10
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40
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60
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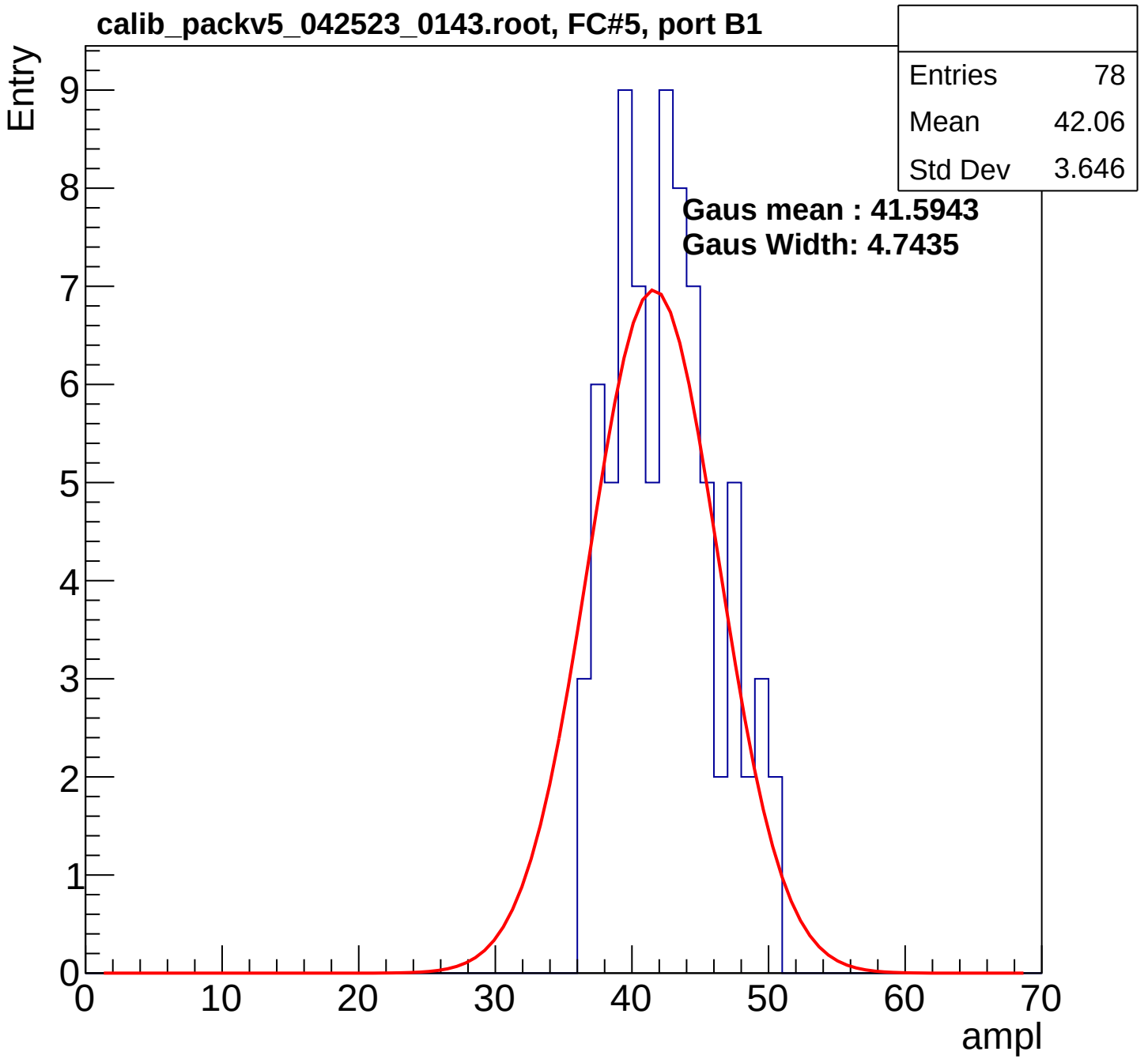
B0L000S, U24-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42.06
Std Dev 3.646
Gaus mean : 41.5943
Gaus Width: 4.7435
Entry
ampl
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2
3
4
5
6
7
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0
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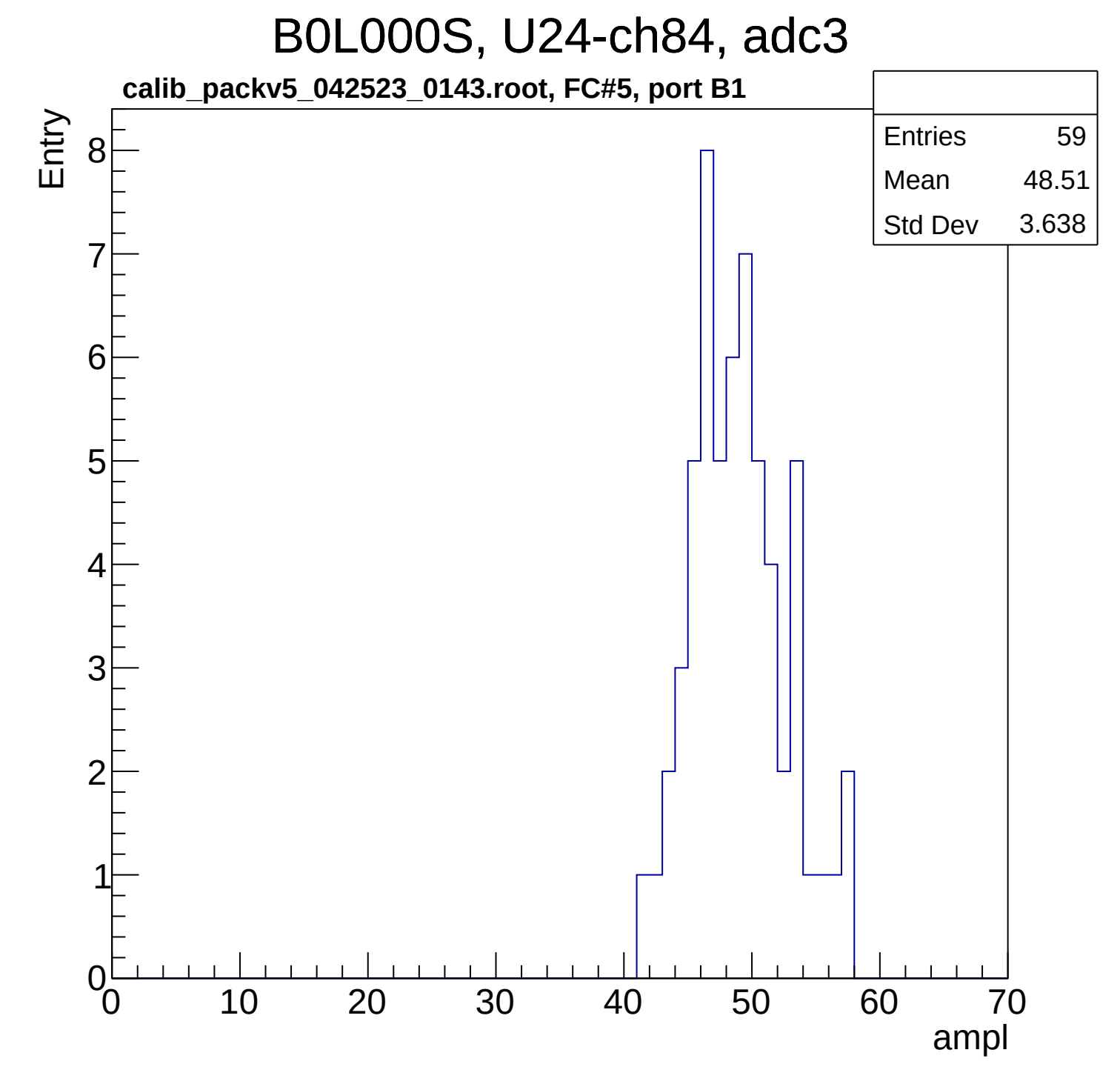
B0L000S, U24-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.51
Std Dev 3.638
Entry
ampl
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4
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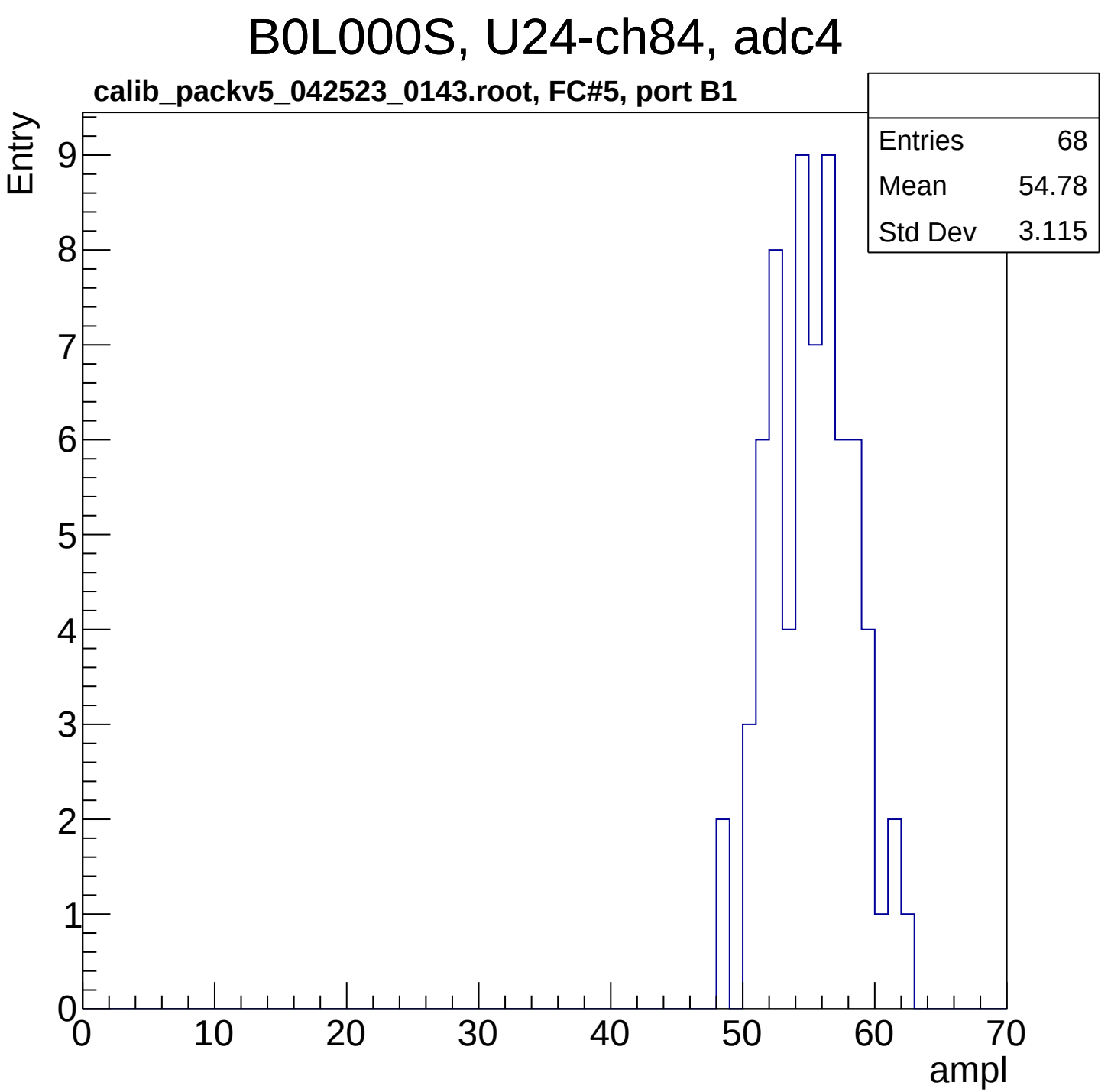
B0L000S, U24-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.78
Std Dev 3.115
Entry
ampl
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3
4
5
6
7
8
9
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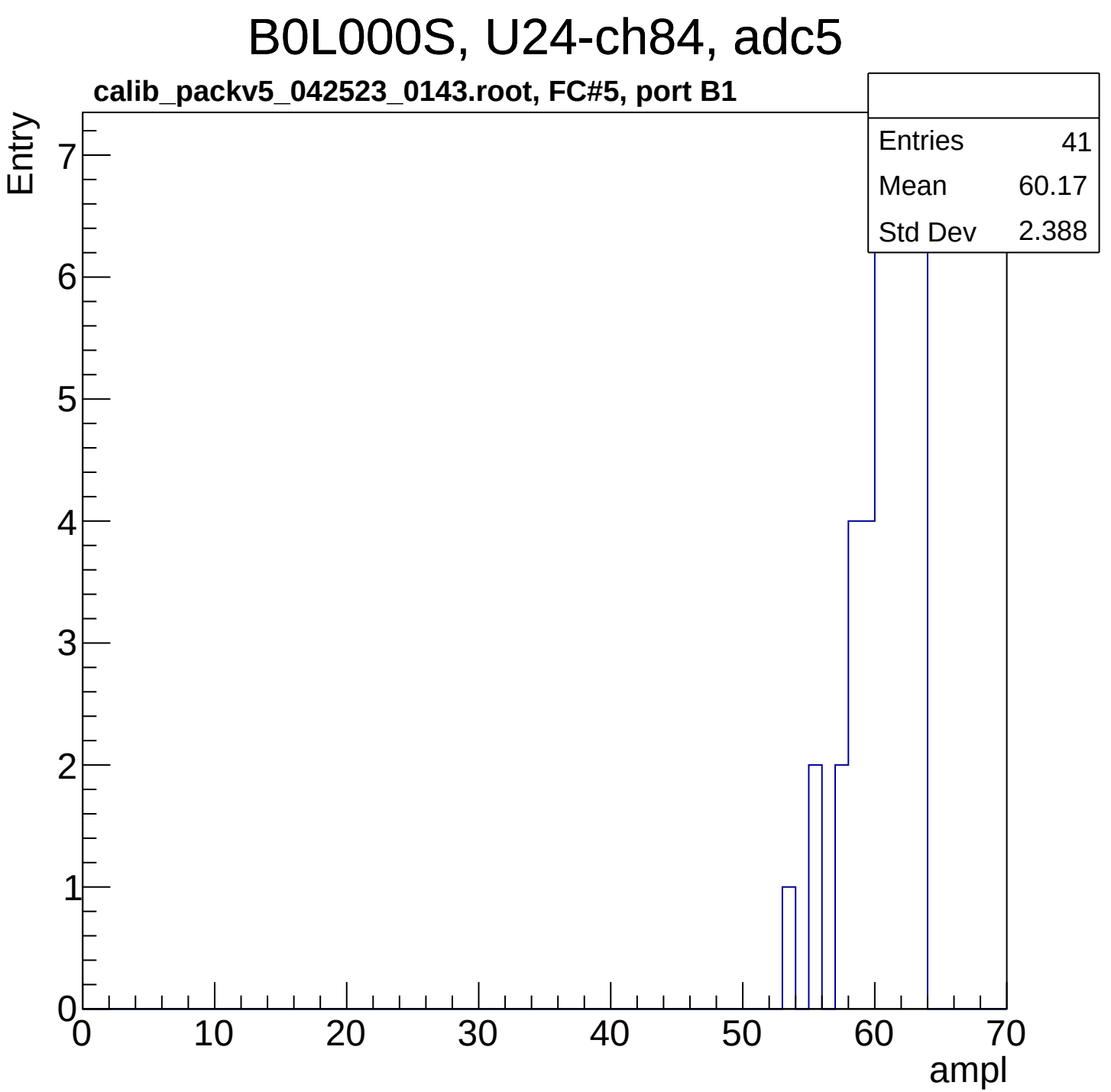
B0L000S, U24-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.17
Std Dev 2.388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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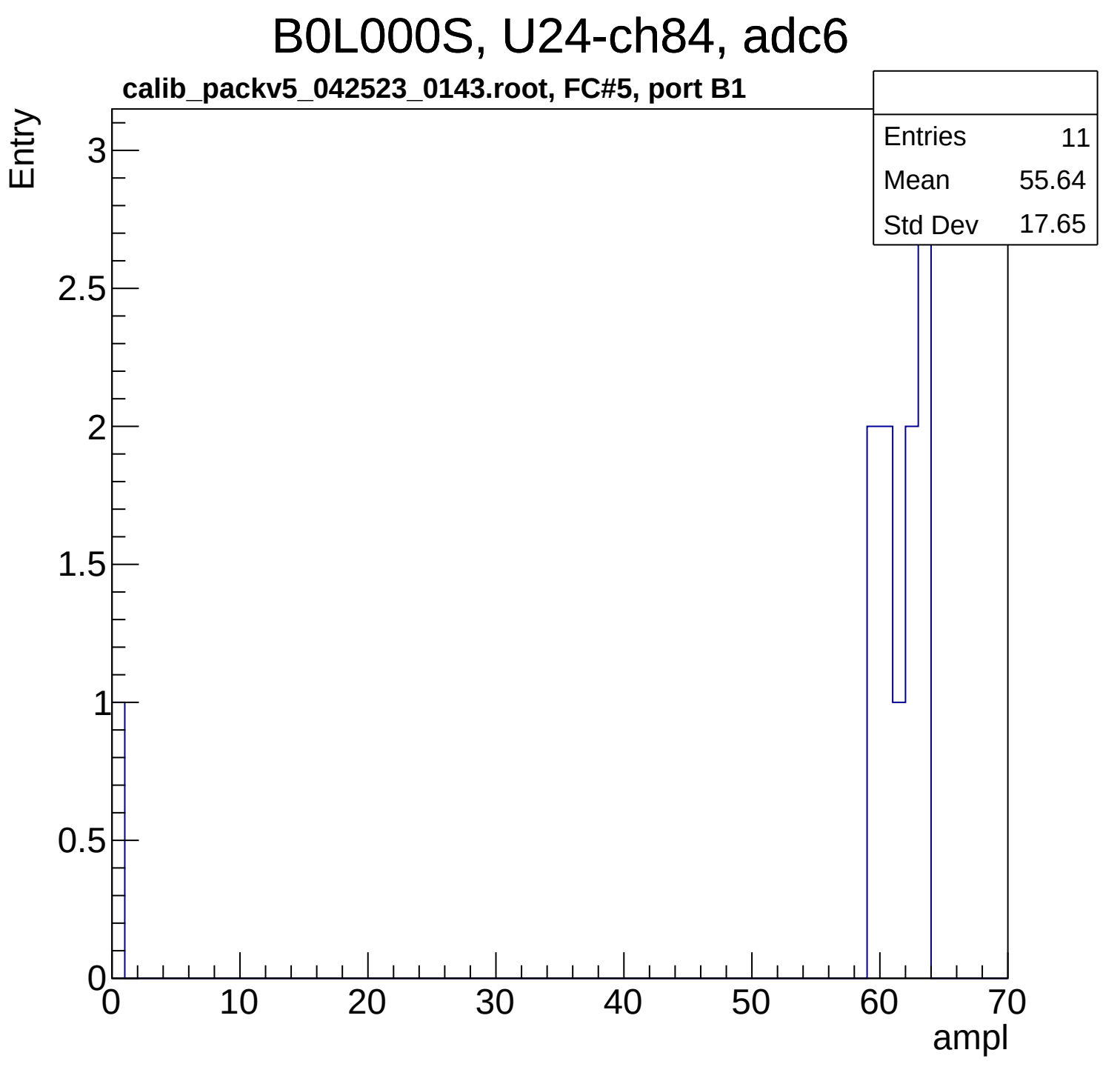
B0L000S, U24-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 55.64
Std Dev 17.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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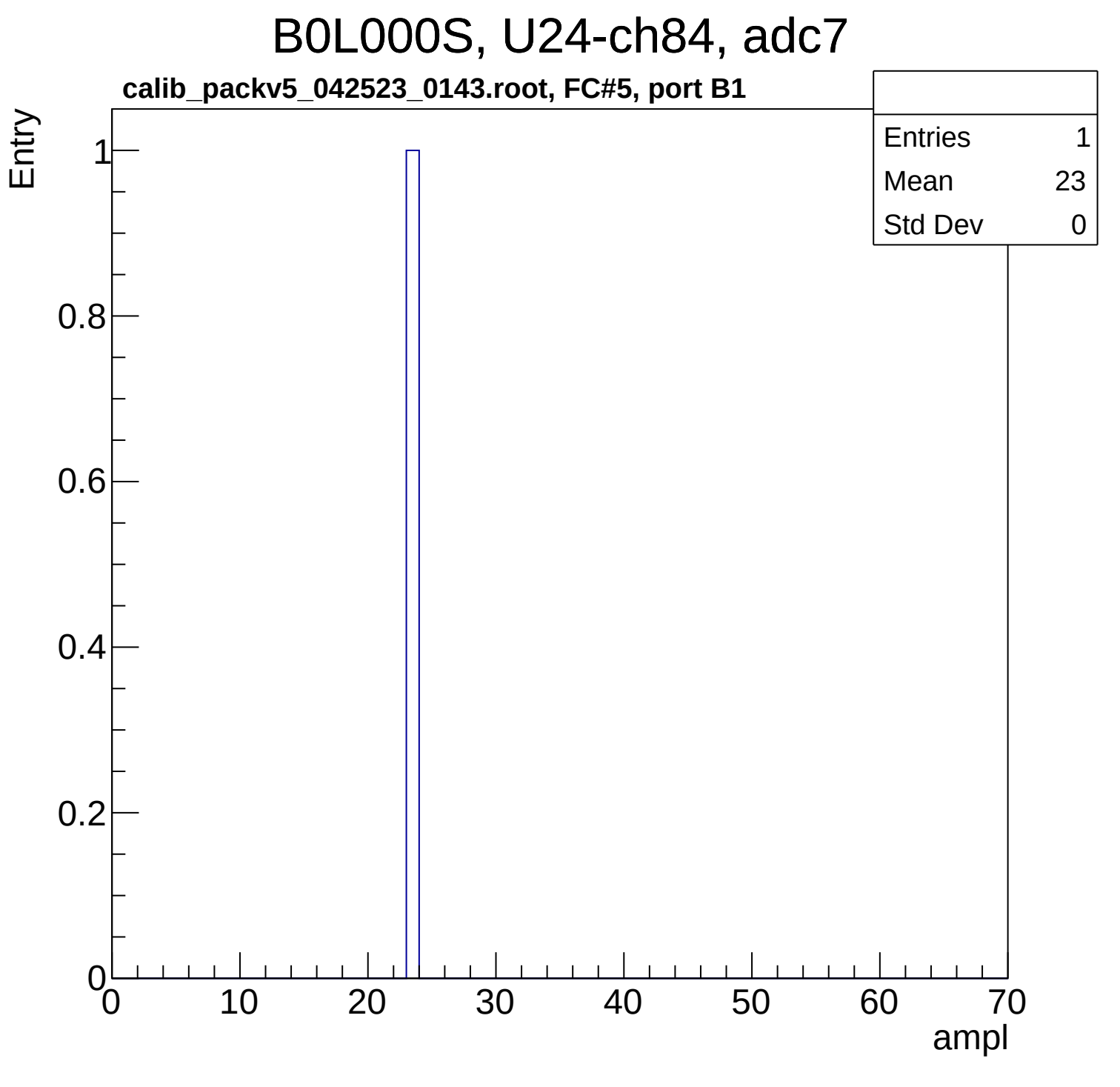
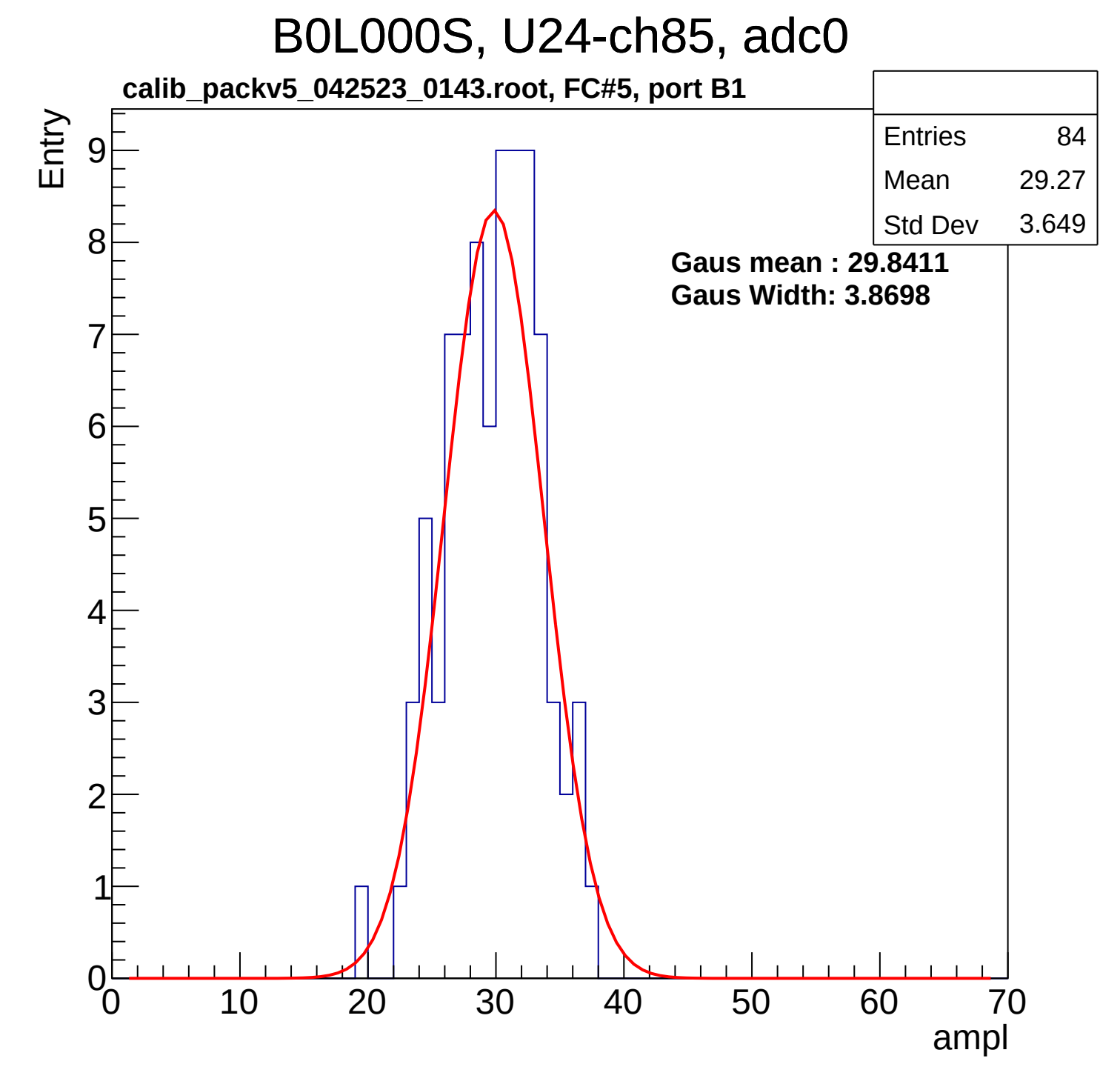

B0L000S, U24-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 29.27
Std Dev 3.649
Gaus mean : 29.8411
Gaus Width: 3.8698
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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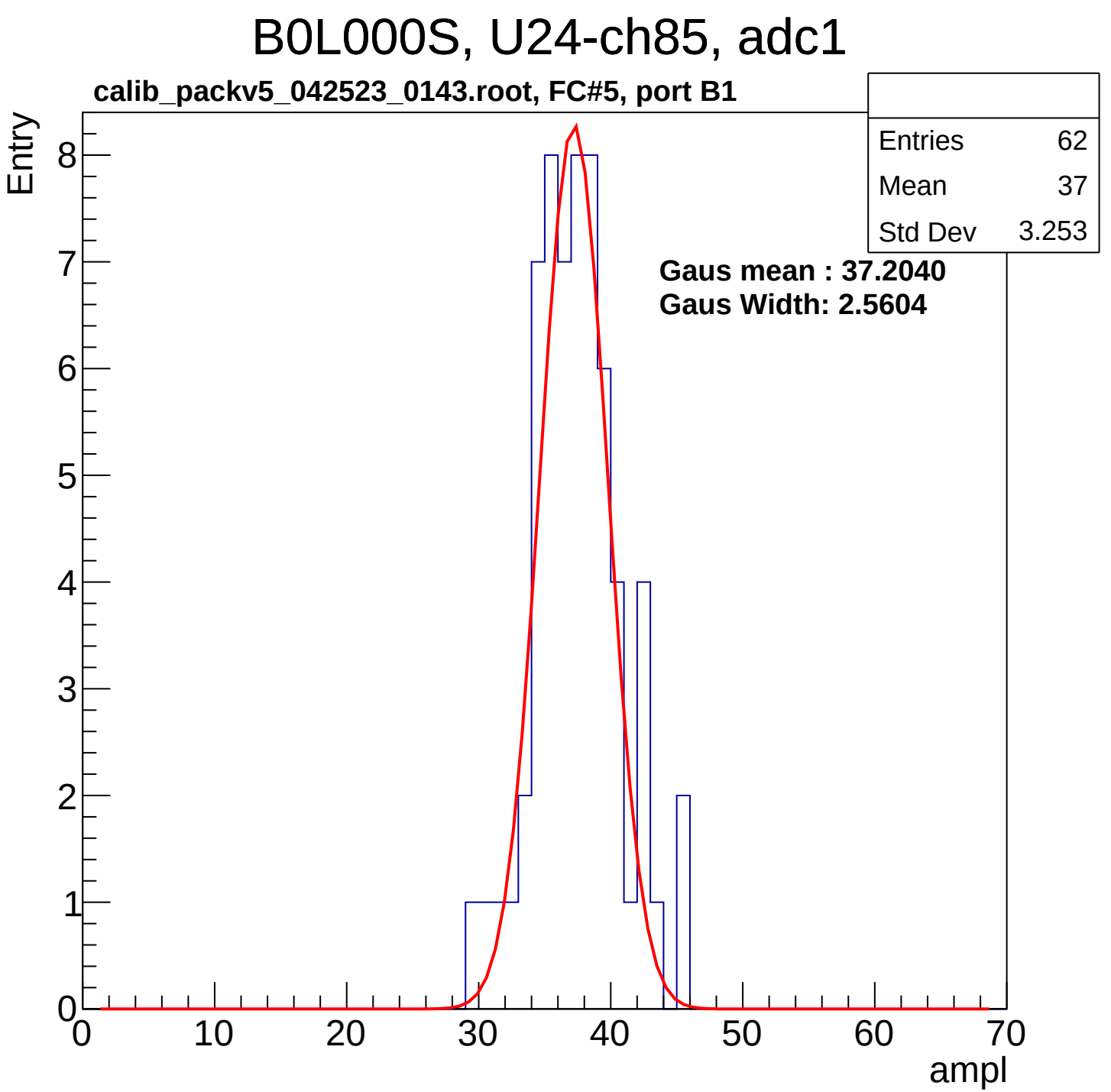
B0L000S, U24-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37
Std Dev 3.253
Gaus mean : 37.2040
Gaus Width: 2.5604
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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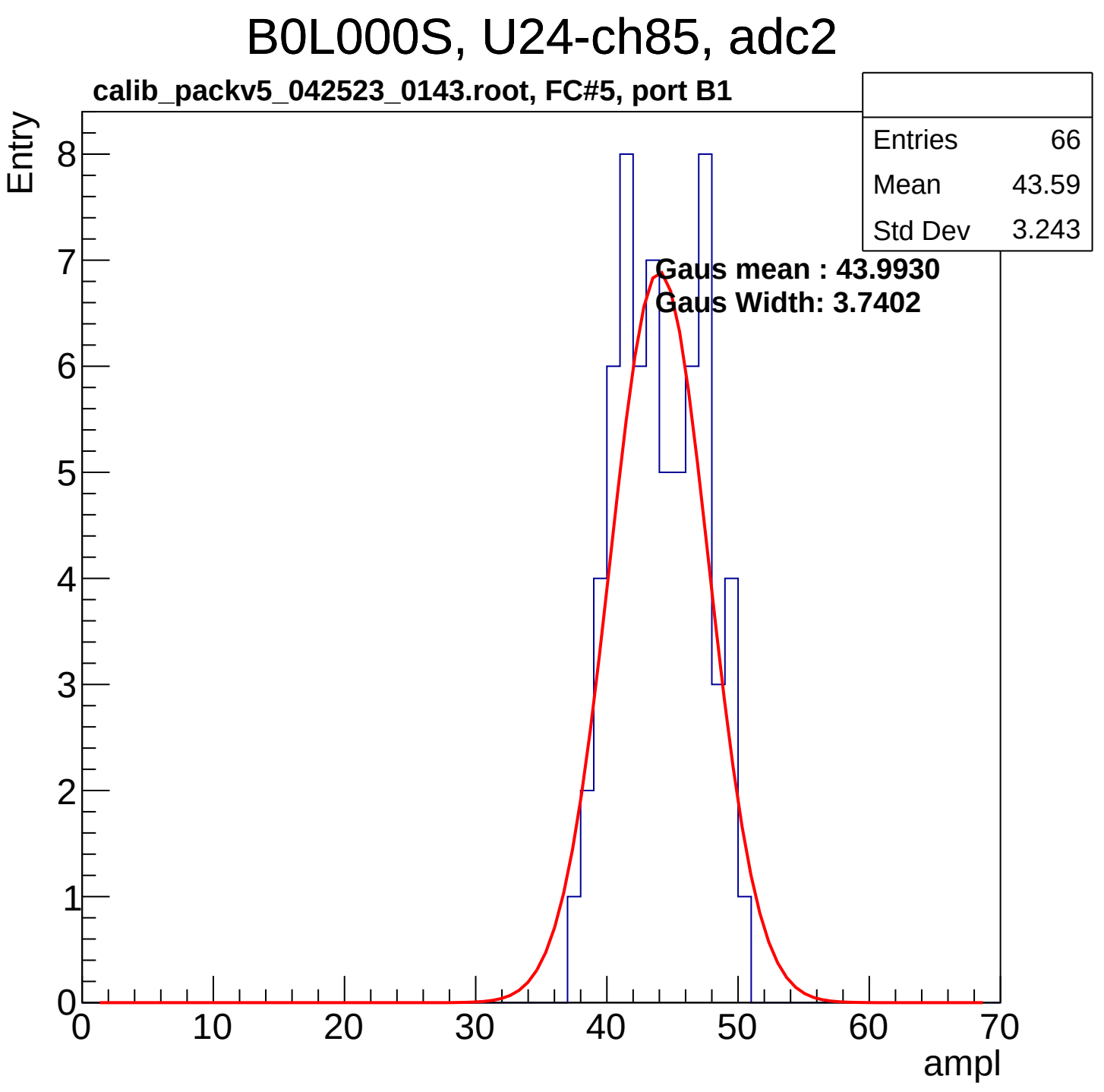
B0L000S, U24-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.59
Std Dev 3.243
Gaus mean : 43.9930
Gaus Width: 3.7402
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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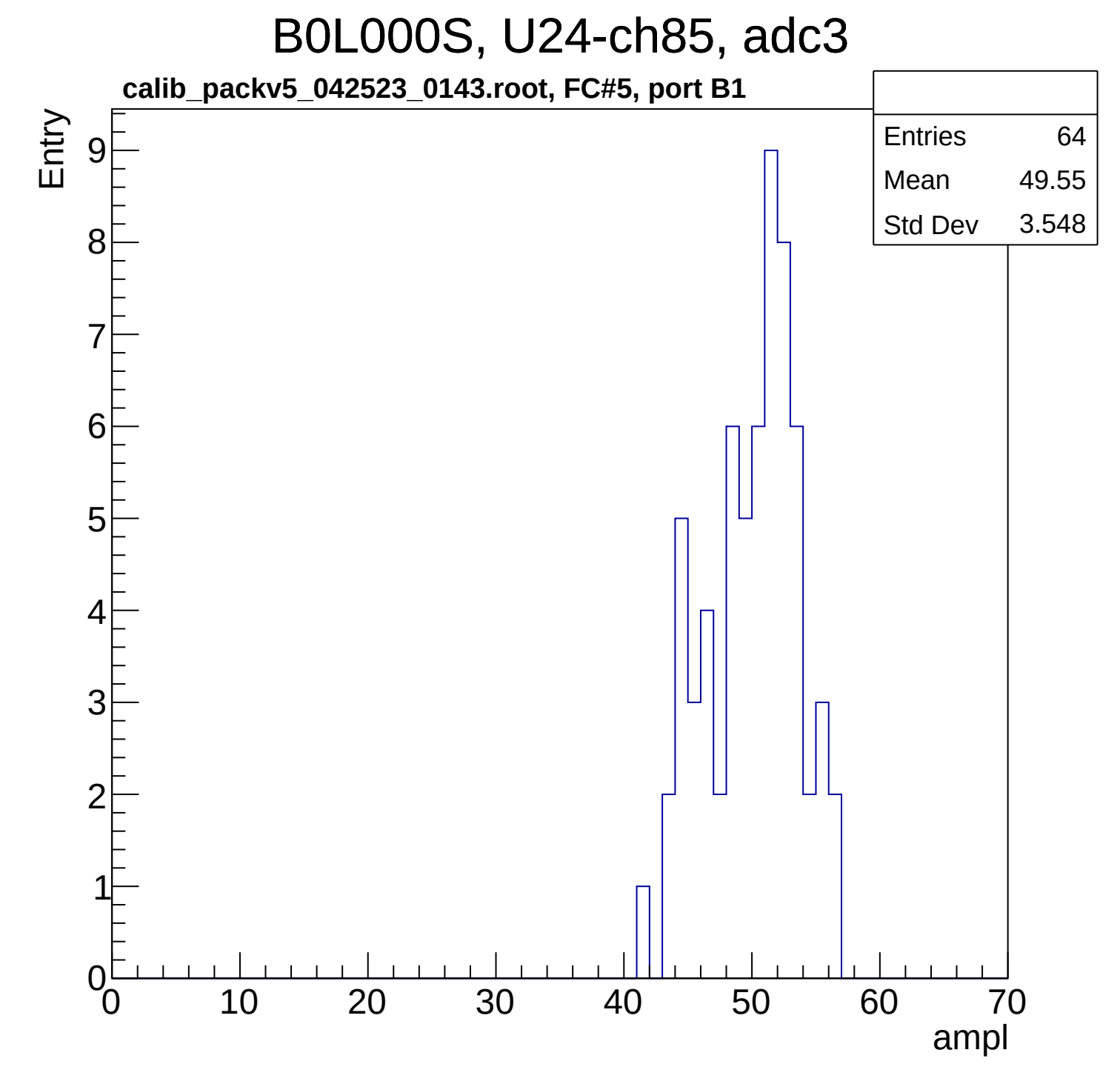
B0L000S, U24-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.55
Std Dev 3.548
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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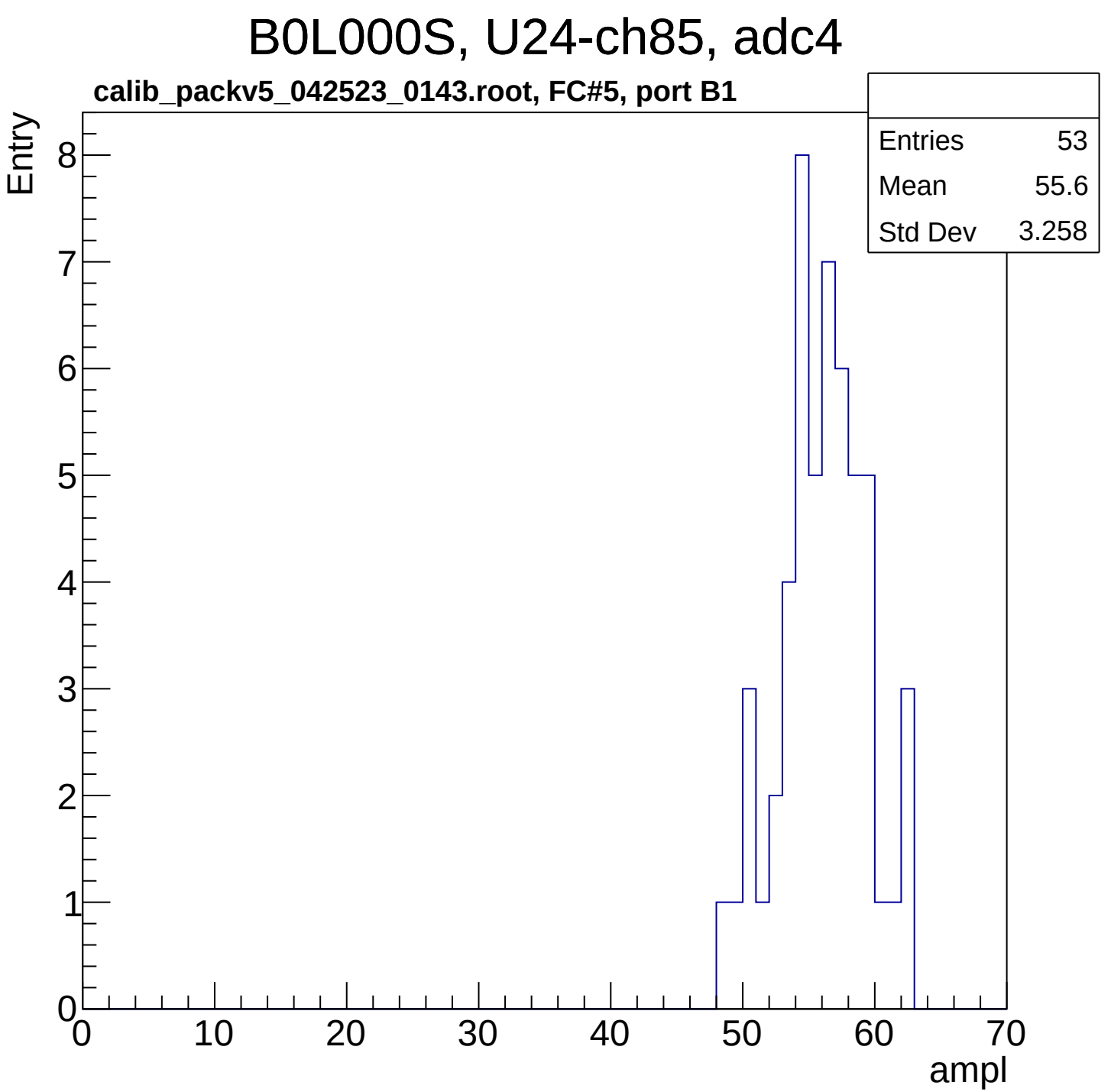
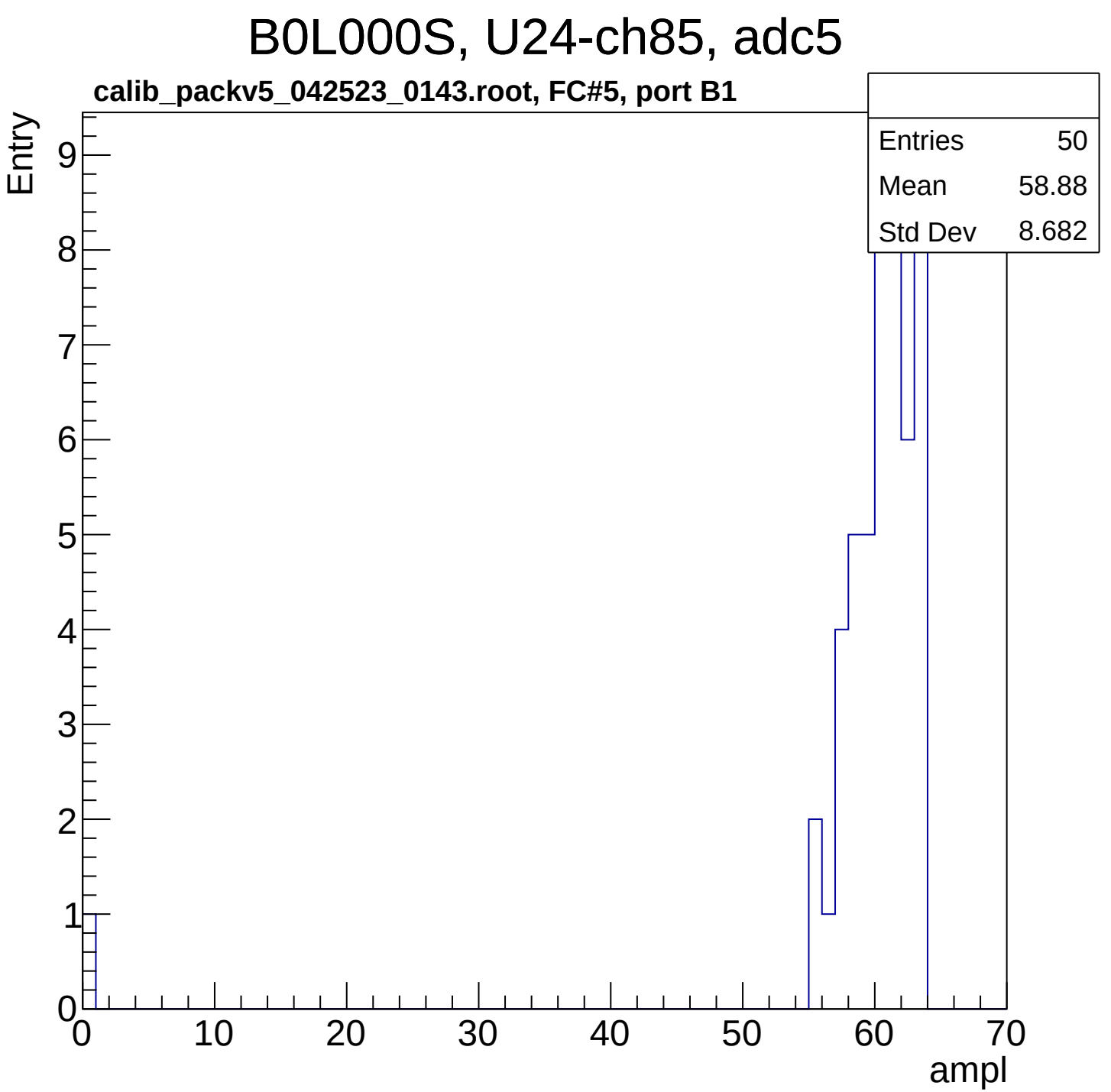

B0L000S, U24-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.6
Std Dev 3.258
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.88
Std Dev 8.682
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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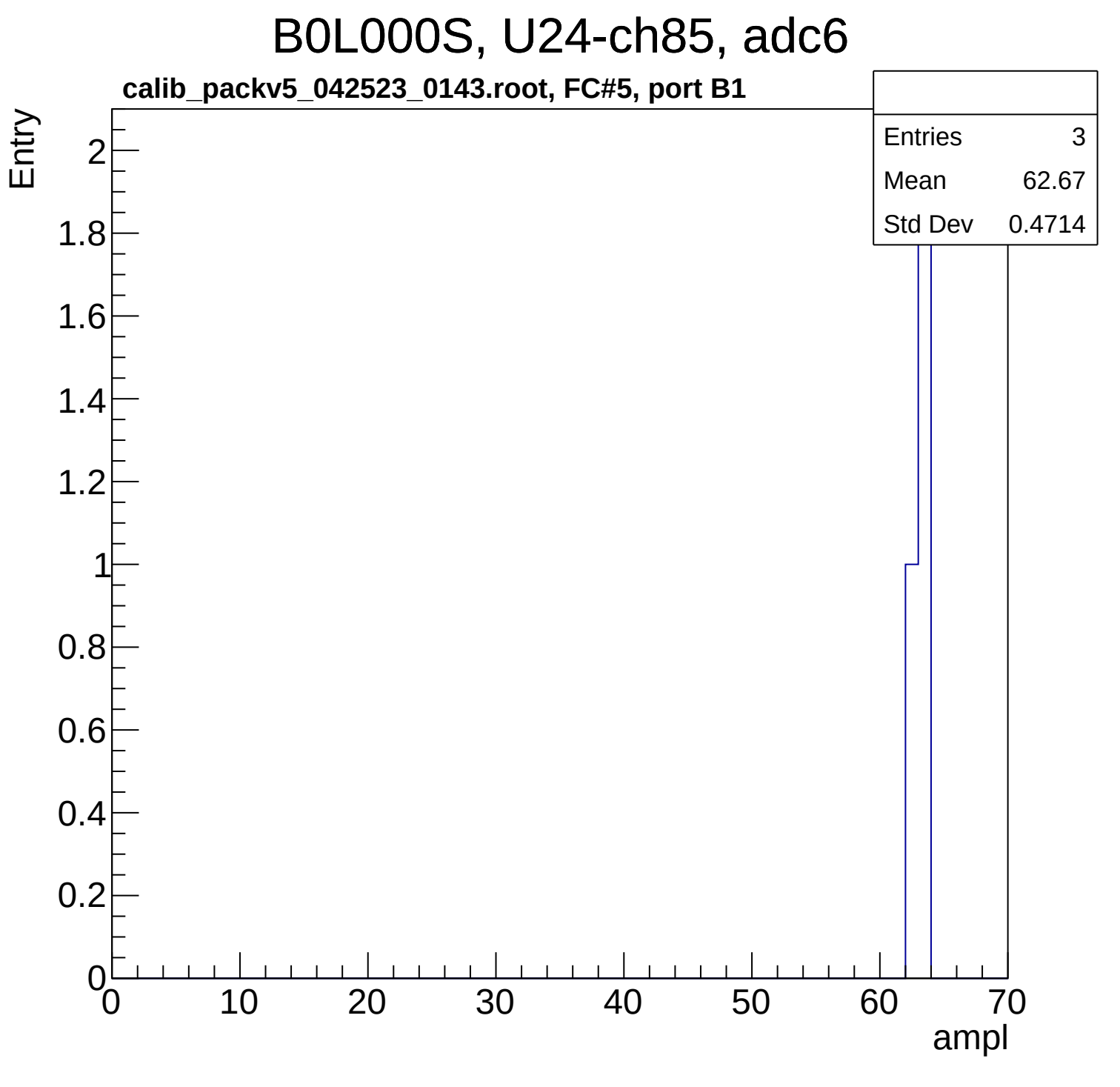
B0L000S, U24-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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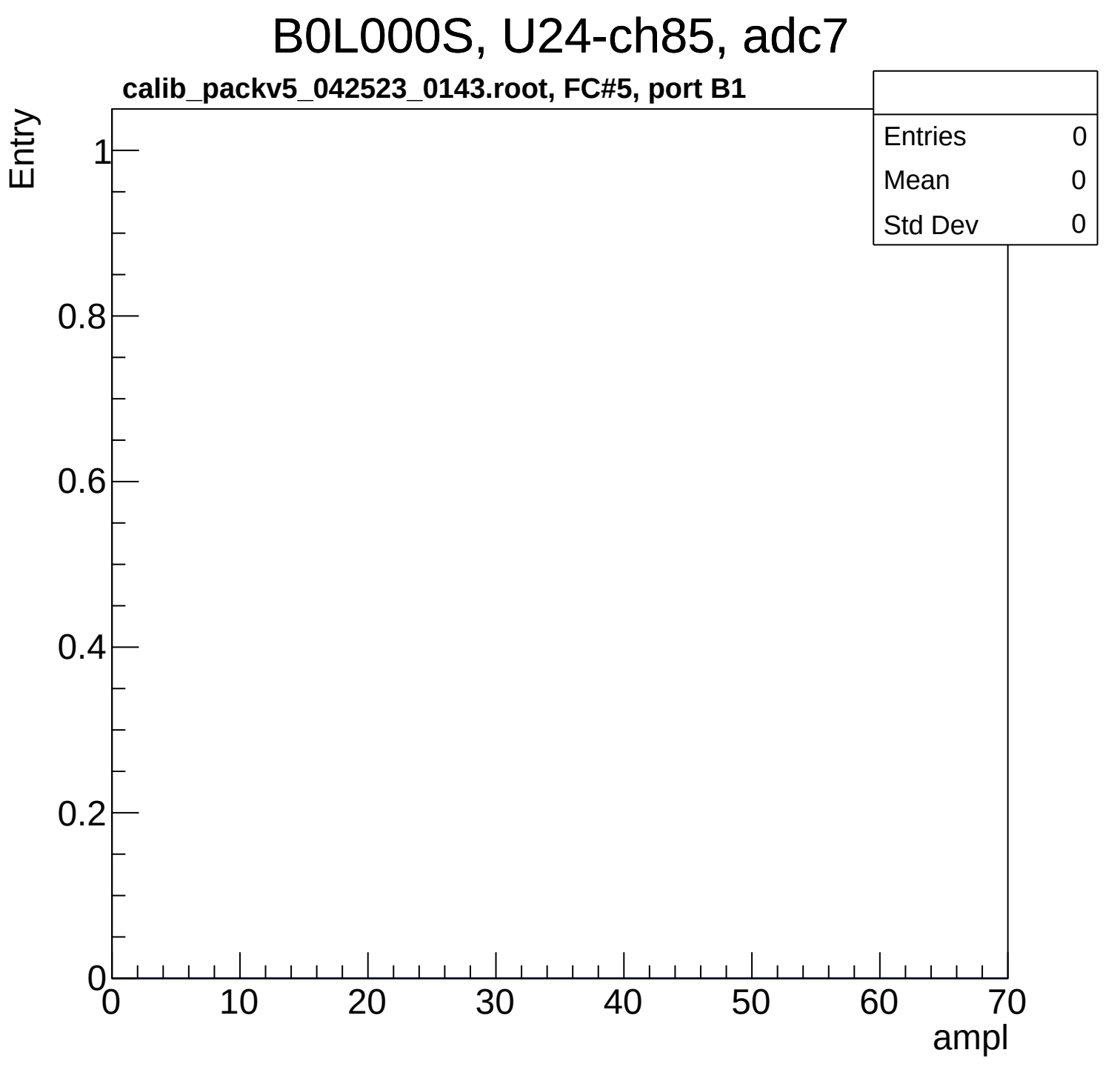
B0L000S, U24-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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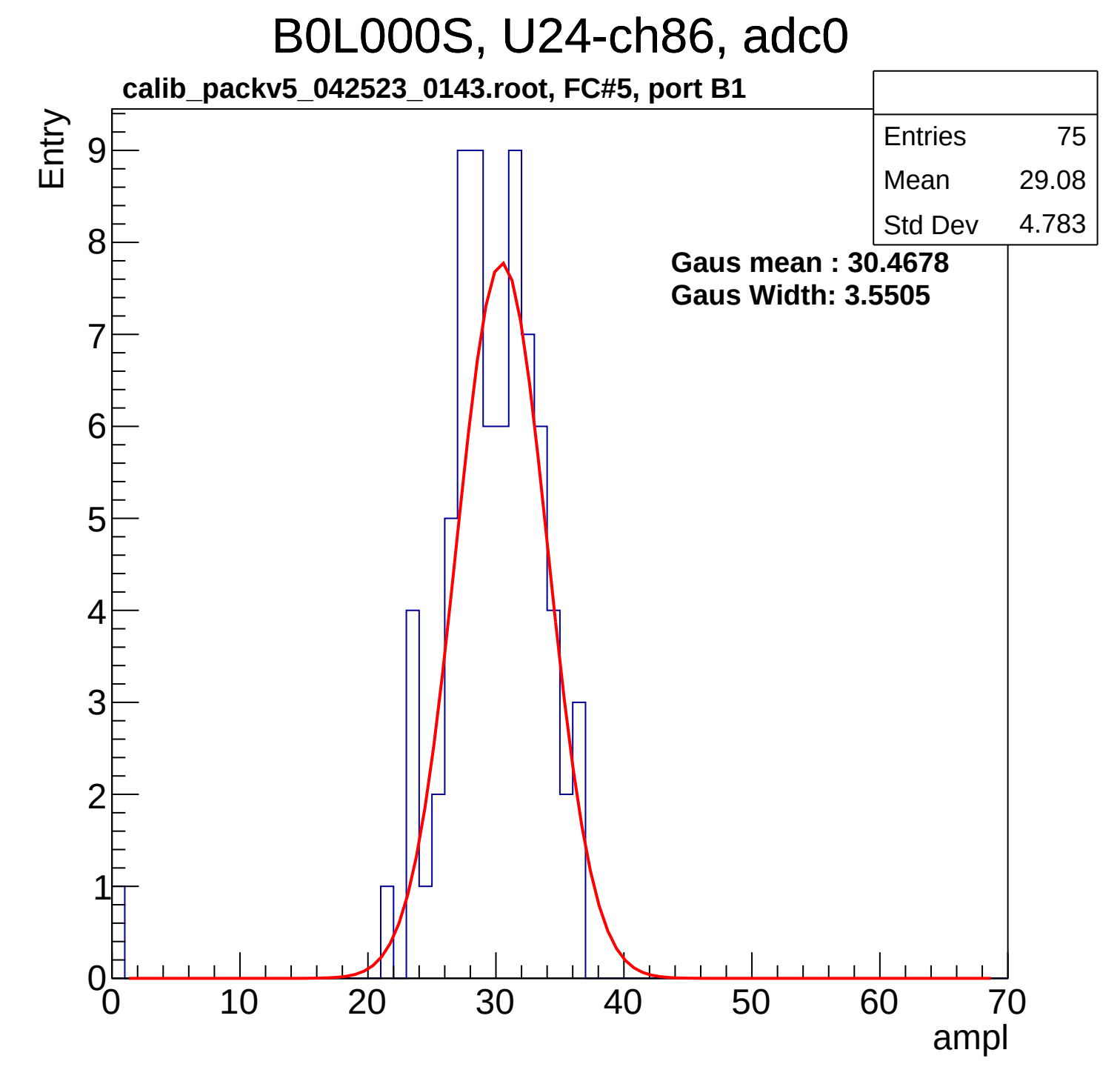
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 29.08
Std Dev 4.783
Gaus mean : 30.4678
Gaus Width: 3.5505
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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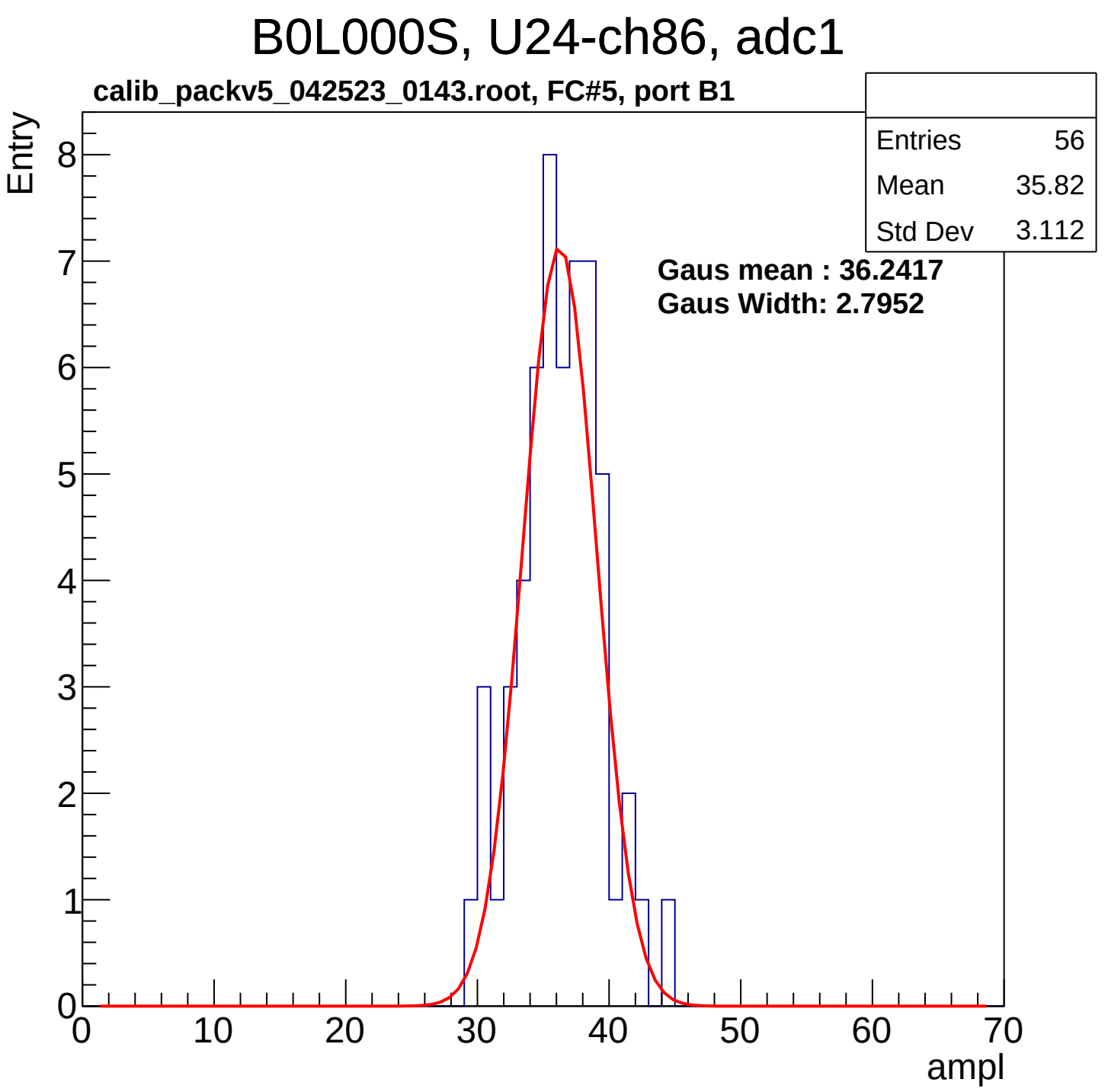
B0L000S, U24-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.82
Std Dev 3.112
Gaus mean : 36.2417
Gaus Width: 2.7952
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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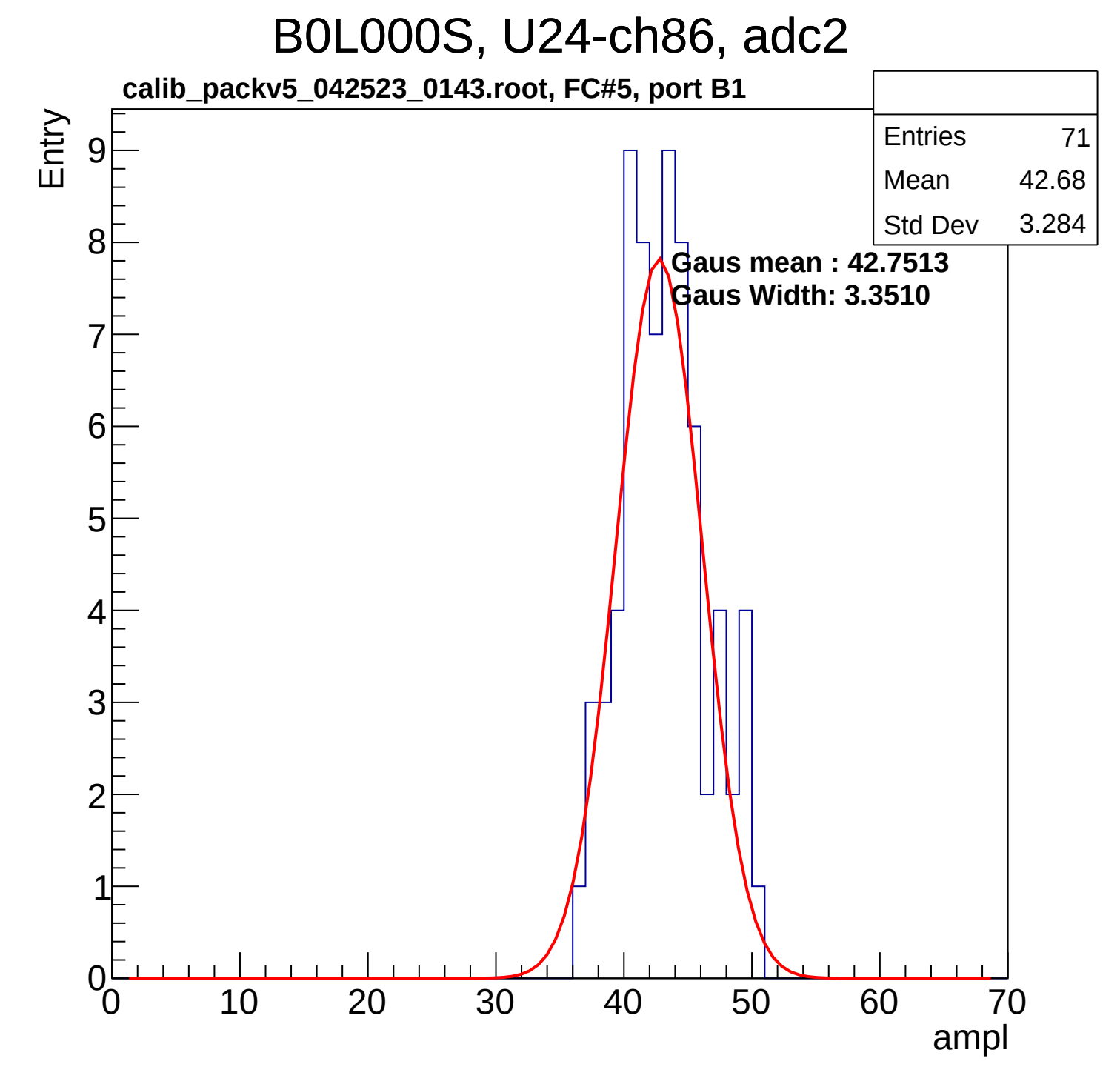
B0L000S, U24-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.68
Std Dev 3.284
Gaus mean : 42.7513
Gaus Width: 3.3510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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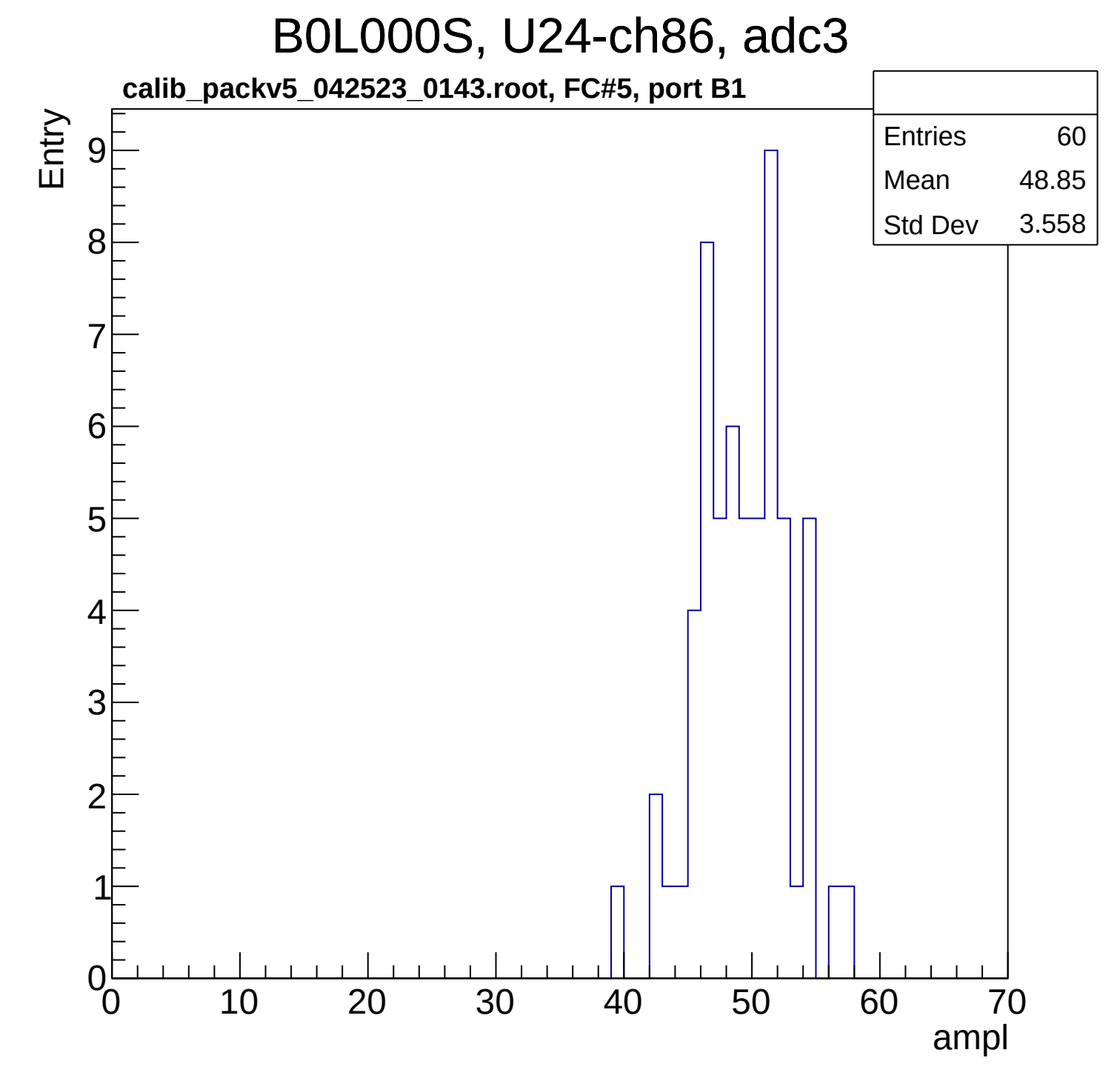
B0L000S, U24-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.85
Std Dev 3.558
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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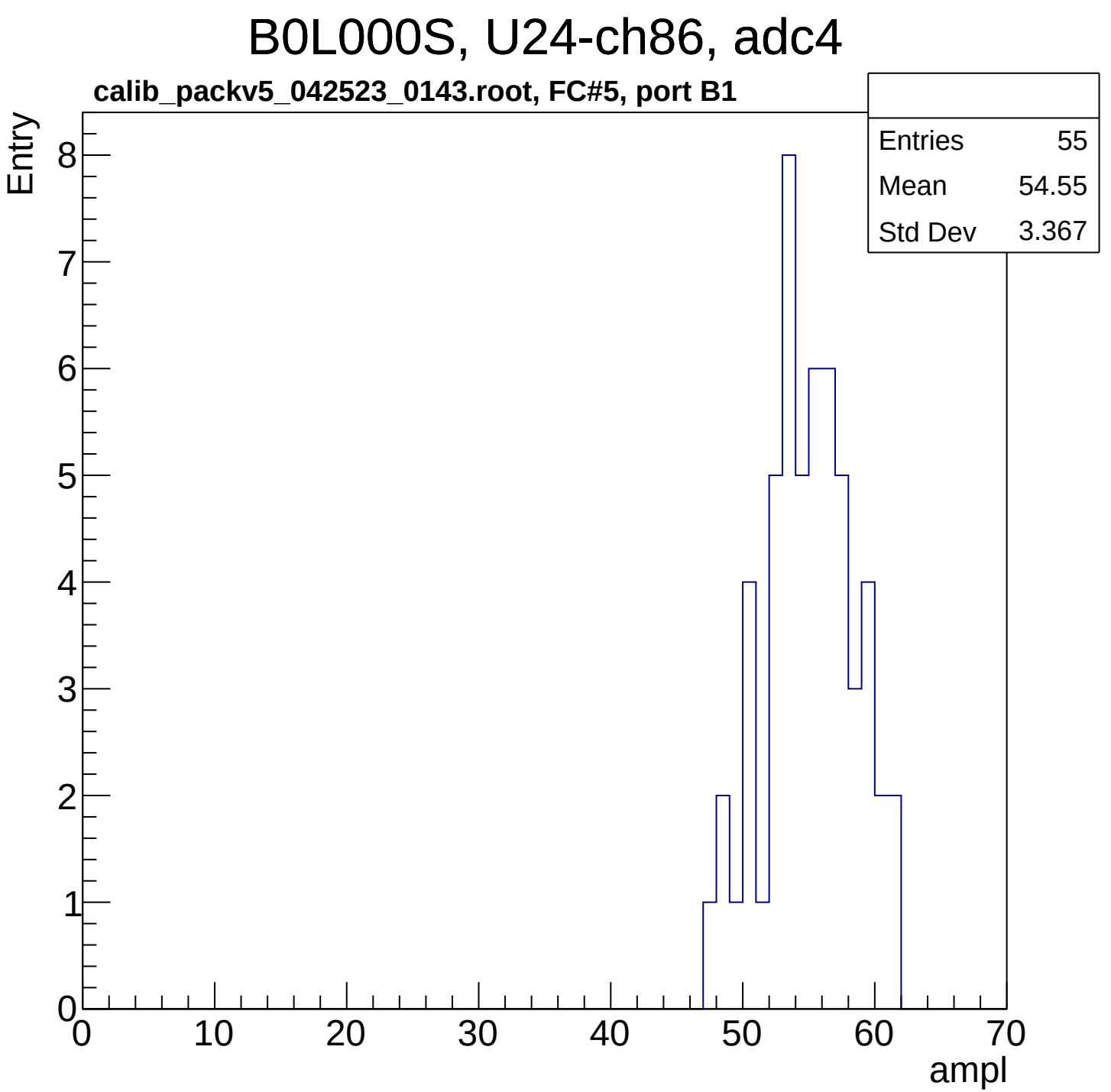
B0L000S, U24-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 54.55
Std Dev 3.367
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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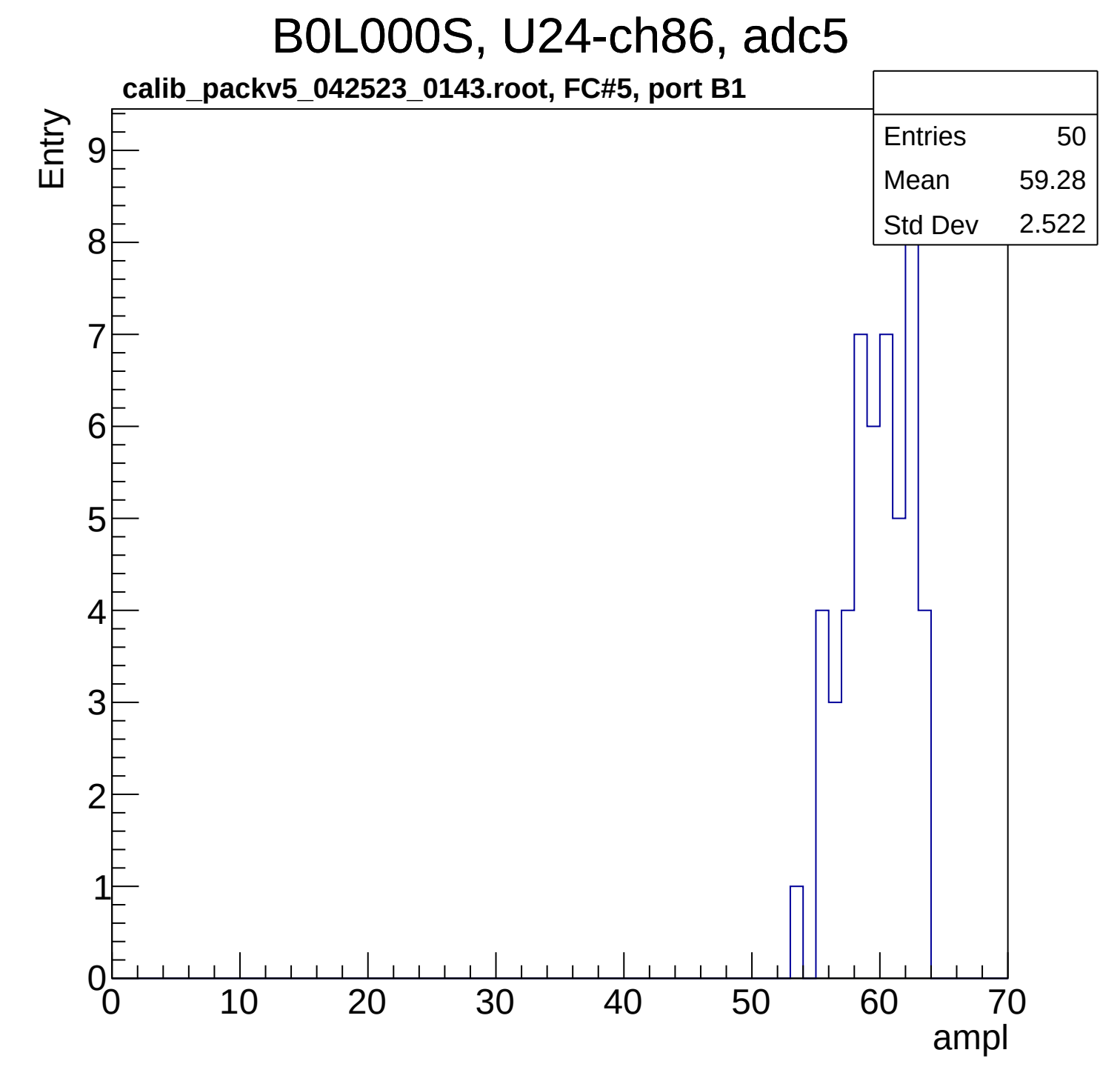
B0L000S, U24-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.28
Std Dev 2.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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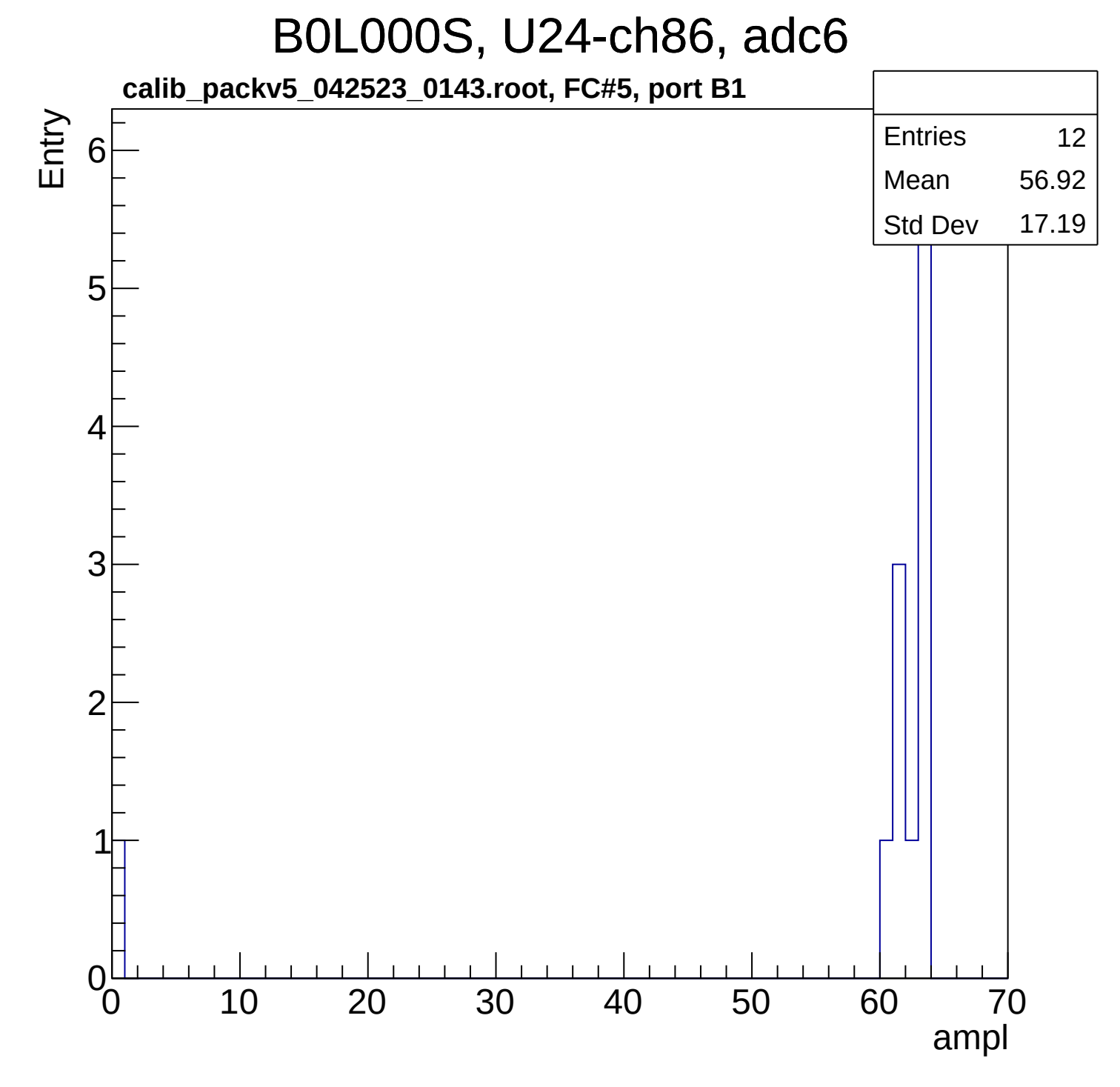
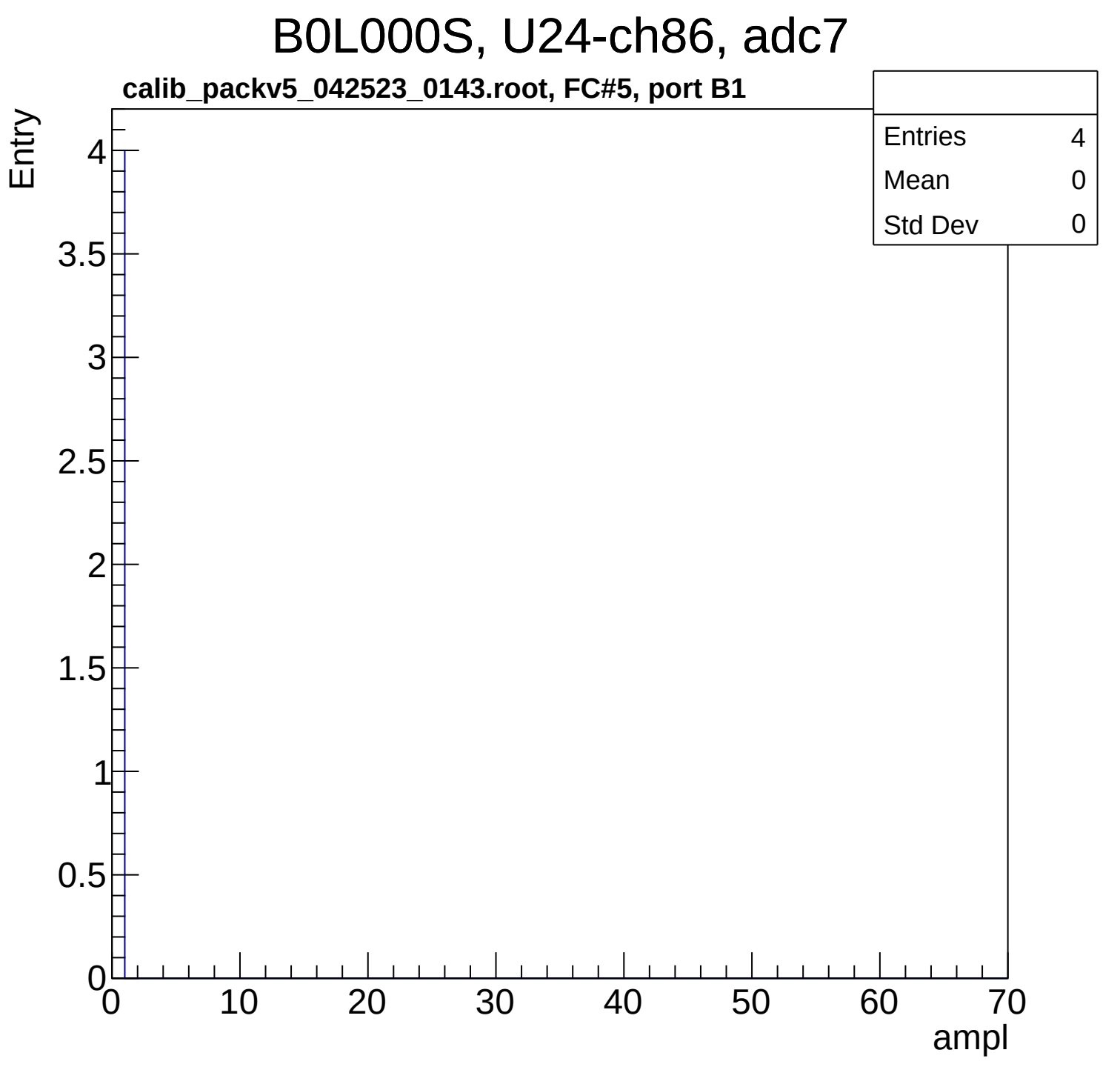
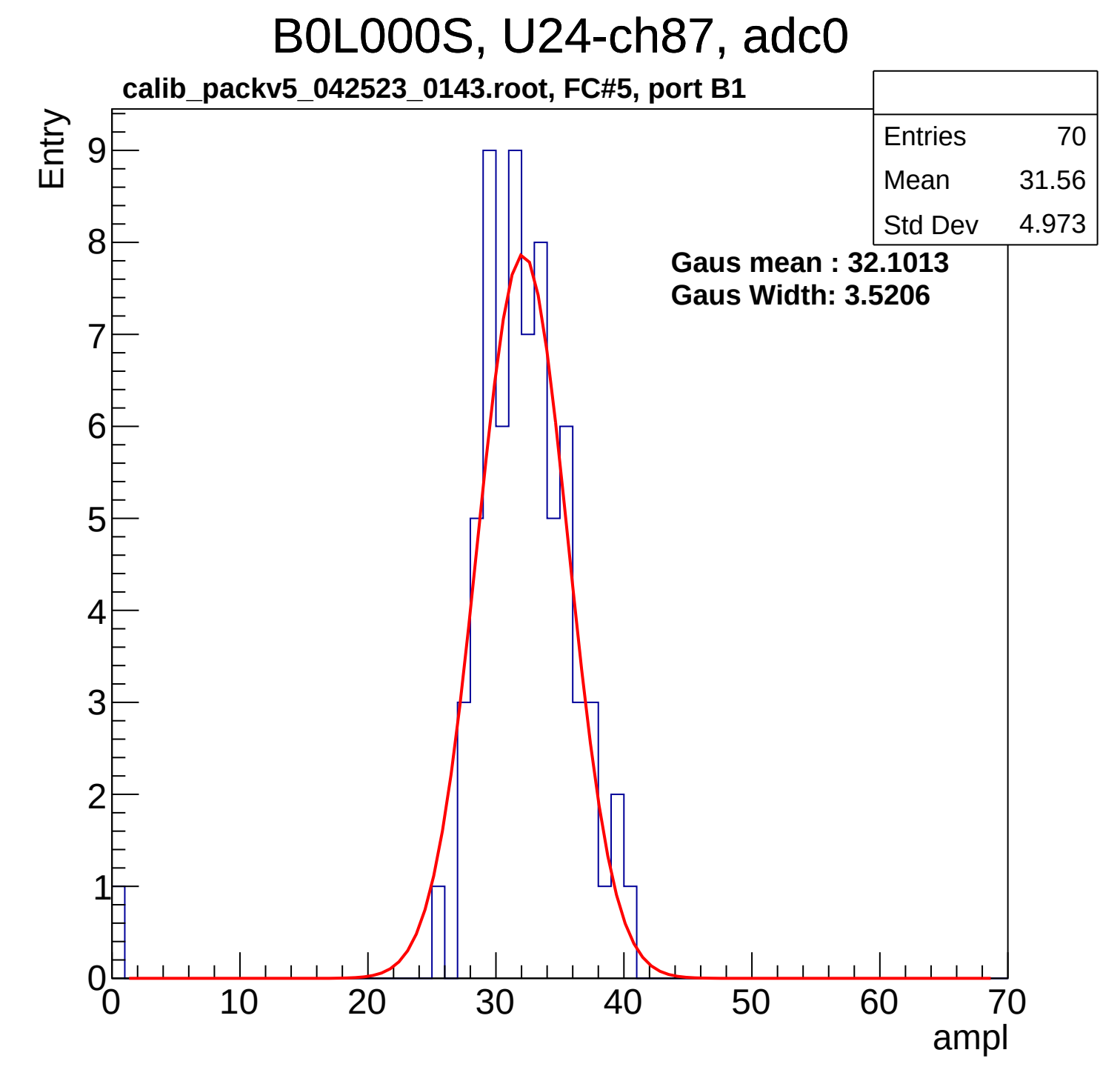

B0L000S, U24-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.92
Std Dev 17.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 31.56
Std Dev 4.973
Gaus mean : 32.1013
Gaus Width: 3.5206
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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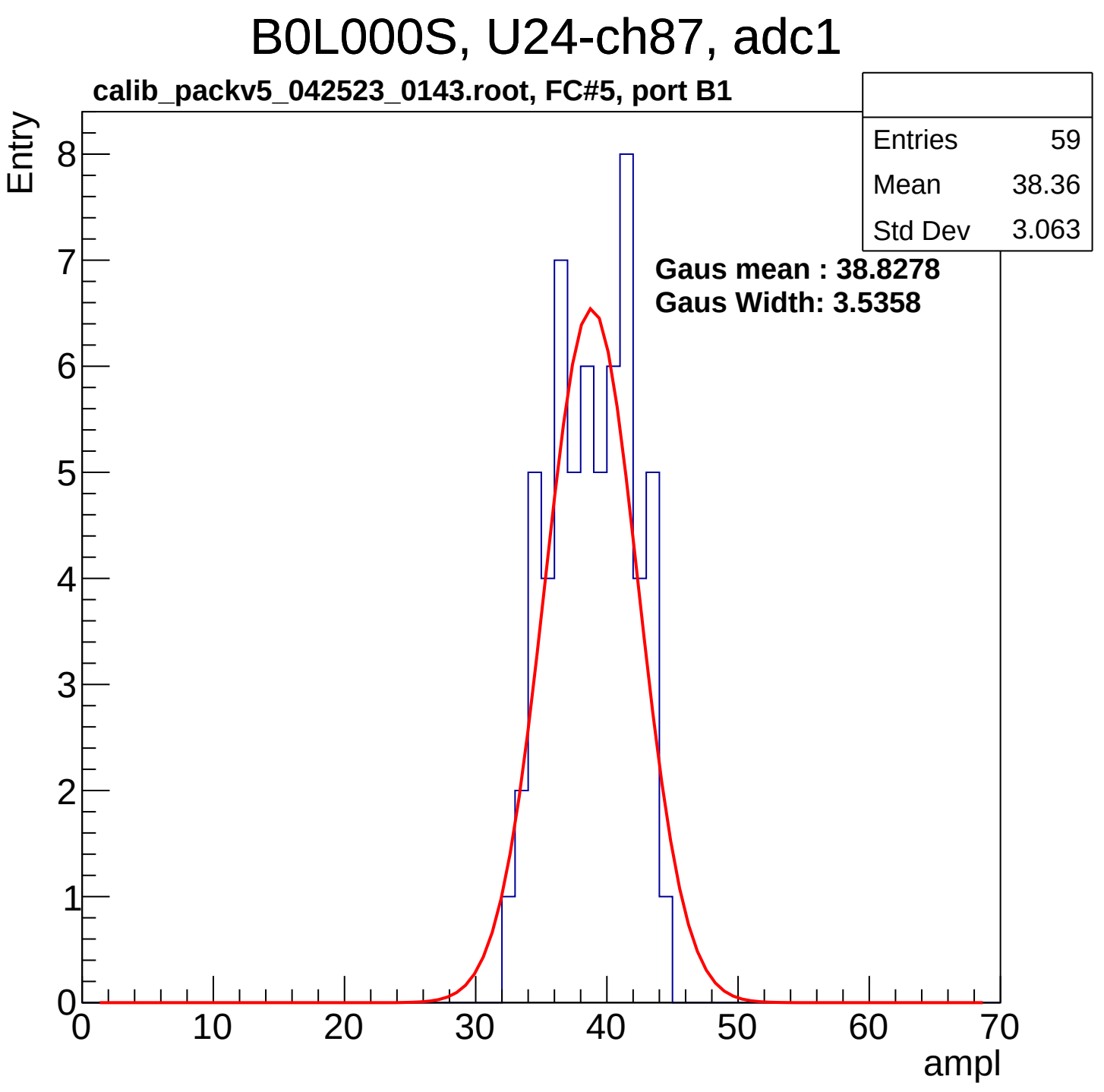
B0L000S, U24-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 38.36
Std Dev 3.063
Gaus mean : 38.8278
Gaus Width: 3.5358
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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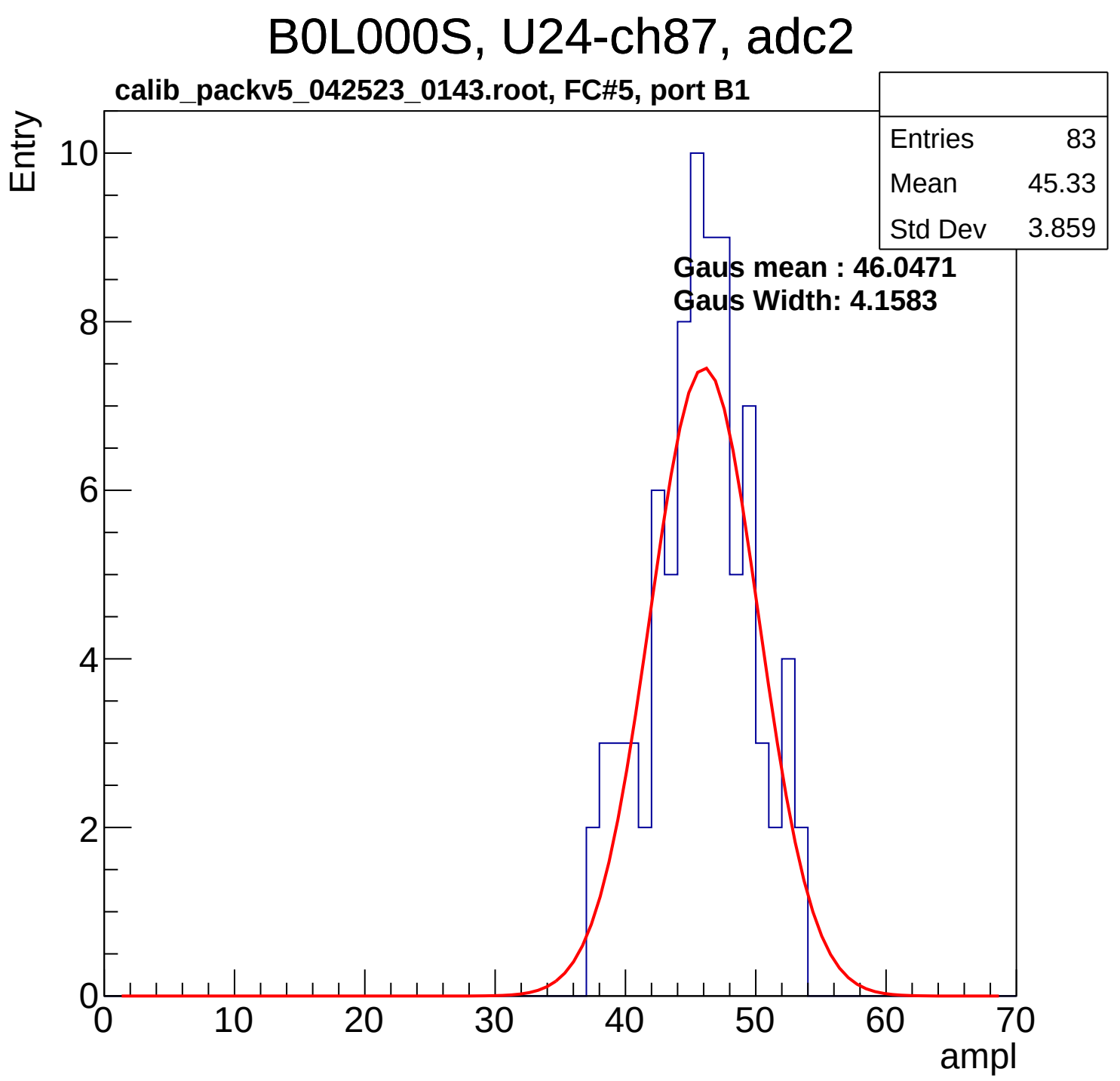
B0L000S, U24-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 45.33
Std Dev 3.859
Gaus mean : 46.0471
Gaus Width: 4.1583
Entry
ampl
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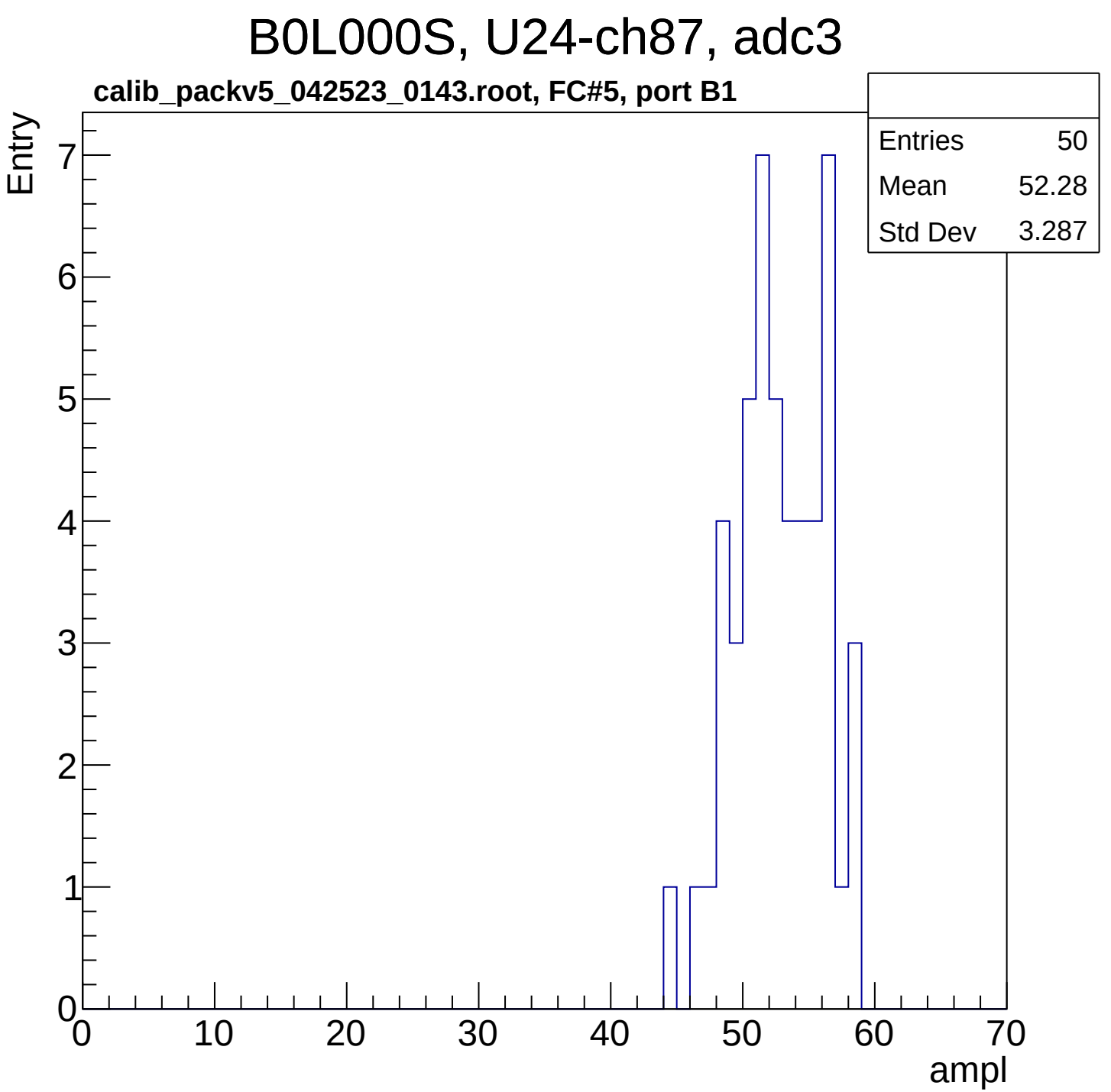
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 52.28
Std Dev 3.287
Entry
ampl
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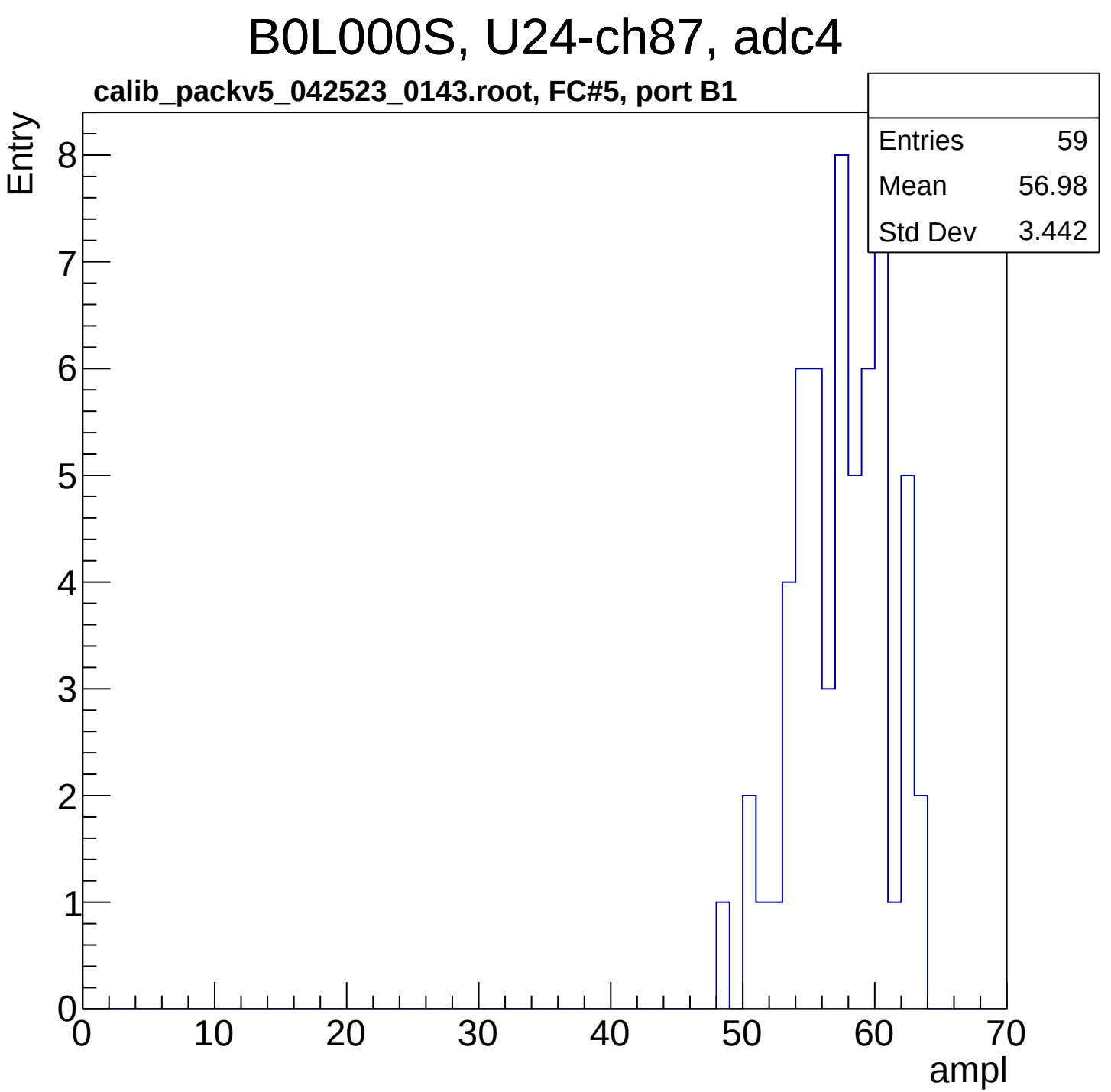
B0L000S, U24-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.98
Std Dev 3.442
Entry
ampl
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5
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7
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0
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60
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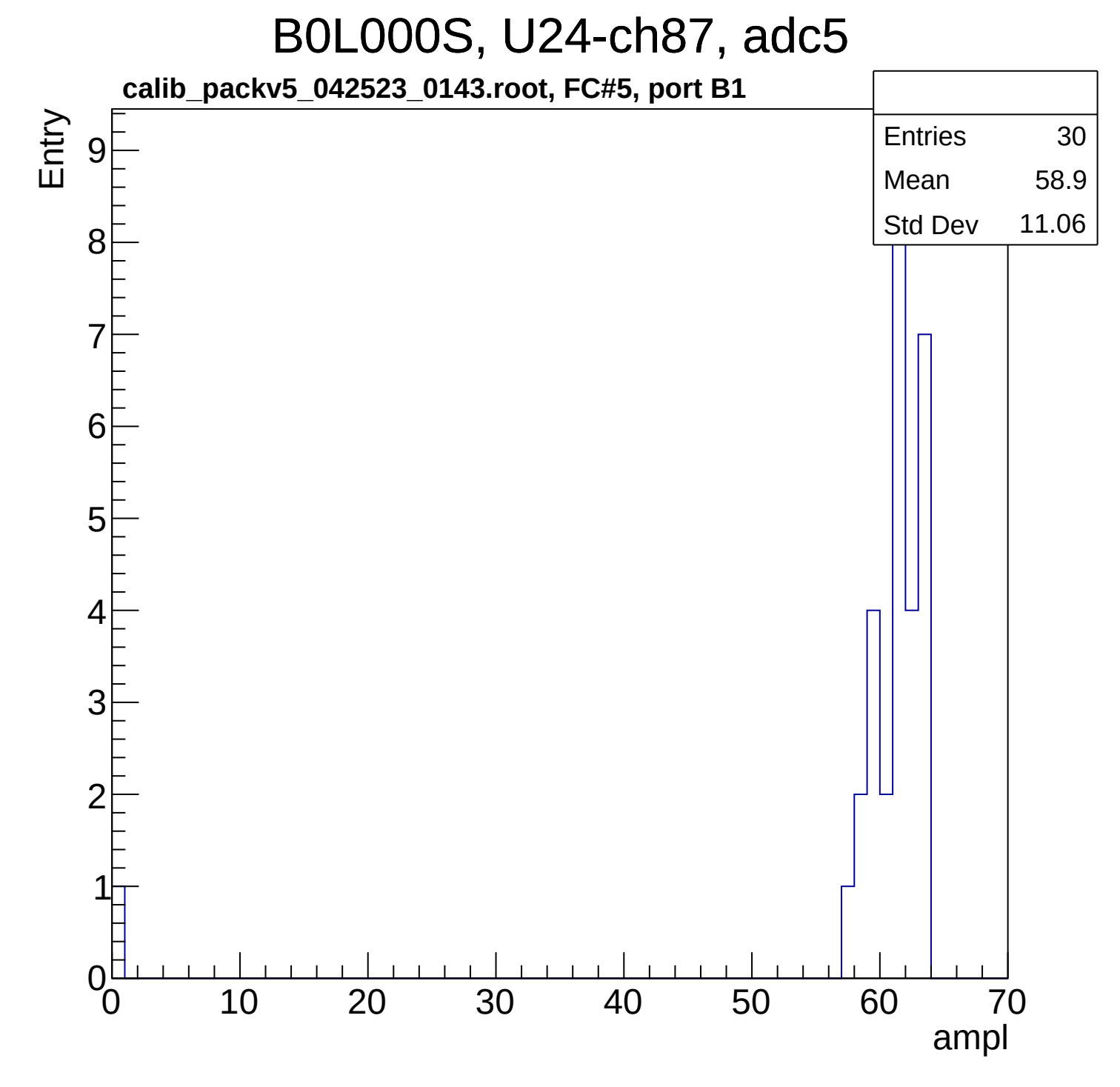
B0L000S, U24-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.9
Std Dev 11.06
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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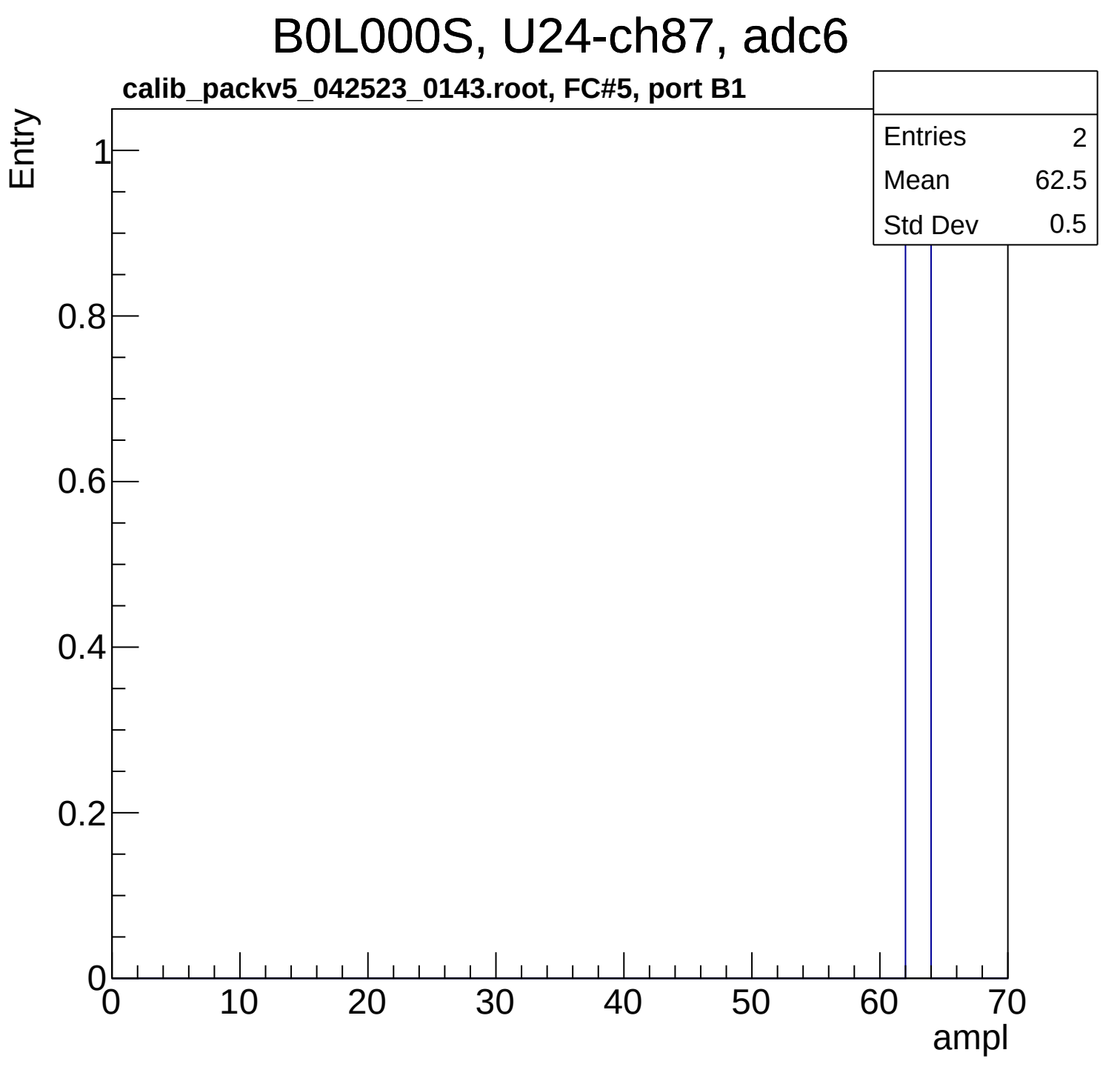
B0L000S, U24-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.6
0.4
0.2
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10
20
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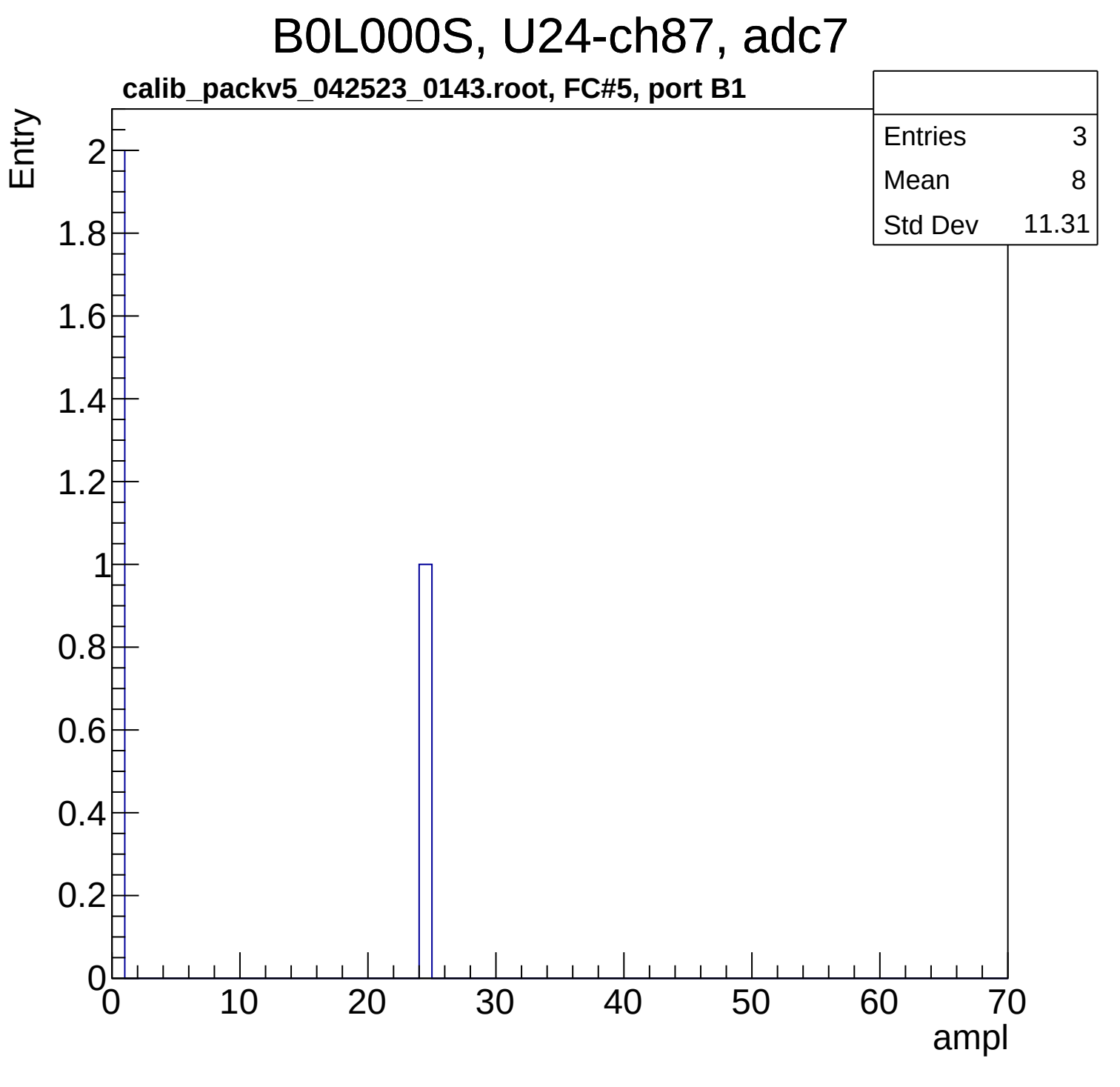
50
60
70
ampl


B0L000S, U24-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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60
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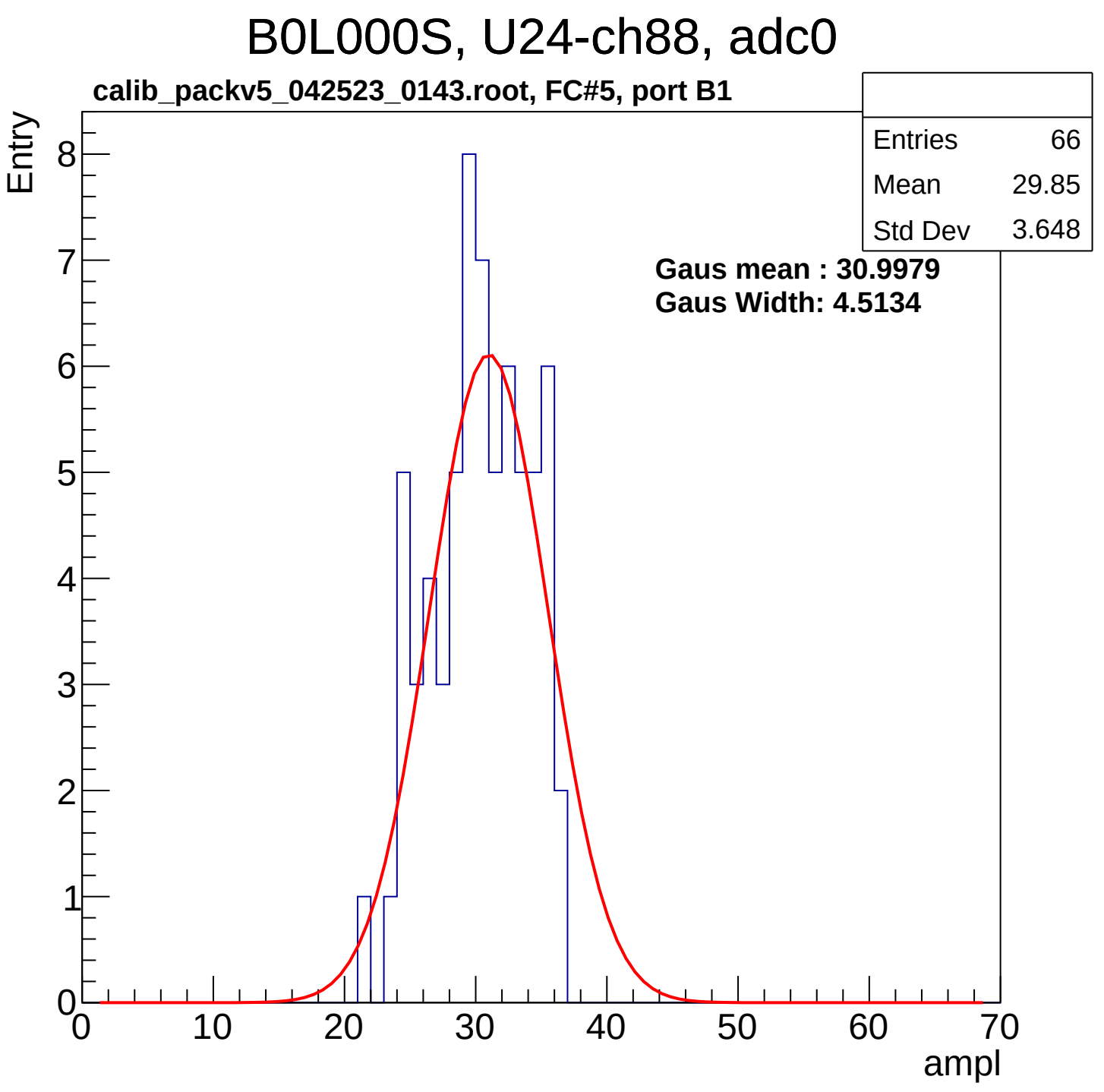
B0L000S, U24-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.85
Std Dev 3.648
Gaus mean : 30.9979
Gaus Width: 4.5134
Entry
ampl
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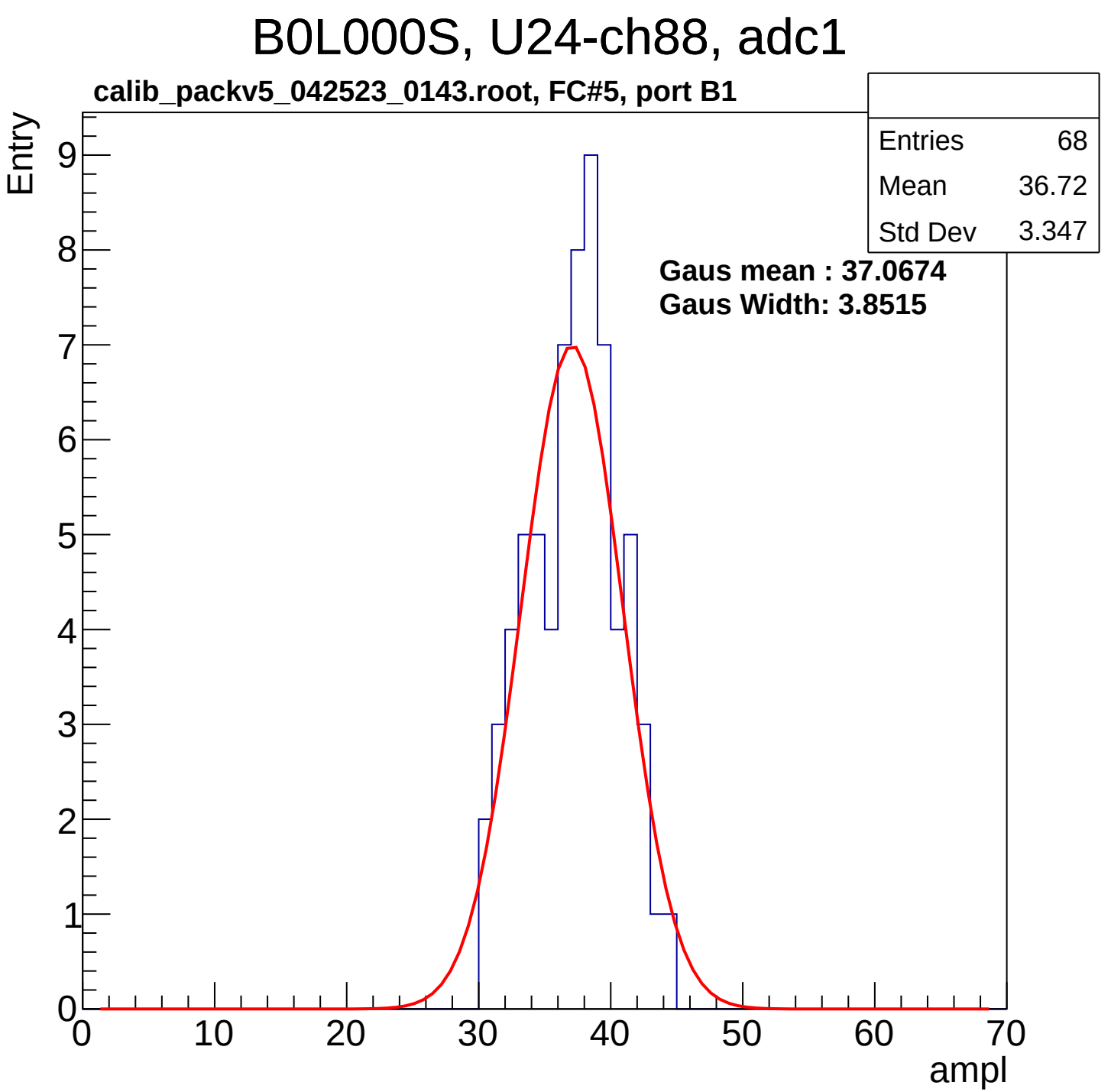
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.72
Std Dev 3.347
Gaus mean : 37.0674
Gaus Width: 3.8515
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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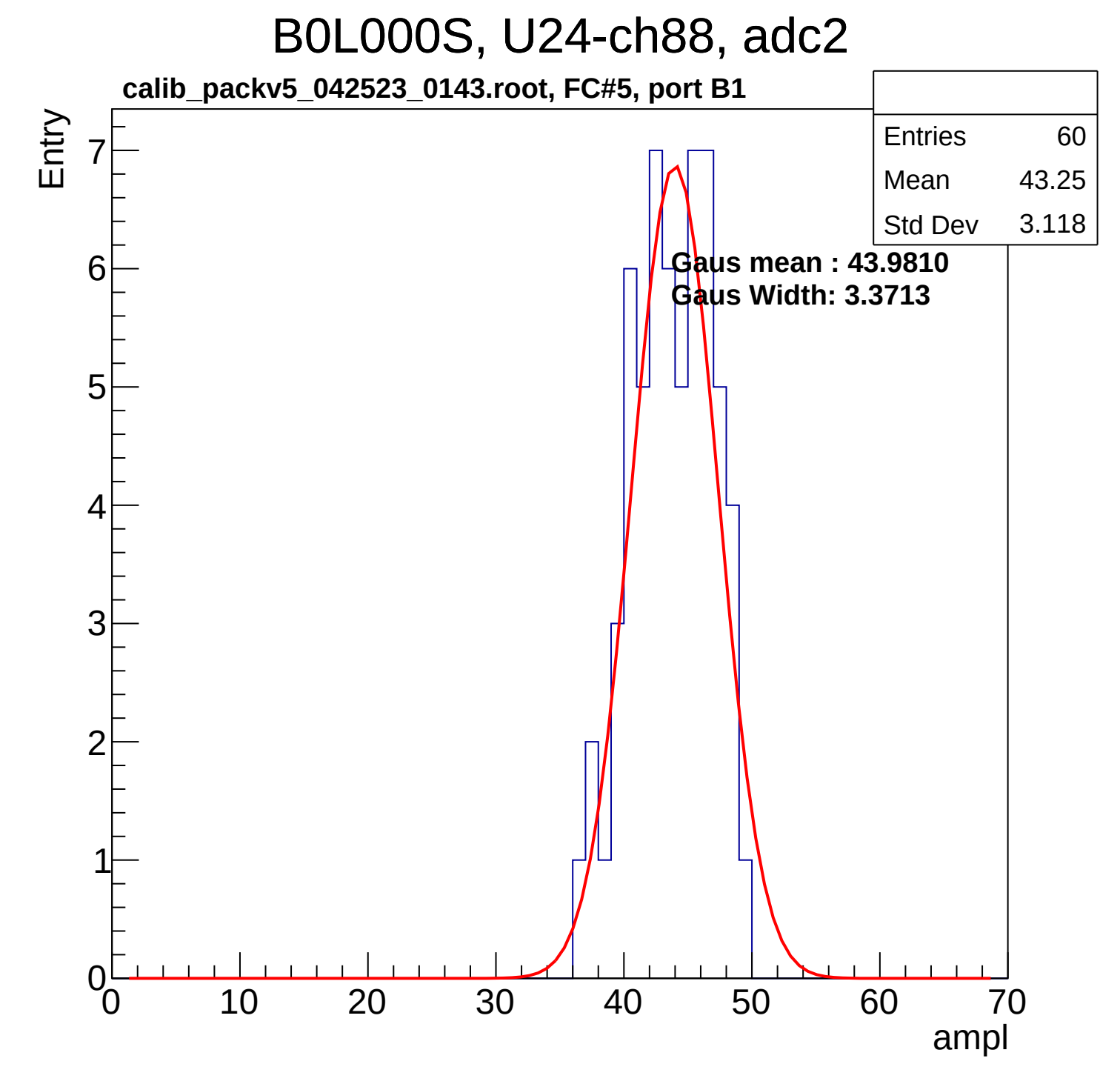
B0L000S, U24-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.25
Std Dev 3.118
Gaus mean : 43.9810
Gaus Width: 3.3713
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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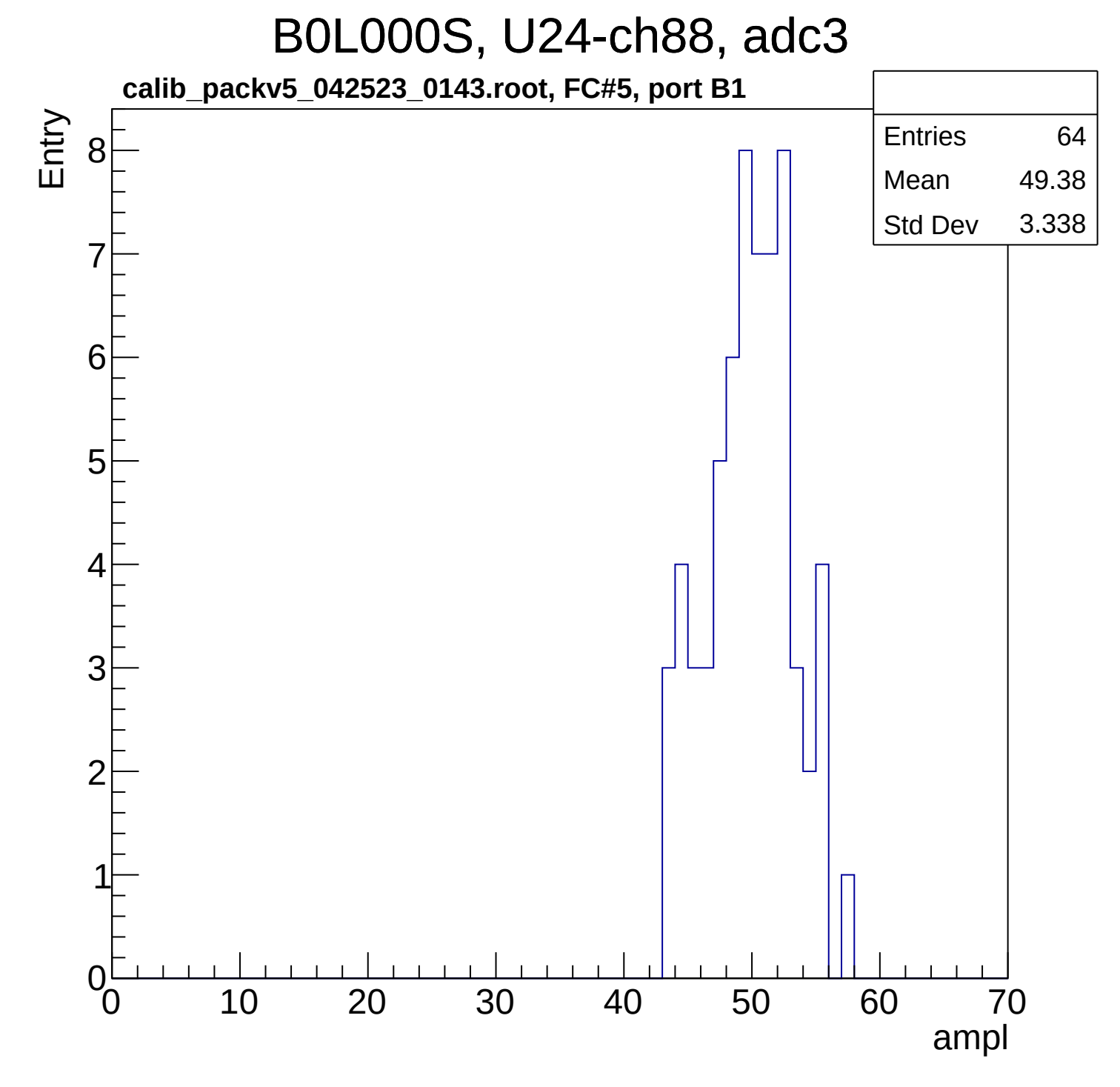
B0L000S, U24-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.38
Std Dev 3.338
Entry
ampl
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2
3
4
5
6
7
8
0
10
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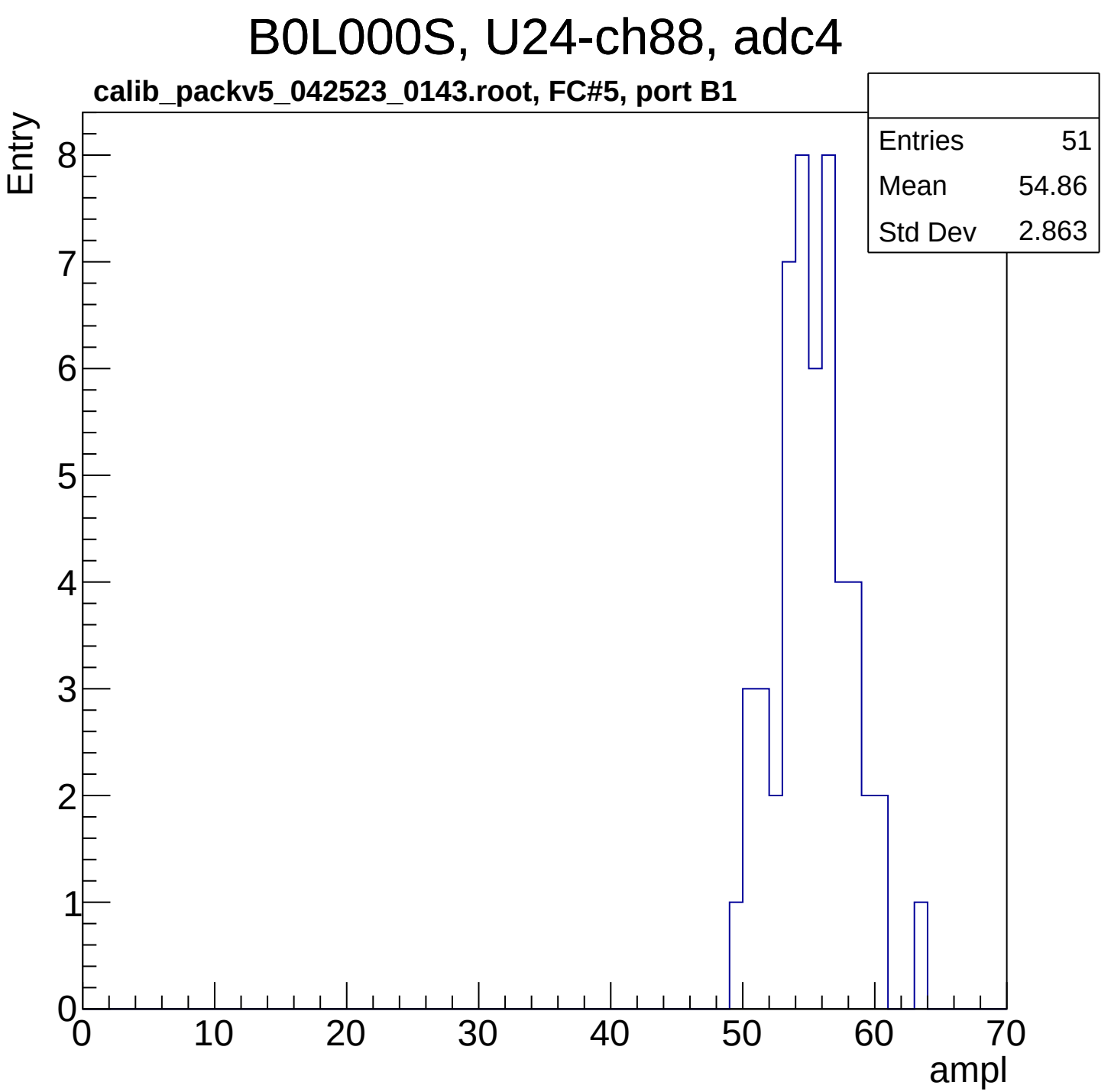
B0L000S, U24-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.86
Std Dev 2.863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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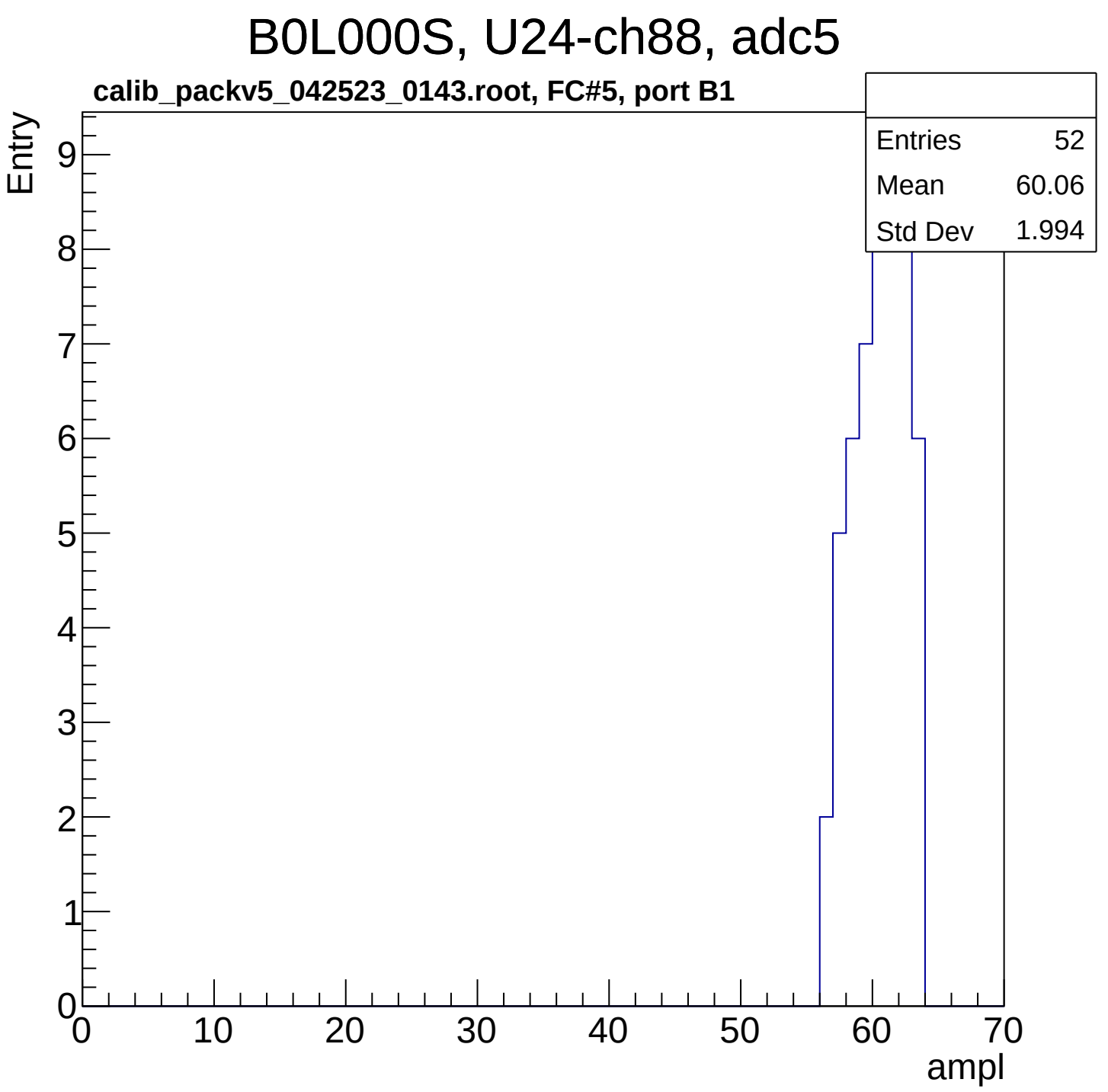
60
70


B0L000S, U24-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 60.06
Std Dev 1.994
Entry
ampl
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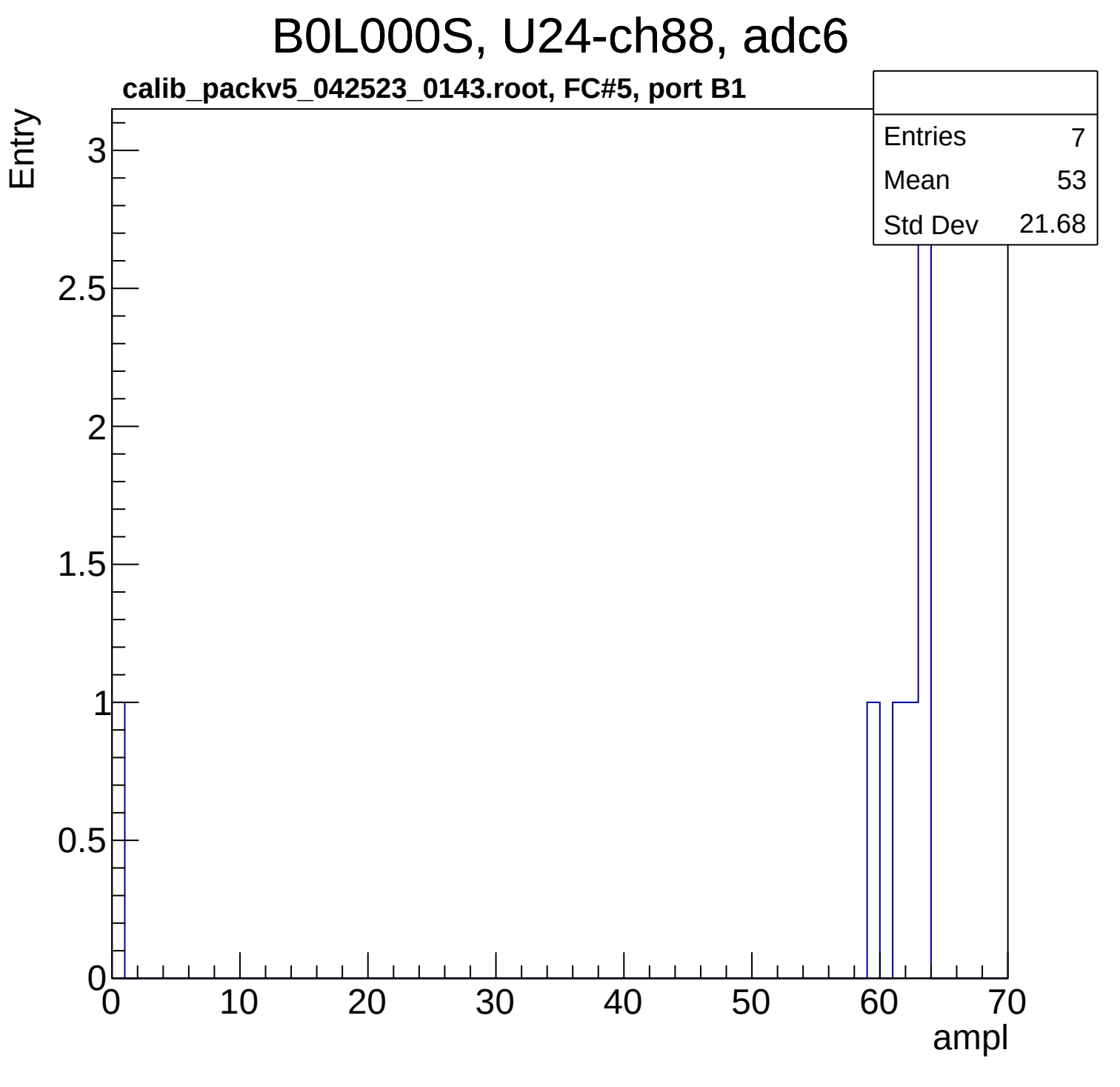
B0L000S, U24-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53
Std Dev 21.68
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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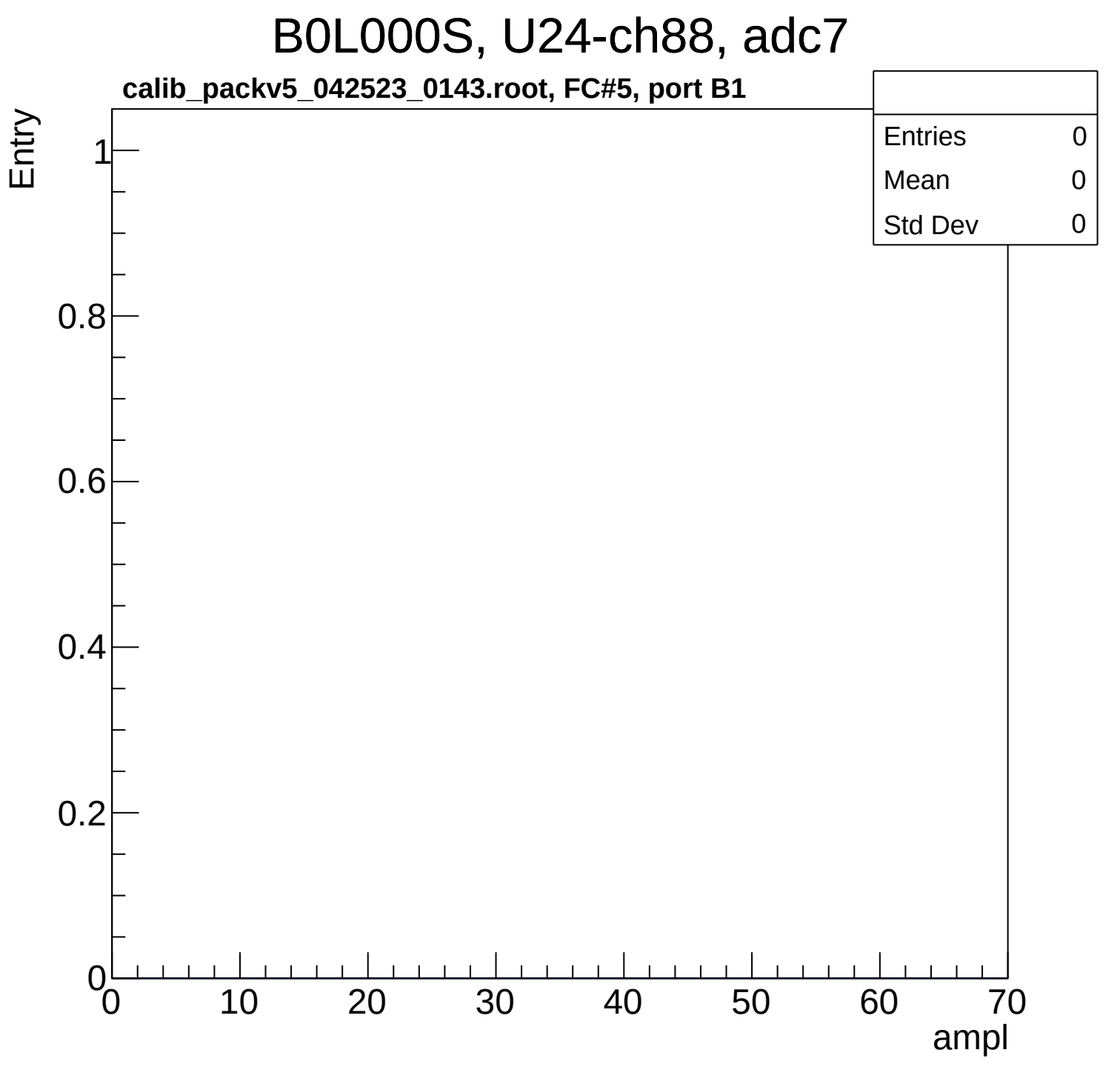
B0L000S, U24-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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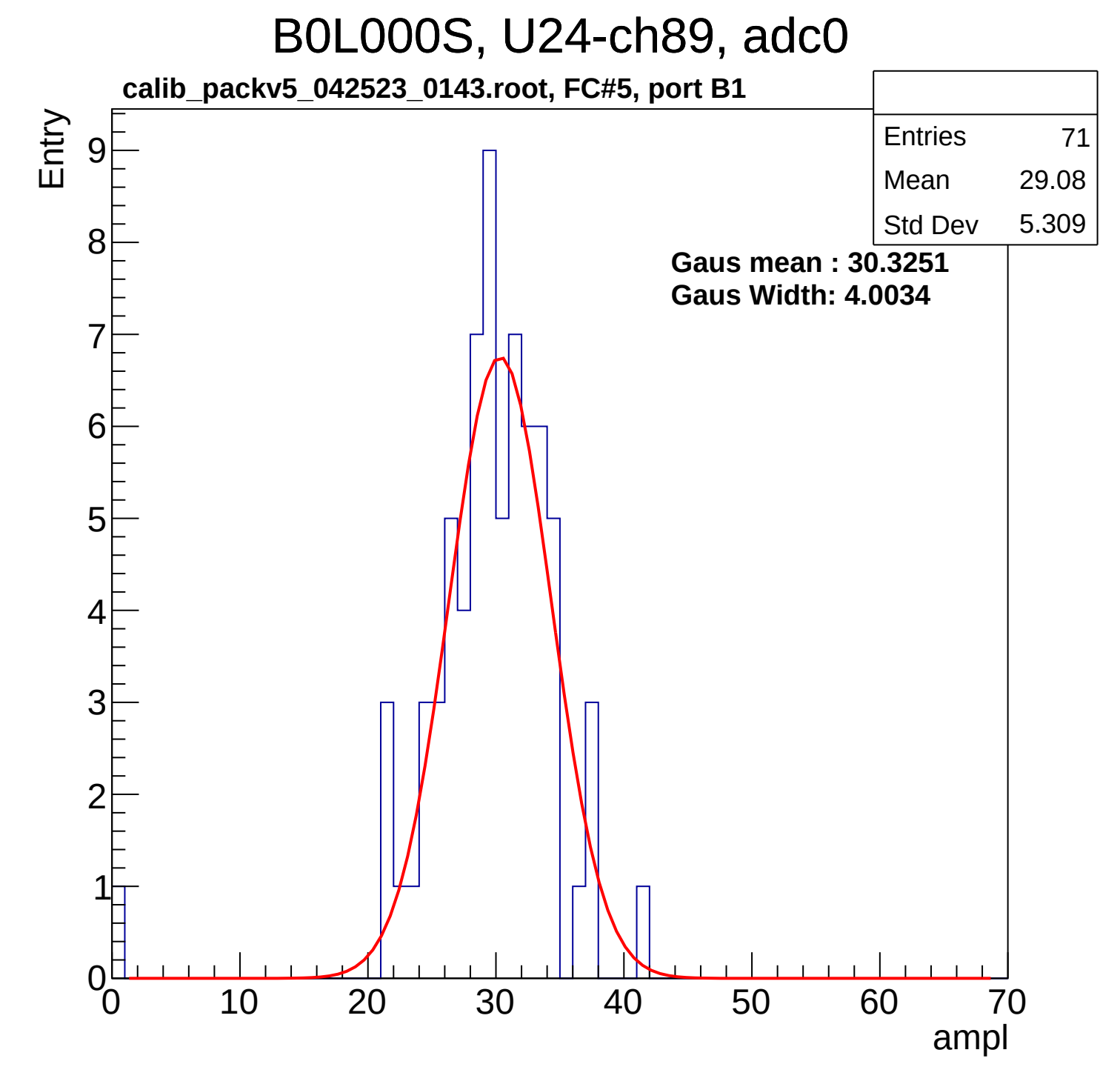
30
40
50
60
70
ampl


B0L000S, U24-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.08
Std Dev 5.309
Gaus mean : 30.3251
Gaus Width: 4.0034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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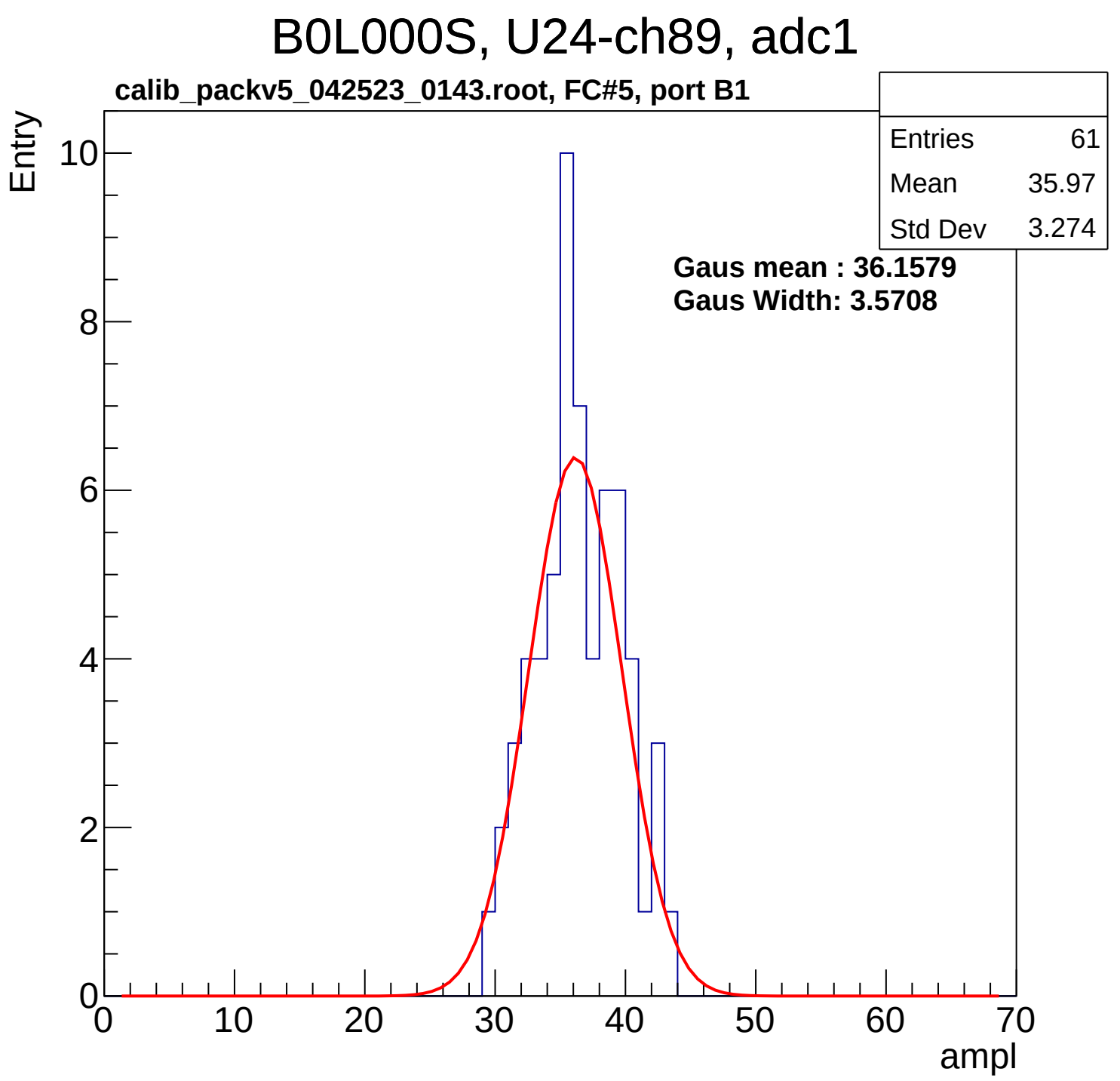
B0L000S, U24-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.97
Std Dev 3.274
Gaus mean : 36.1579
Gaus Width: 3.5708
Entry
ampl
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2
4
6
8
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0
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40
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60
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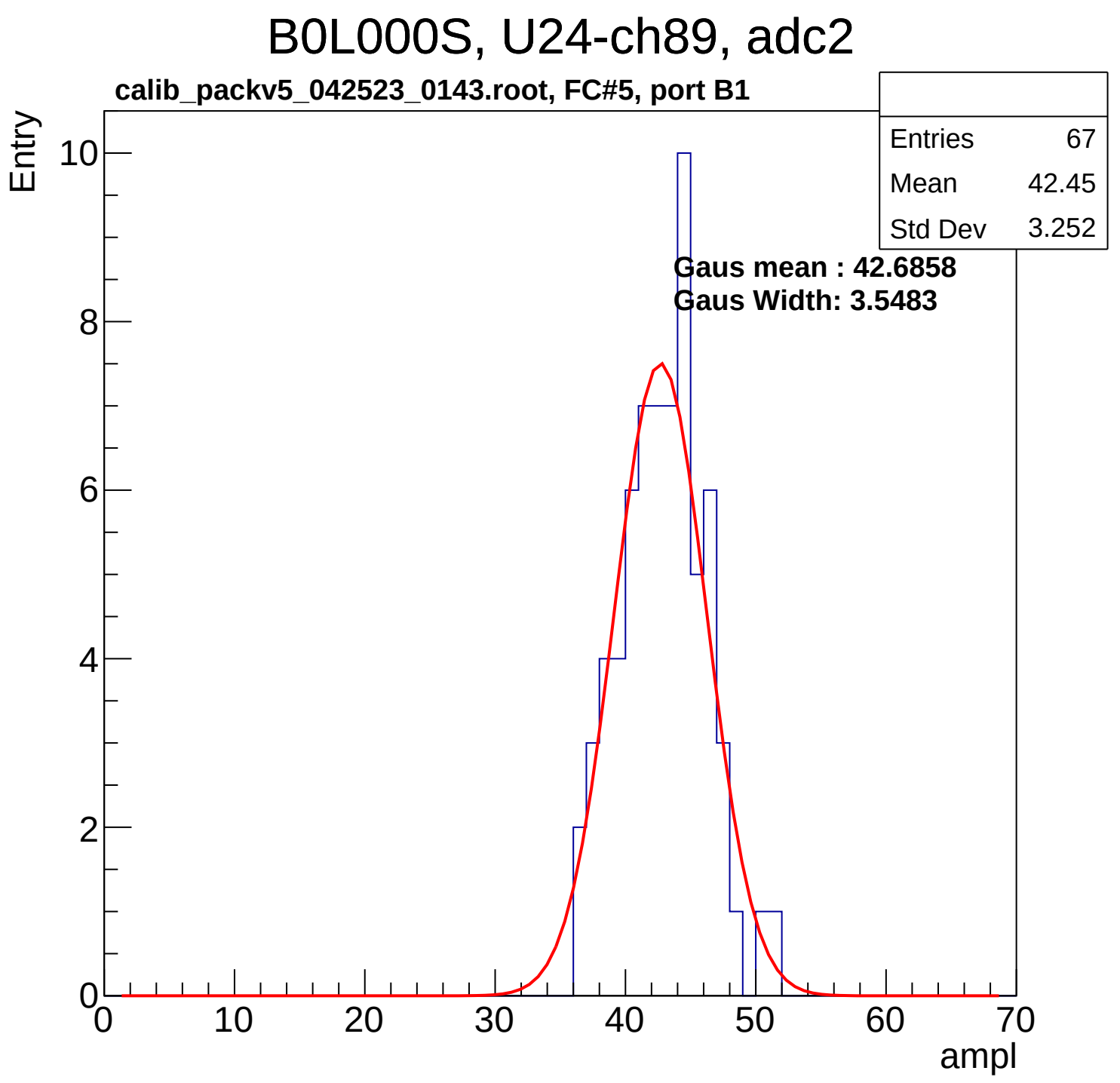
B0L000S, U24-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.45
Std Dev 3.252
Gaus mean : 42.6858
Gaus Width: 3.5483
Entry
ampl
0
2
4
6
8
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0
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40
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60
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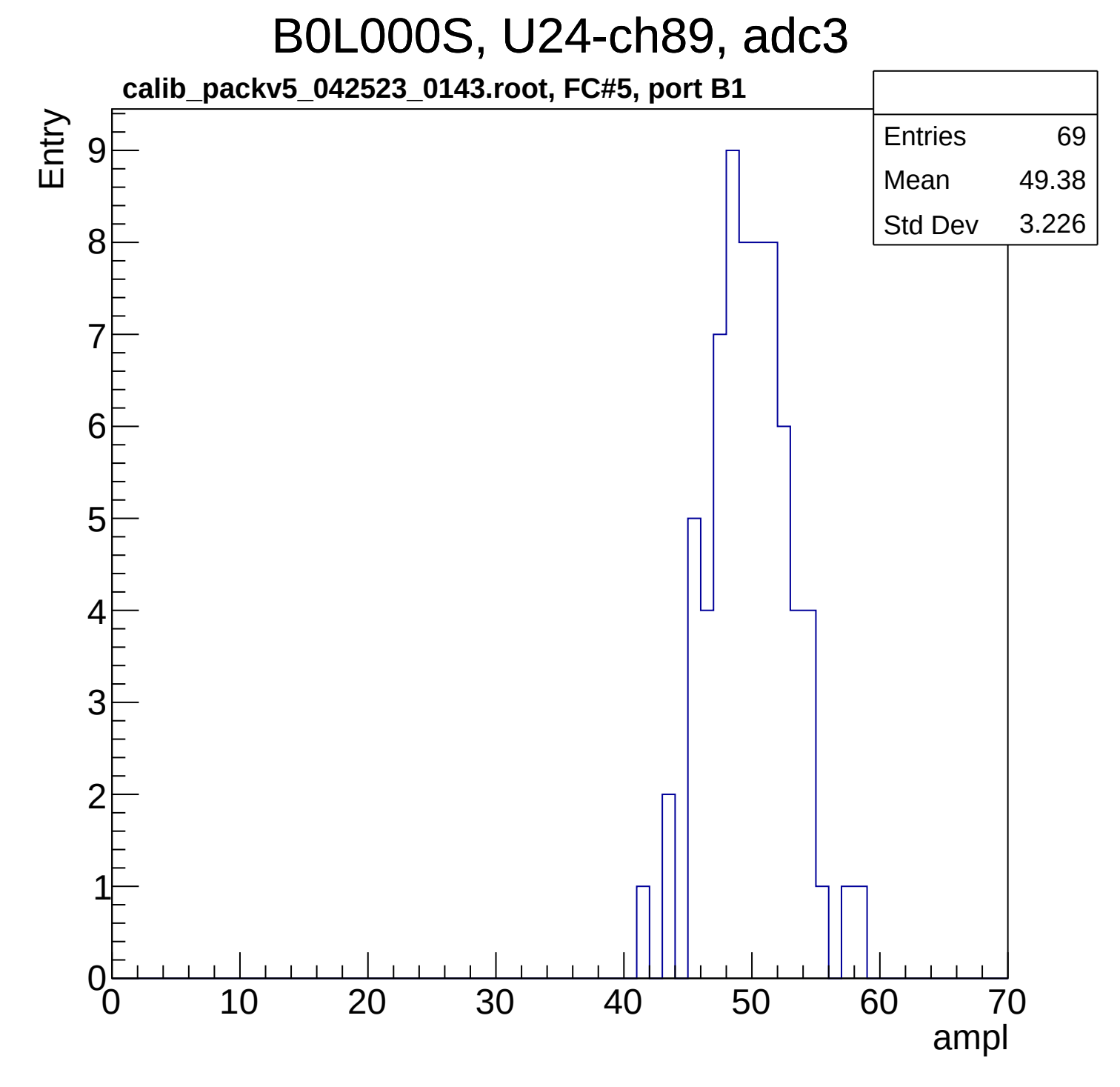
B0L000S, U24-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.38
Std Dev 3.226
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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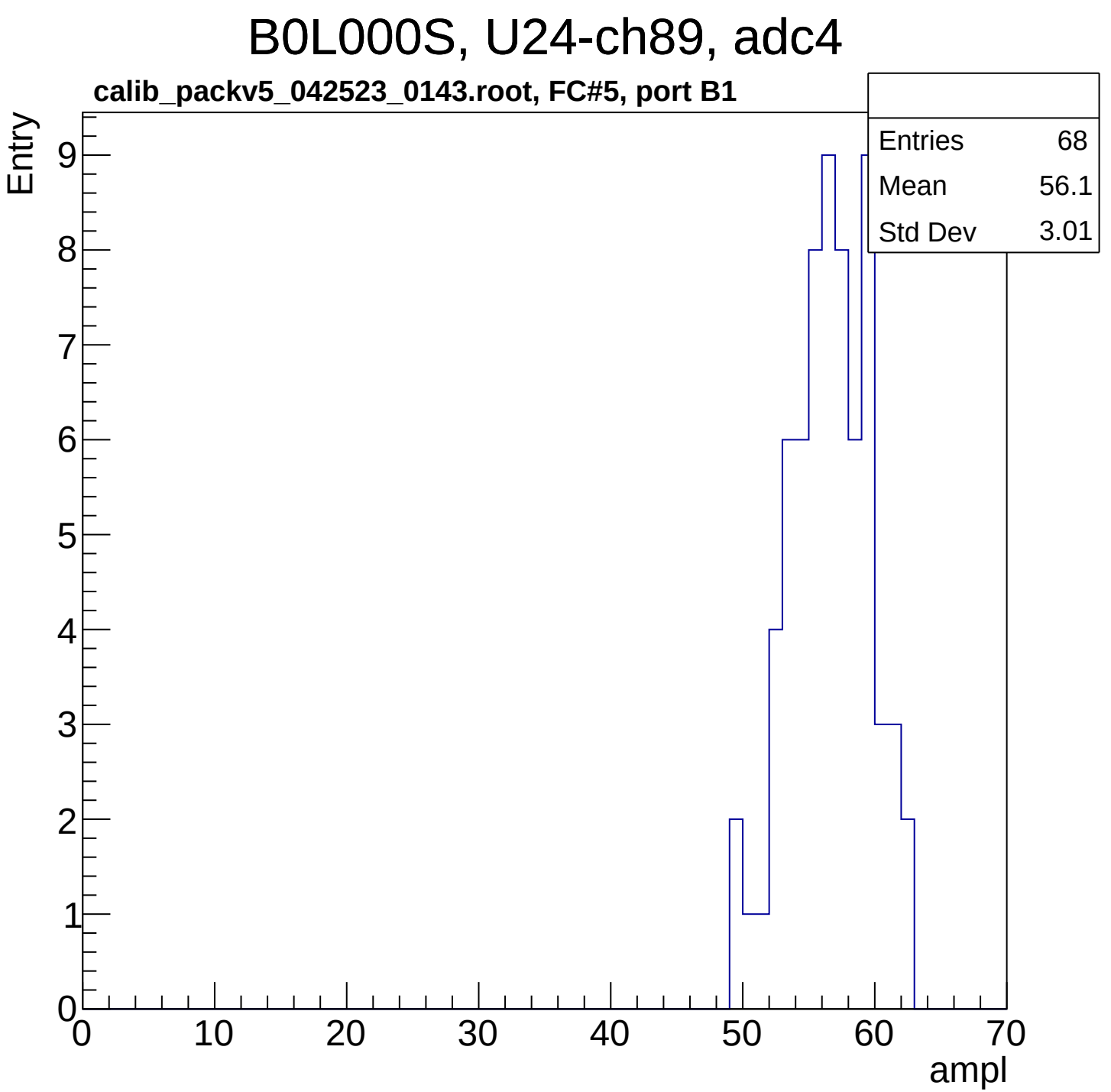
B0L000S, U24-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 56.1
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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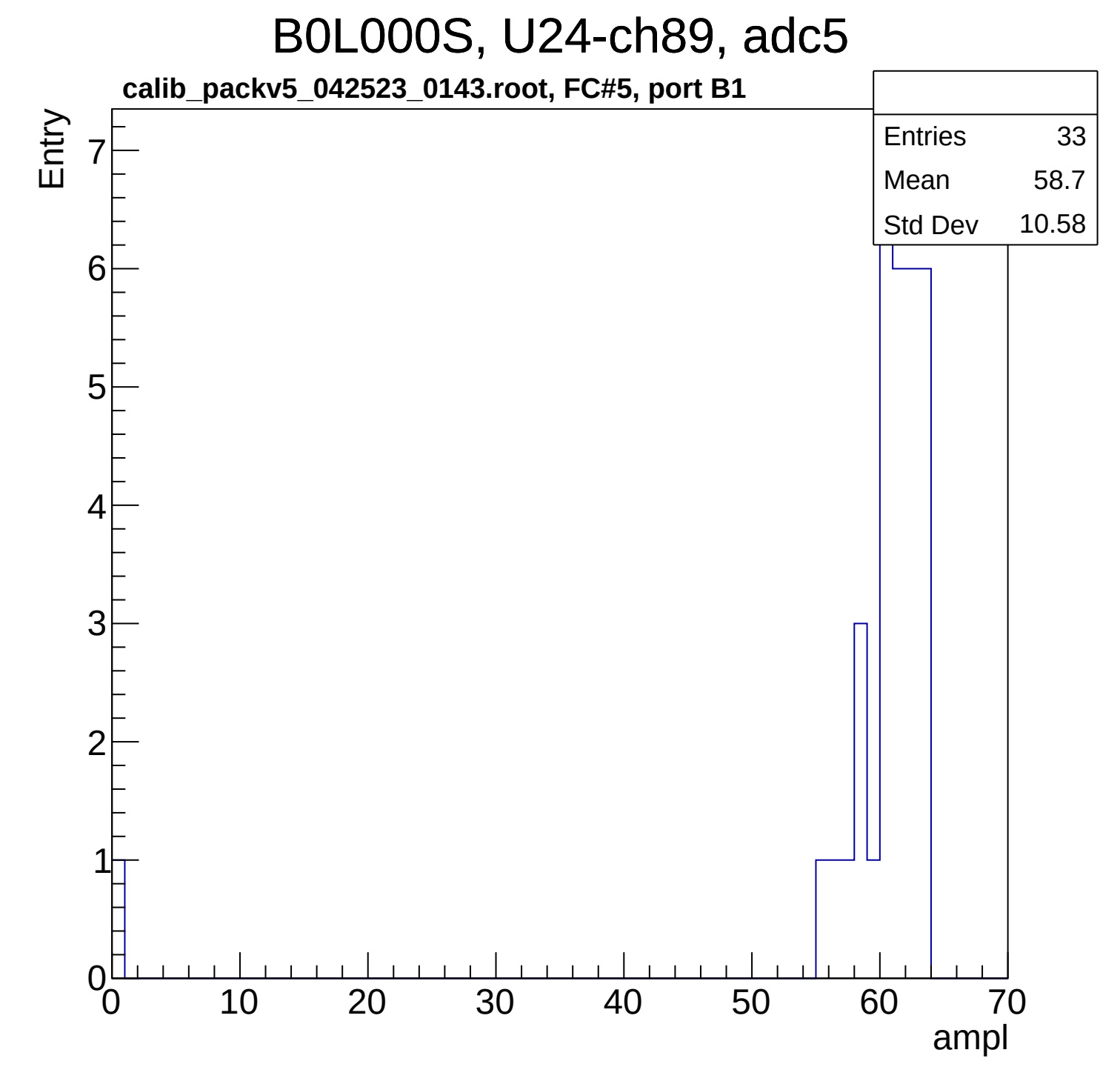
B0L000S, U24-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.7
Std Dev 10.58
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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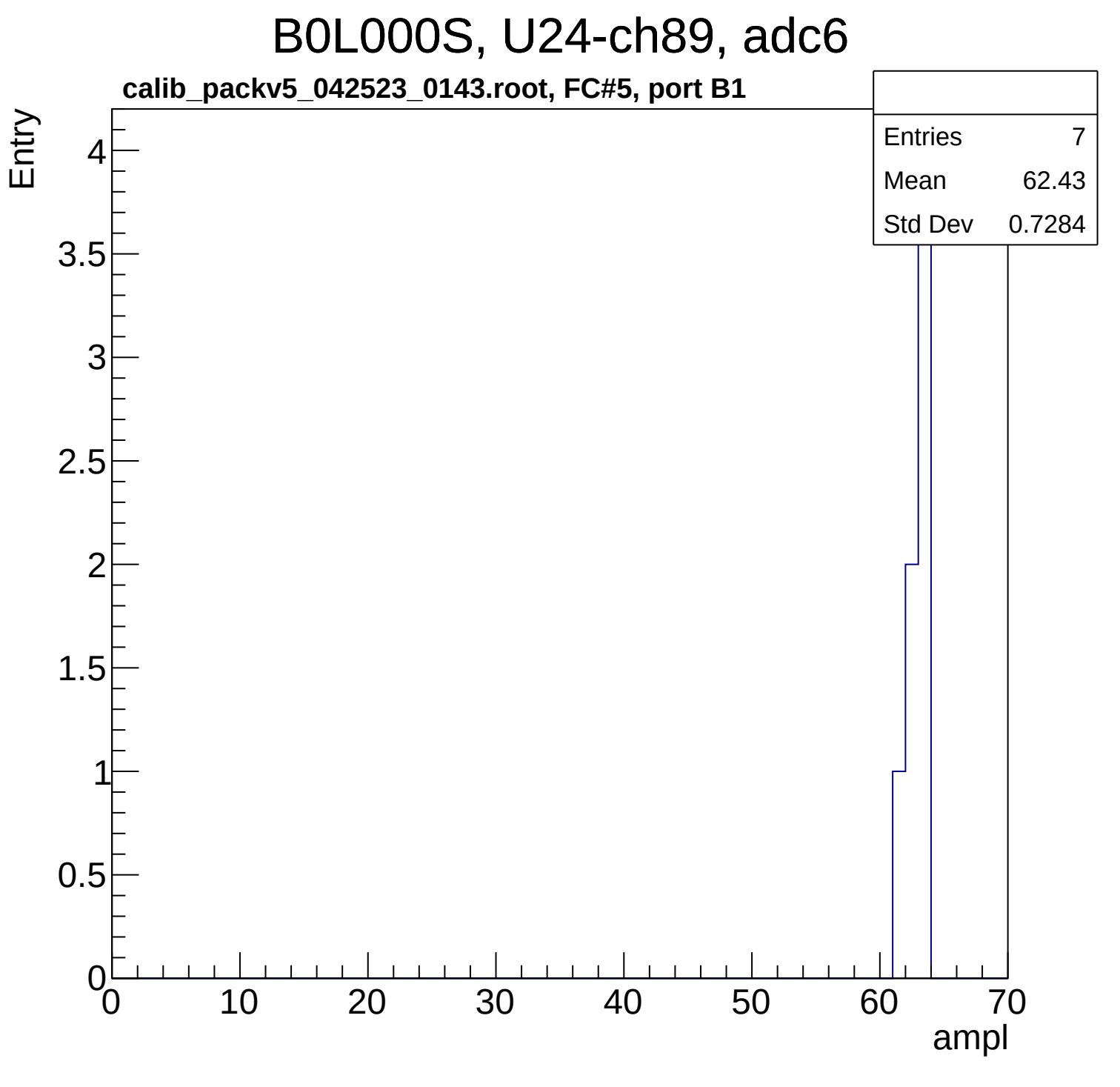
B0L000S, U24-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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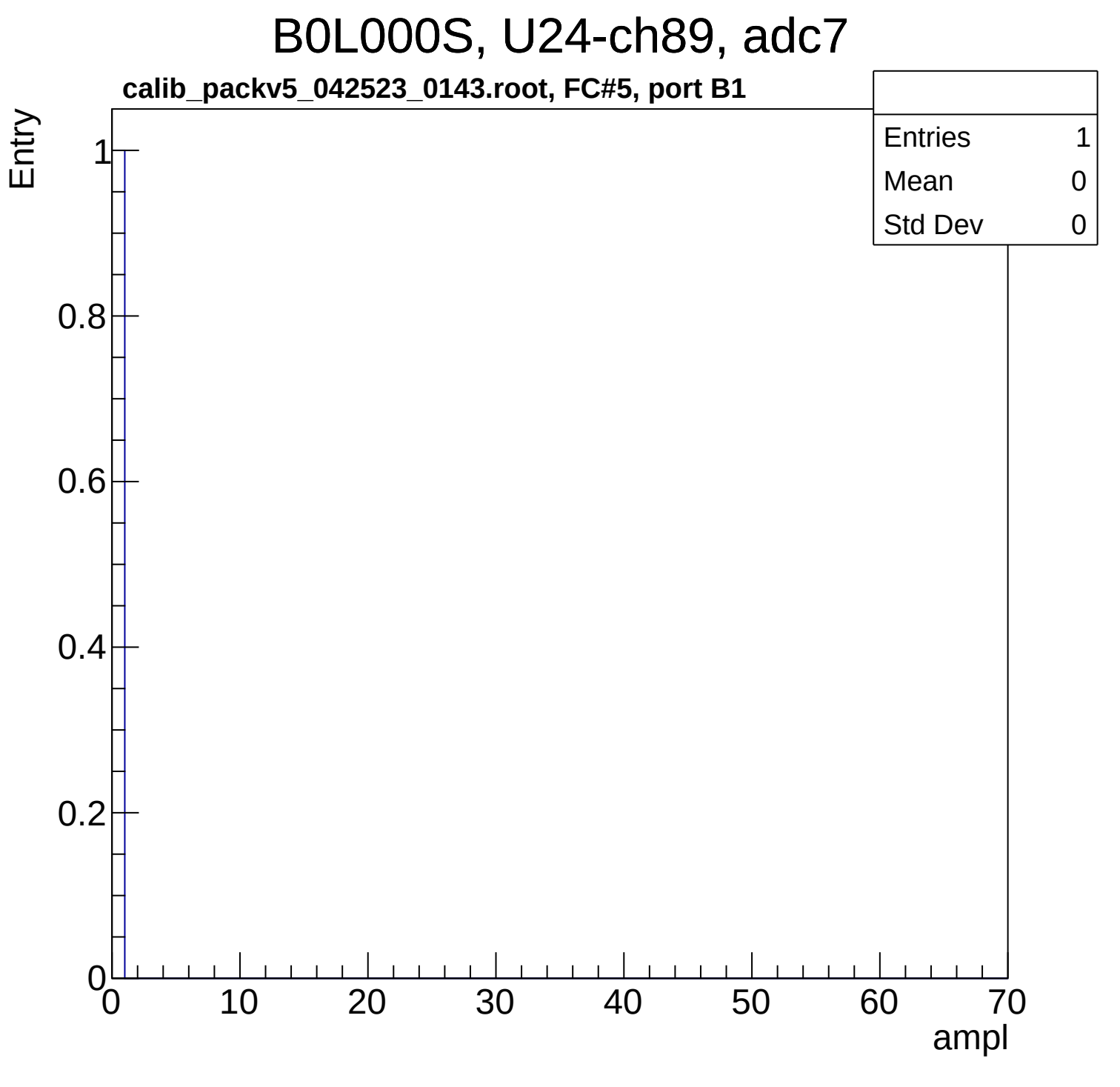
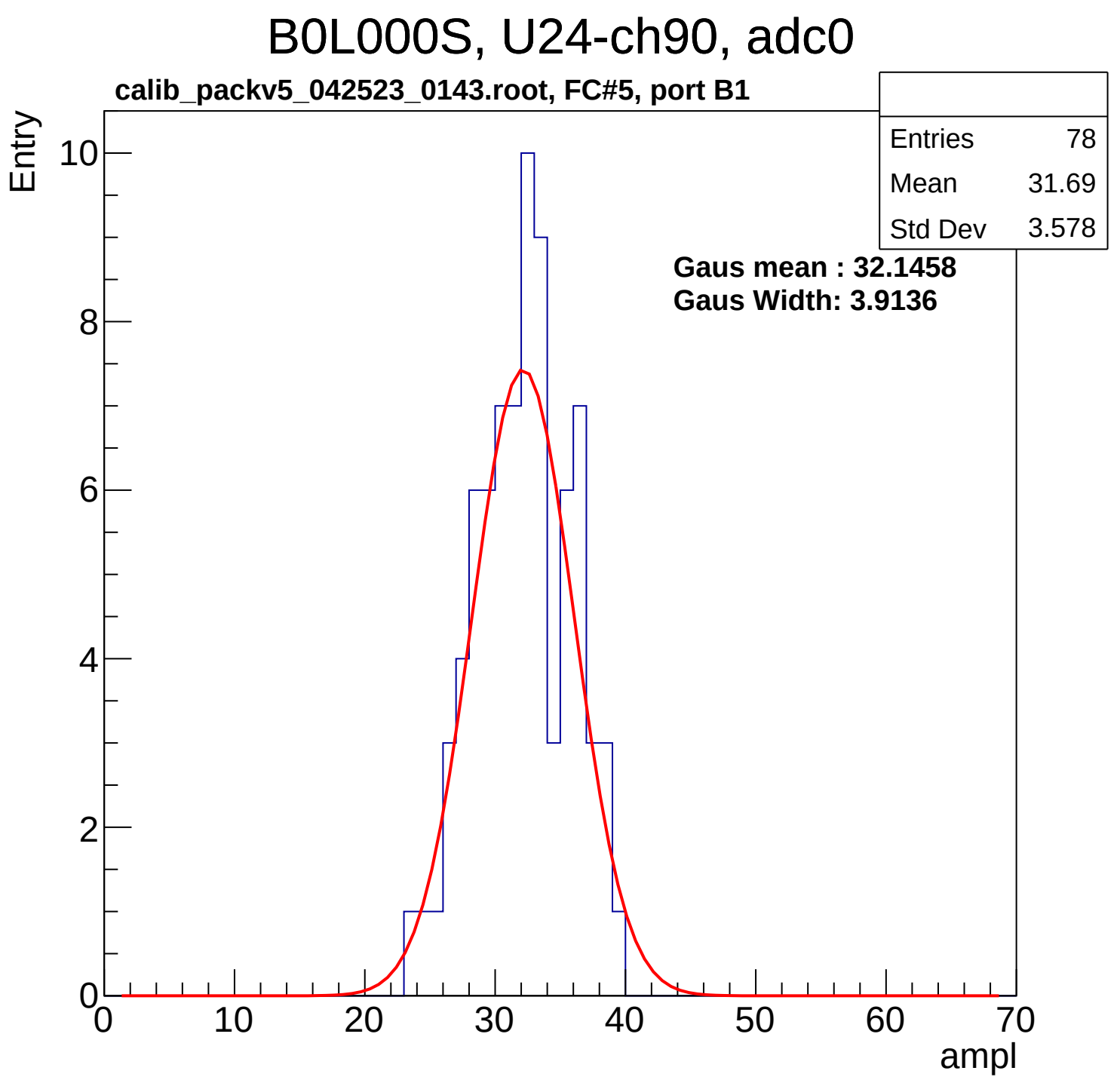
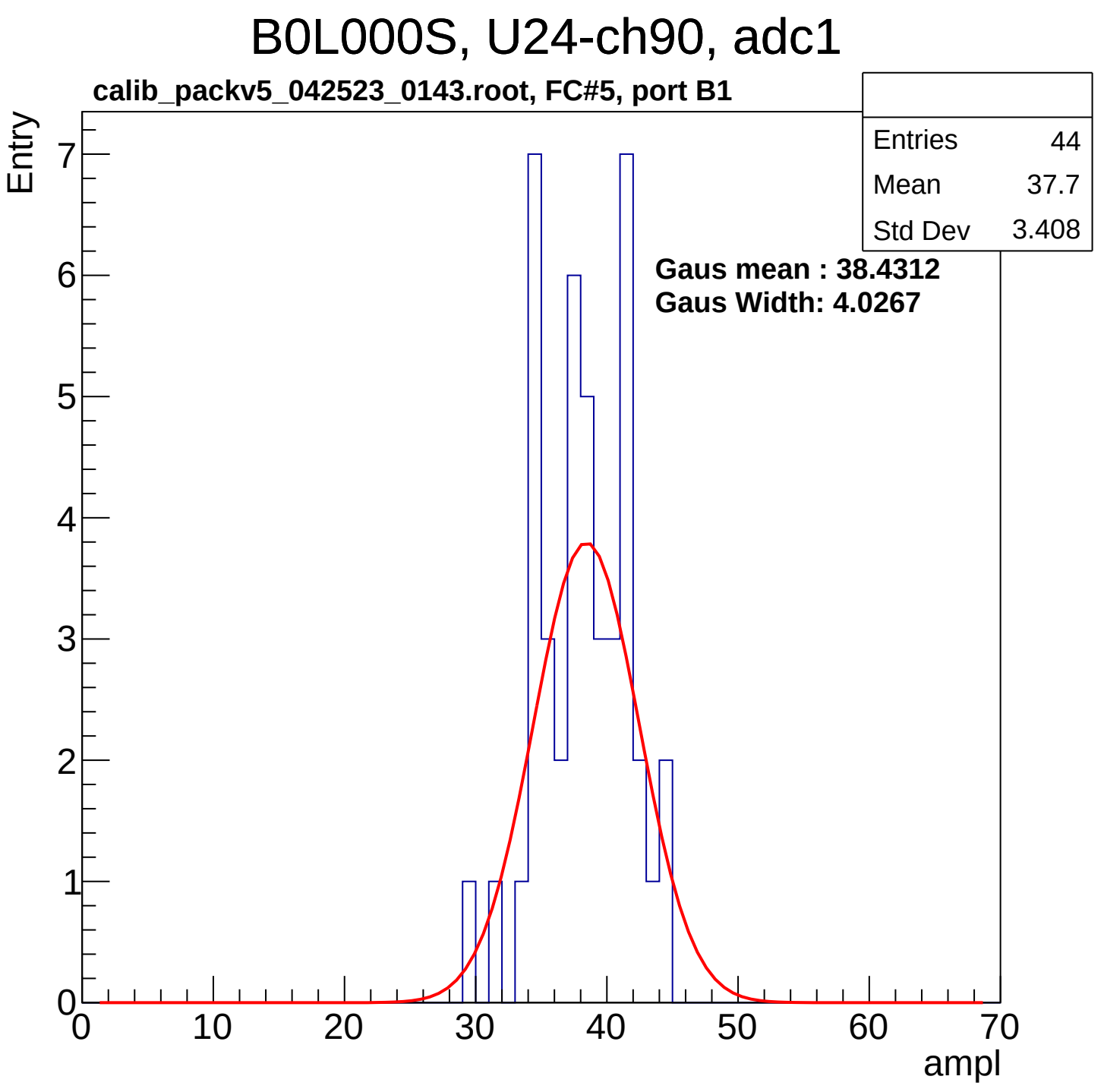

B0L000S, U24-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 31.69
Std Dev 3.578
Gaus mean : 32.1458
Gaus Width: 3.9136
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 37.7
Std Dev 3.408
Gaus mean : 38.4312
Gaus Width: 4.0267
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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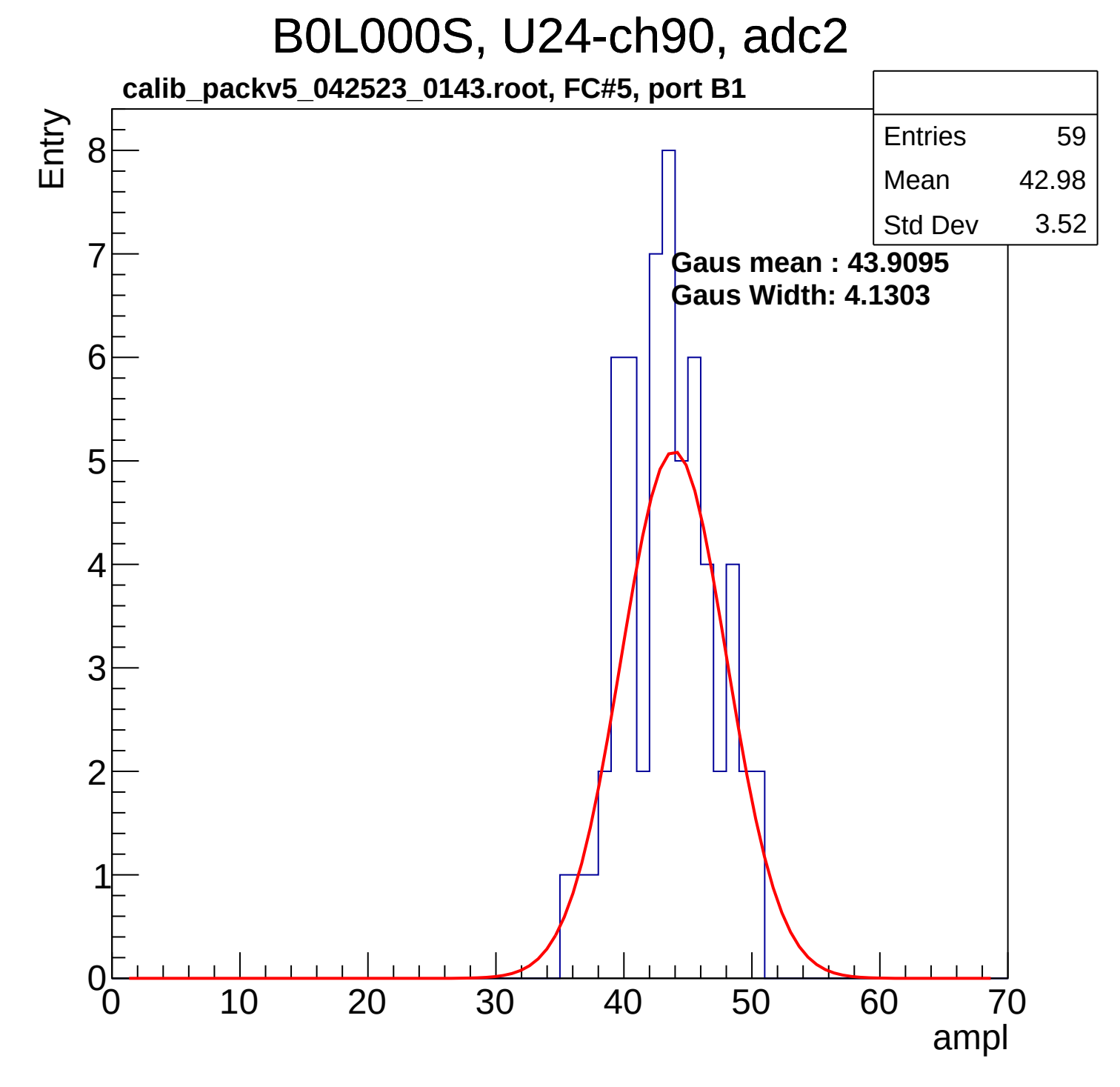
B0L000S, U24-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.98
Std Dev 3.52
Gaus mean : 43.9095
Gaus Width: 4.1303
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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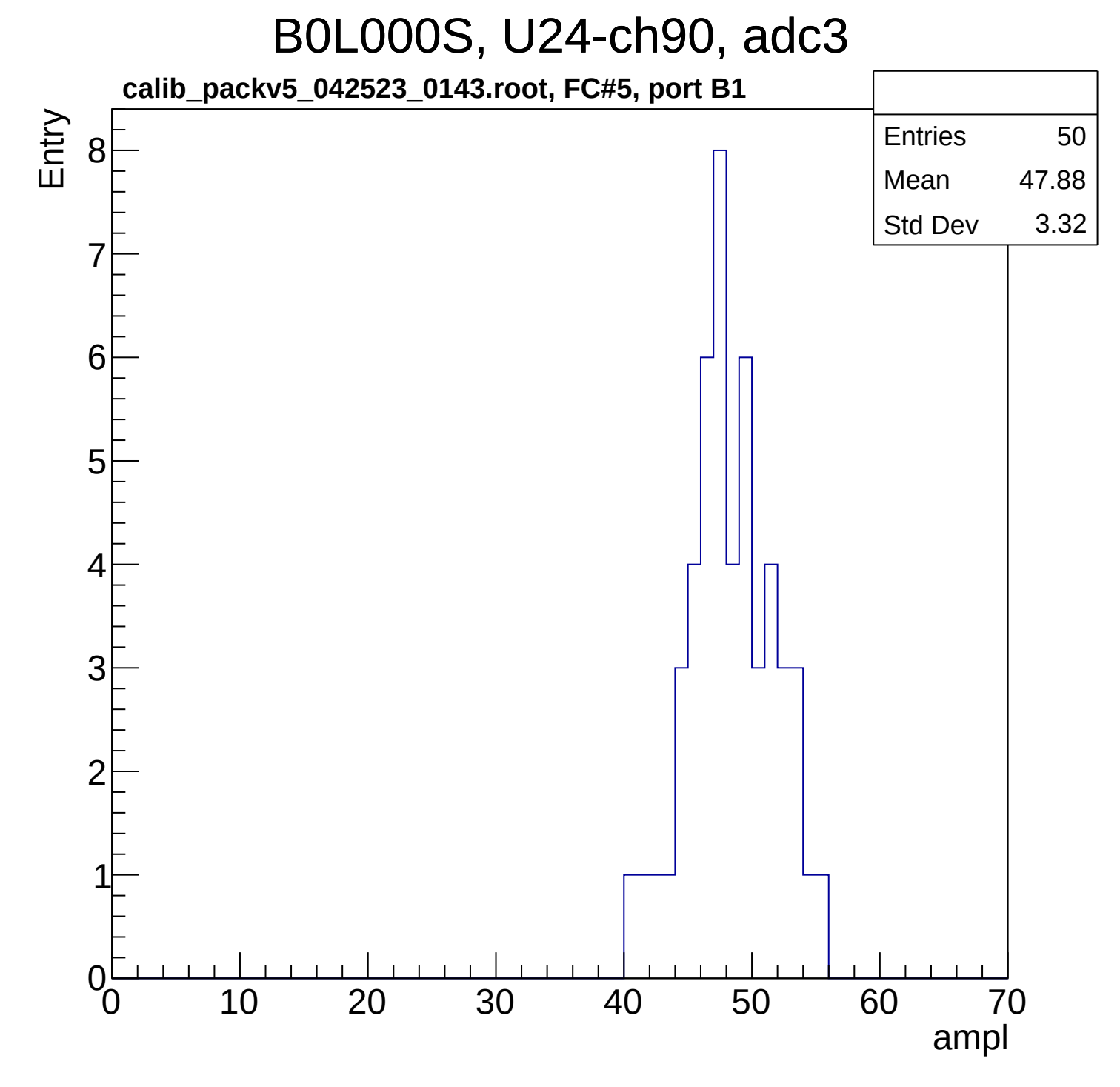
B0L000S, U24-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 47.88
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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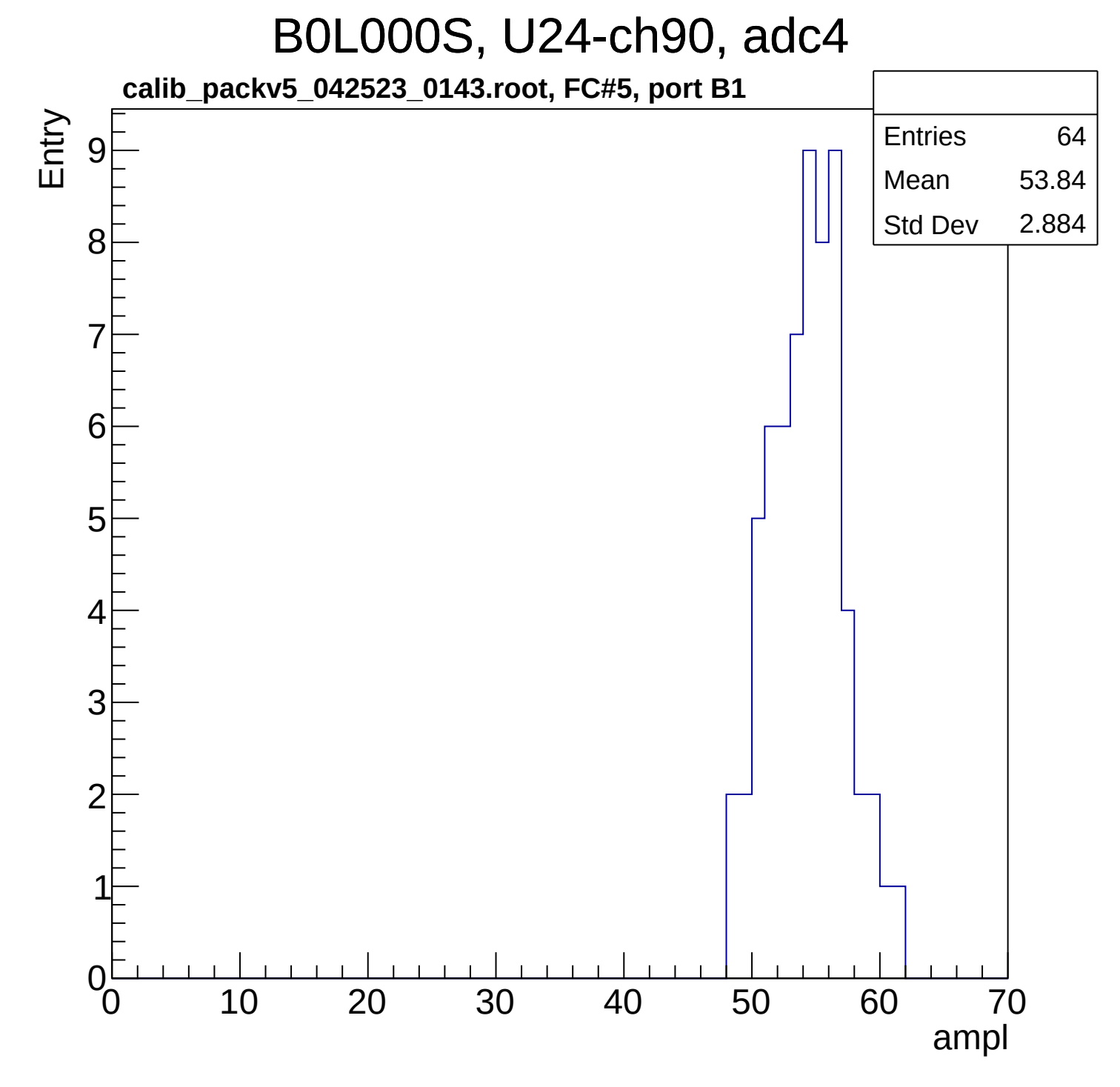
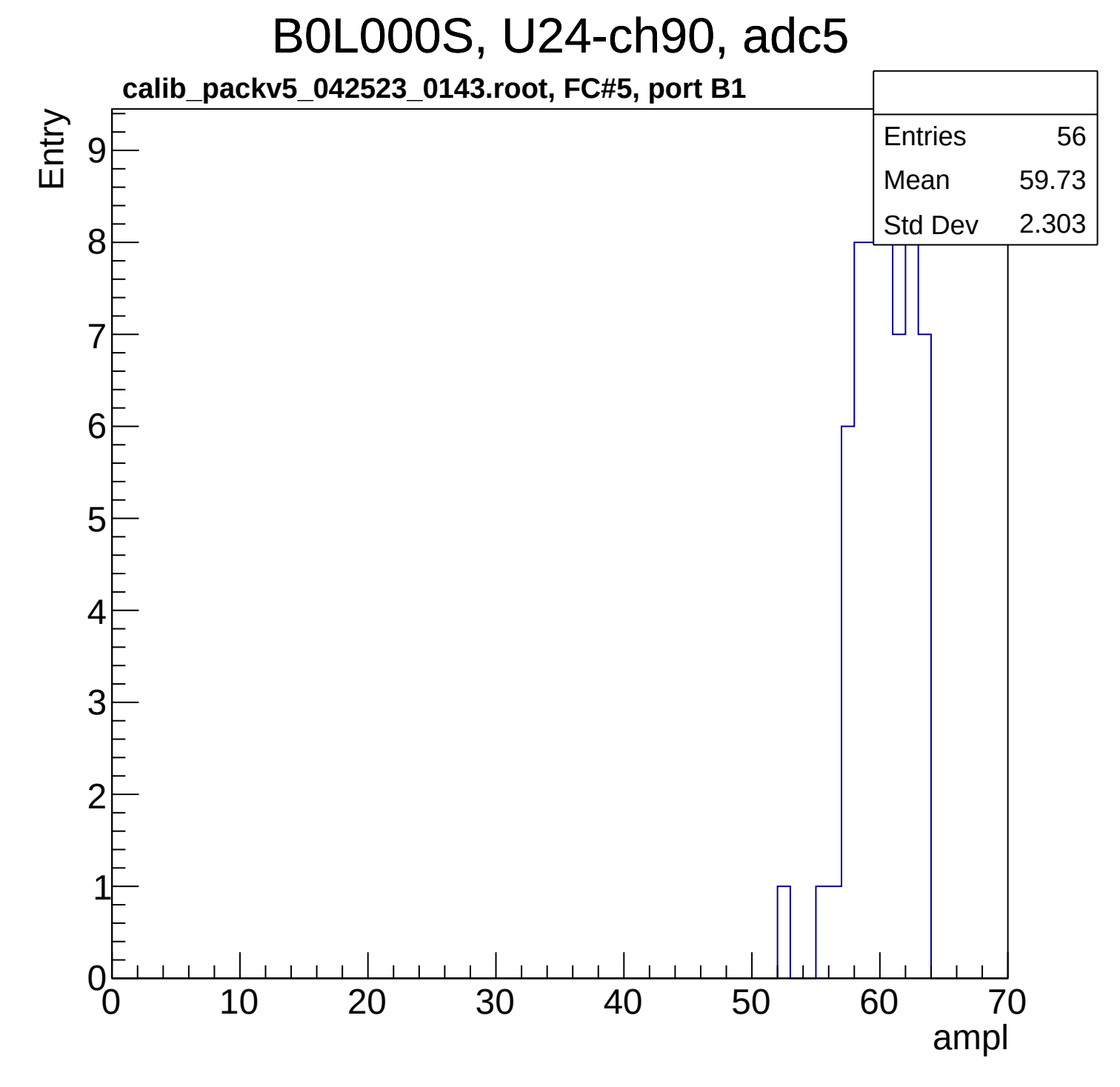

B0L000S, U24-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 53.84
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.73
Std Dev 2.303
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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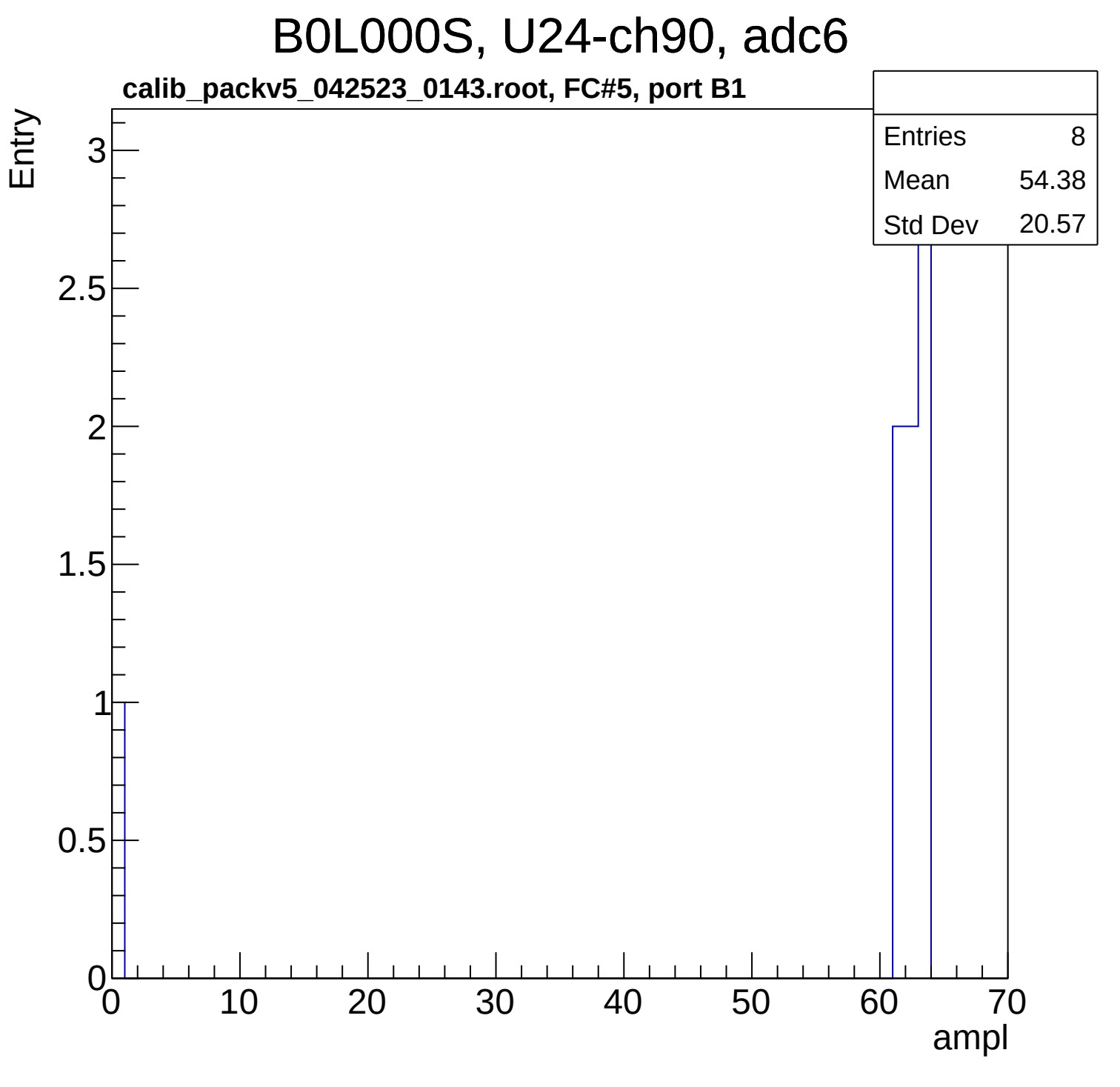
B0L000S, U24-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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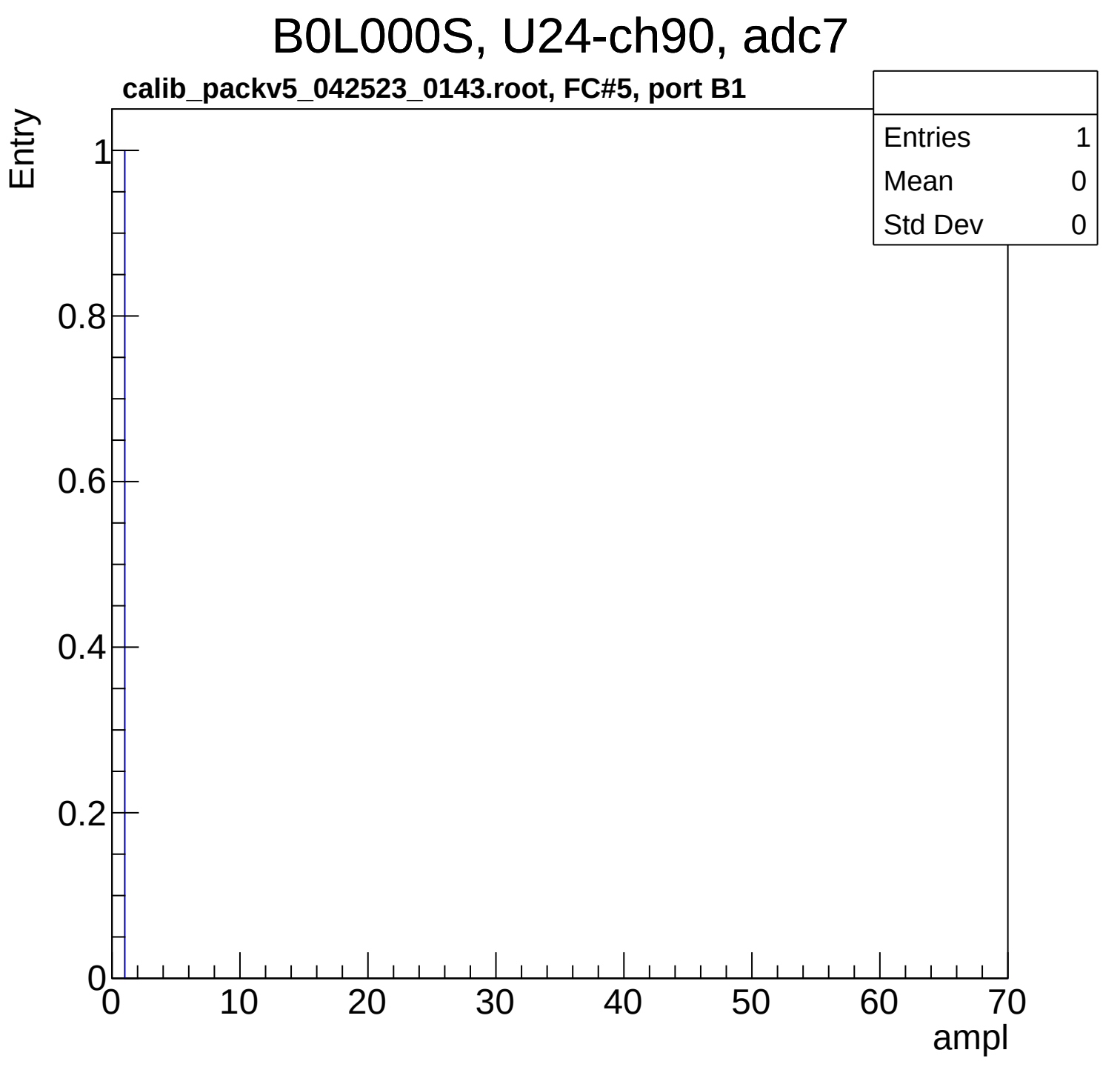
40
50
60
70


B0L000S, U24-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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30
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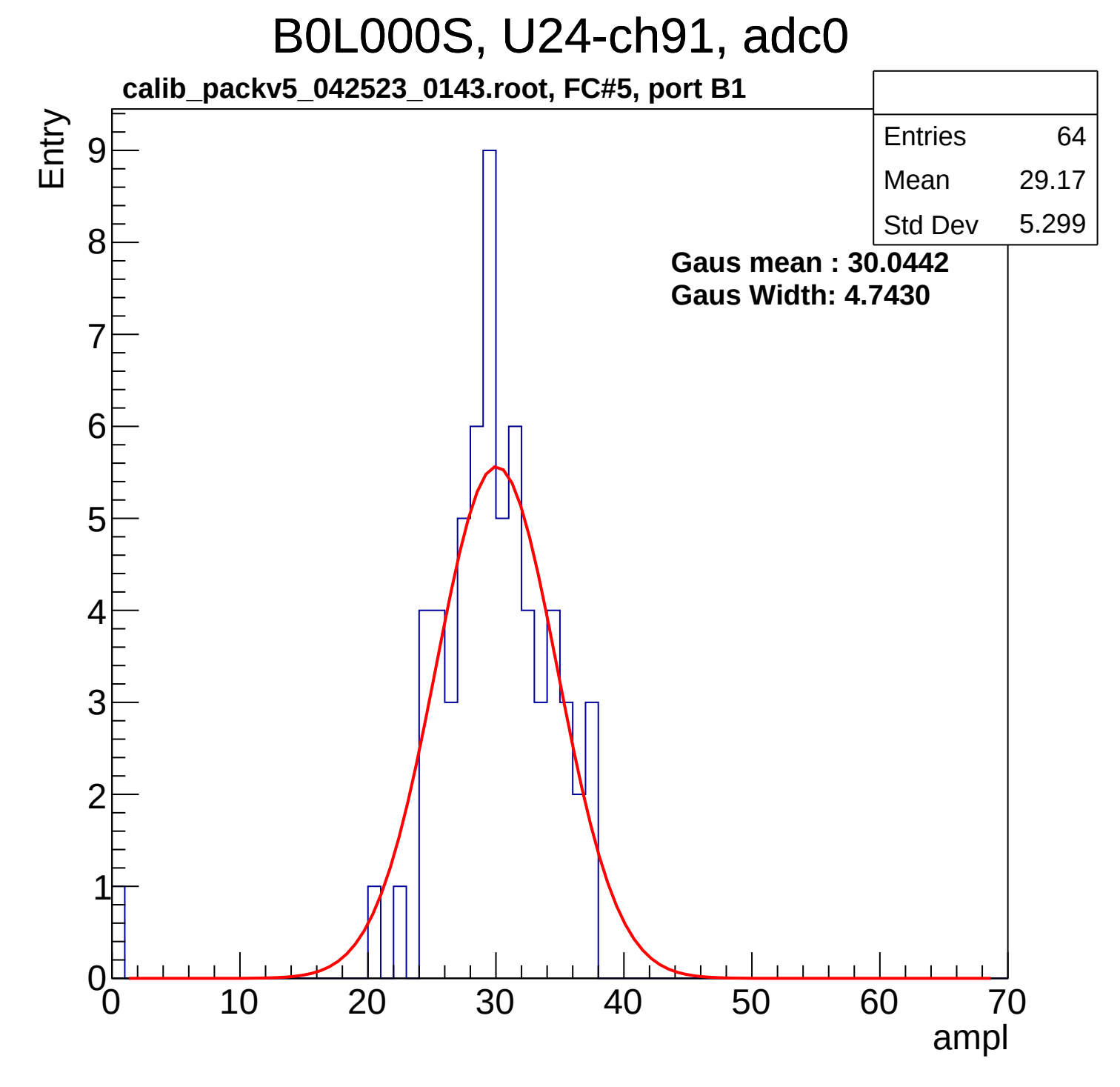
50
60
70
ampl


B0L000S, U24-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.17
Std Dev 5.299
Gaus mean : 30.0442
Gaus Width: 4.7430
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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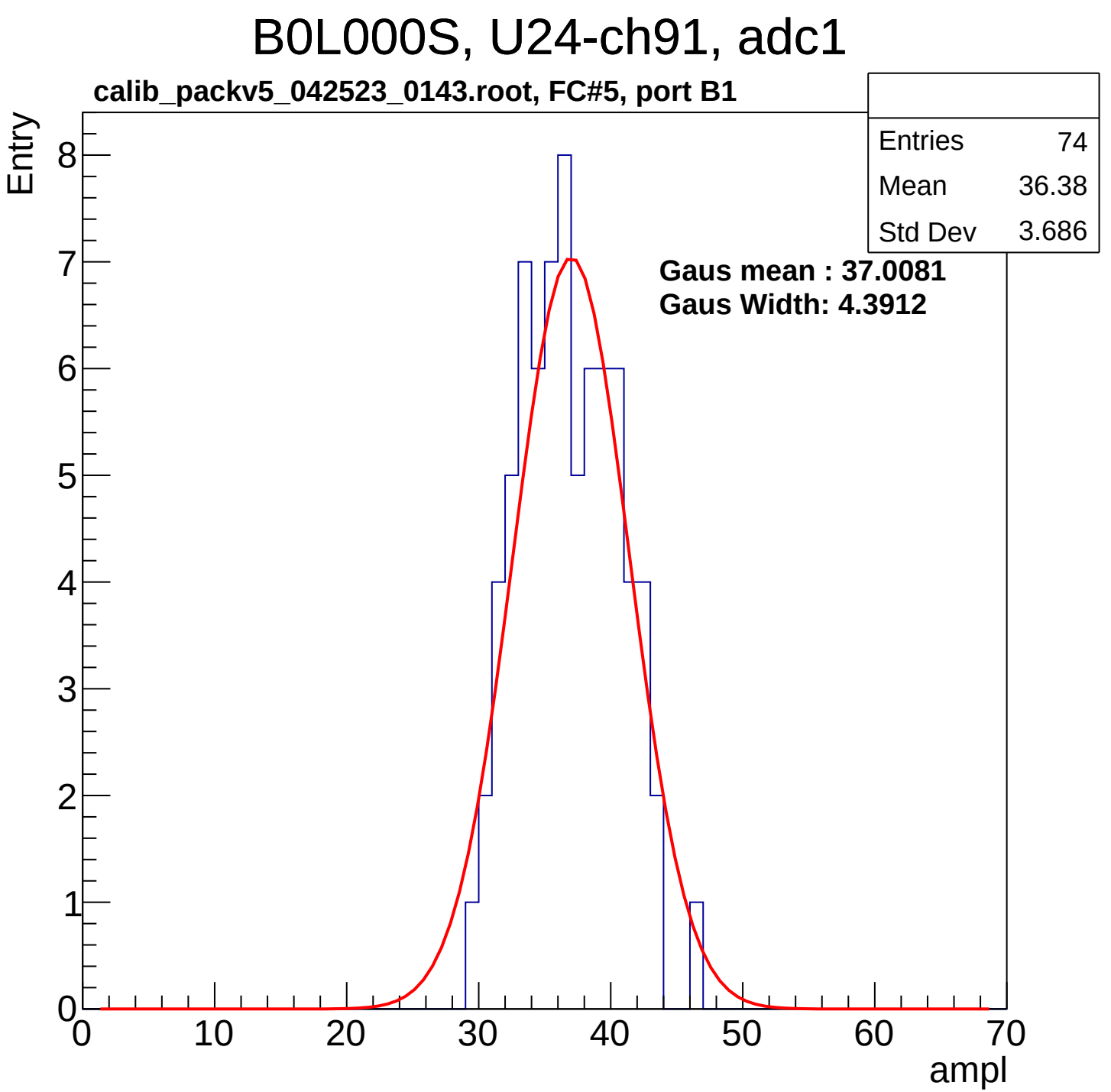
60
70


B0L000S, U24-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.38
Std Dev 3.686
Gaus mean : 37.0081
Gaus Width: 4.3912
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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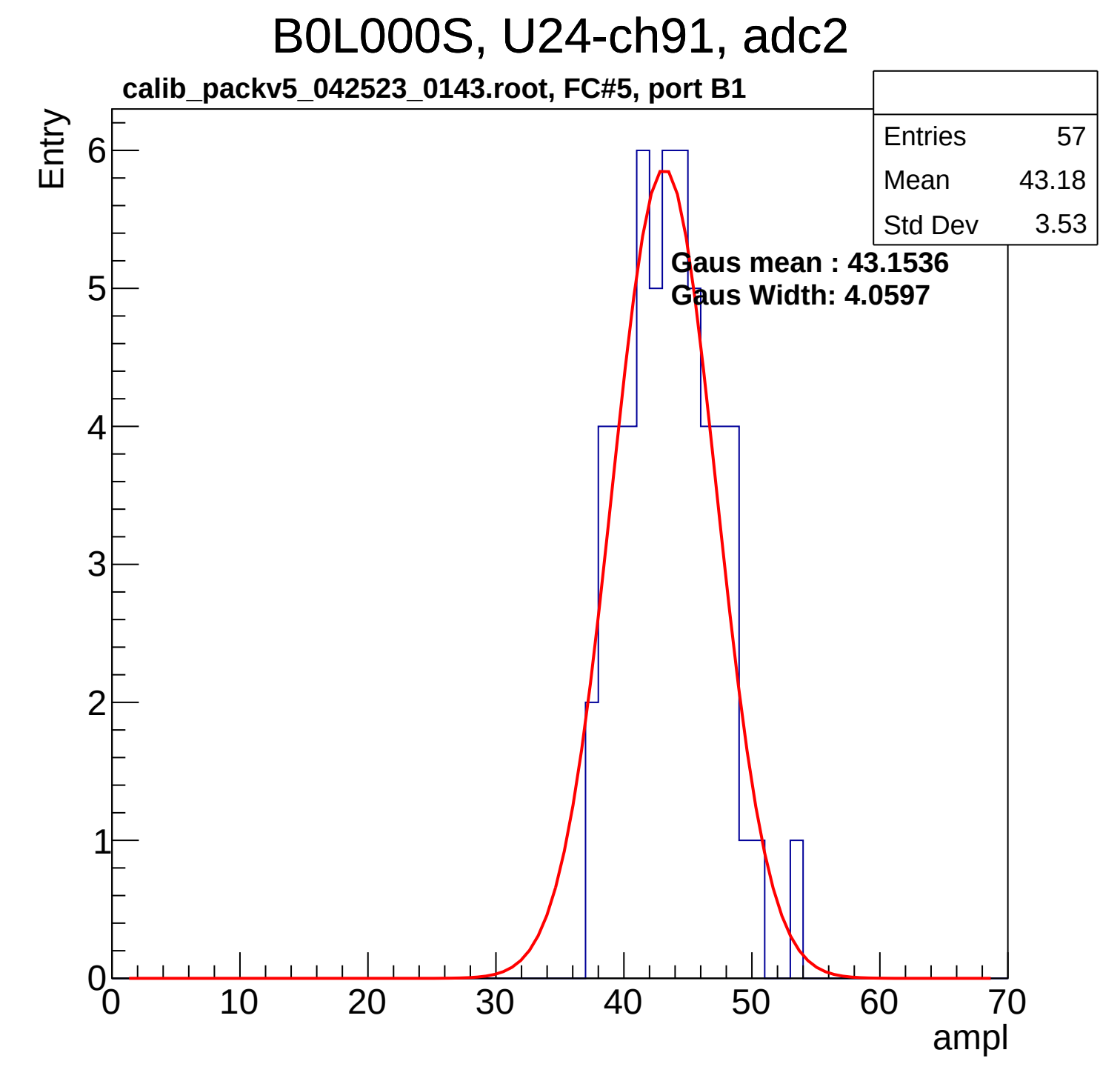
B0L000S, U24-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.18
Std Dev 3.53
Gaus mean : 43.1536
Gaus Width: 4.0597
Entry
ampl
0
1
2
3
4
5
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0
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20
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40
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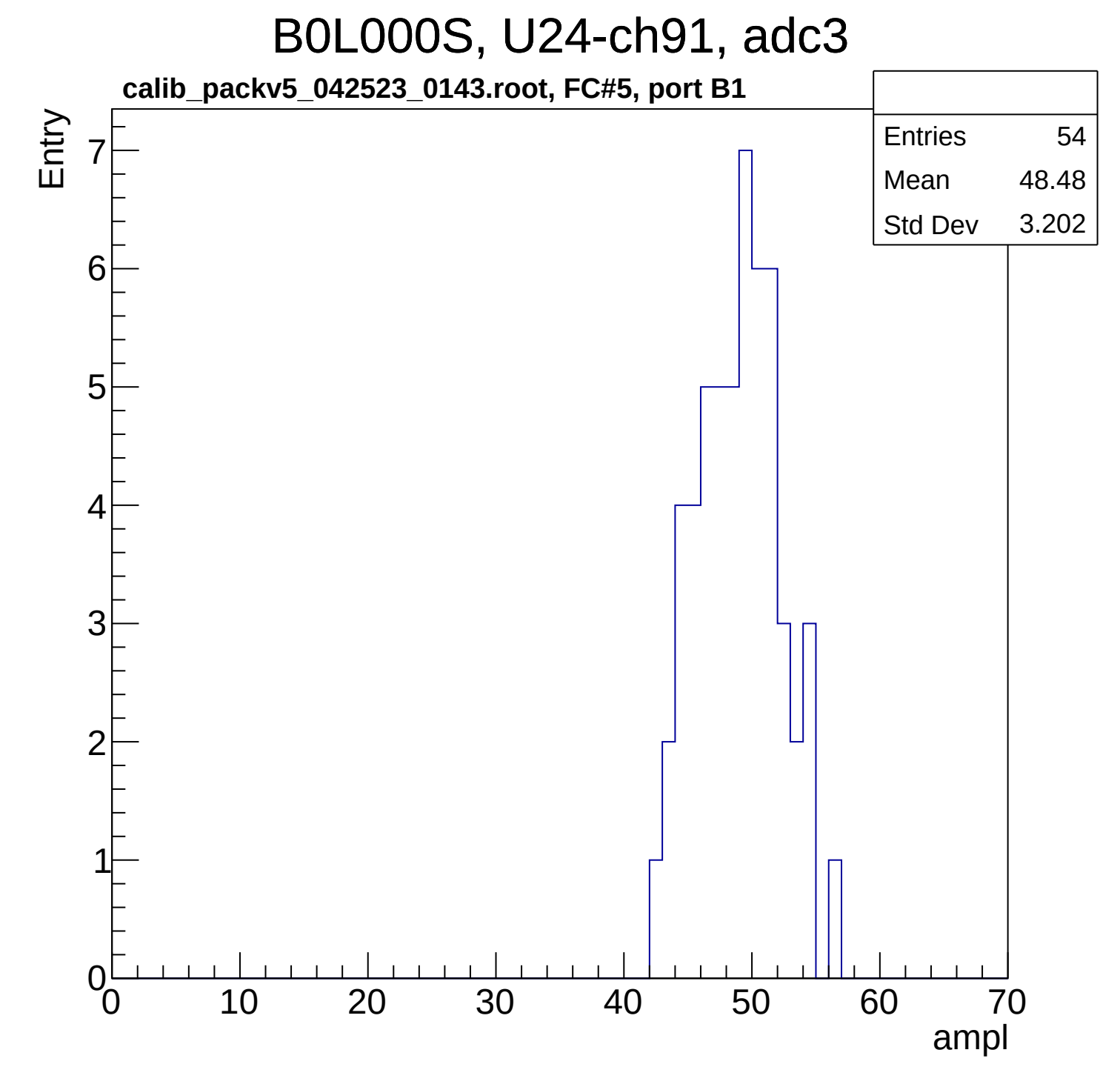
60
70


B0L000S, U24-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 48.48
Std Dev 3.202
Entry
ampl
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1
2
3
4
5
6
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0
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40
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60
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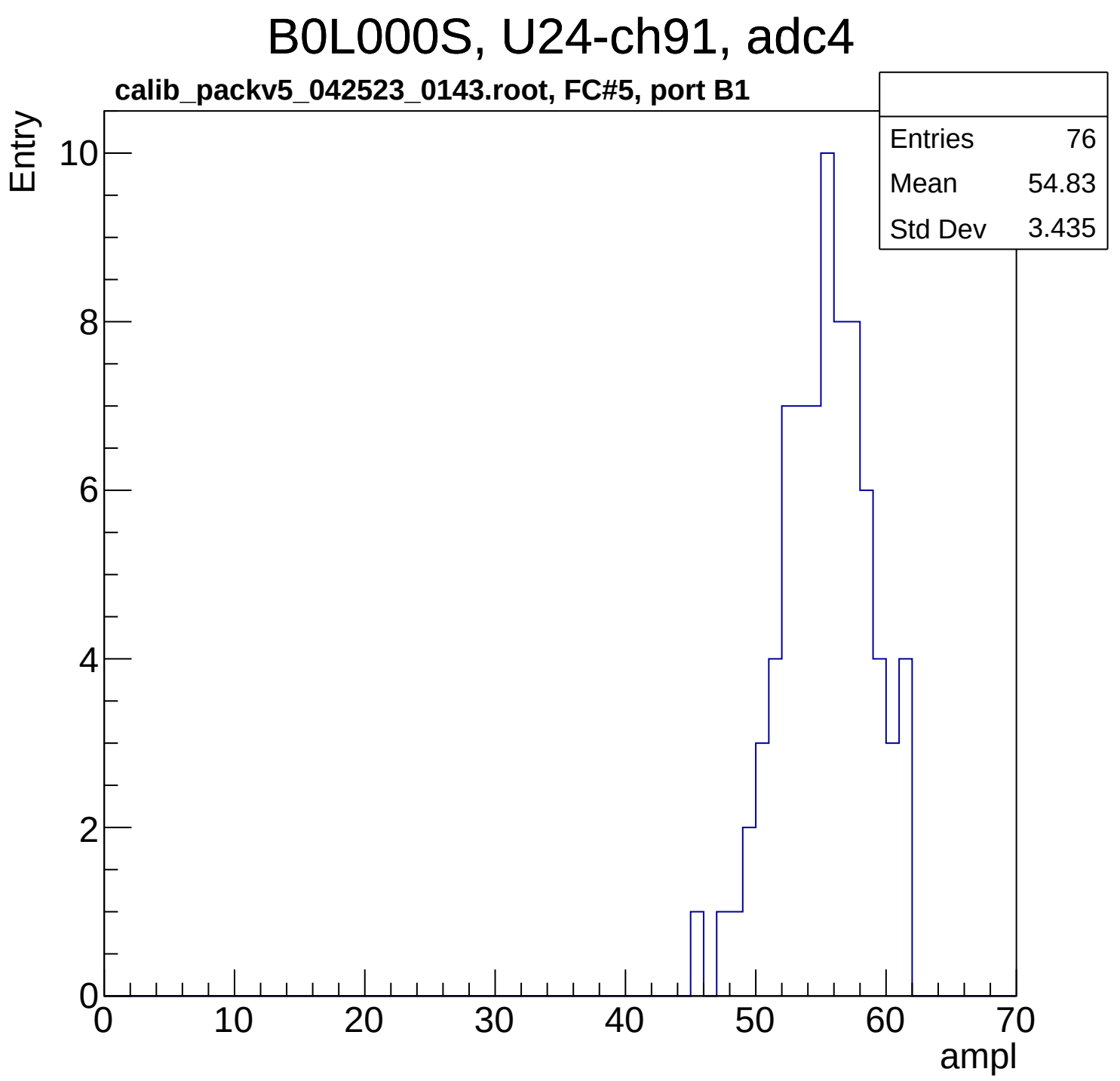
B0L000S, U24-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 54.83
Std Dev 3.435
Entry
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6
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10
0
10
20
30
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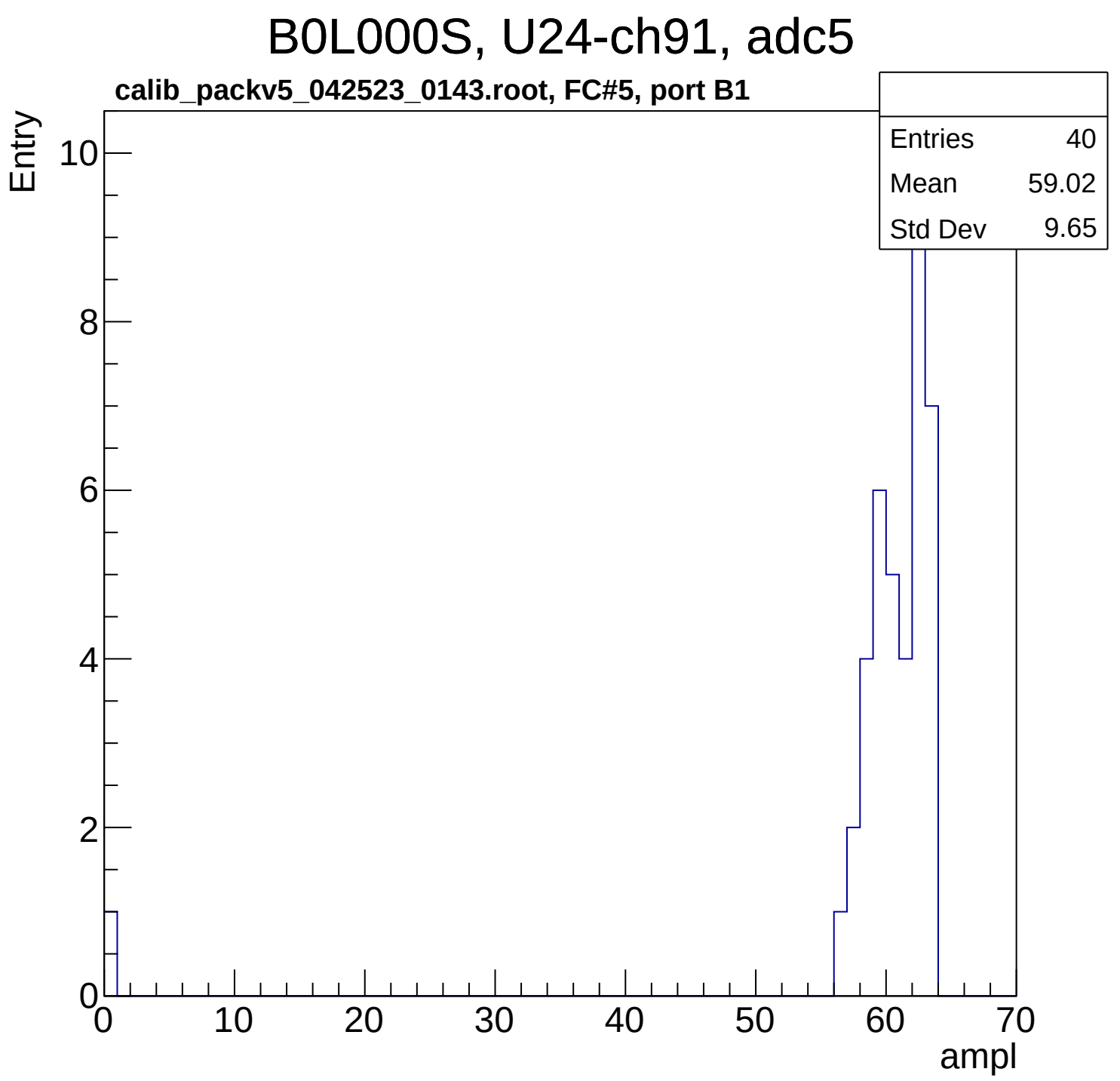
50
60
70
ampl


B0L000S, U24-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.02
Std Dev 9.65
Entry
ampl
0
2
4
6
8
10
0
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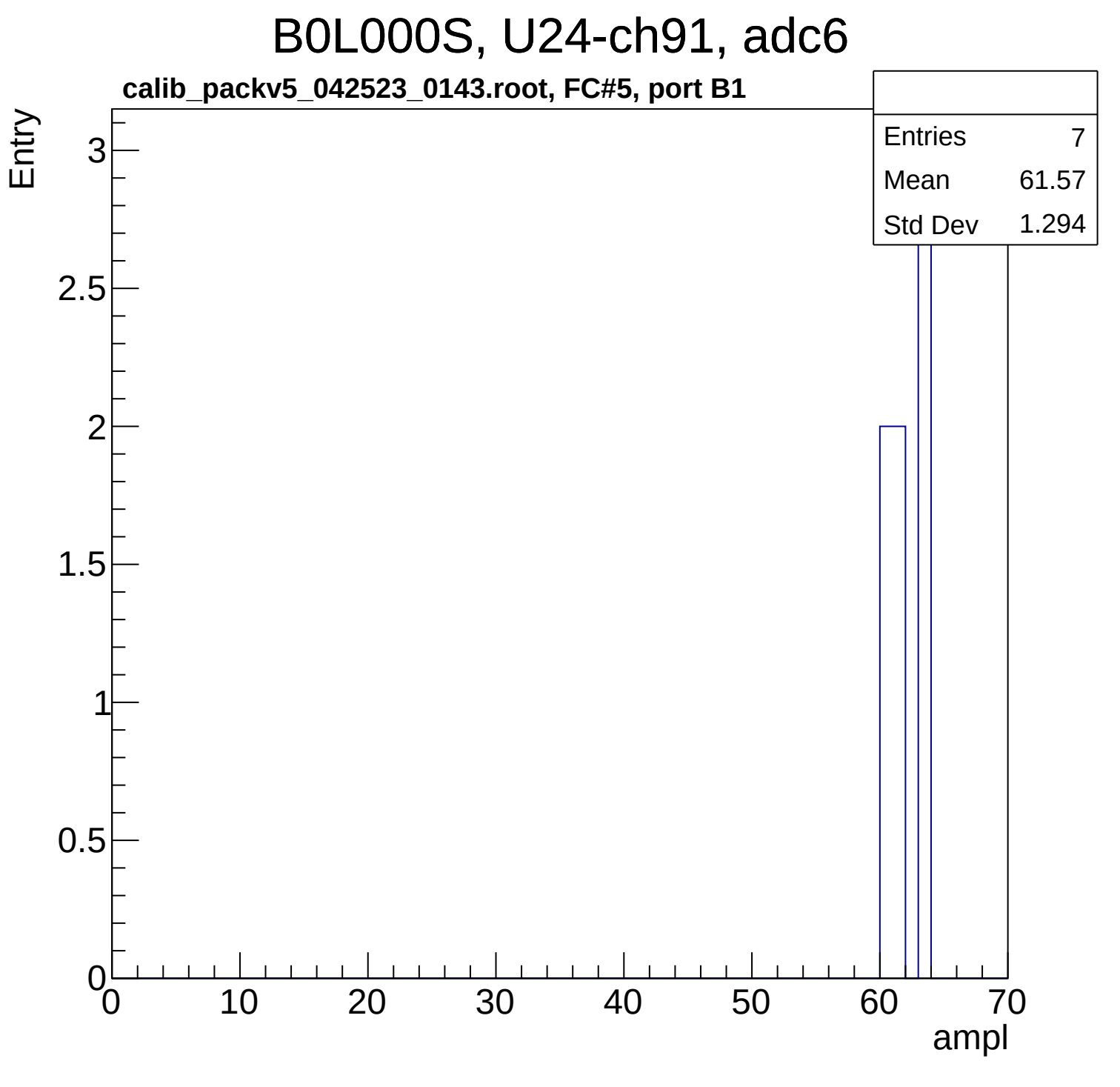
20
30
40
50
60
70


B0L000S, U24-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.57
Std Dev 1.294
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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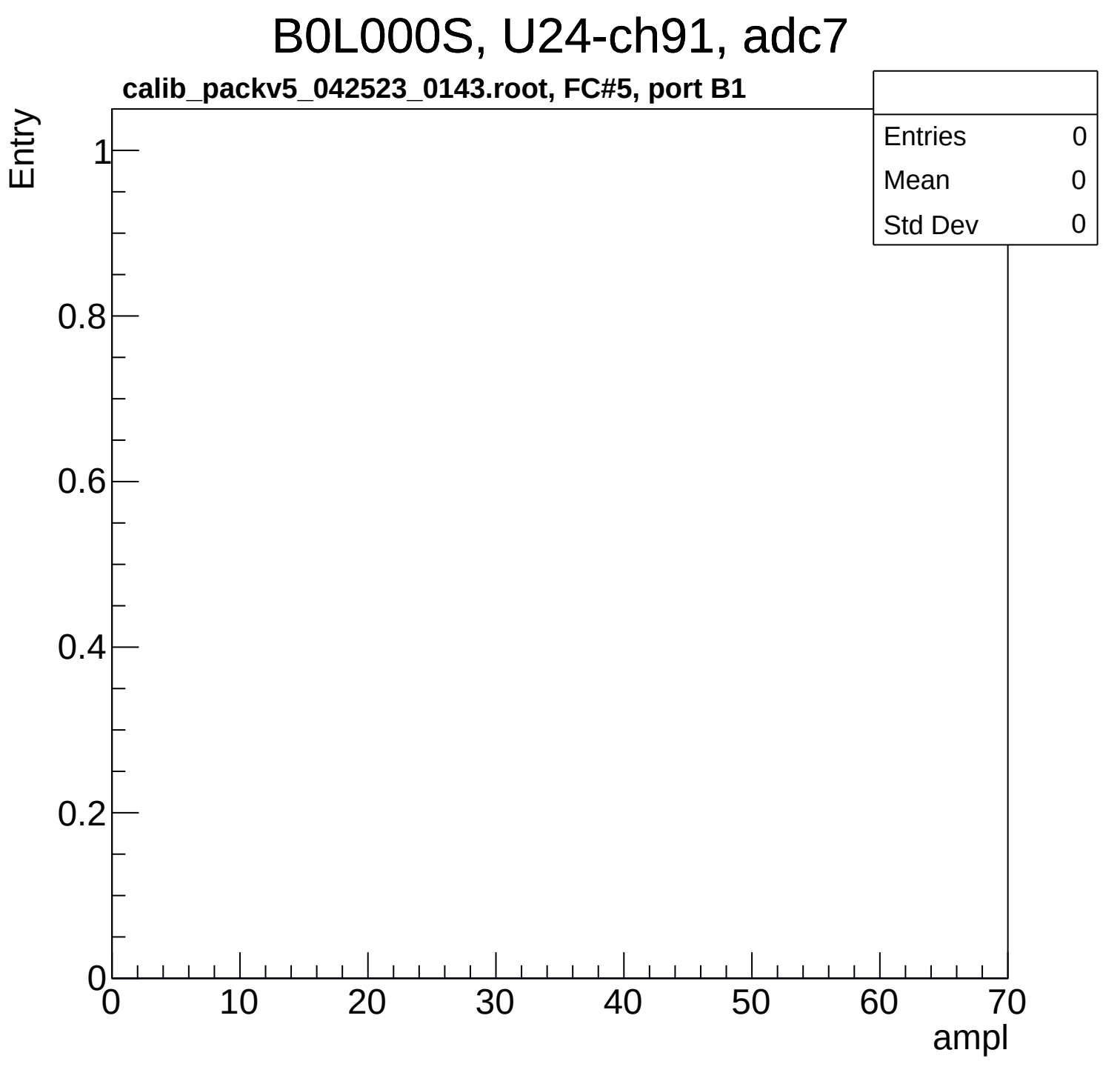
B0L000S, U24-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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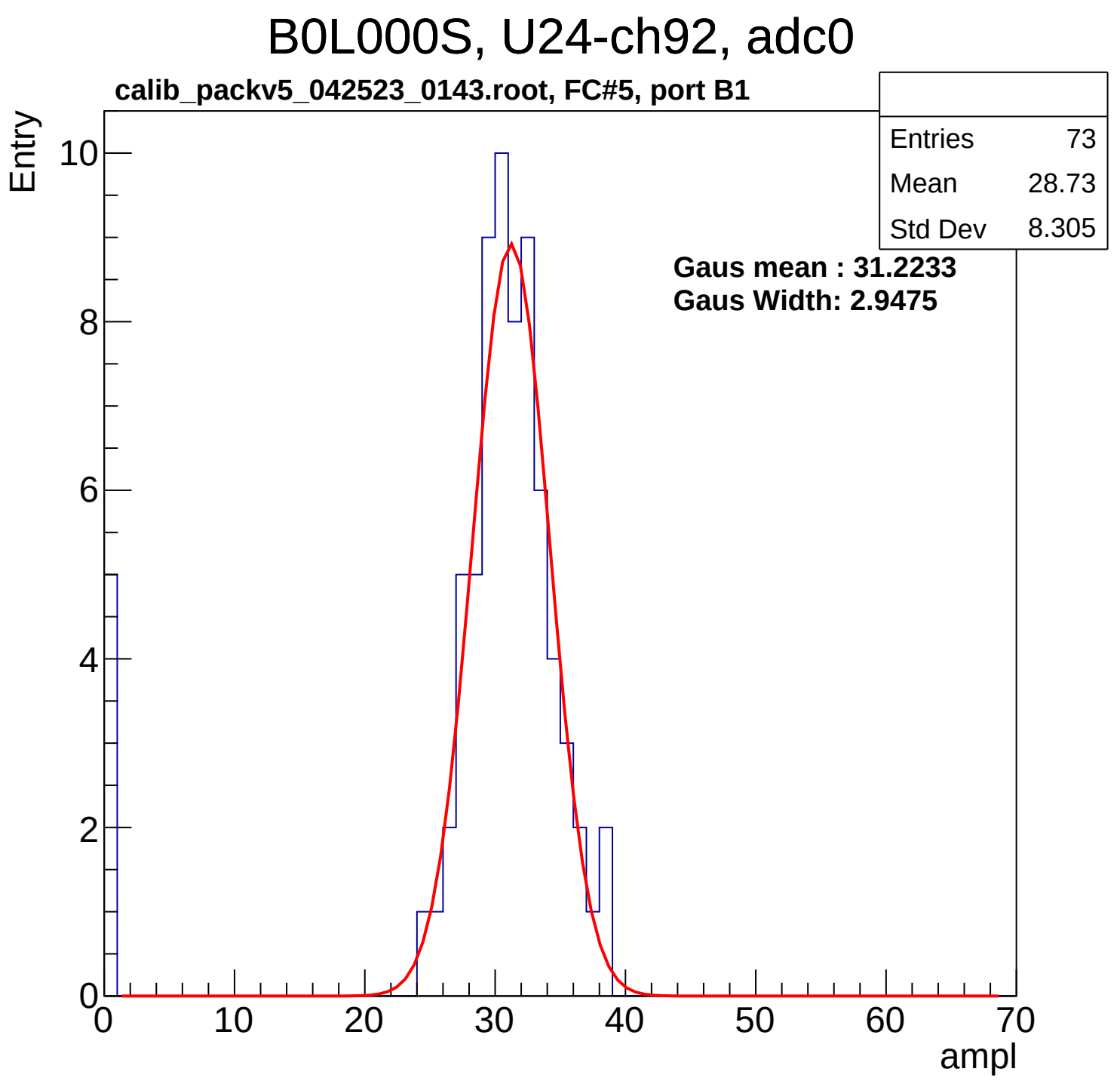
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.73
Std Dev 8.305
Gaus mean : 31.2233
Gaus Width: 2.9475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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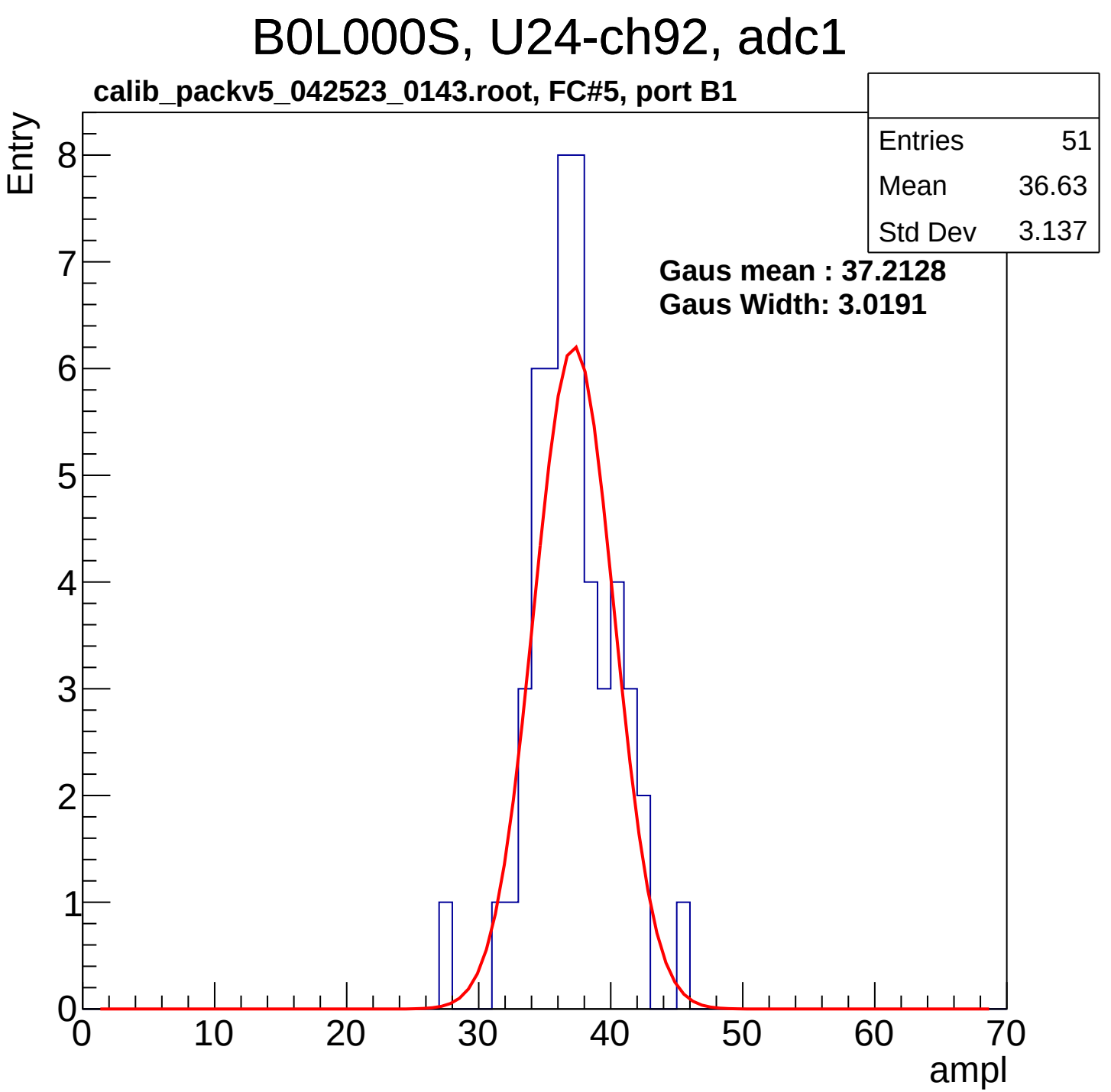
B0L000S, U24-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 36.63
Std Dev 3.137
Gaus mean : 37.2128
Gaus Width: 3.0191
Entry
ampl
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2
3
4
5
6
7
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0
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20
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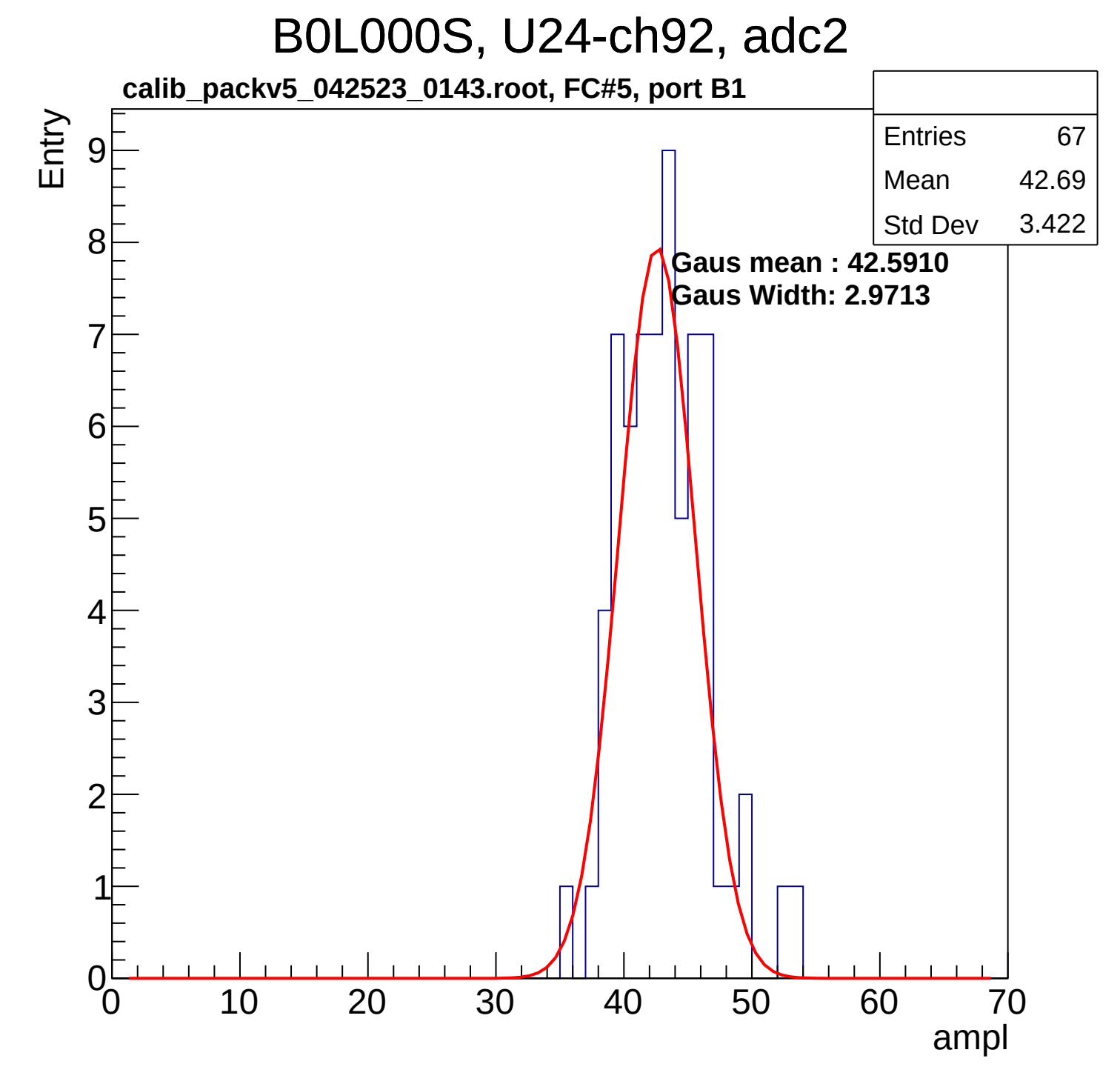
B0L000S, U24-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.69
Std Dev 3.422
Gaus mean : 42.5910
Gaus Width: 2.9713
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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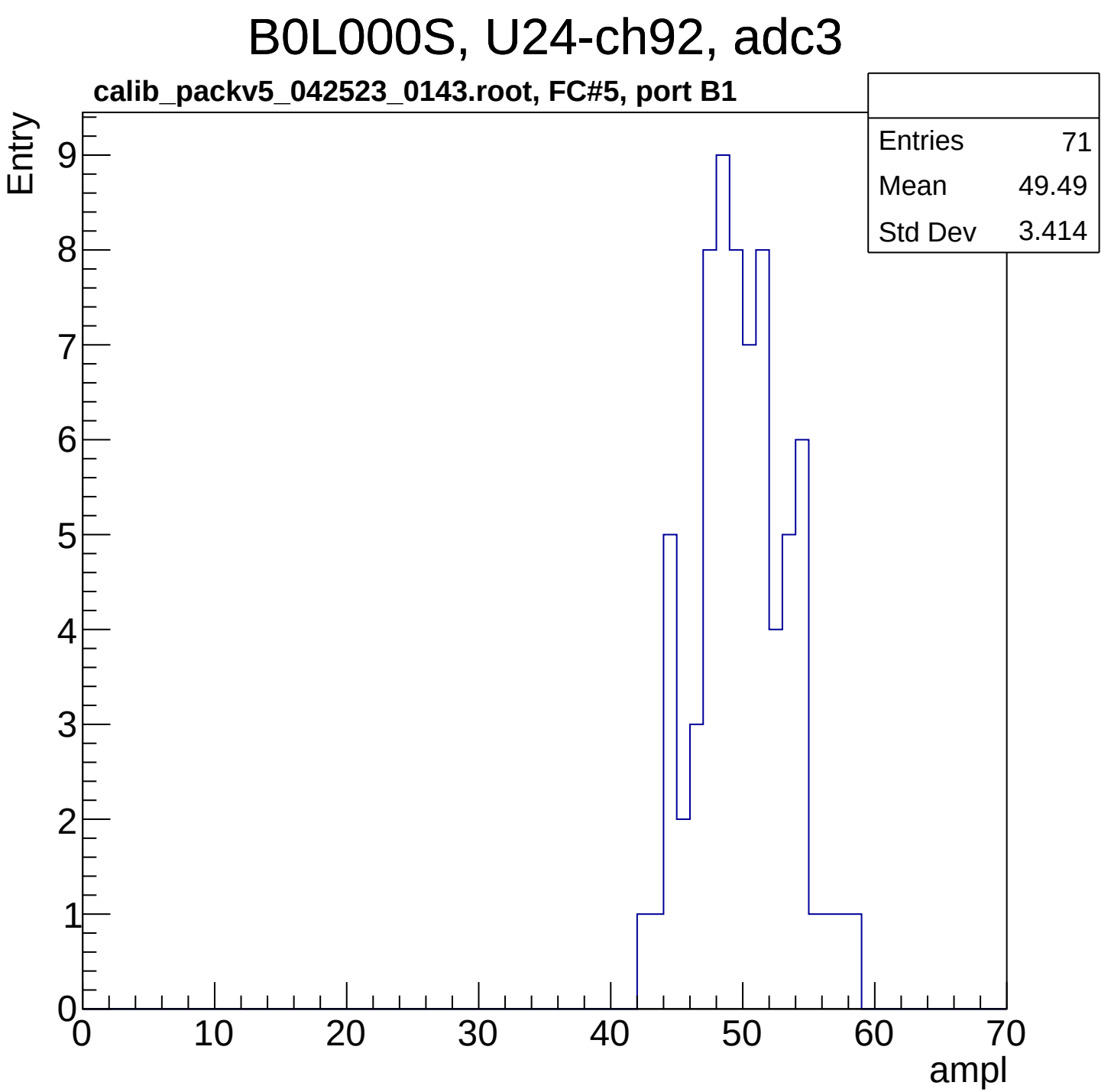
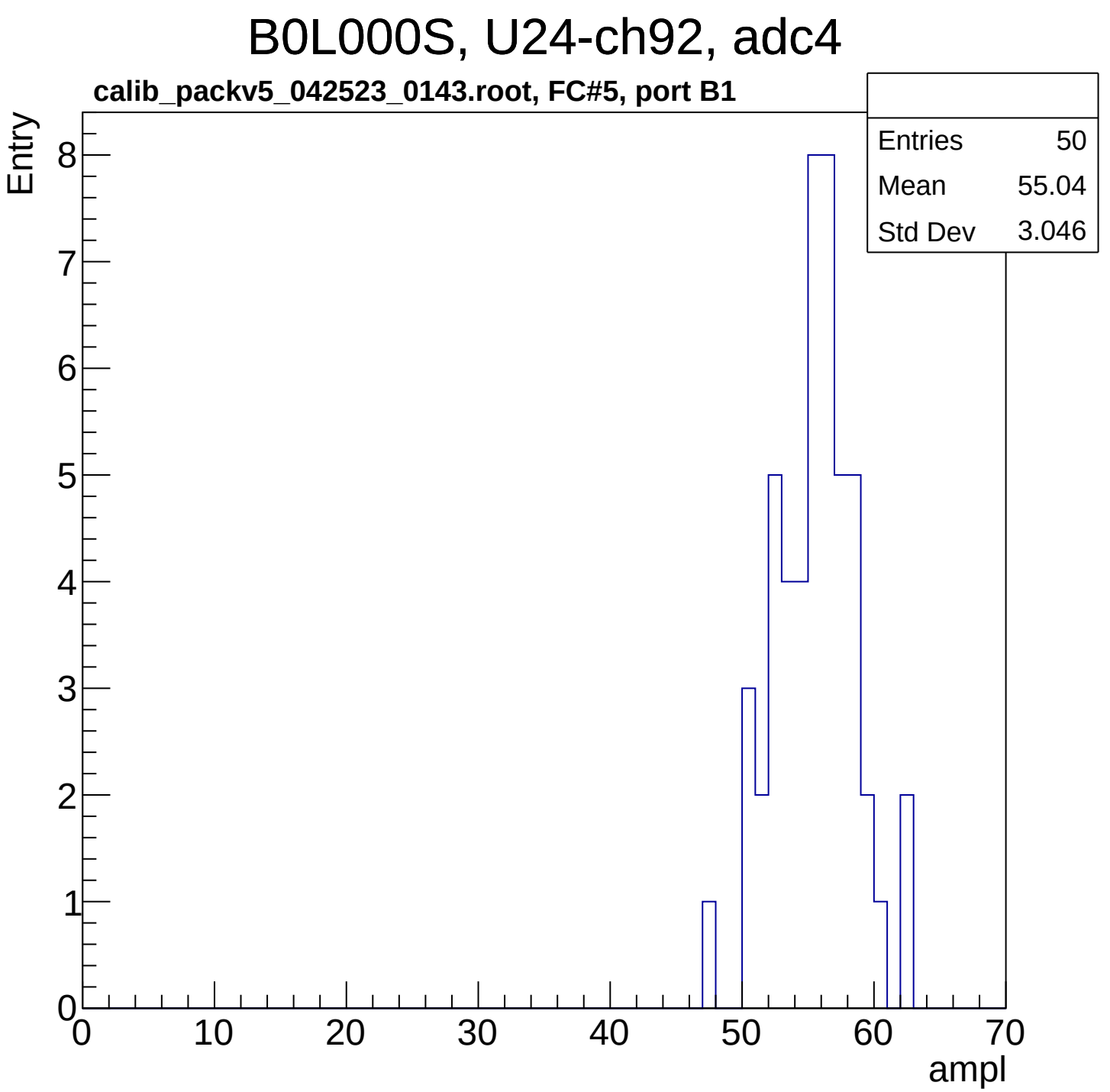
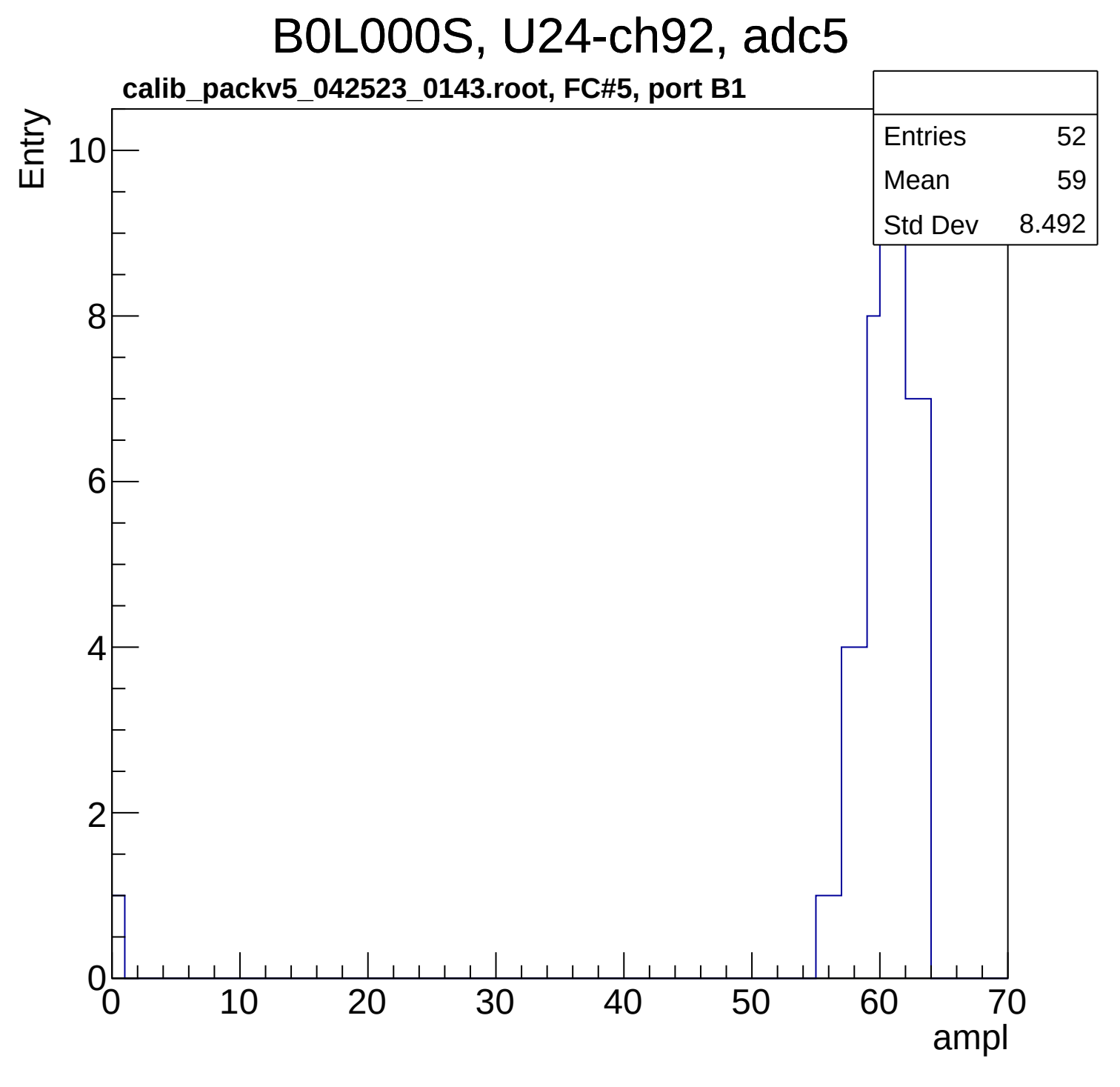
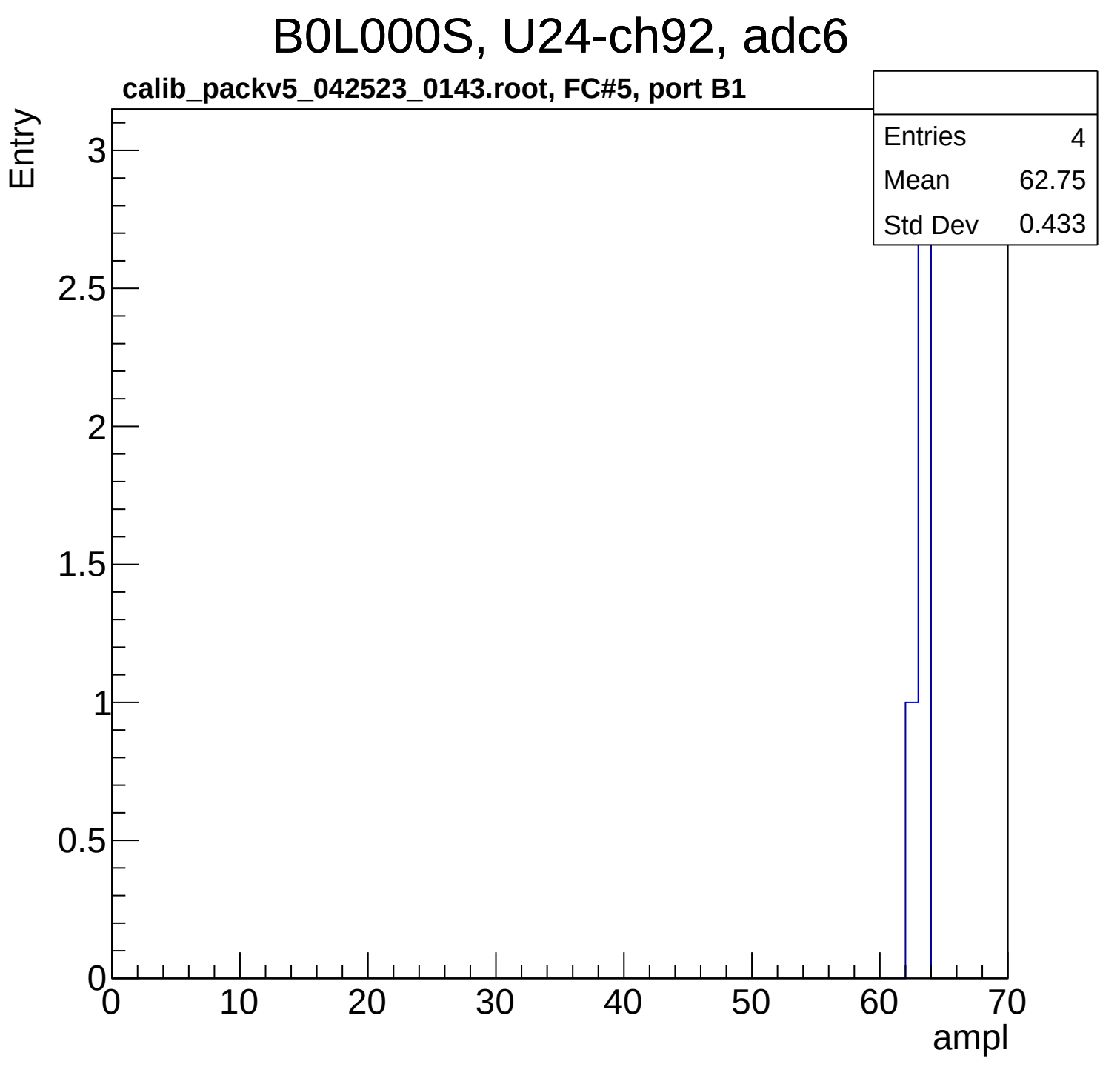
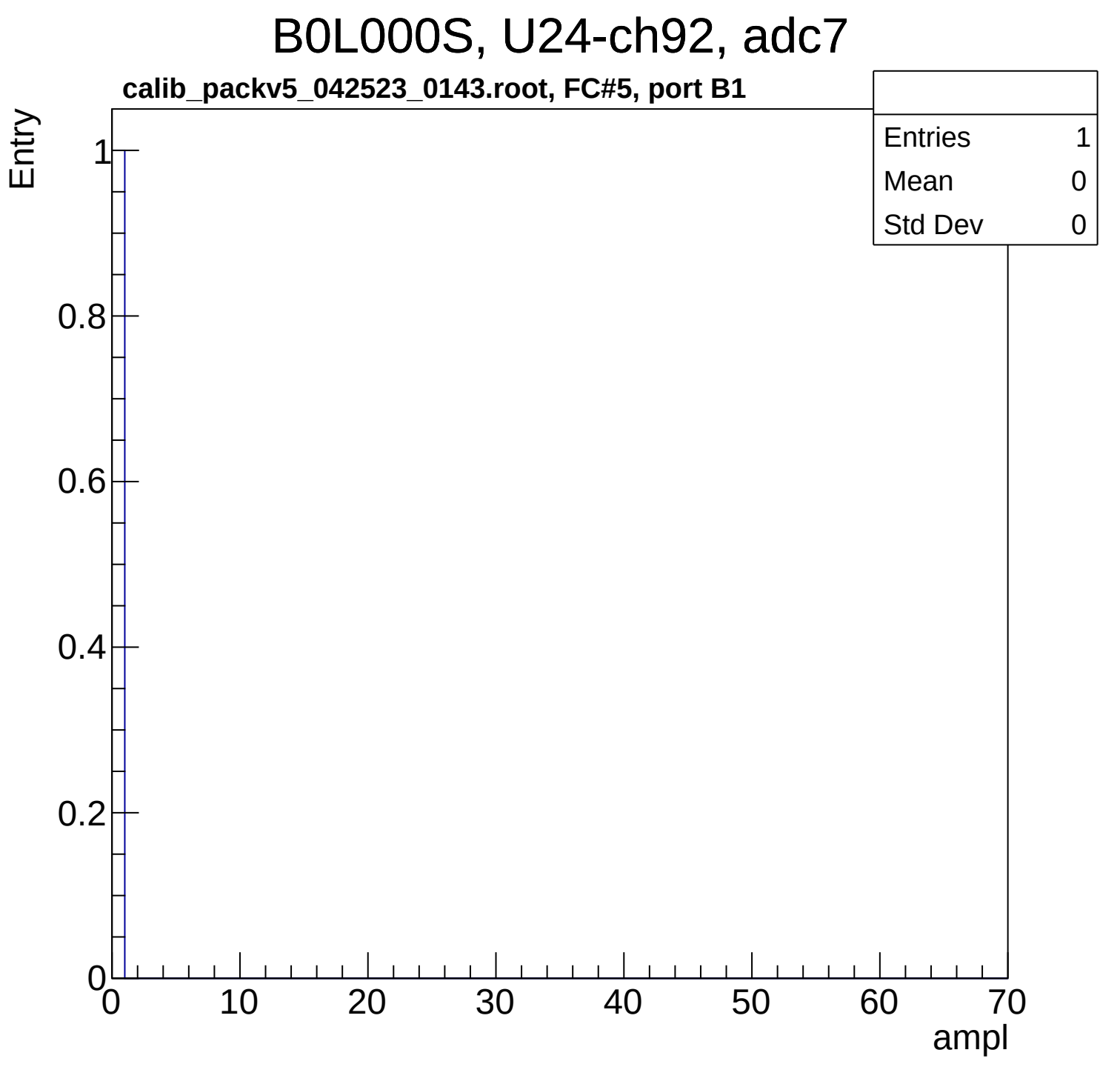
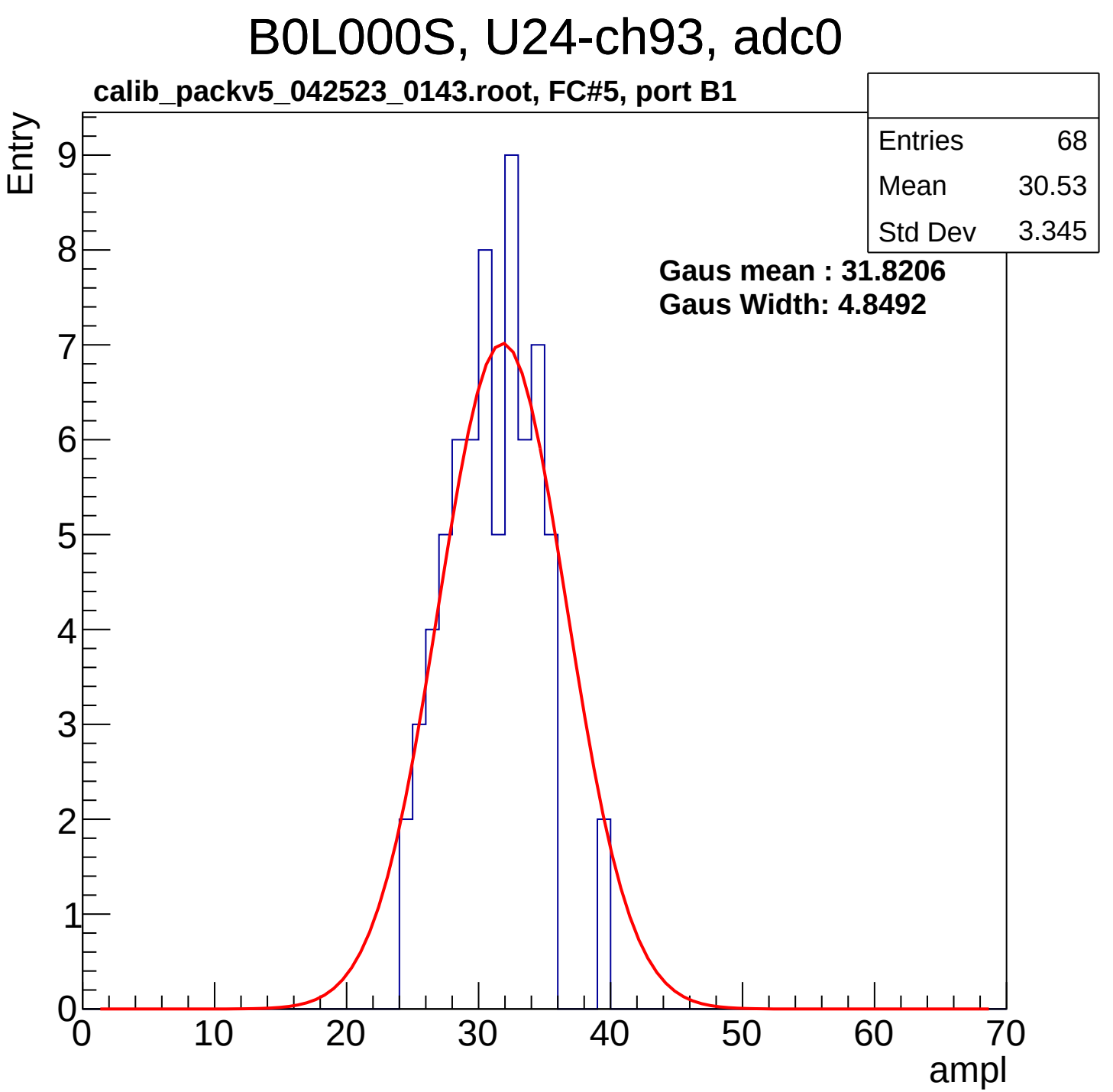

B0L000S, U24-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 49.49
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

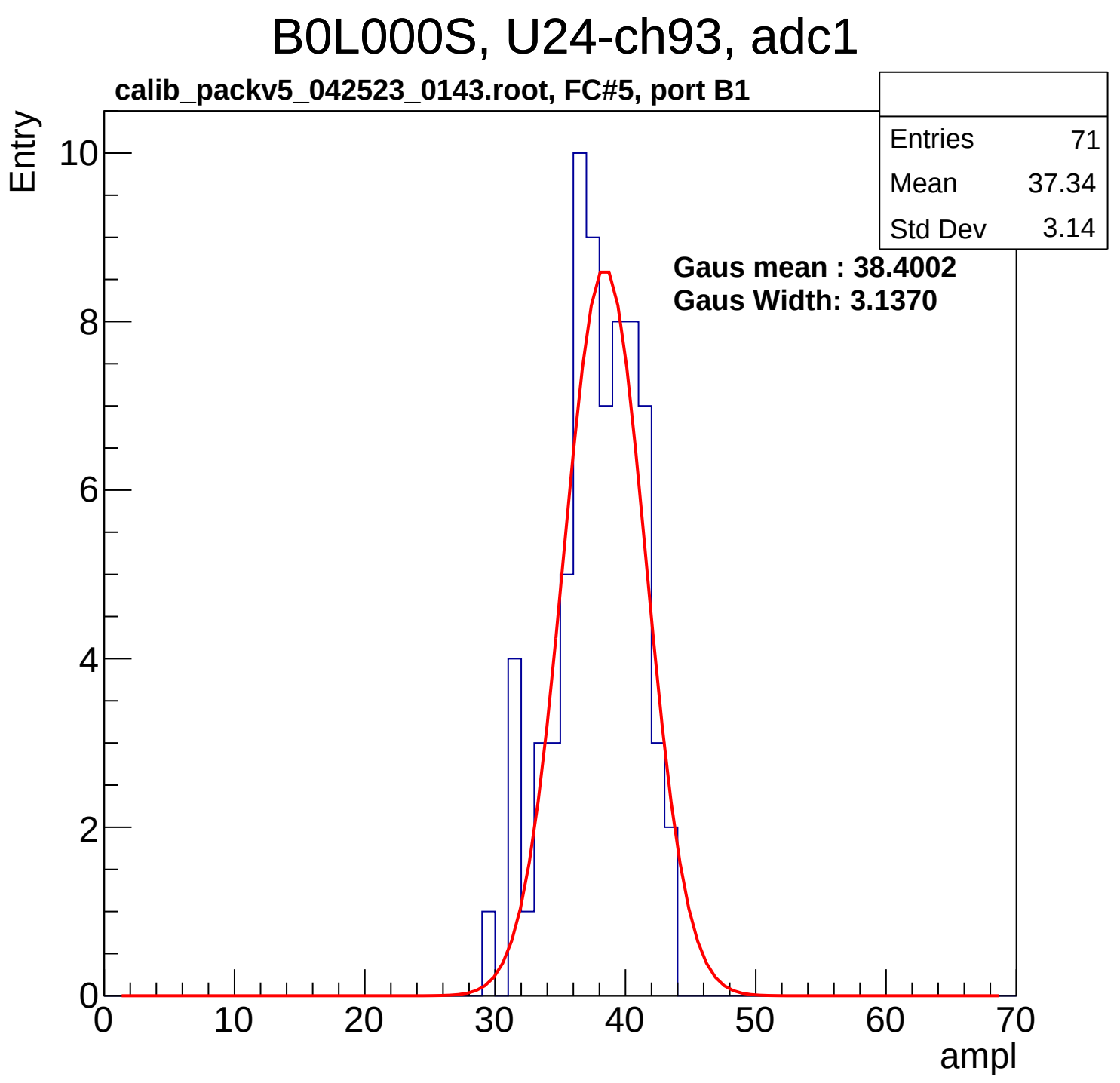

B0L000S, U24-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.04
Std Dev 3.046
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59
Std Dev 8.492
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.53
Std Dev 3.345
Gaus mean : 31.8206
Gaus Width: 4.8492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.34
Std Dev 3.14
Gaus mean : 38.4002
Gaus Width: 3.1370
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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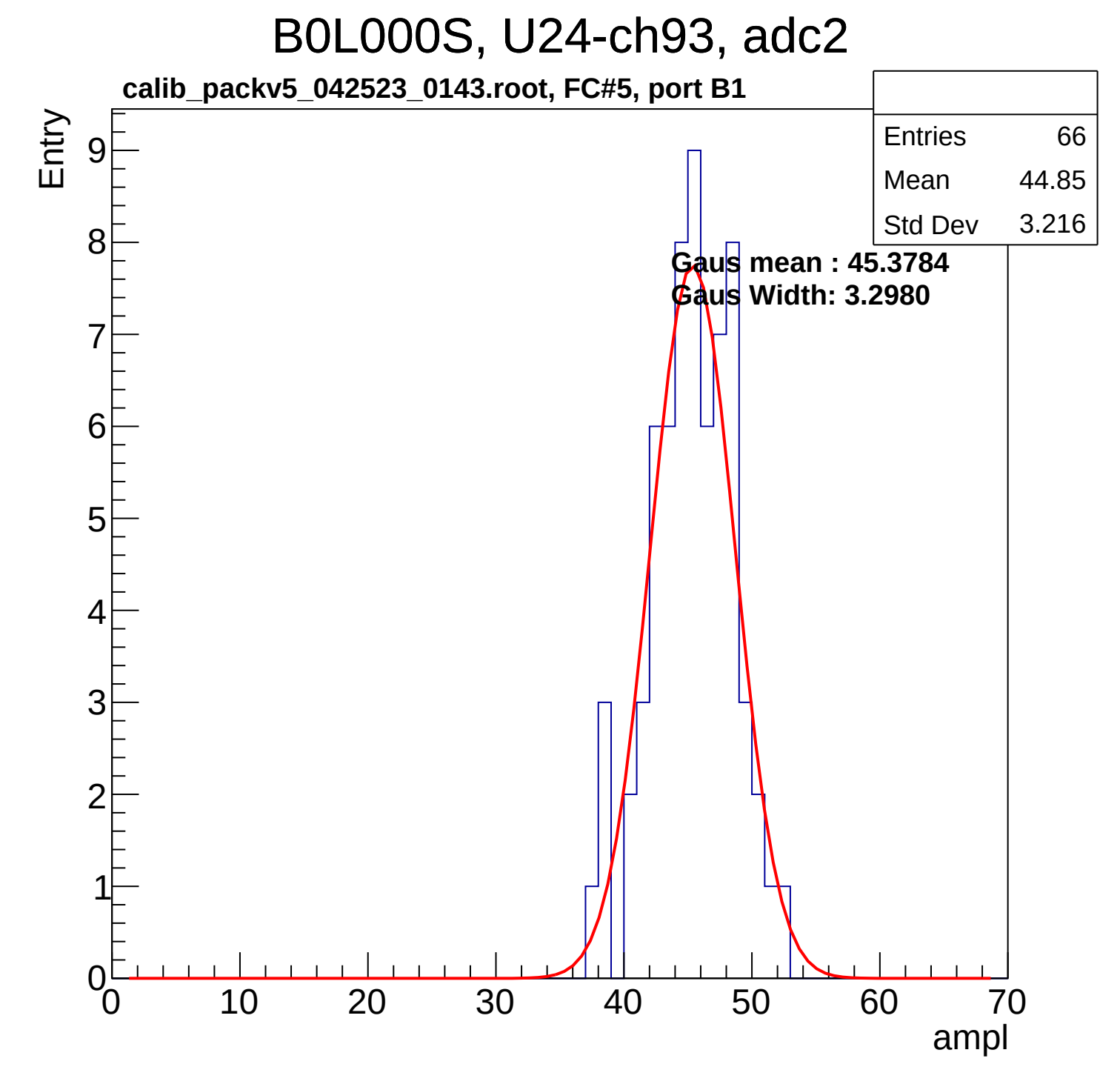
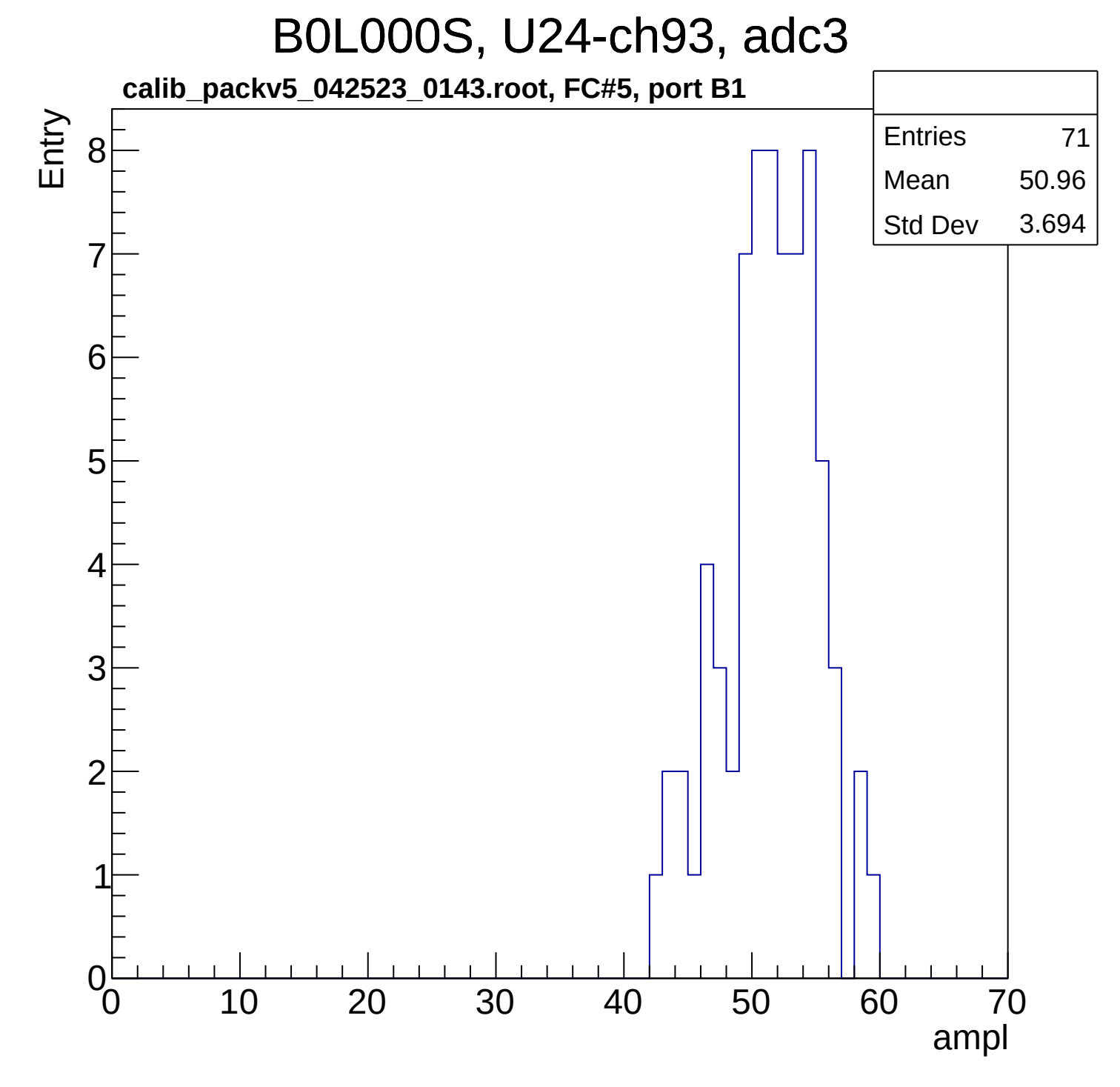
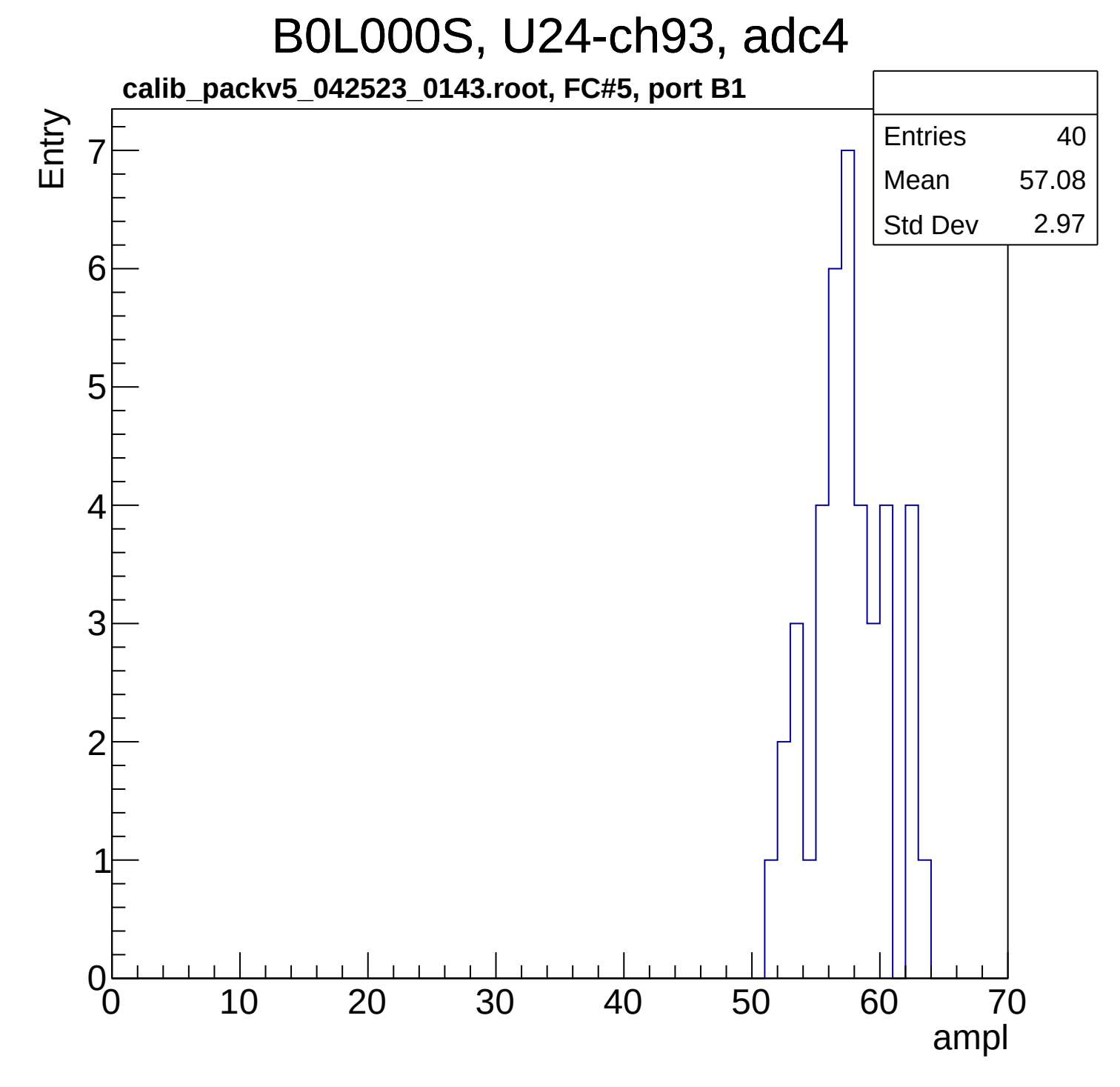
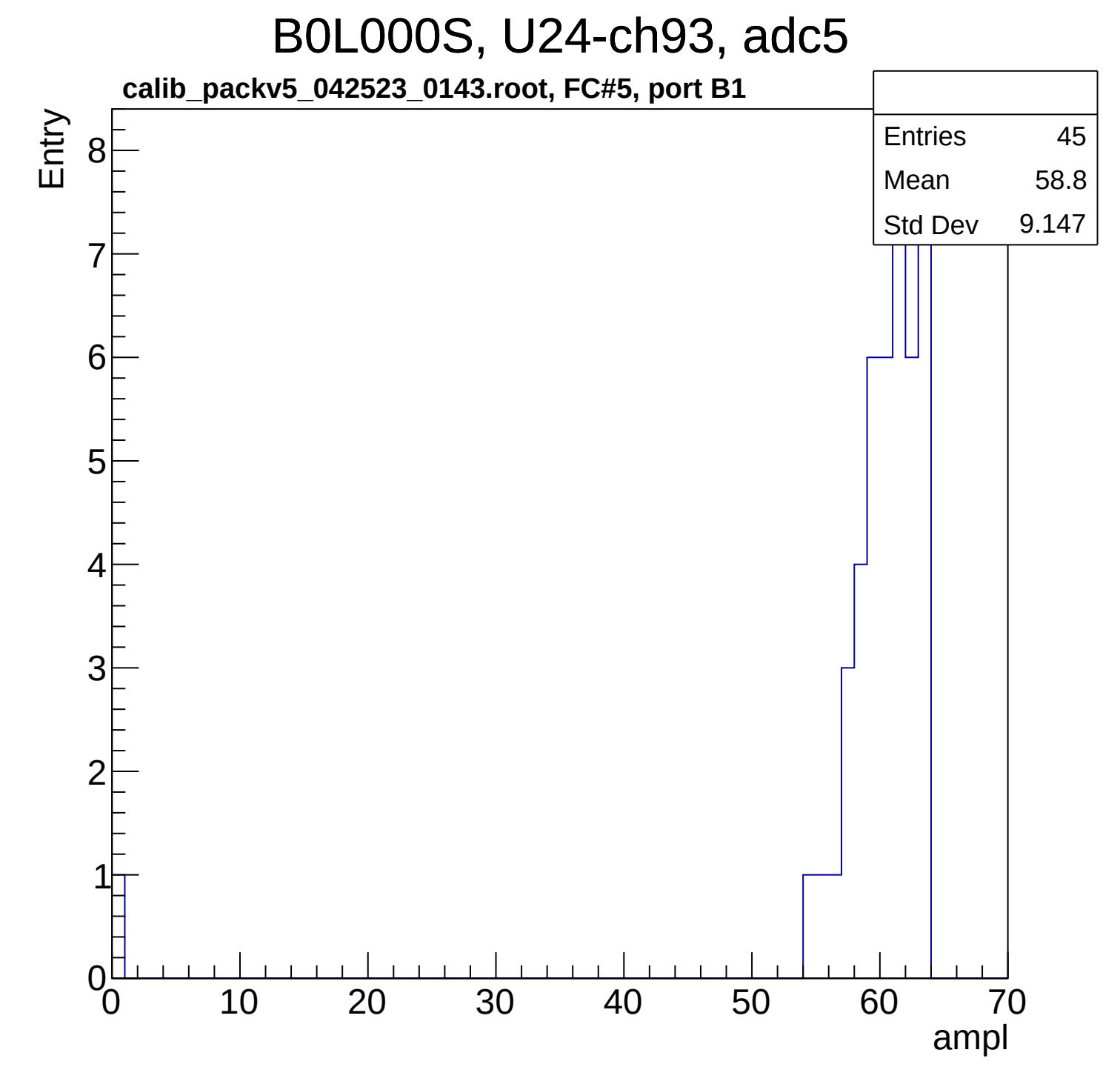

B0L000S, U24-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 44.85
Std Dev 3.216
Gaus mean : 45.3784
Gaus Width: 3.2980
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U24-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.96
Std Dev 3.694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 57.08
Std Dev 2.97
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.8
Std Dev 9.147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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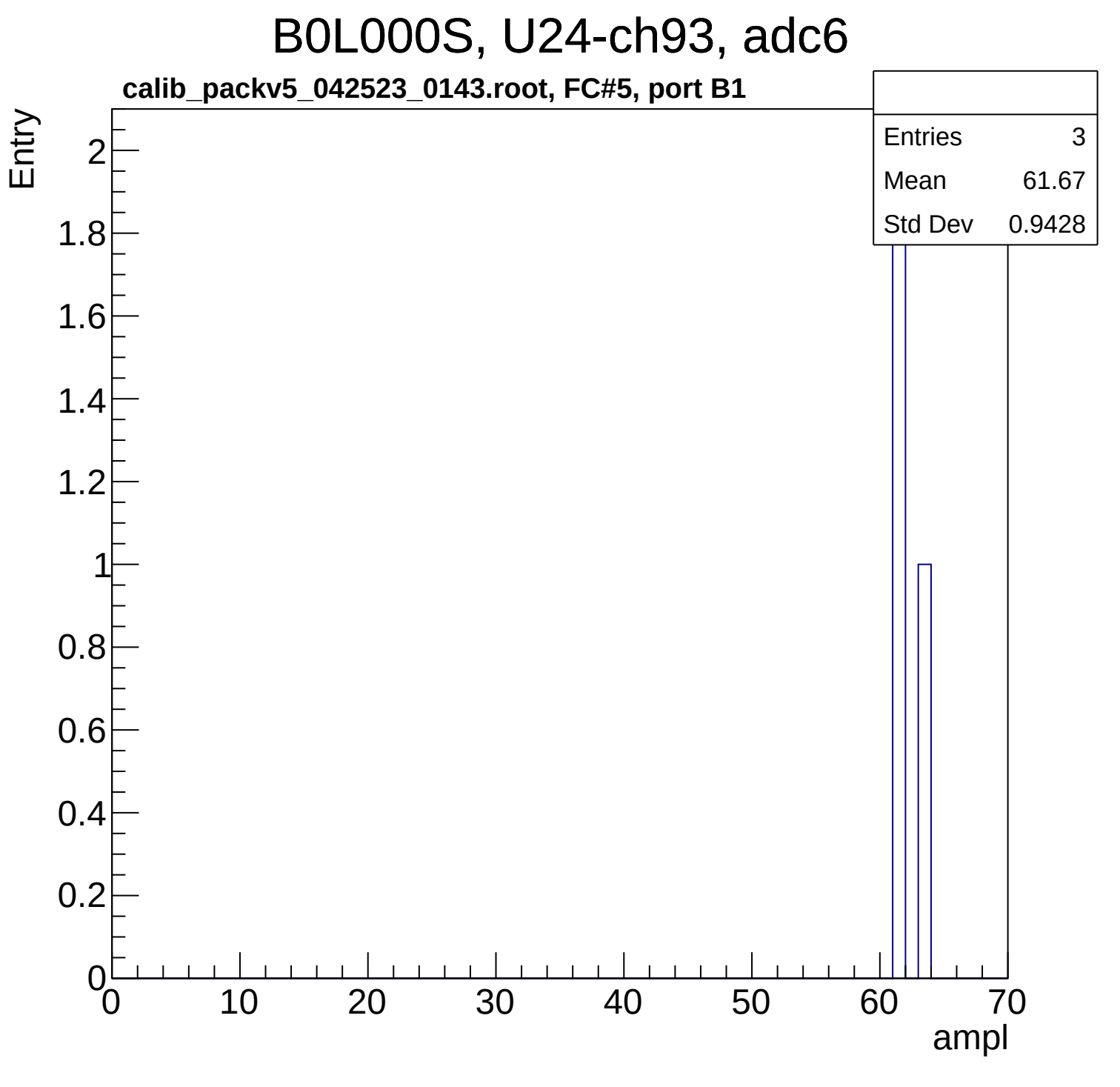
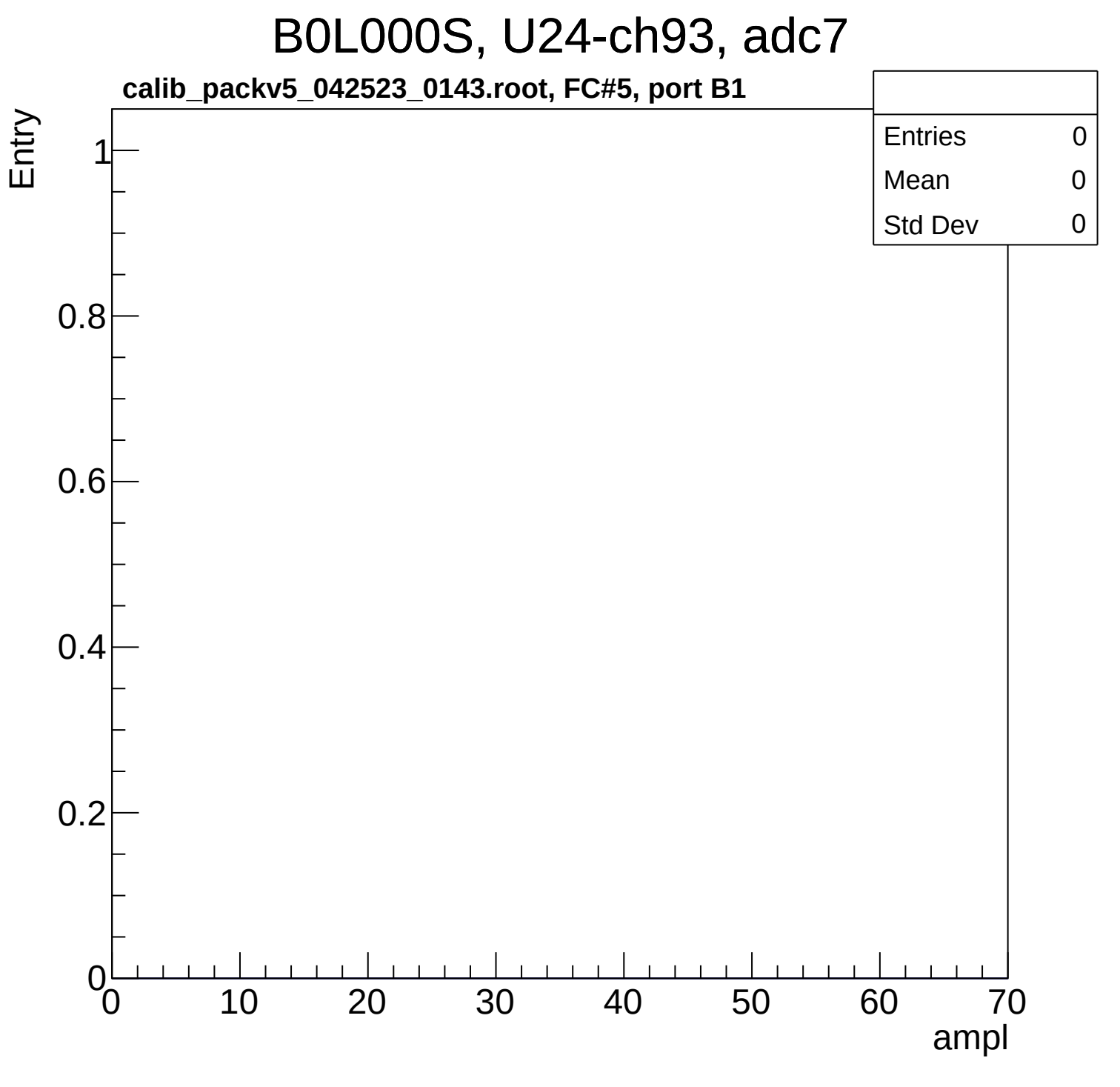
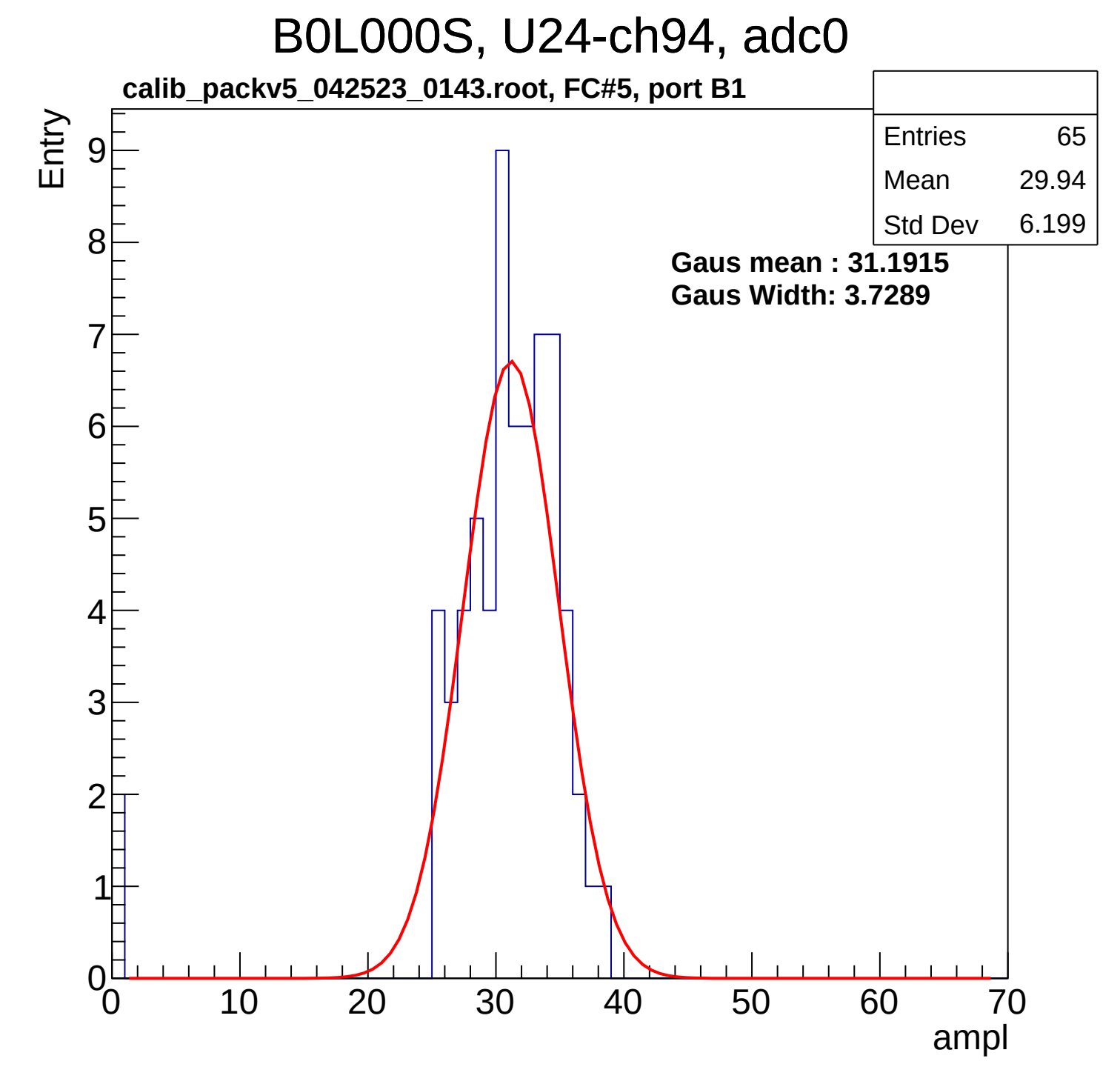
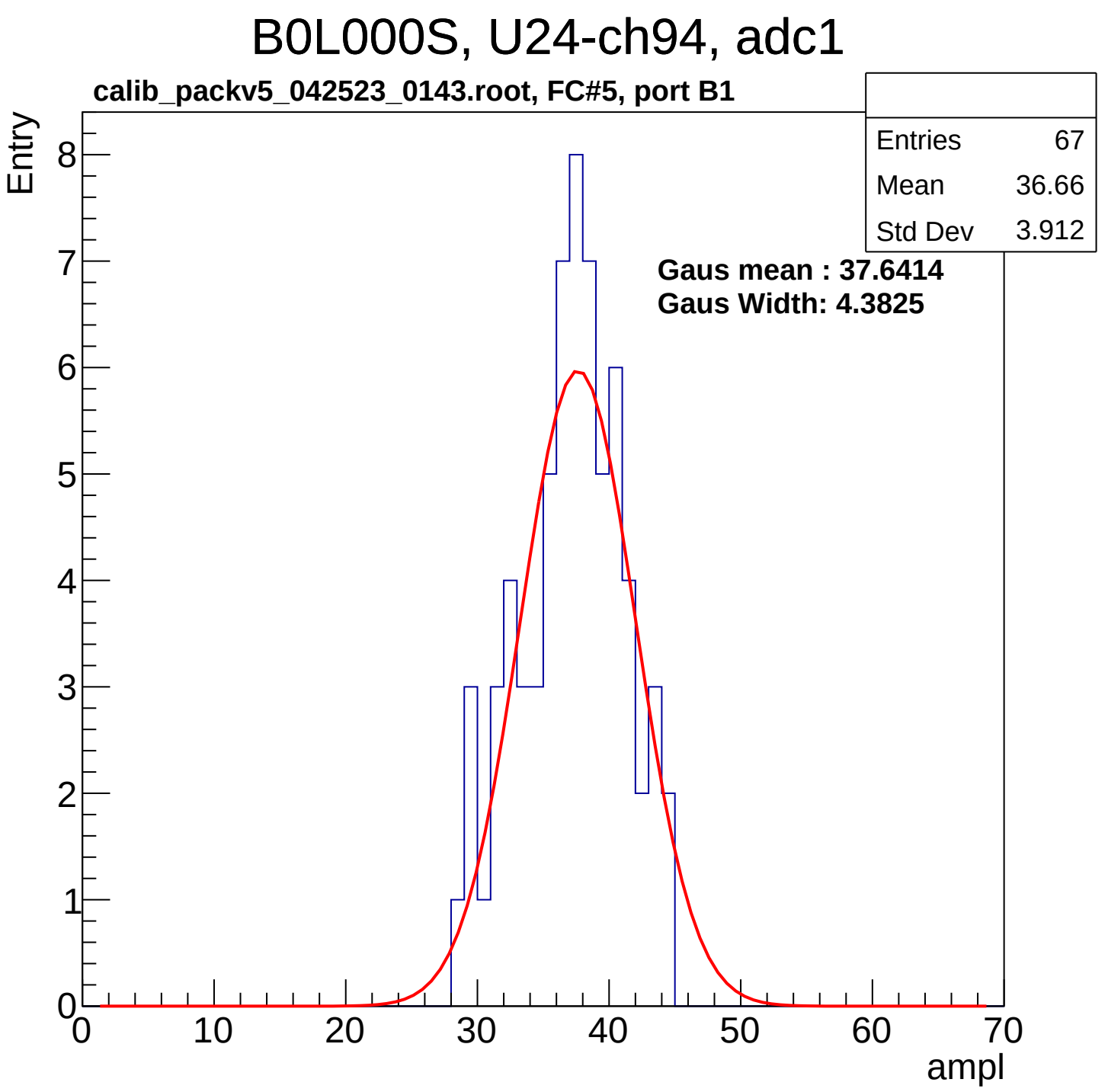
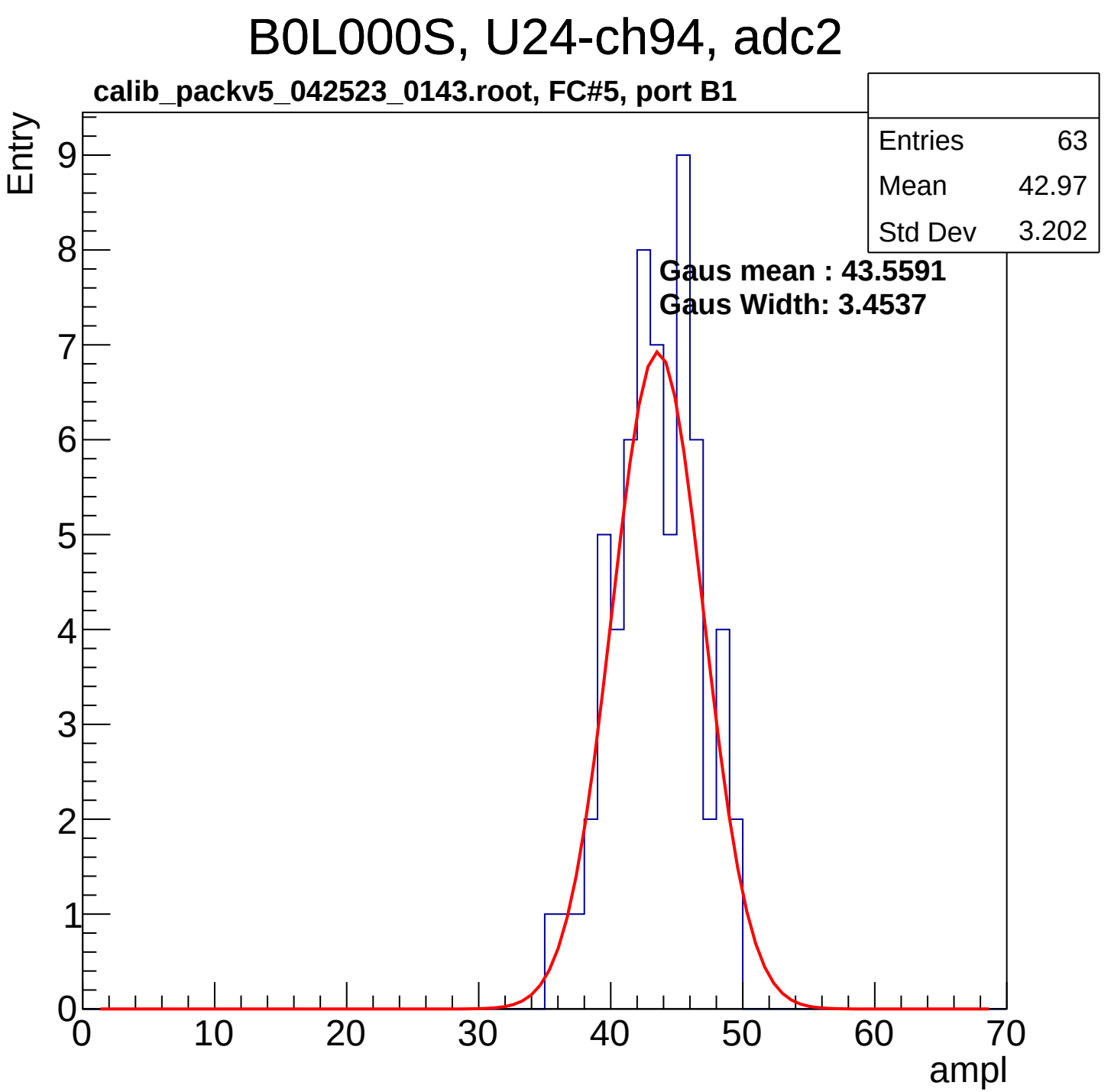
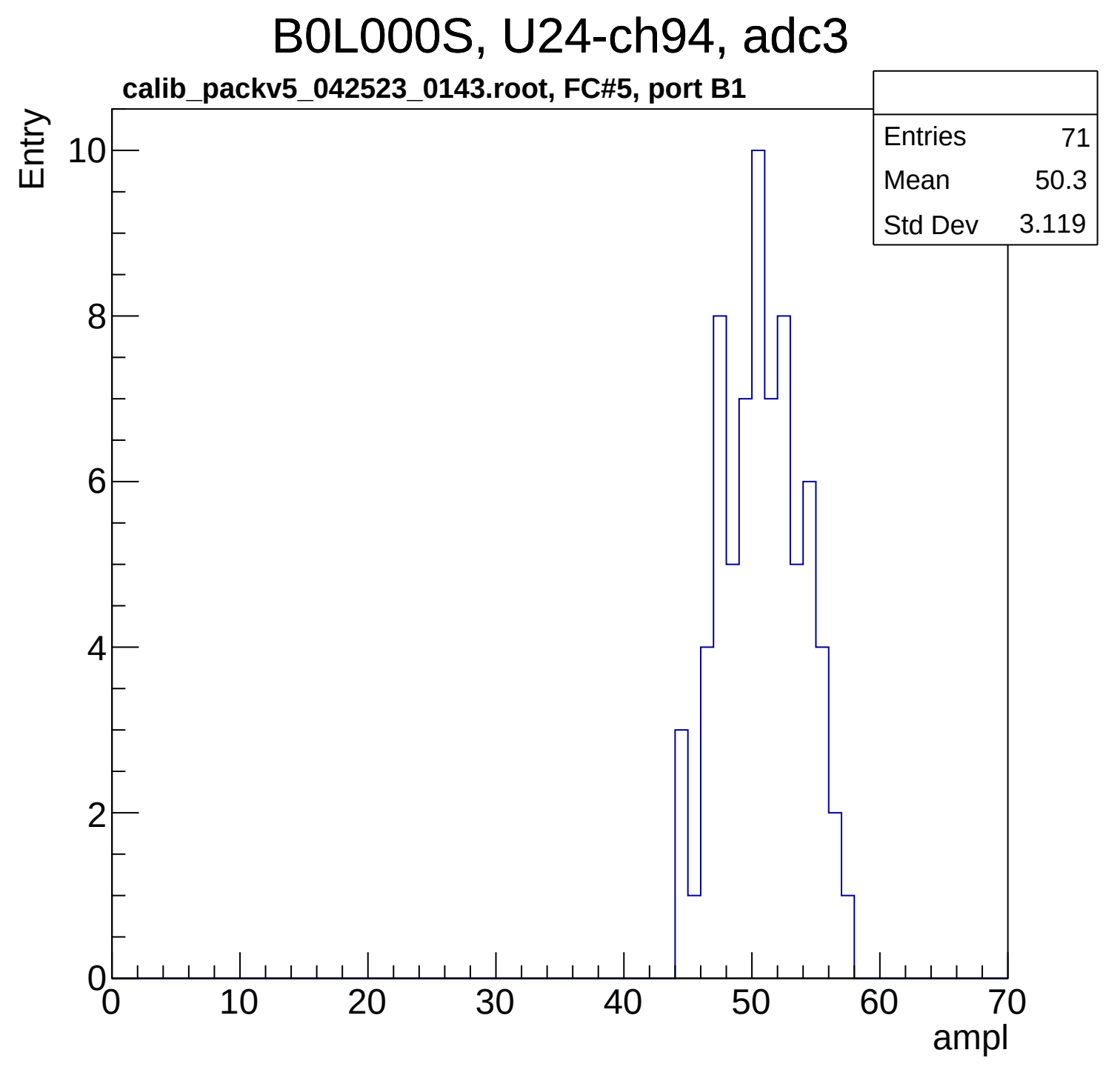

B0L000S, U24-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

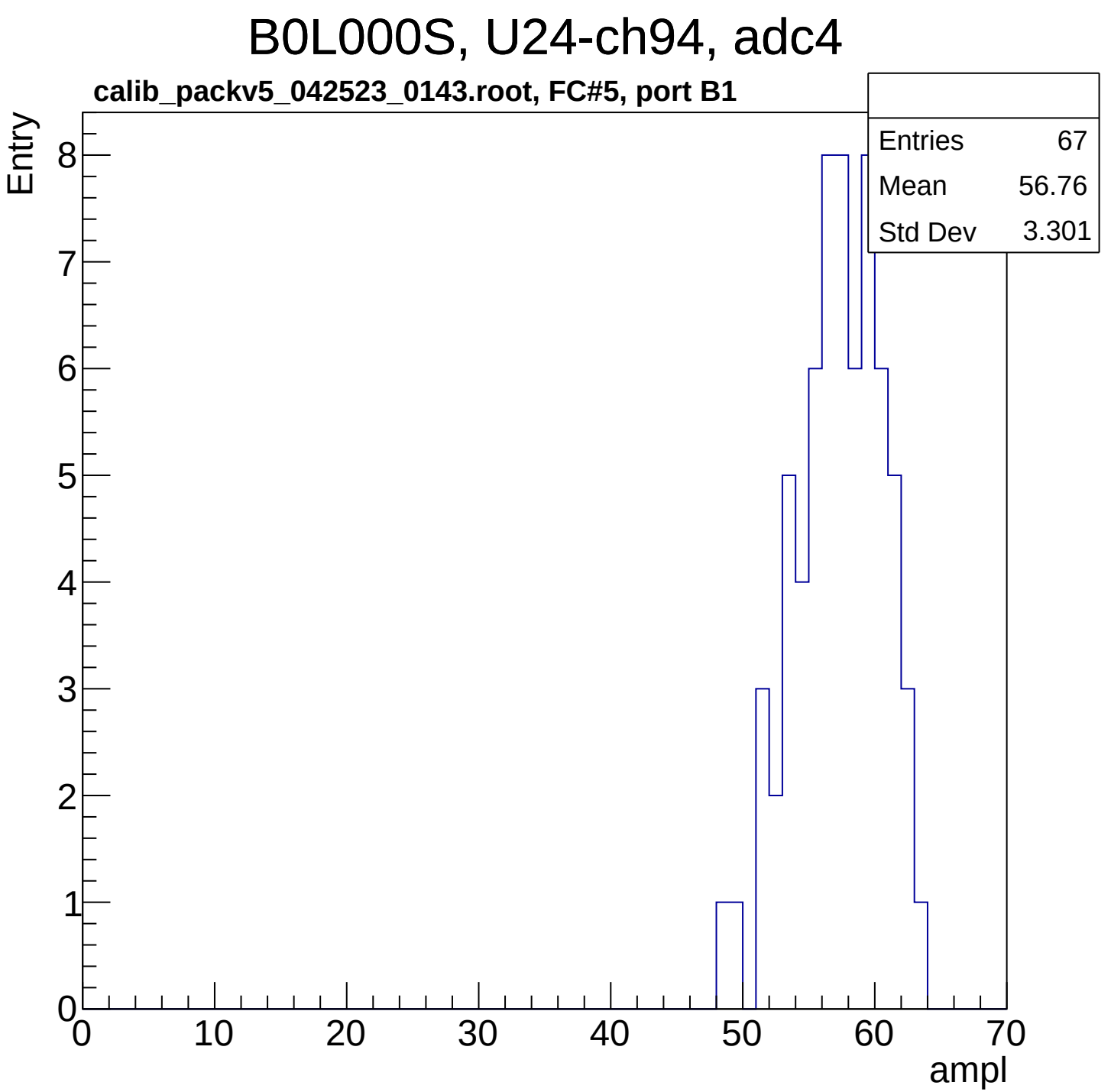

B0L000S, U24-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

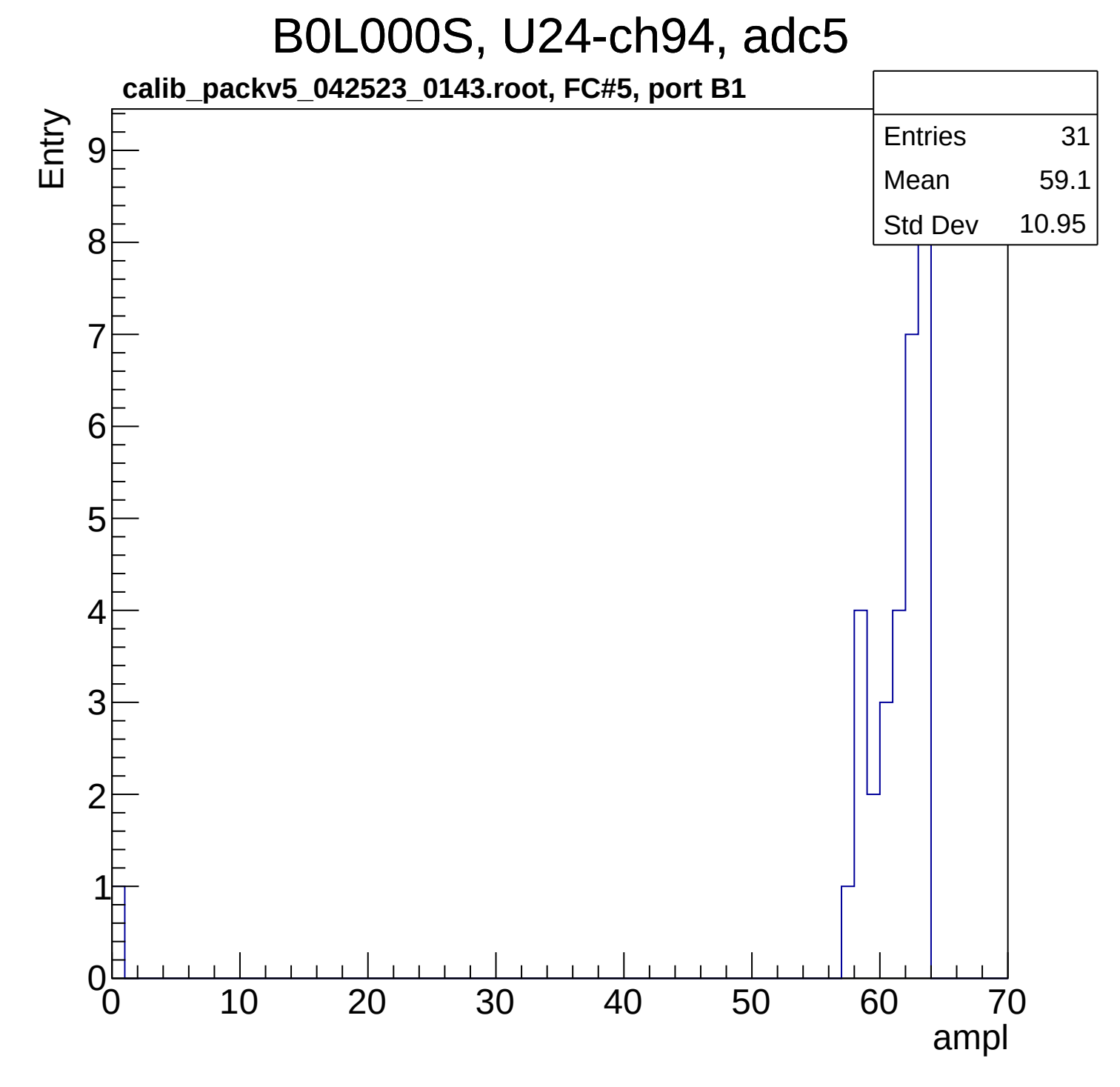

B0L000S, U24-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.94
Std Dev 6.199
Gaus mean : 31.1915
Gaus Width: 3.7289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

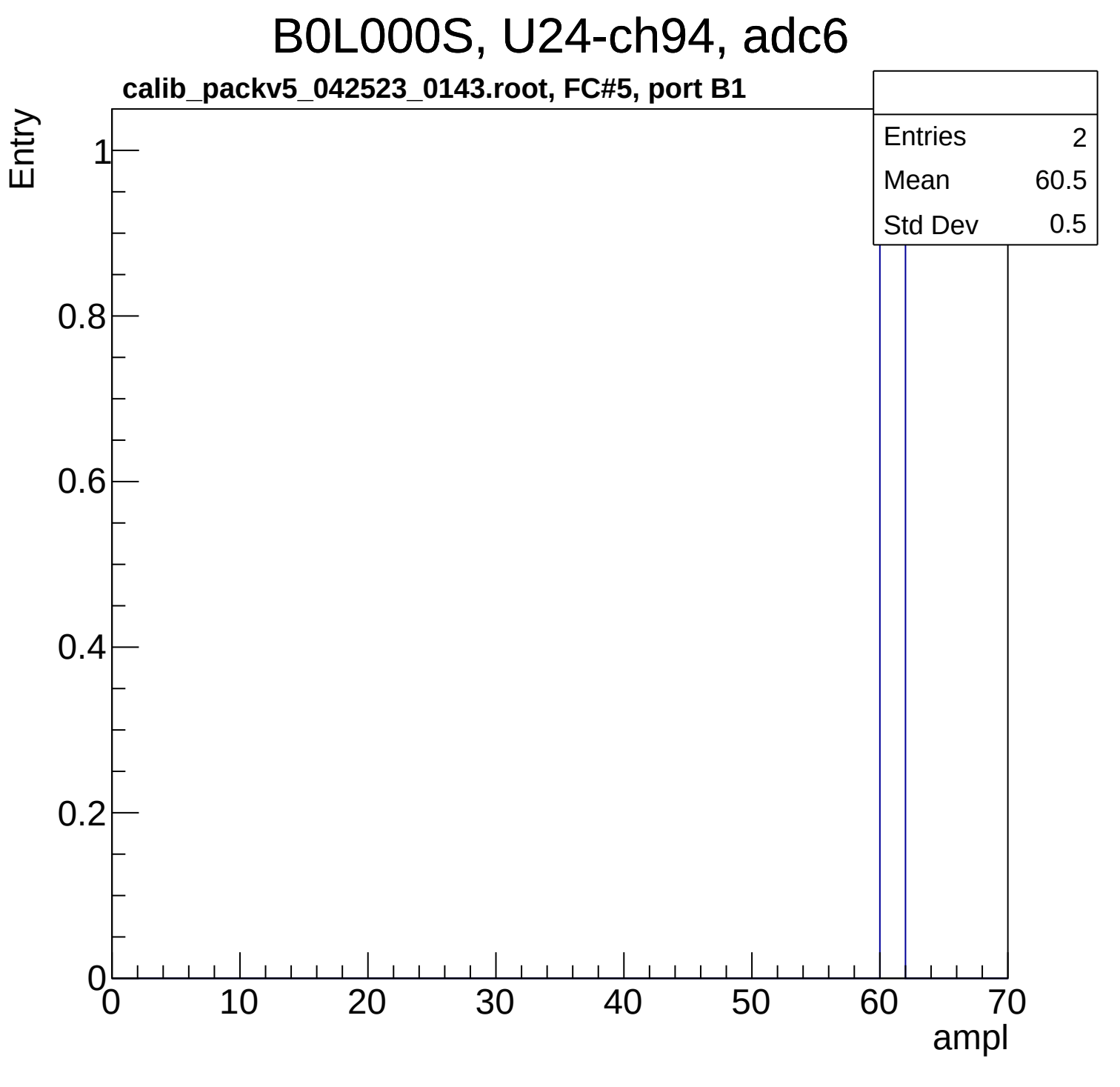

B0L000S, U24-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.66
Std Dev 3.912
Gaus mean : 37.6414
Gaus Width: 4.3825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

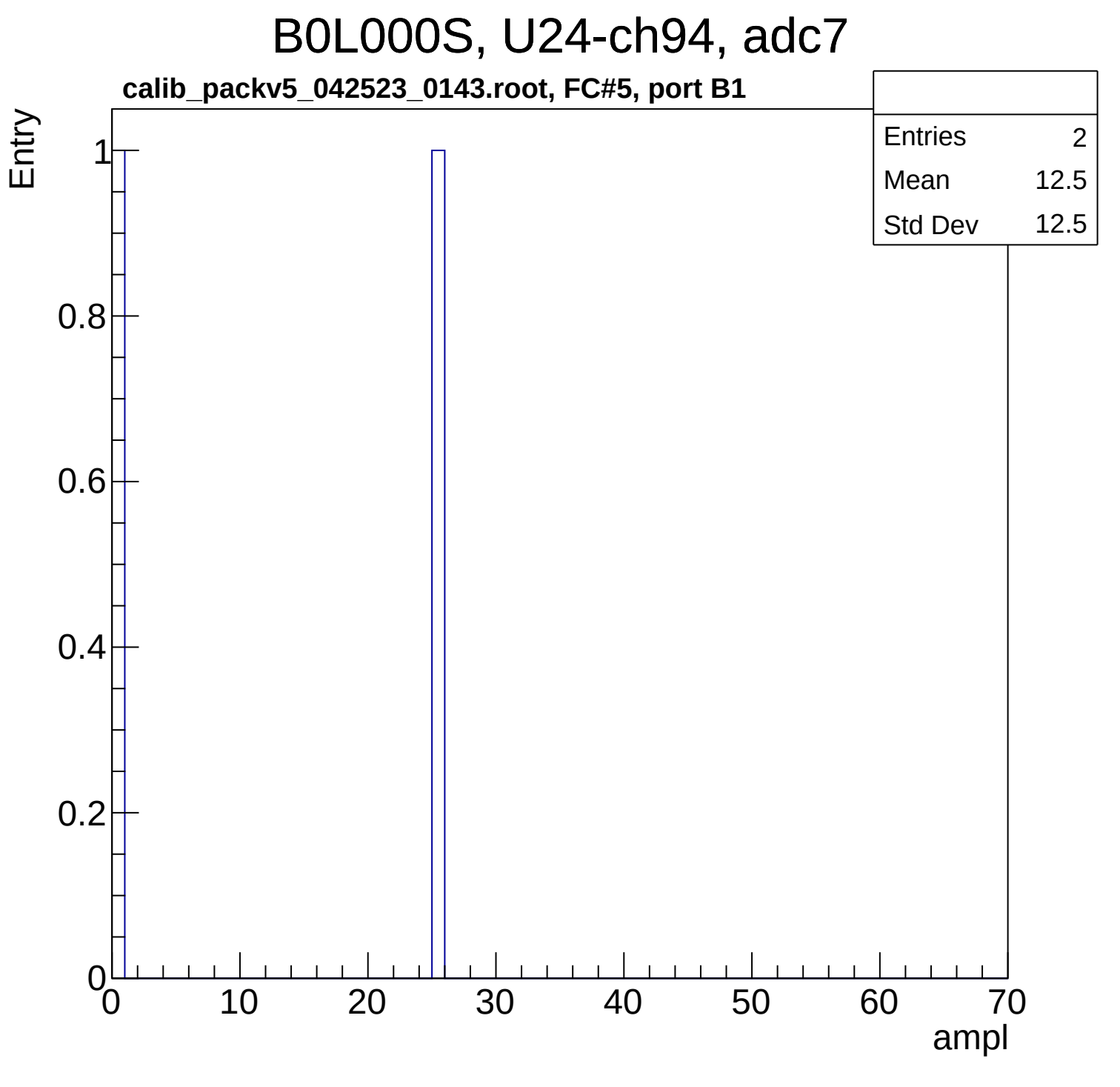

B0L000S, U24-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.97
Std Dev 3.202
Gaus mean : 43.5591
Gaus Width: 3.4537
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

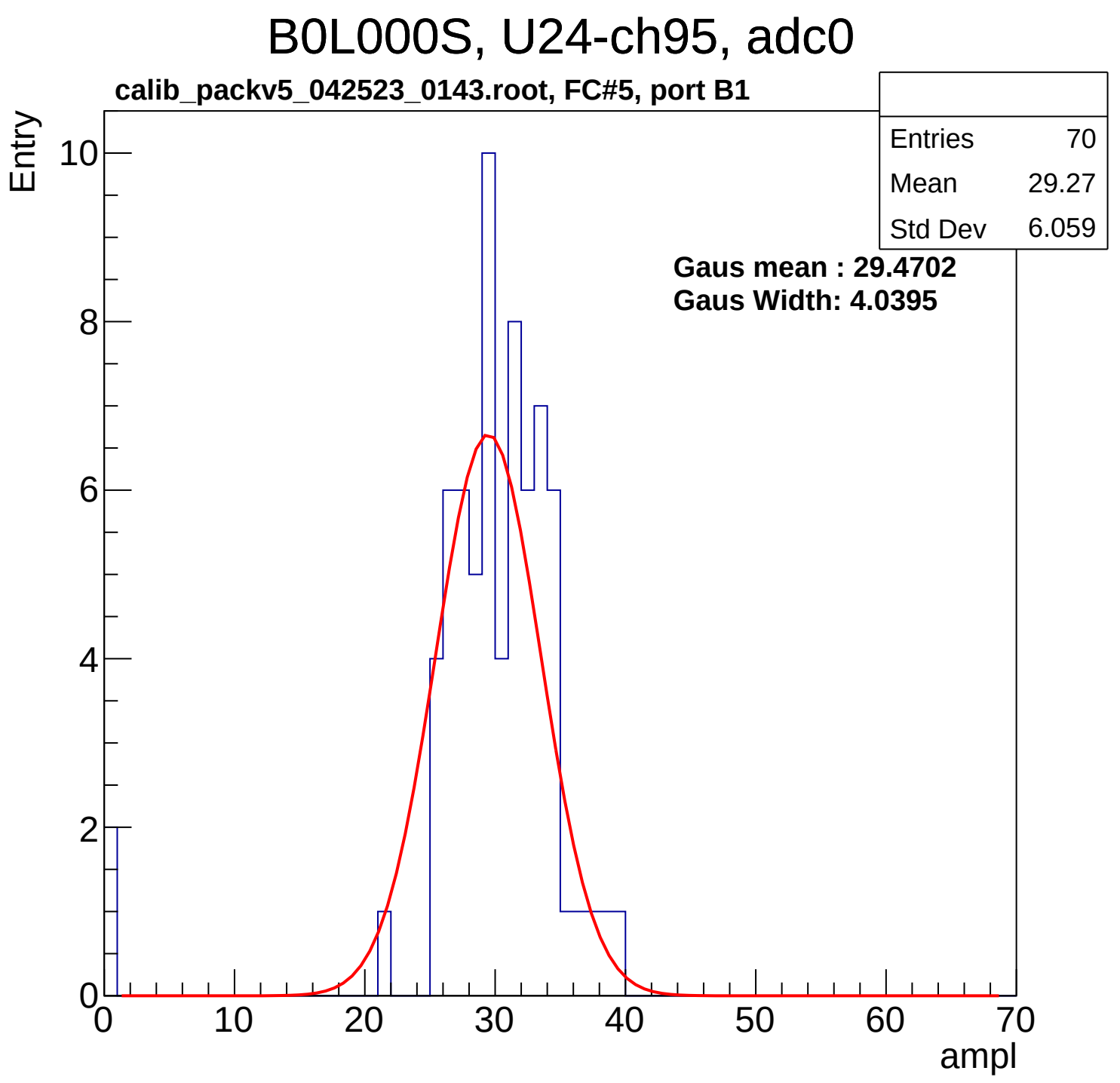

B0L000S, U24-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.3
Std Dev 3.119
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

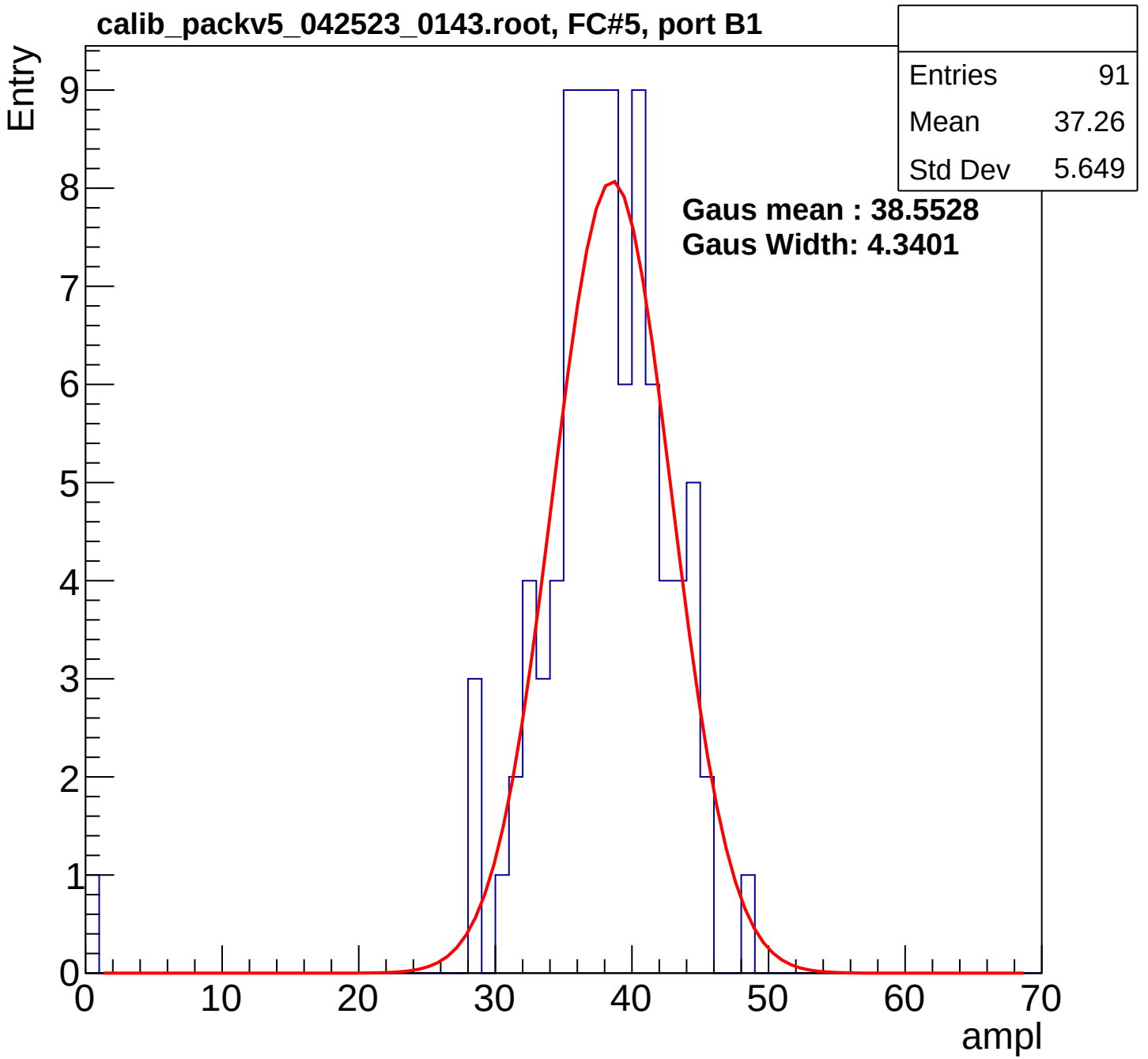

B0L000S, U24-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 56.76
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

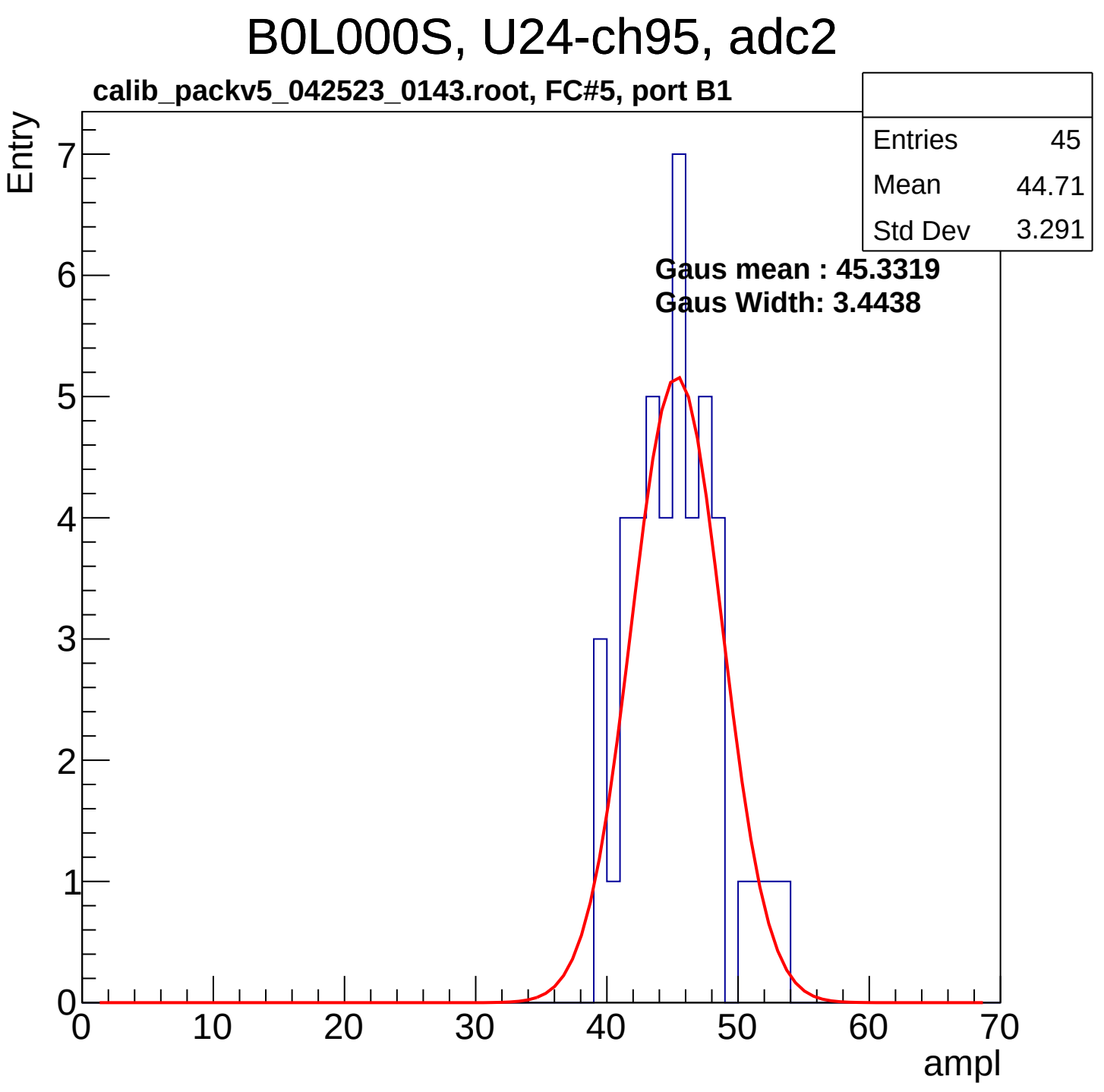

B0L000S, U24-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.1
Std Dev 10.95
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

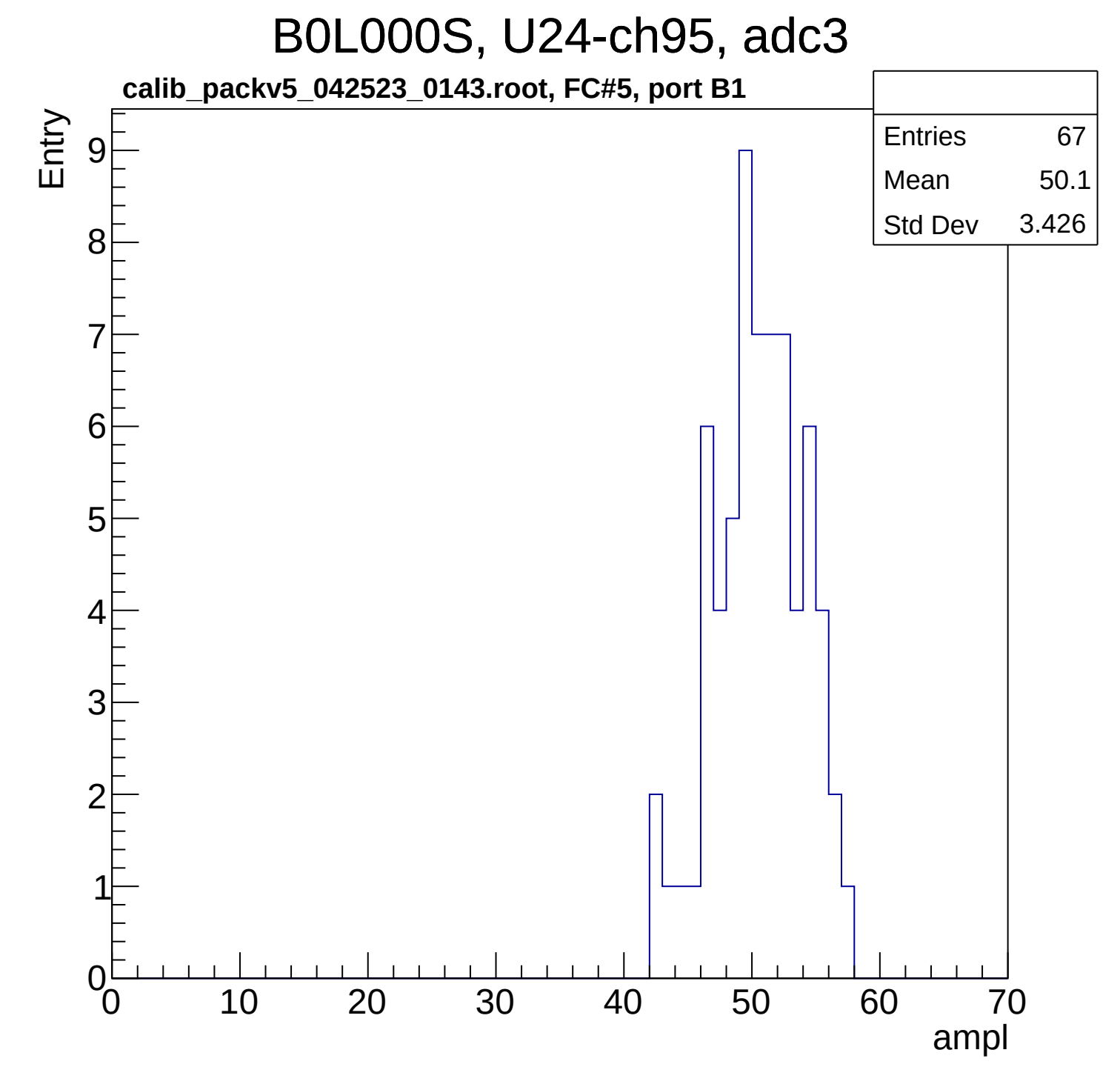

B0L000S, U24-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.27
Std Dev 6.059
Gaus mean : 29.4702
Gaus Width: 4.0395
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 37.26
Std Dev 5.649
Gaus mean : 38.5528
Gaus Width: 4.3401
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 44.71
Std Dev 3.291
Gaus mean : 45.3319
Gaus Width: 3.4438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.1
Std Dev 3.426
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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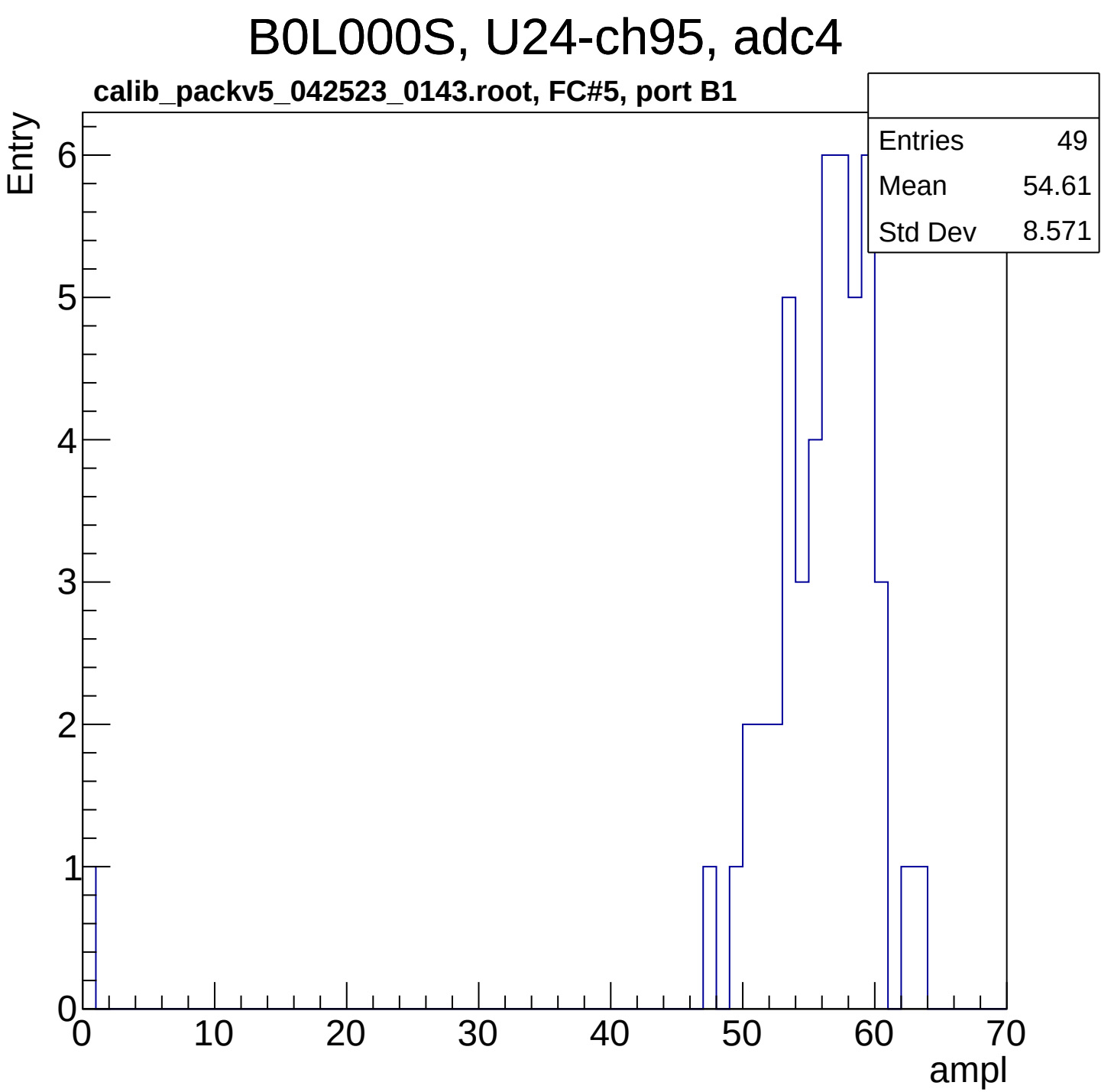
B0L000S, U24-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.61
Std Dev 8.571
Entry
ampl
0
1
2
3
4
5
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0
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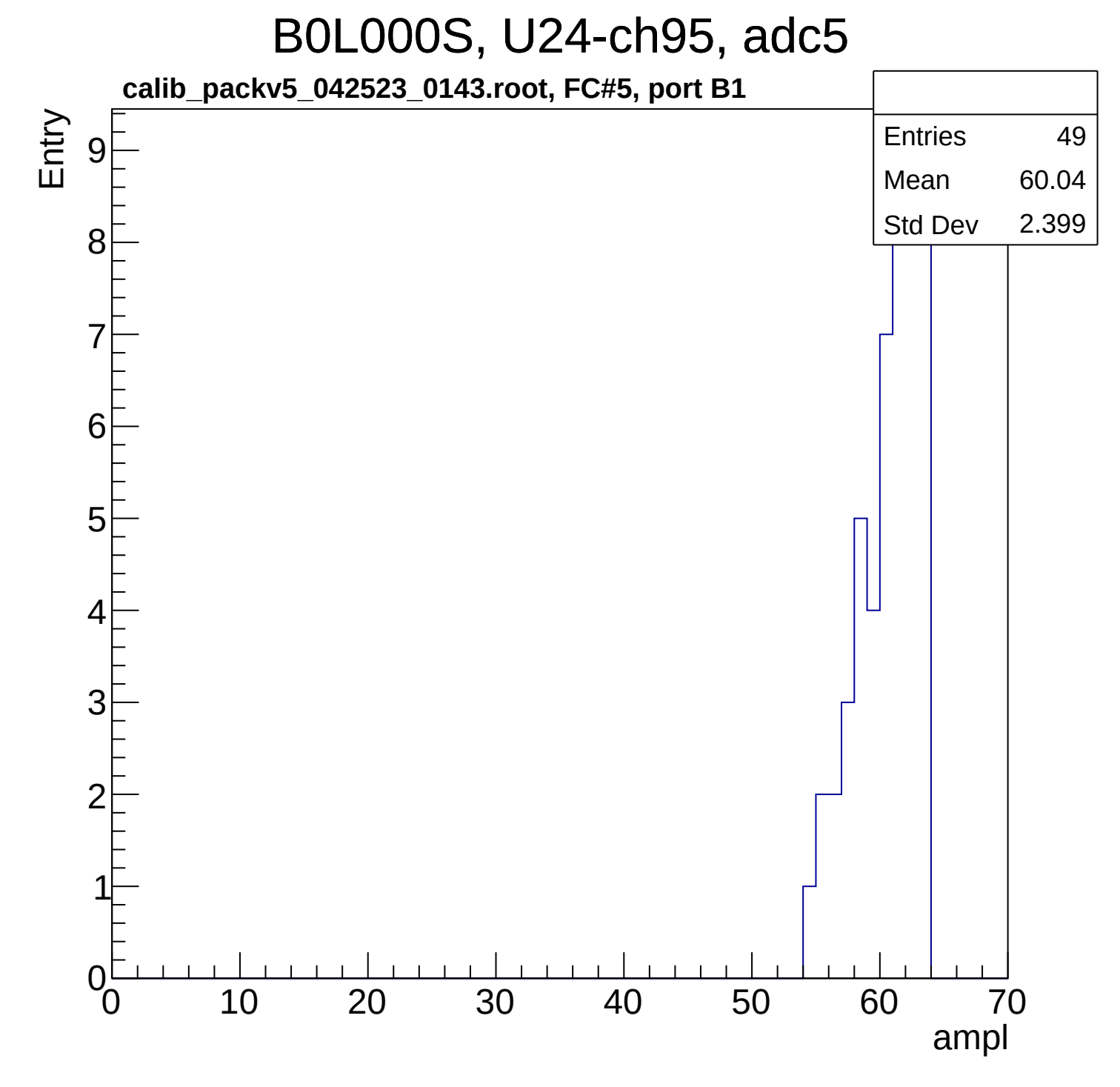
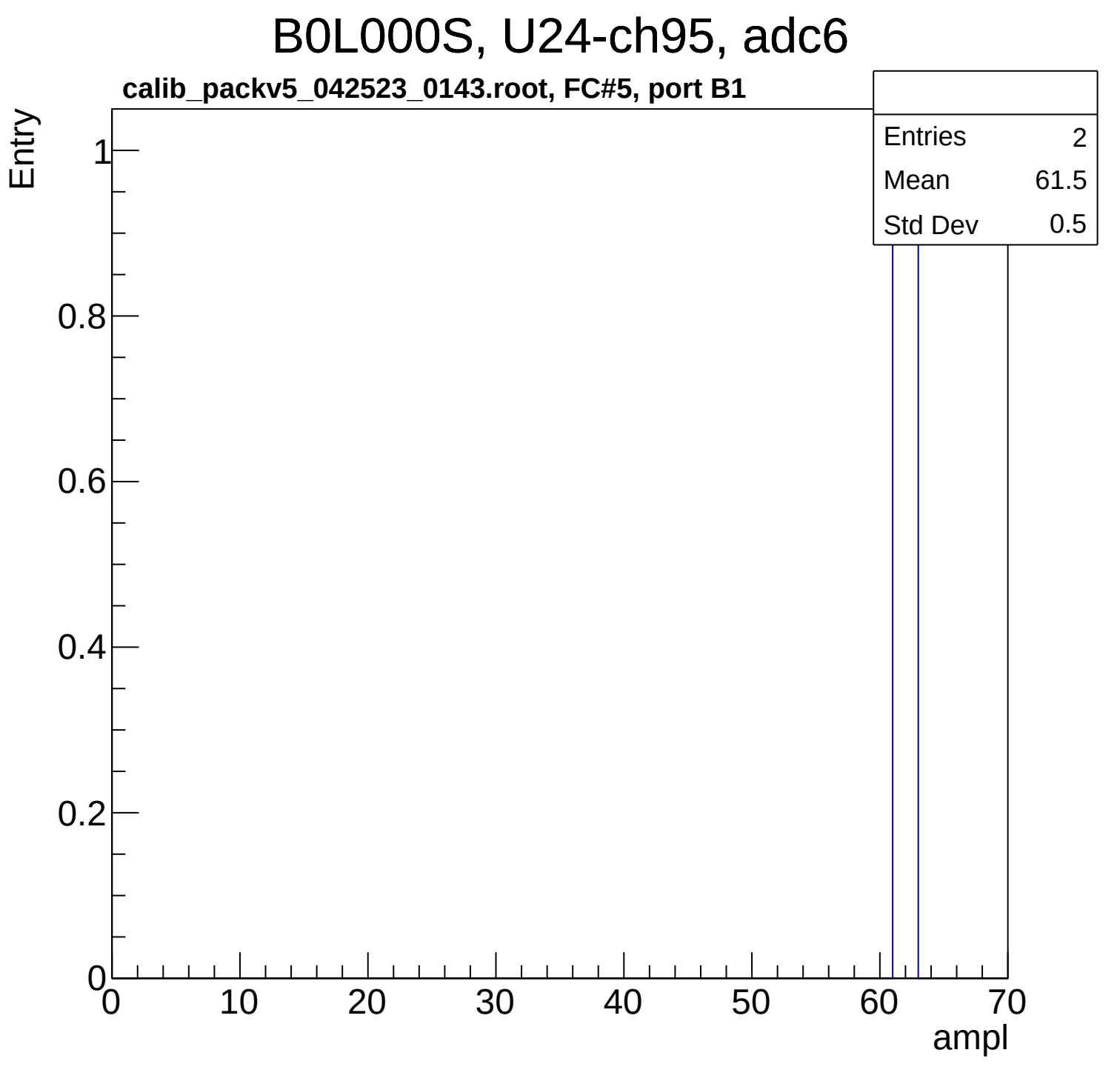
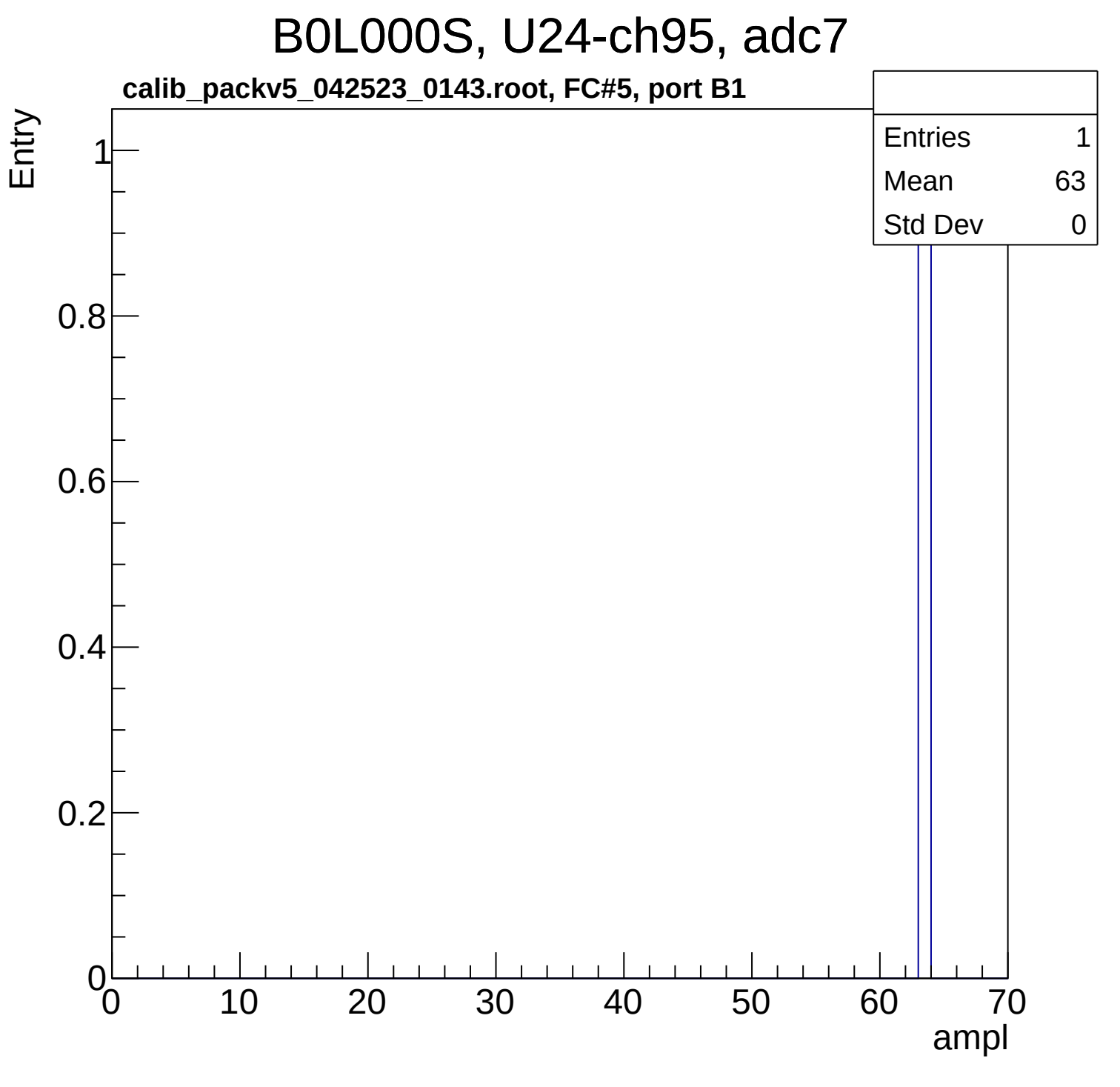
20
30
40
50
60
70


B0L000S, U24-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 60.04
Std Dev 2.399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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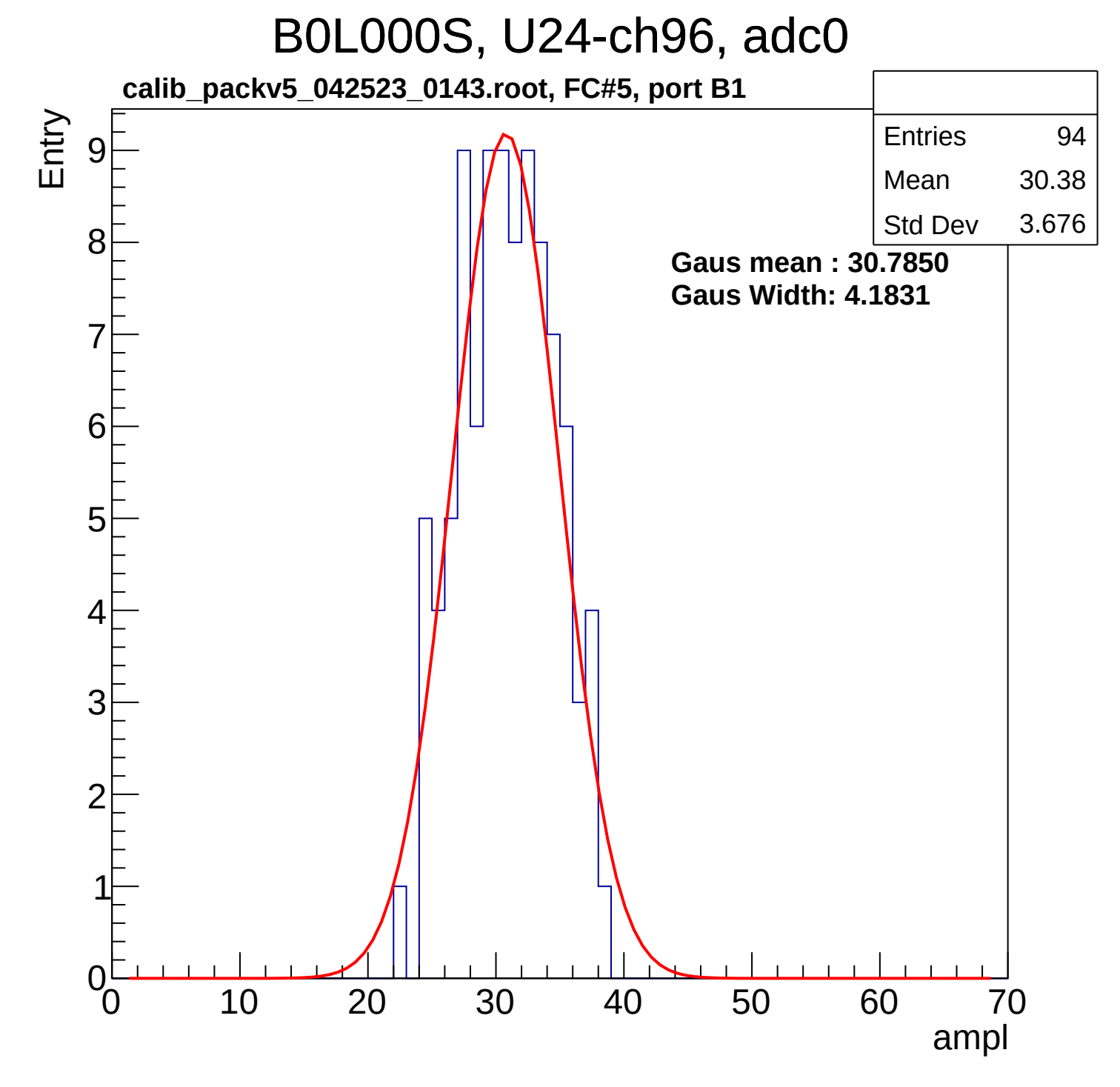
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 94
Mean 30.38
Std Dev 3.676
Gaus mean : 30.7850
Gaus Width: 4.1831
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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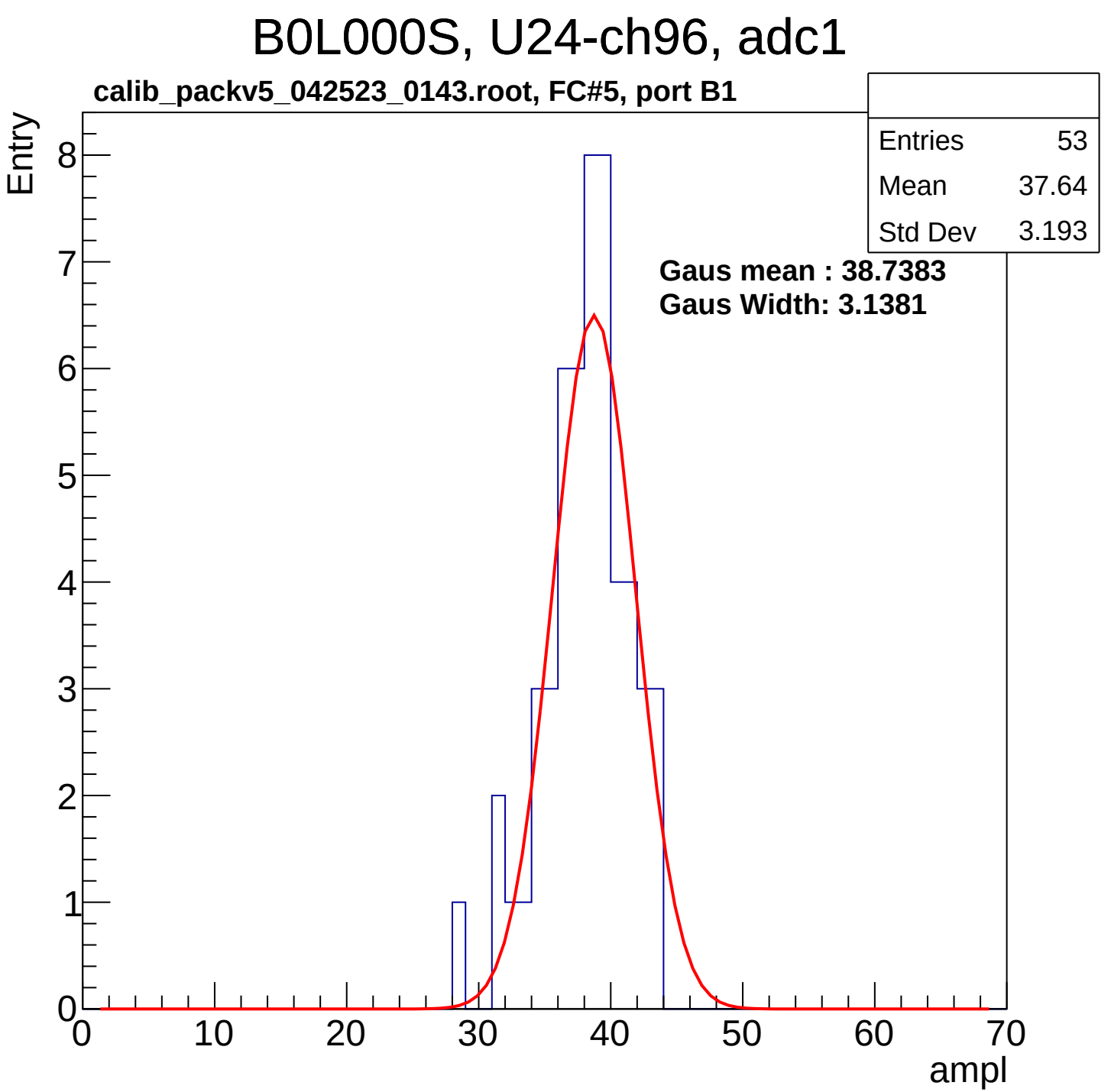
B0L000S, U24-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 37.64
Std Dev 3.193
Gaus mean : 38.7383
Gaus Width: 3.1381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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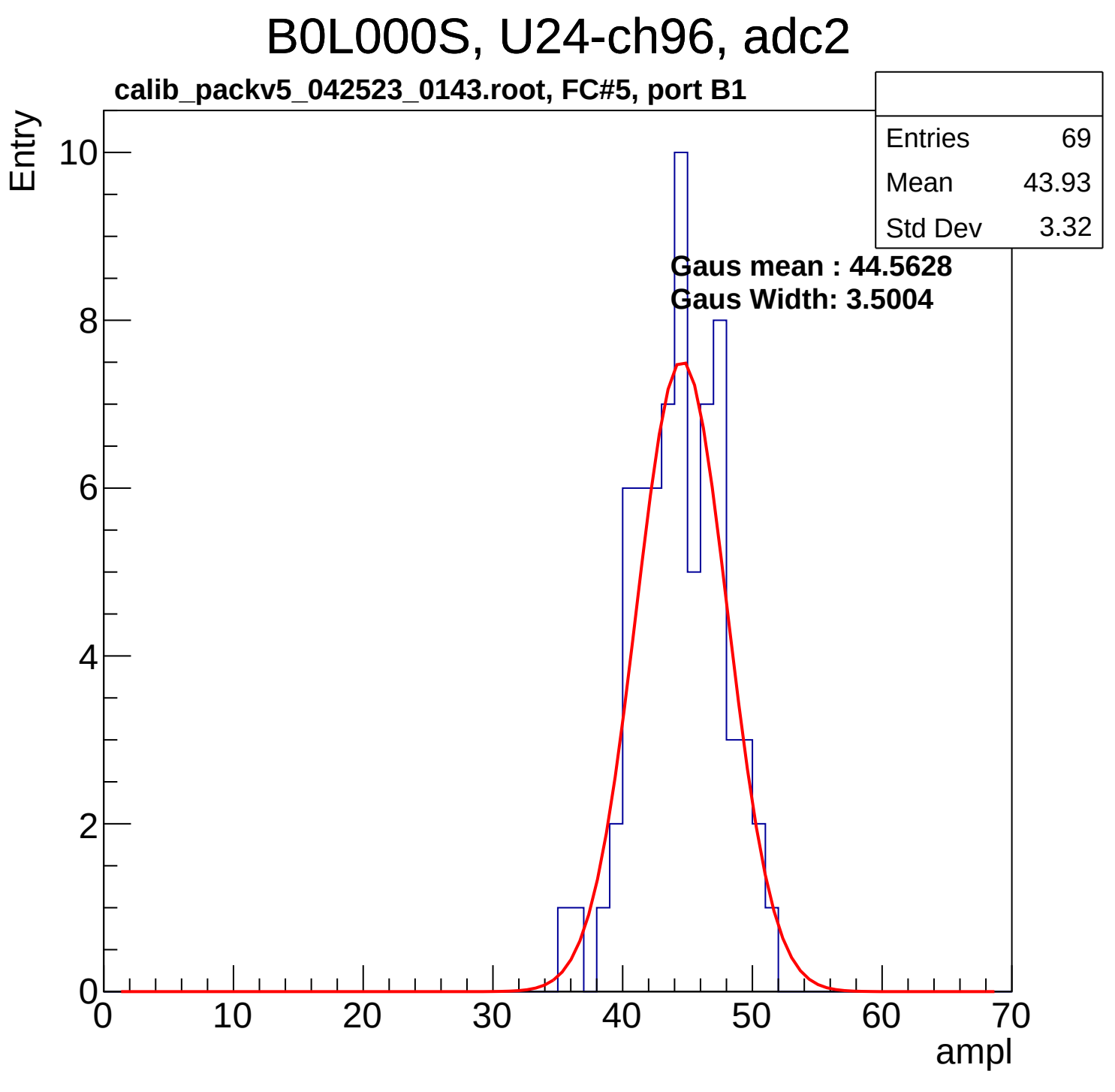
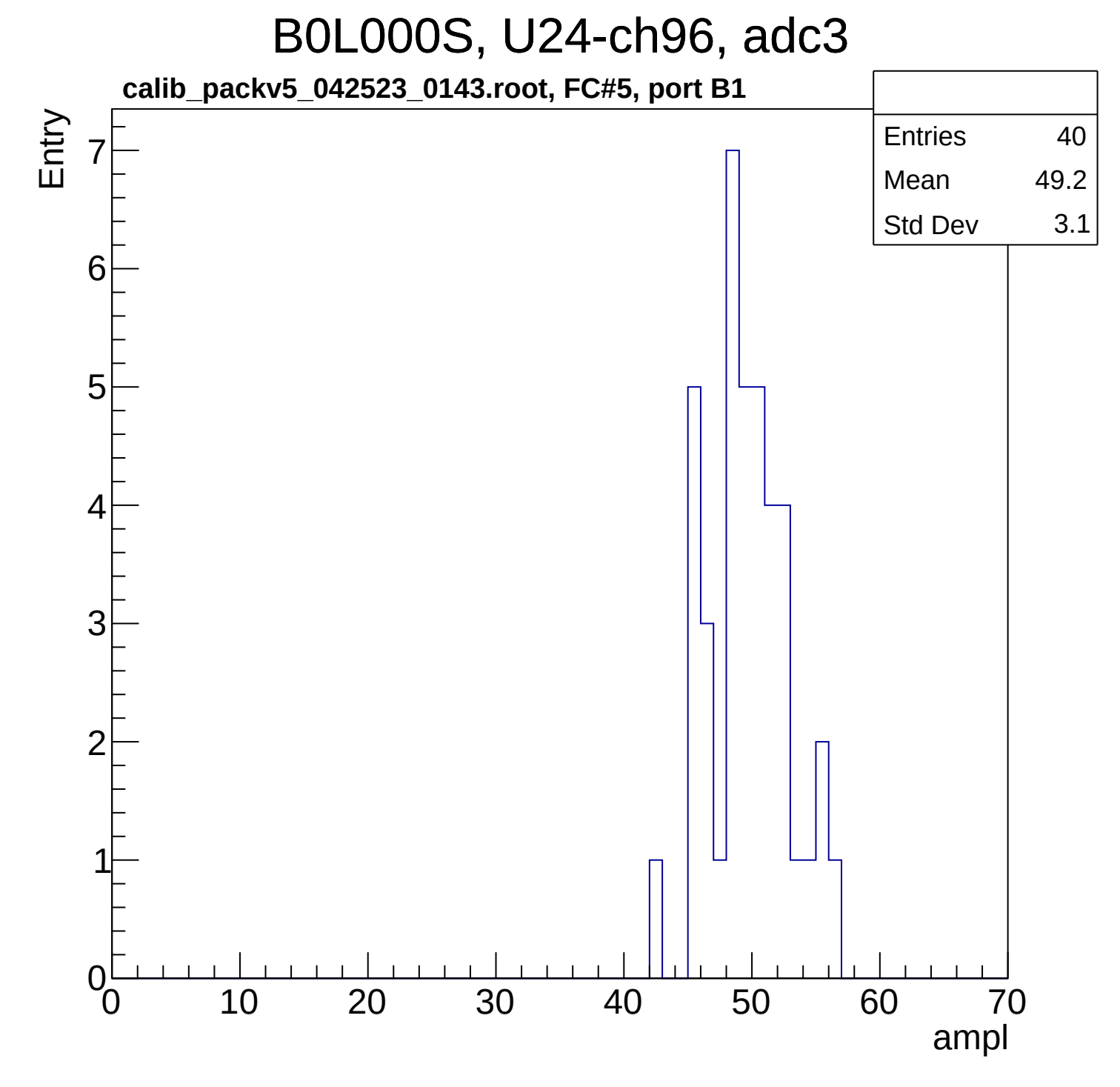

B0L000S, U24-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.93
Std Dev 3.32
Gaus mean : 44.5628
Gaus Width: 3.5004
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 49.2
Std Dev 3.1
Entry
0
1
2
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4
5
6
7
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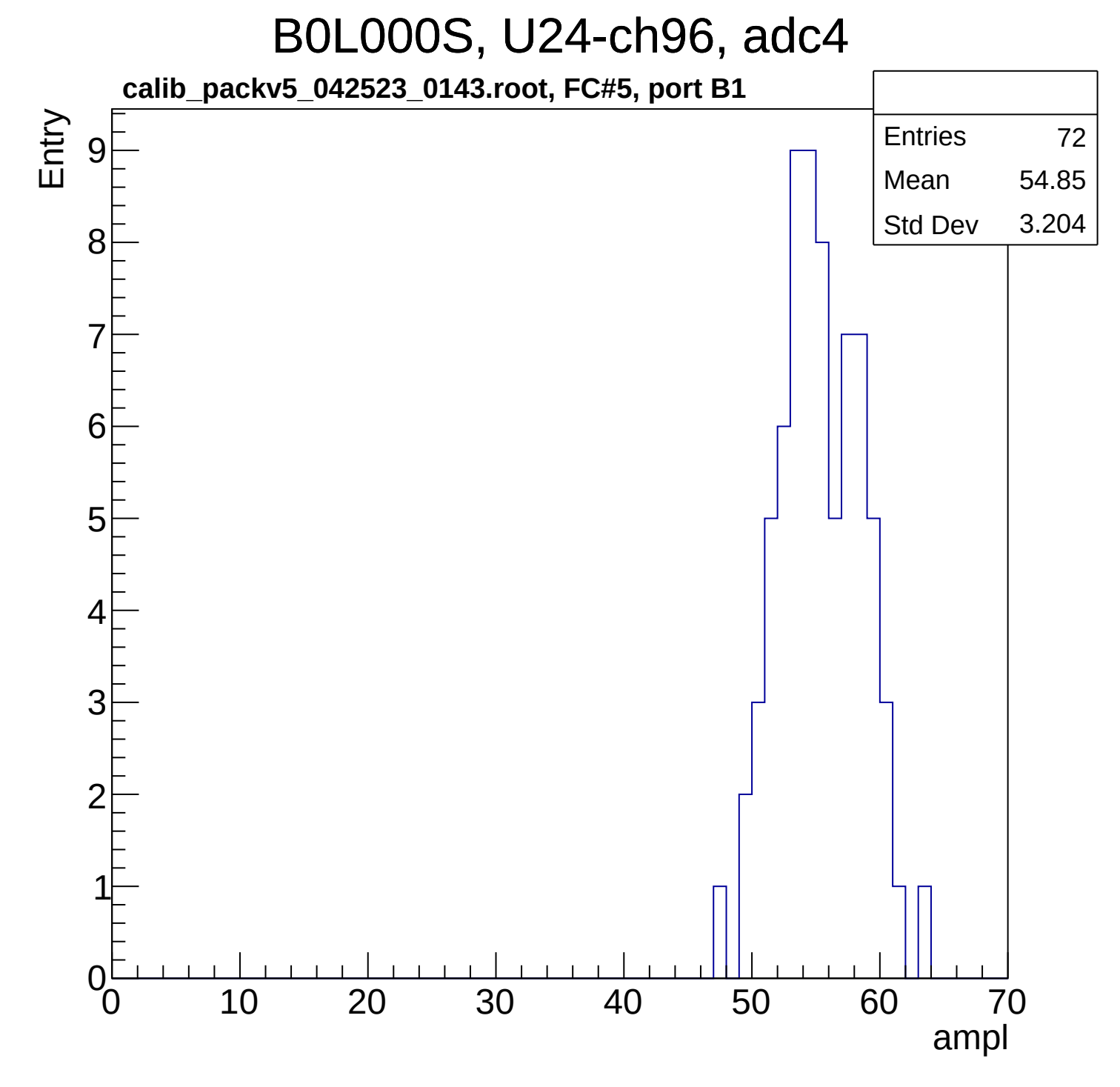
50
60
70
ampl


B0L000S, U24-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 54.85
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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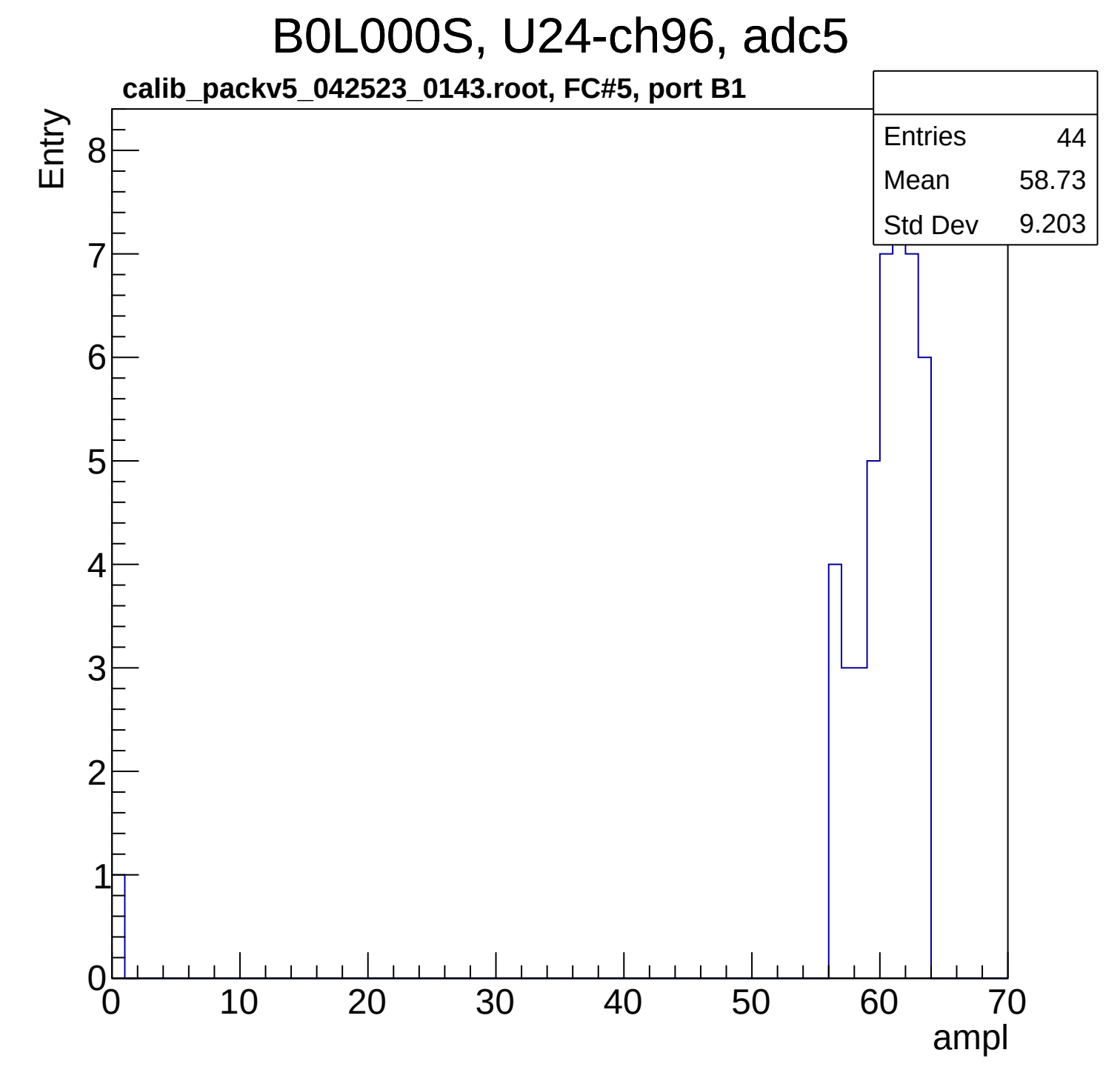
B0L000S, U24-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.73
Std Dev 9.203
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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60
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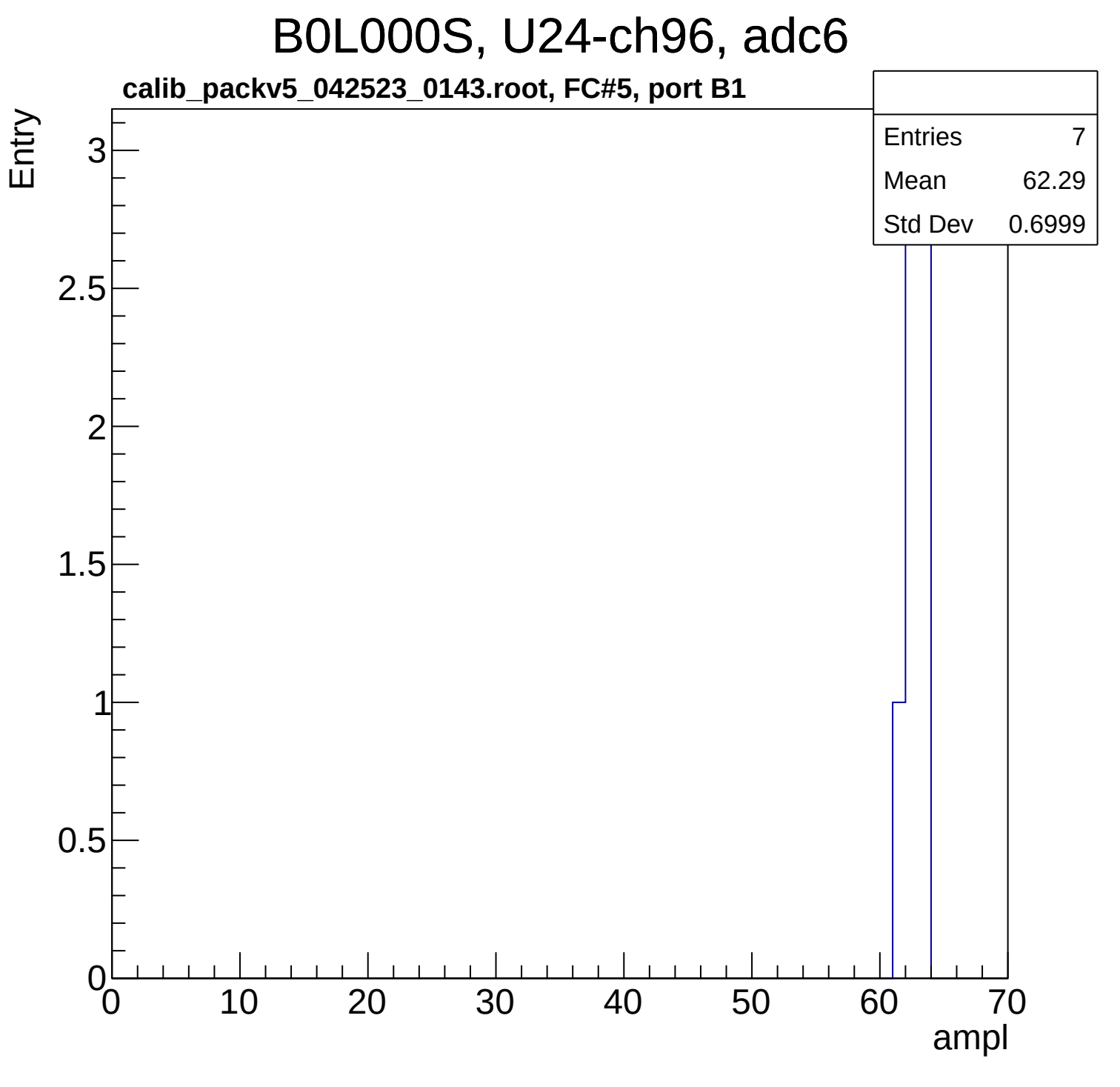
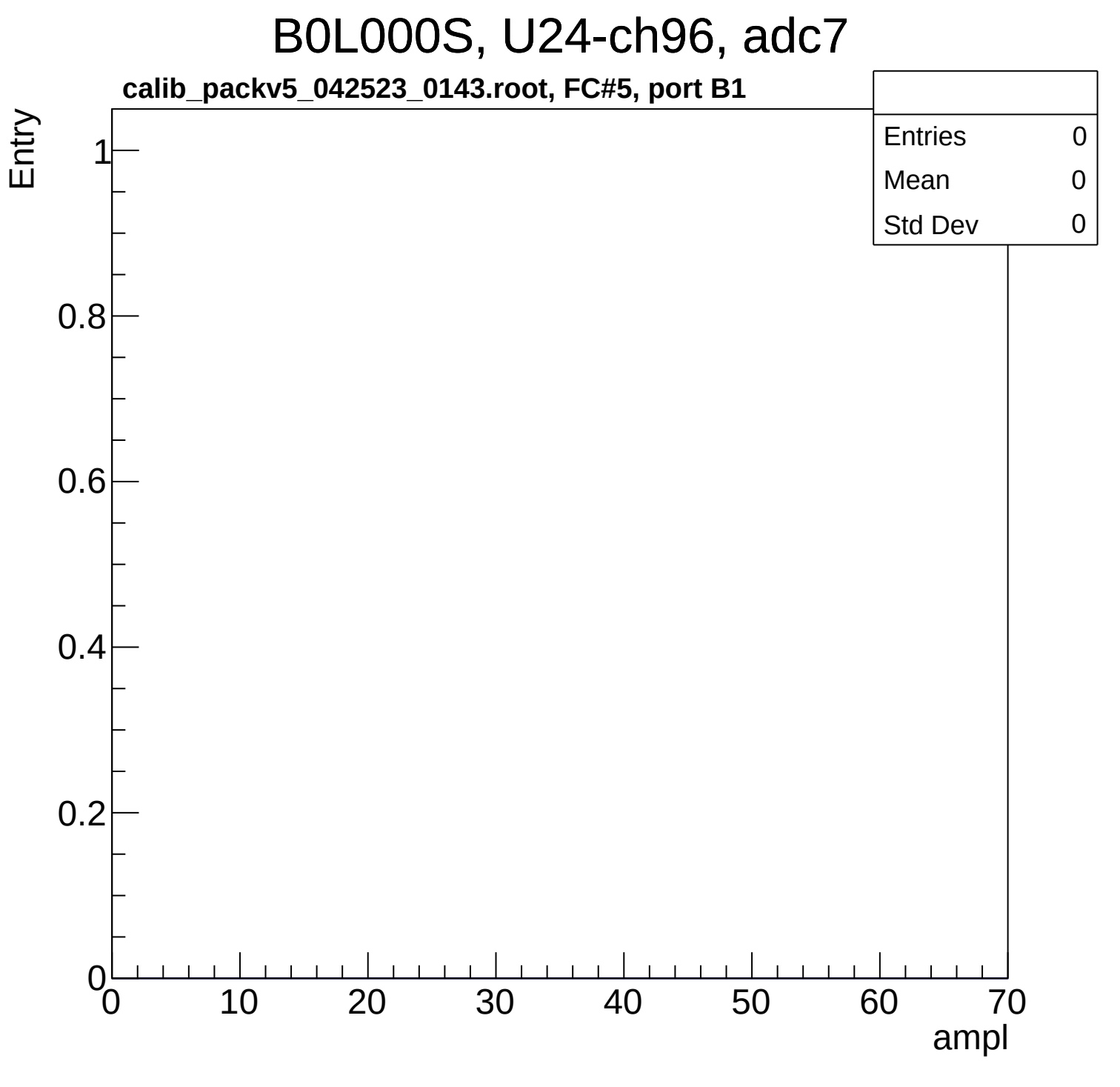
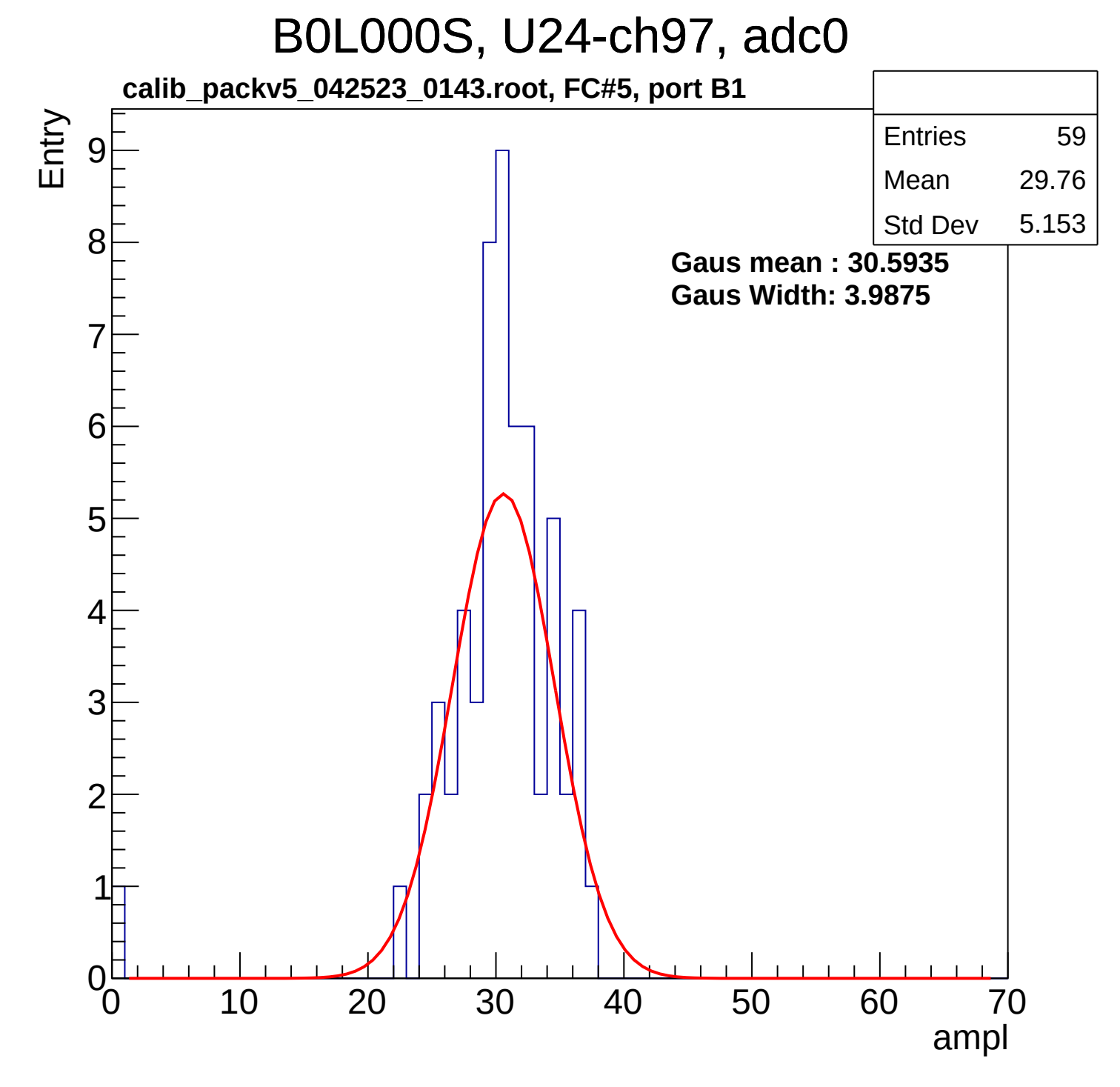

B0L000S, U24-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 29.76
Std Dev 5.153
Gaus mean : 30.5935
Gaus Width: 3.9875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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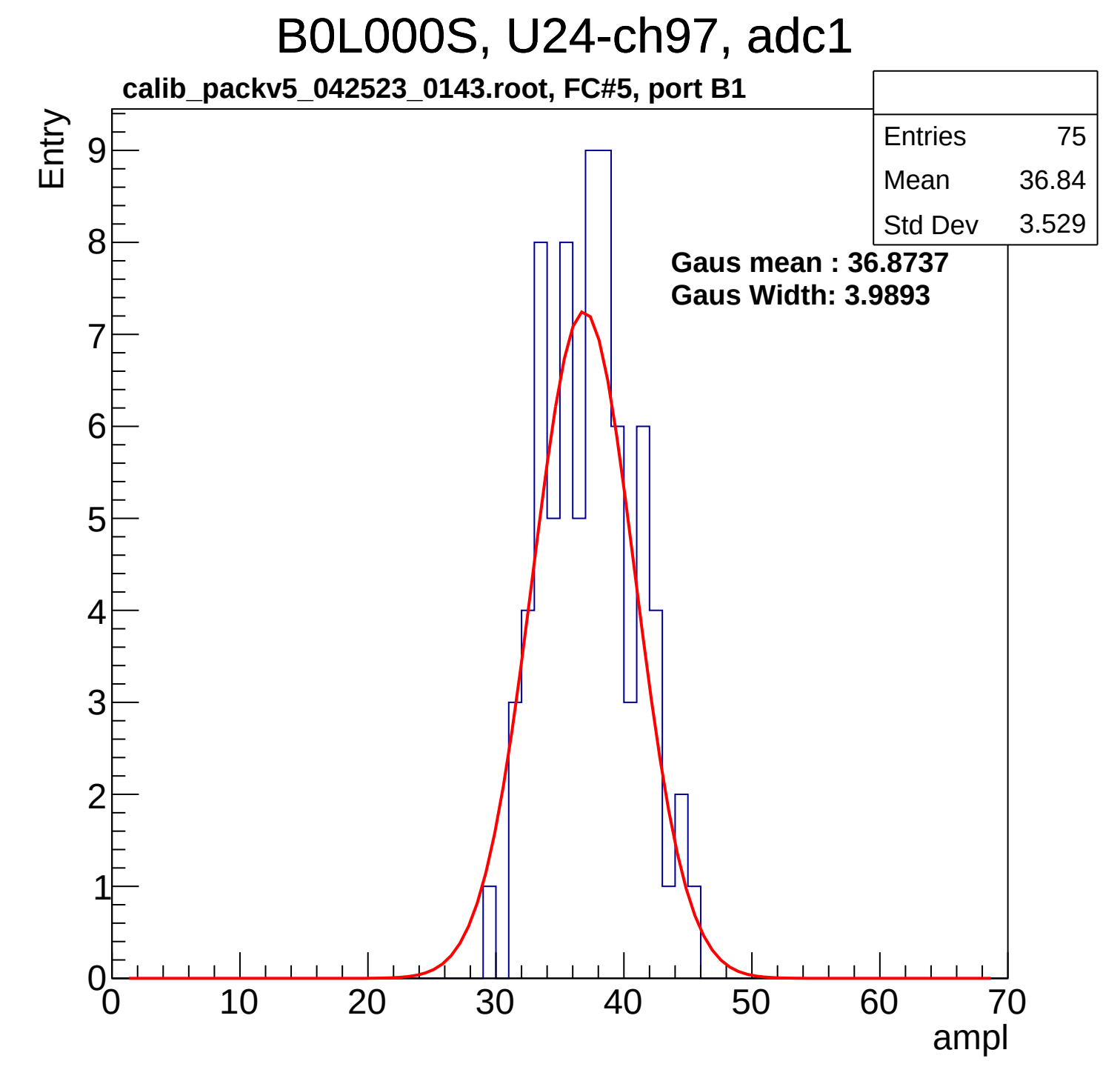
B0L000S, U24-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.84
Std Dev 3.529
Gaus mean : 36.8737
Gaus Width: 3.9893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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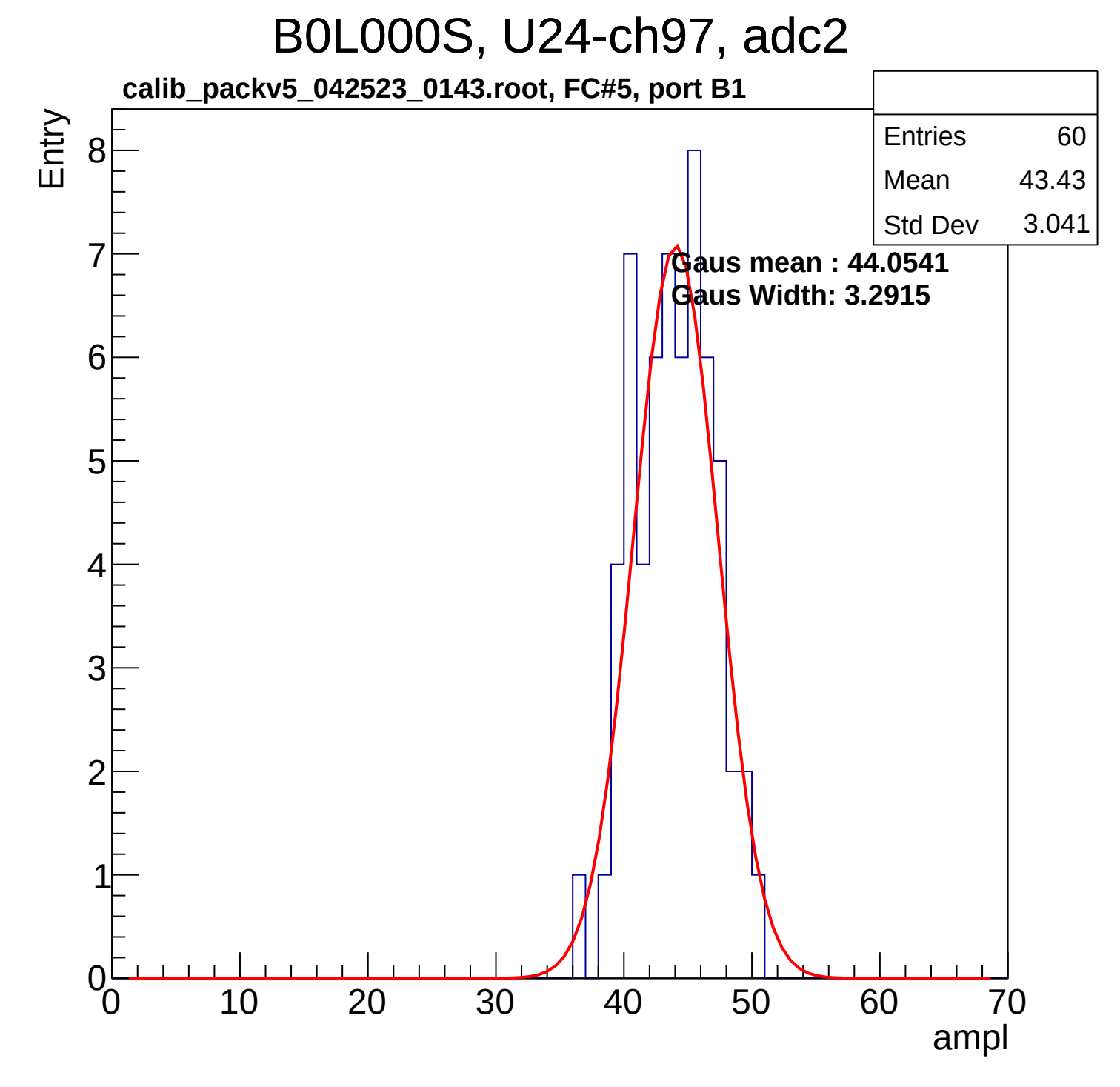
B0L000S, U24-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.43
Std Dev 3.041
Gaus mean : 44.0541
Gaus Width: 3.2915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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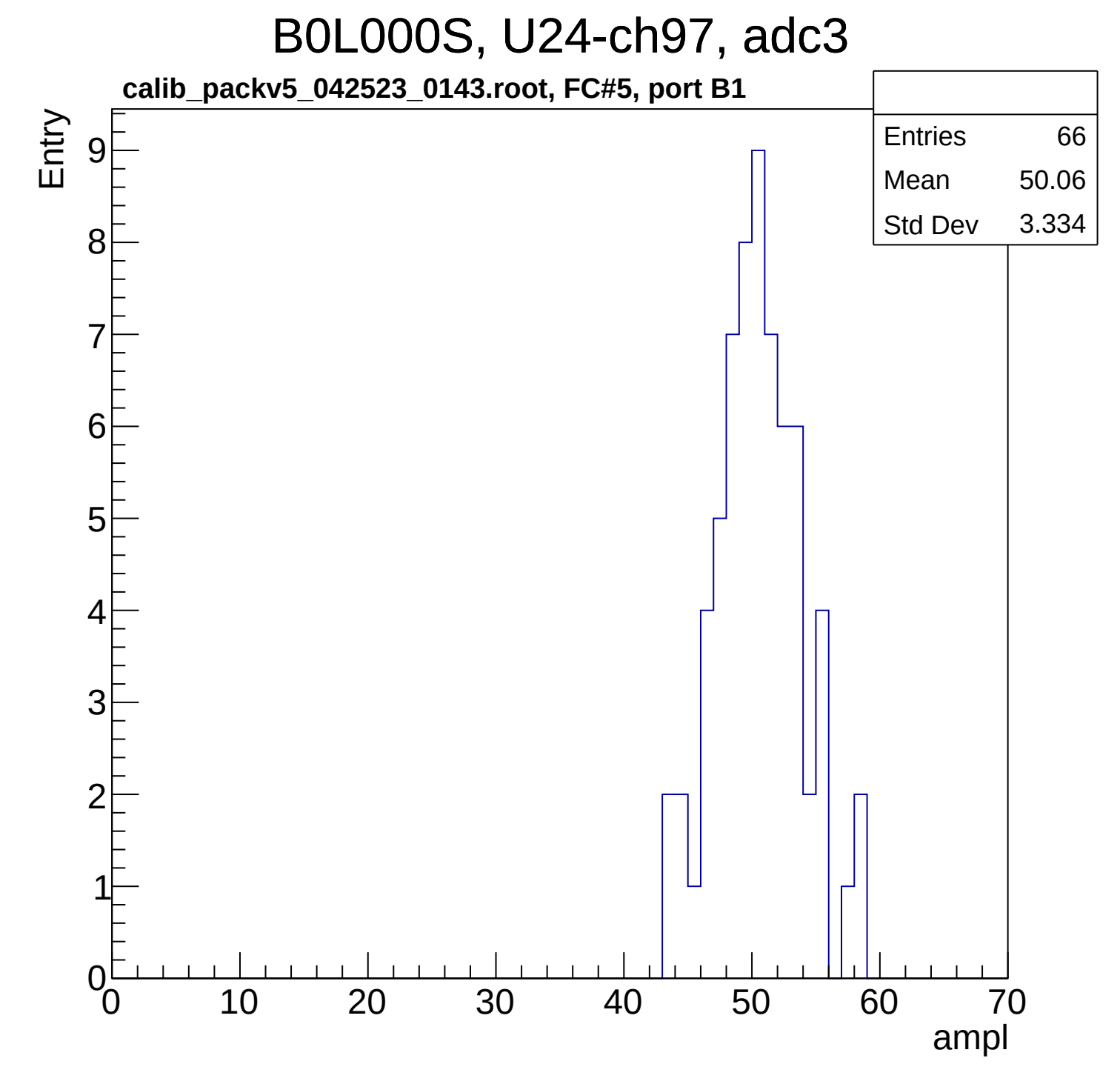
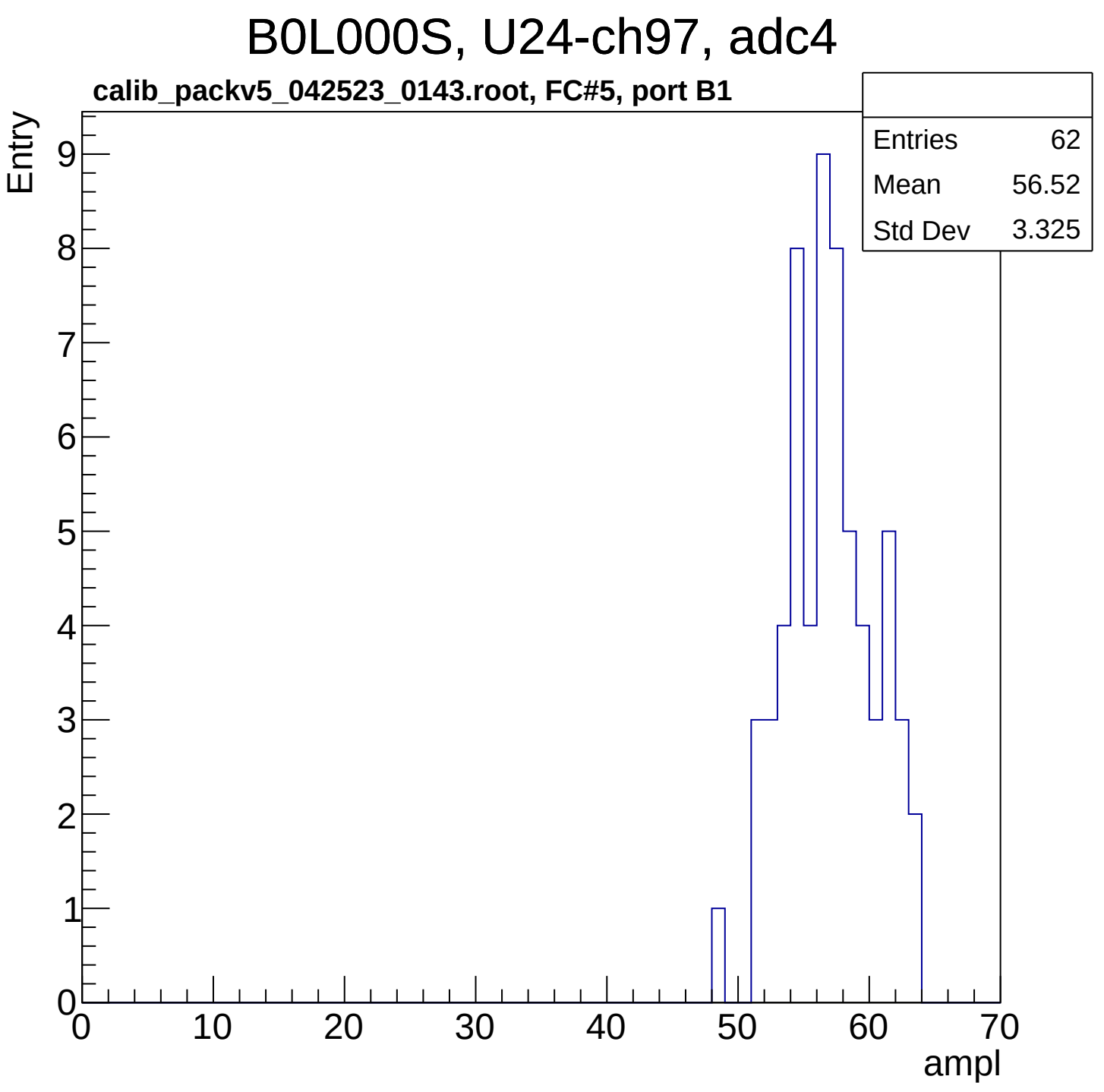

B0L000S, U24-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.06
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.52
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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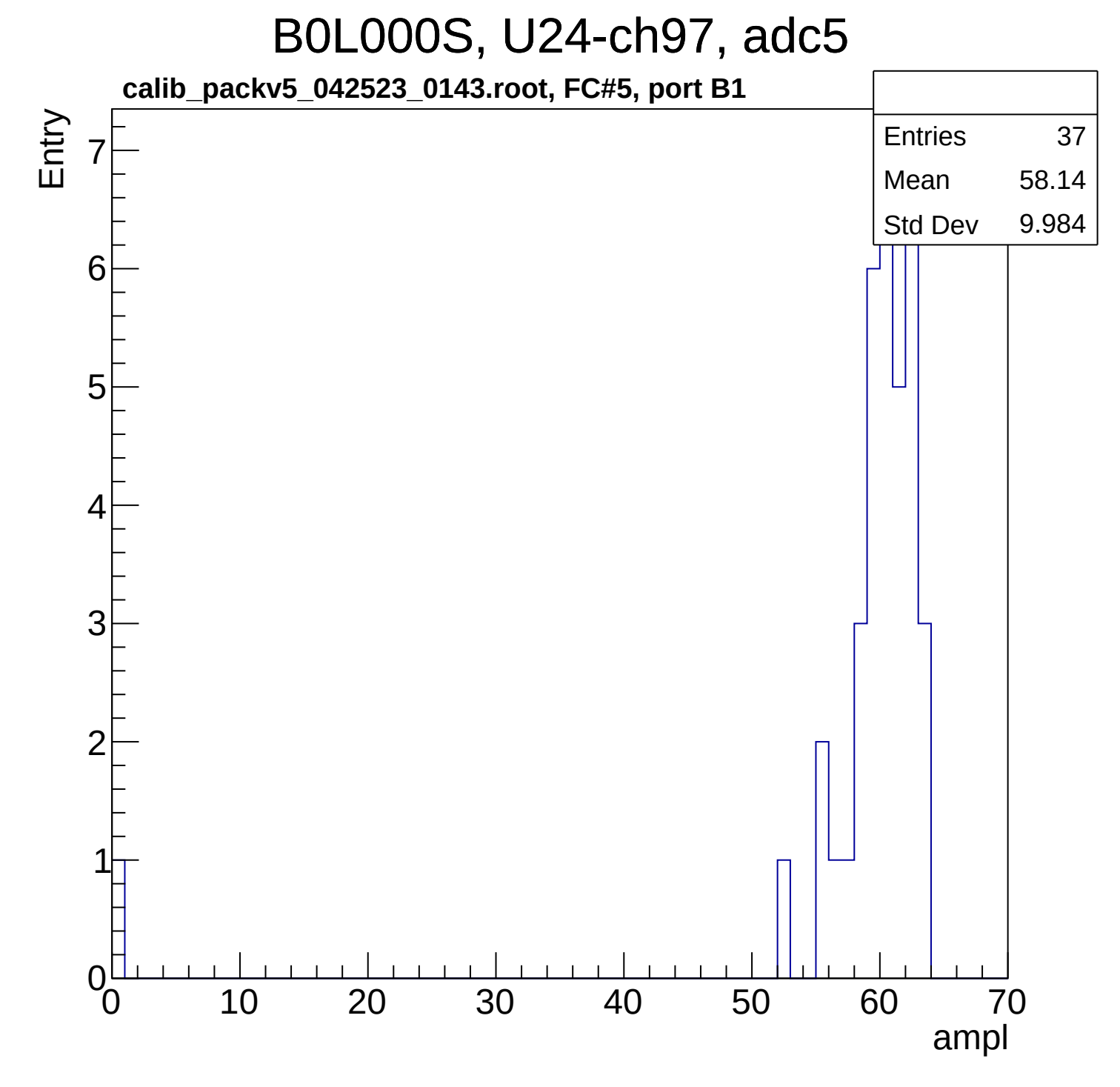
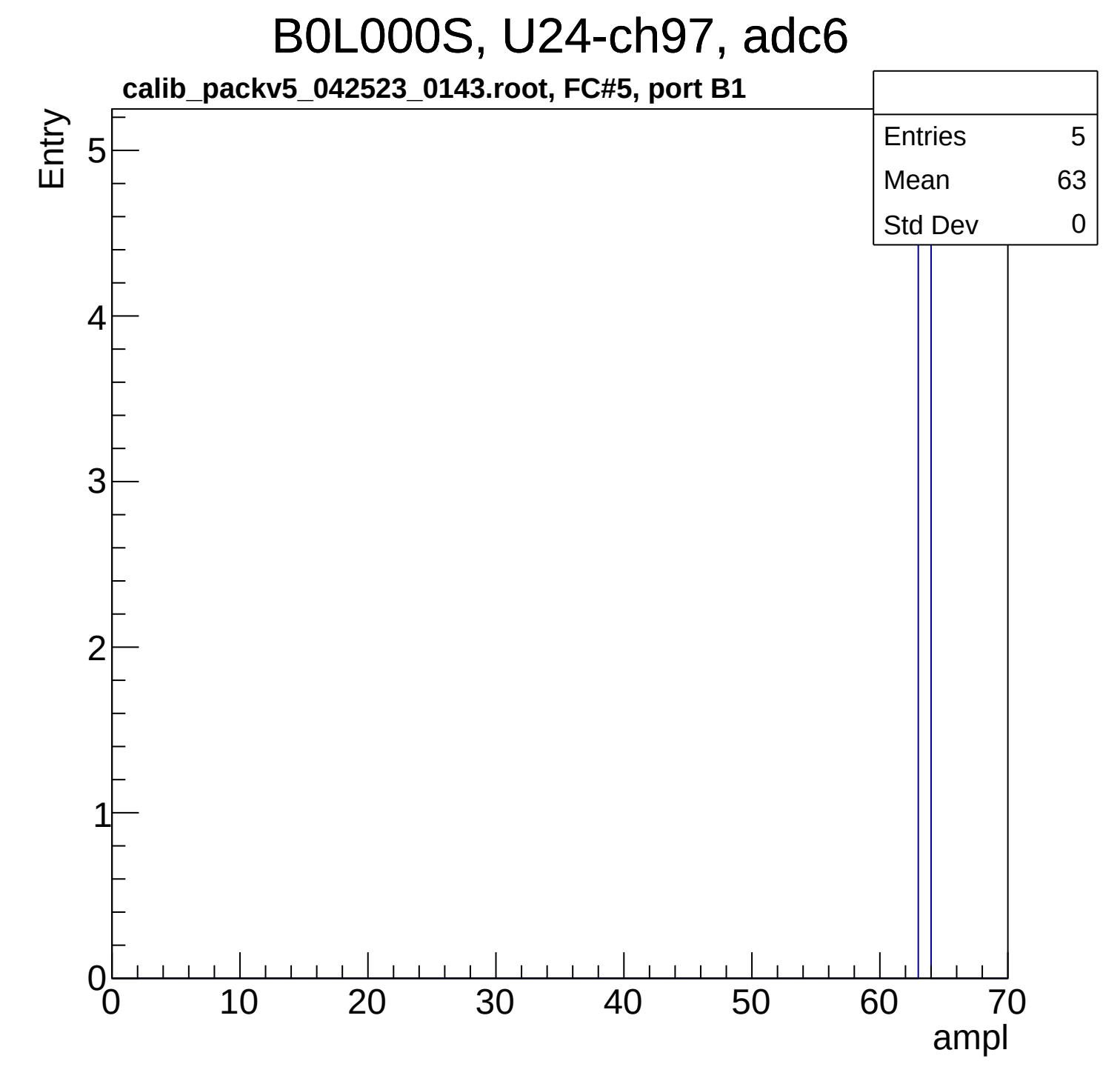
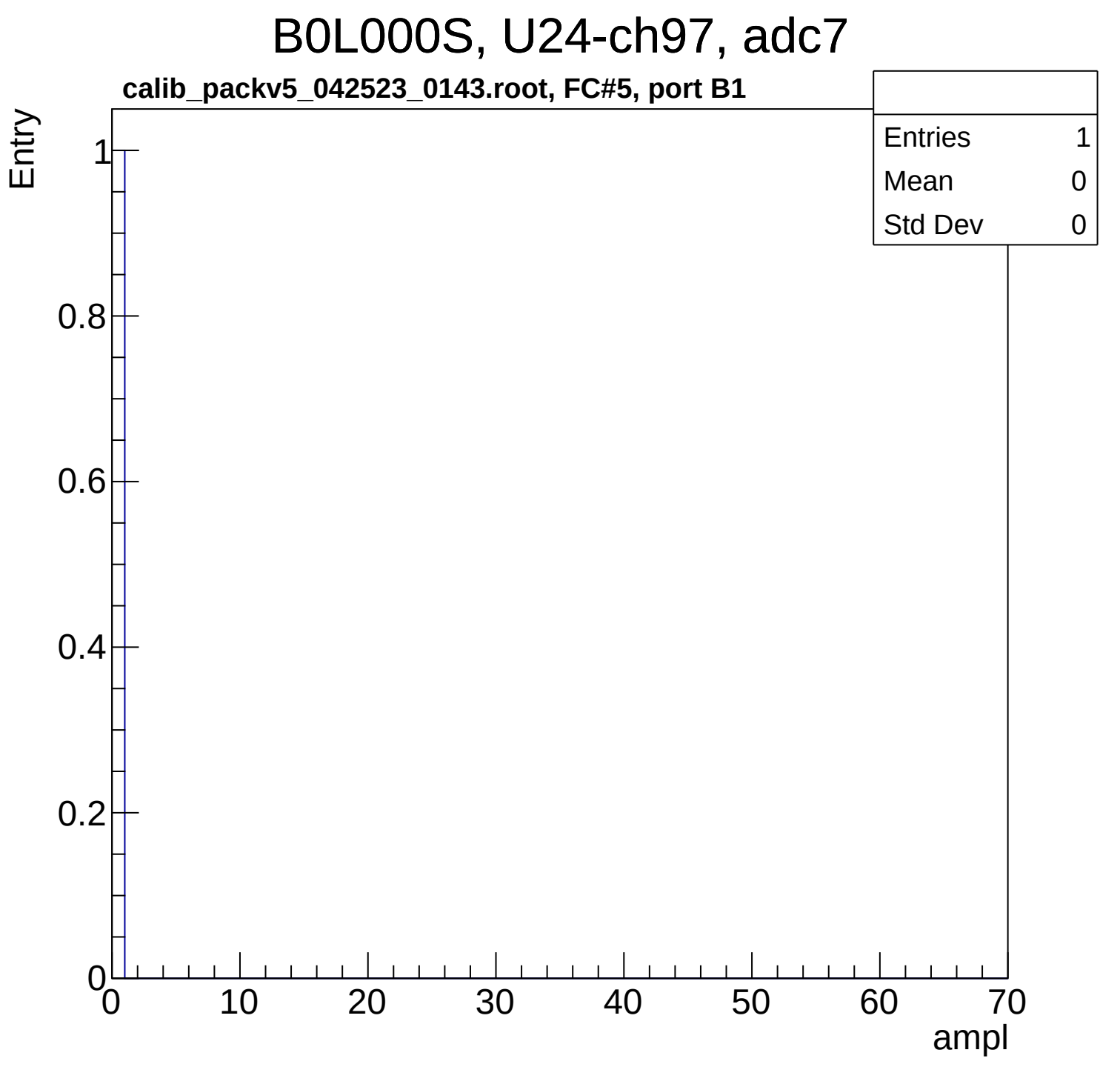
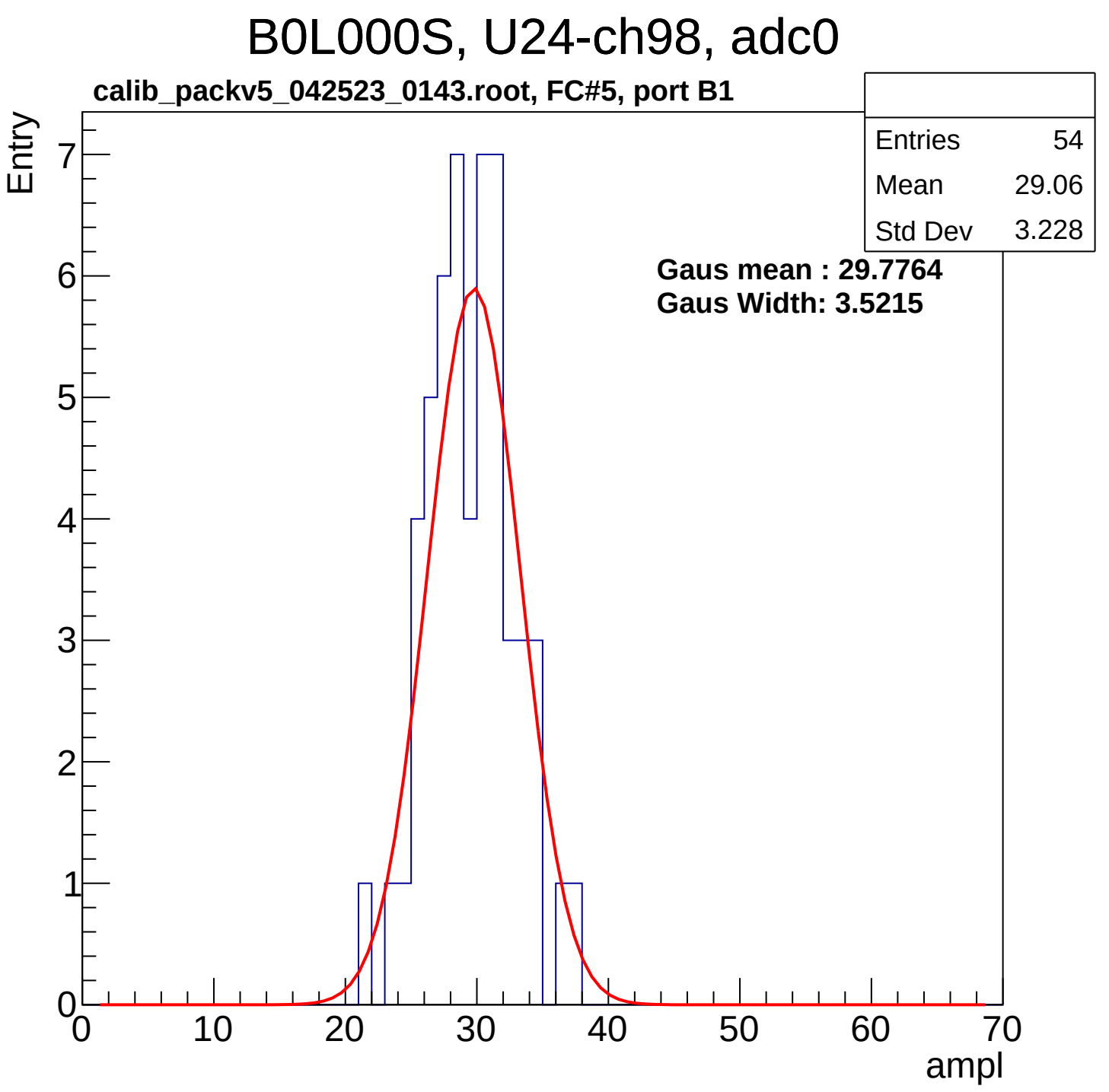
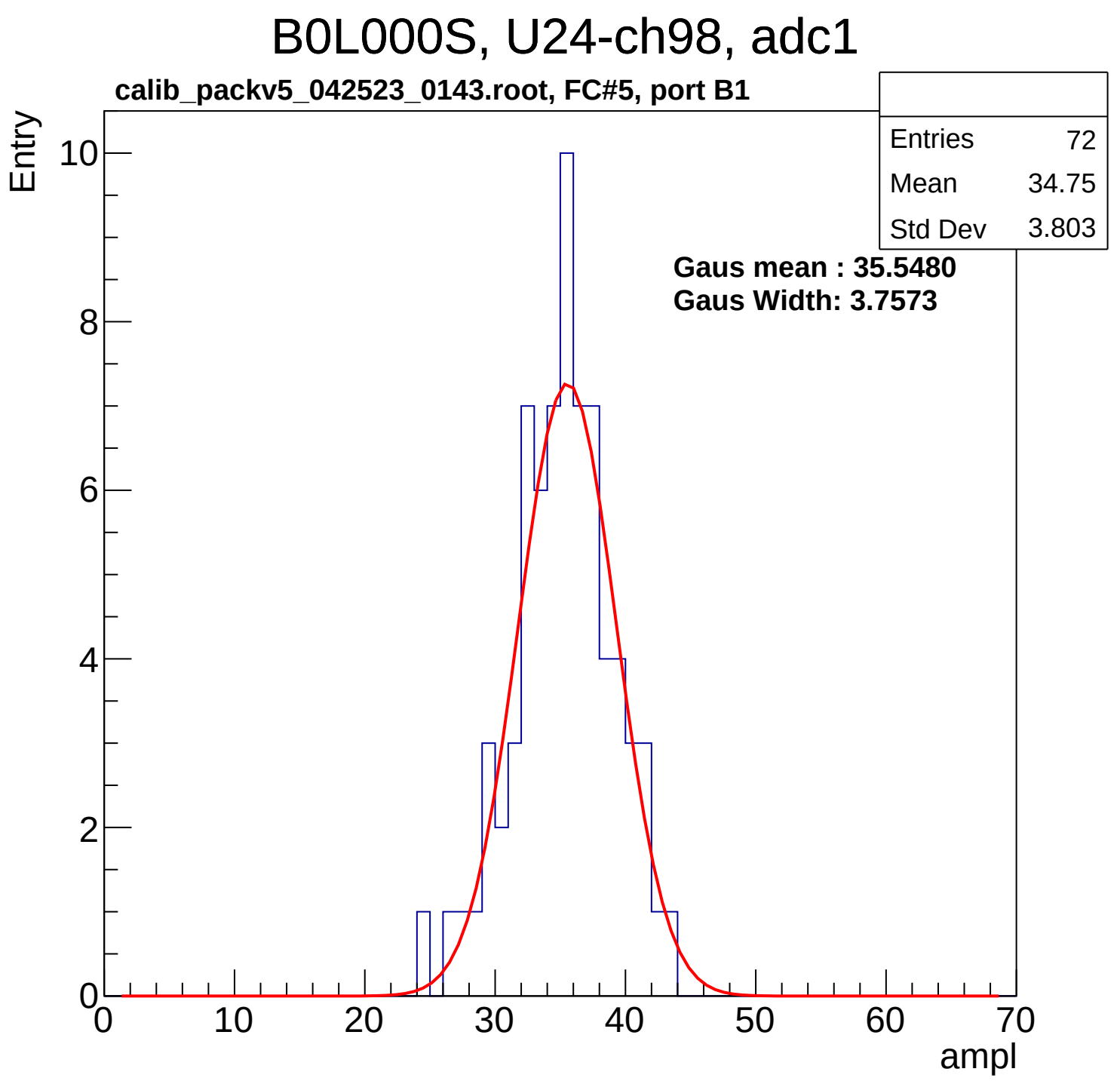
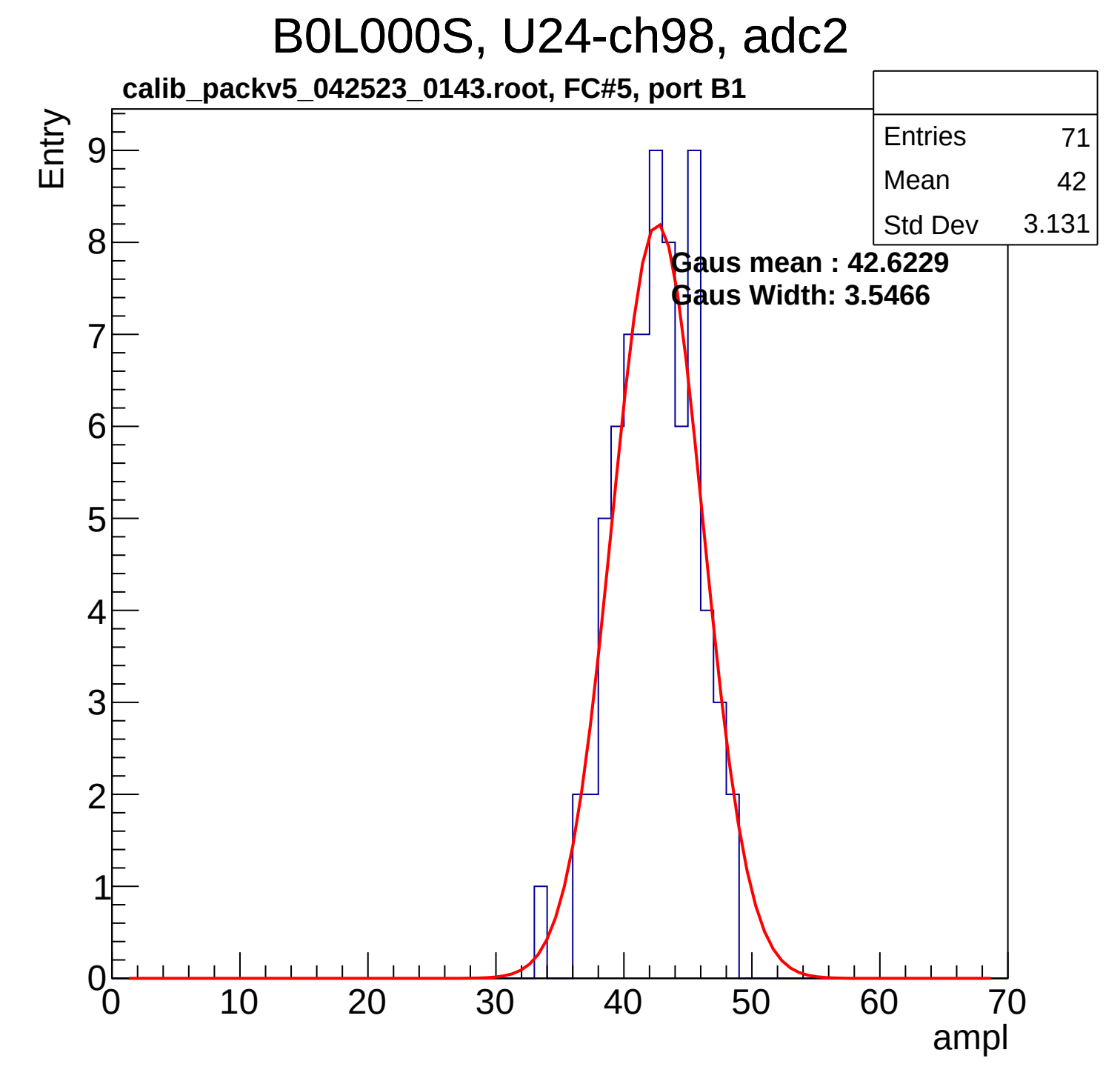

B0L000S, U24-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.14
Std Dev 9.984
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 63
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.06
Std Dev 3.228
Gaus mean : 29.7764
Gaus Width: 3.5215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 34.75
Std Dev 3.803
Gaus mean : 35.5480
Gaus Width: 3.7573
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42
Std Dev 3.131
Gaus mean : 42.6229
Gaus Width: 3.5466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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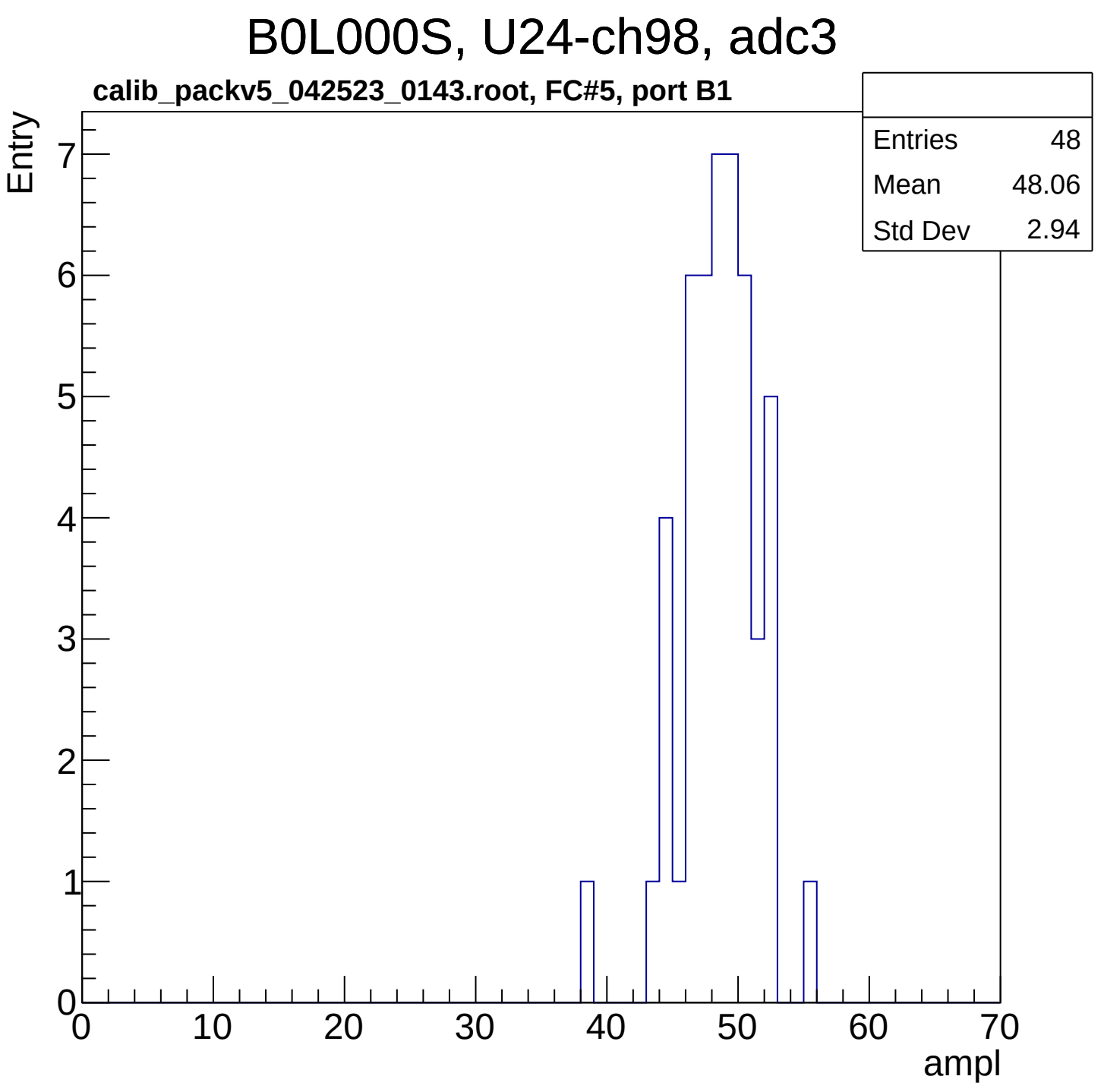
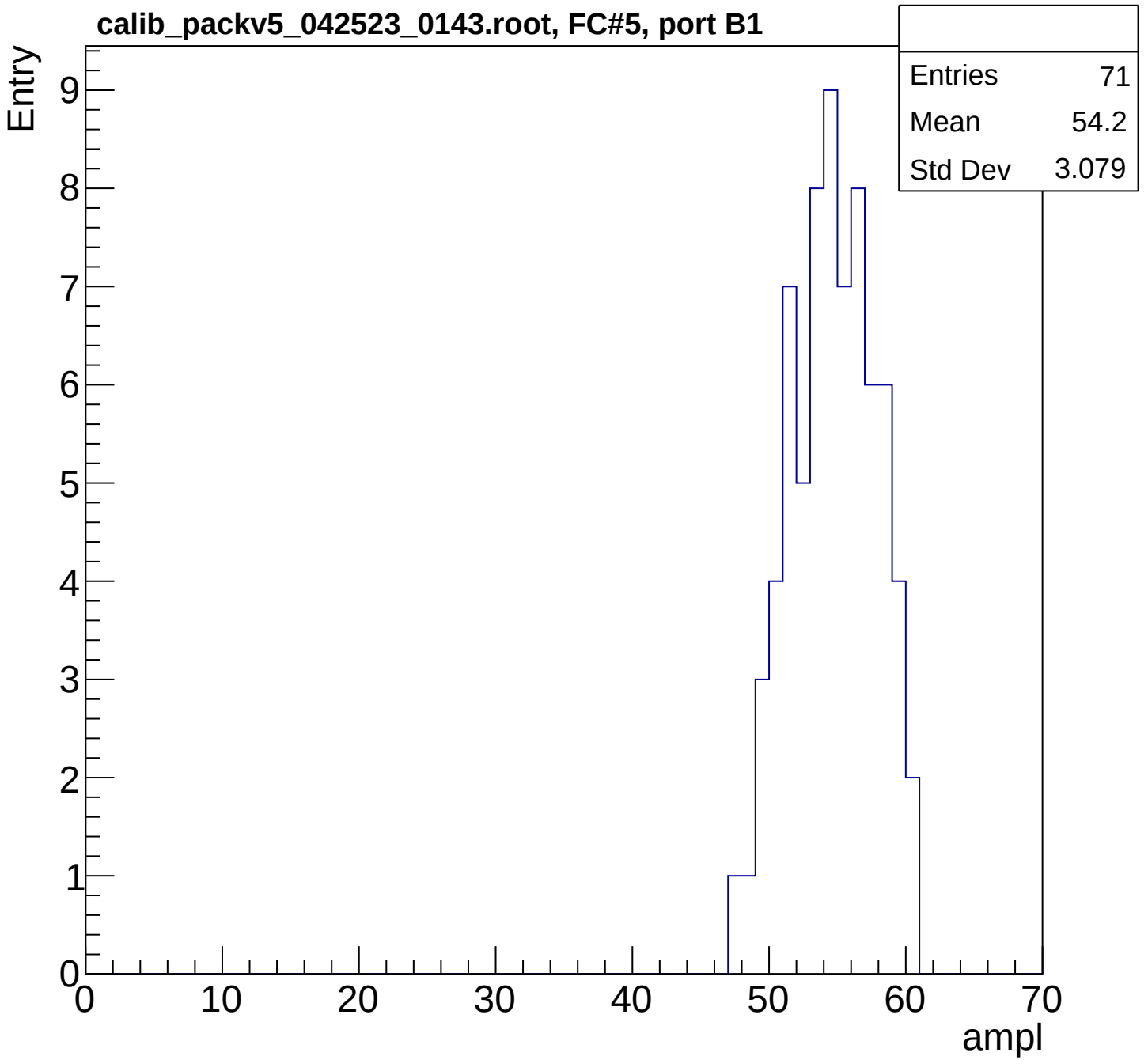

B0L000S, U24-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 48.06
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.2
Std Dev 3.079
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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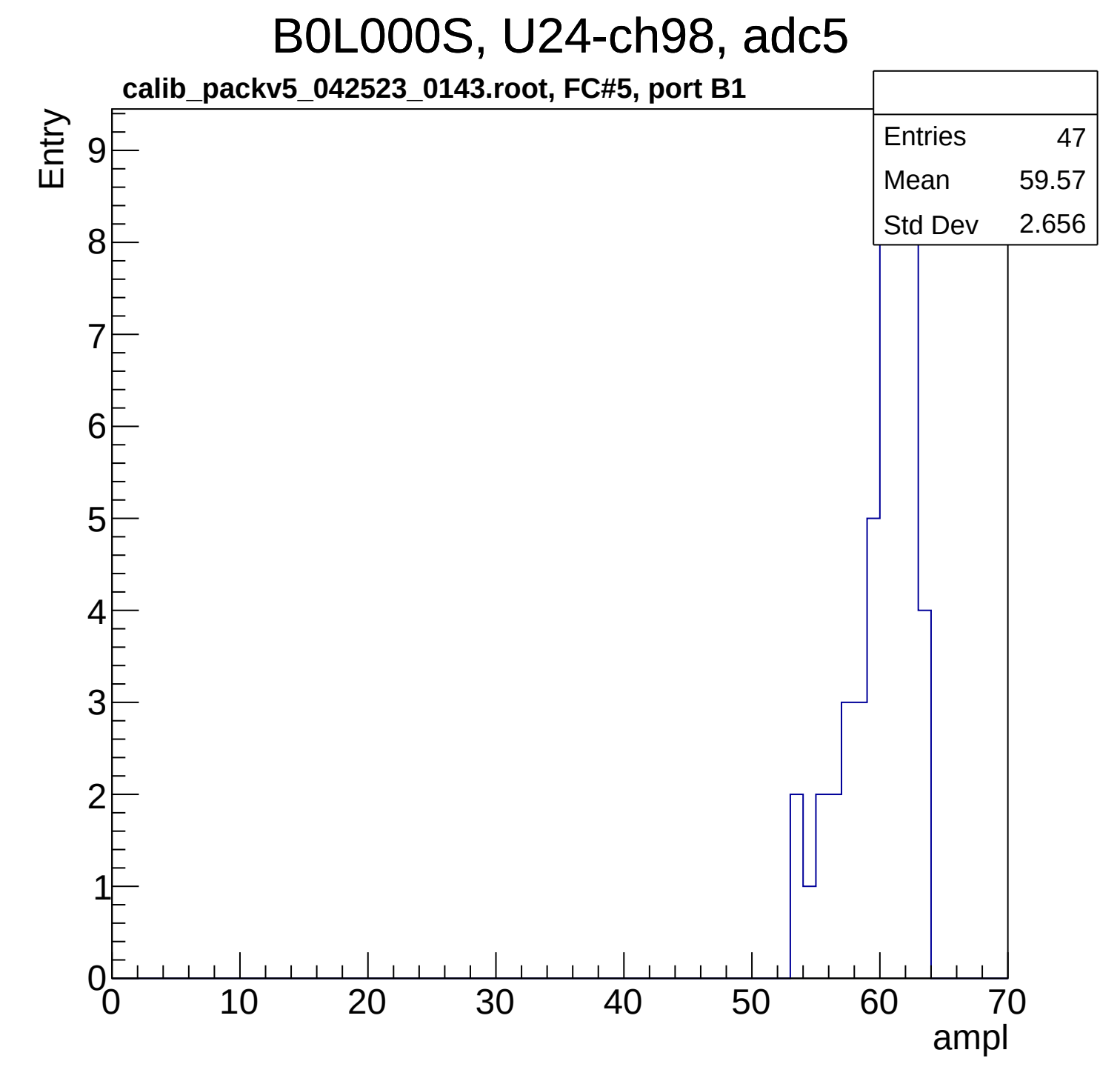
60
70


B0L000S, U24-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.57
Std Dev 2.656
Entry
ampl
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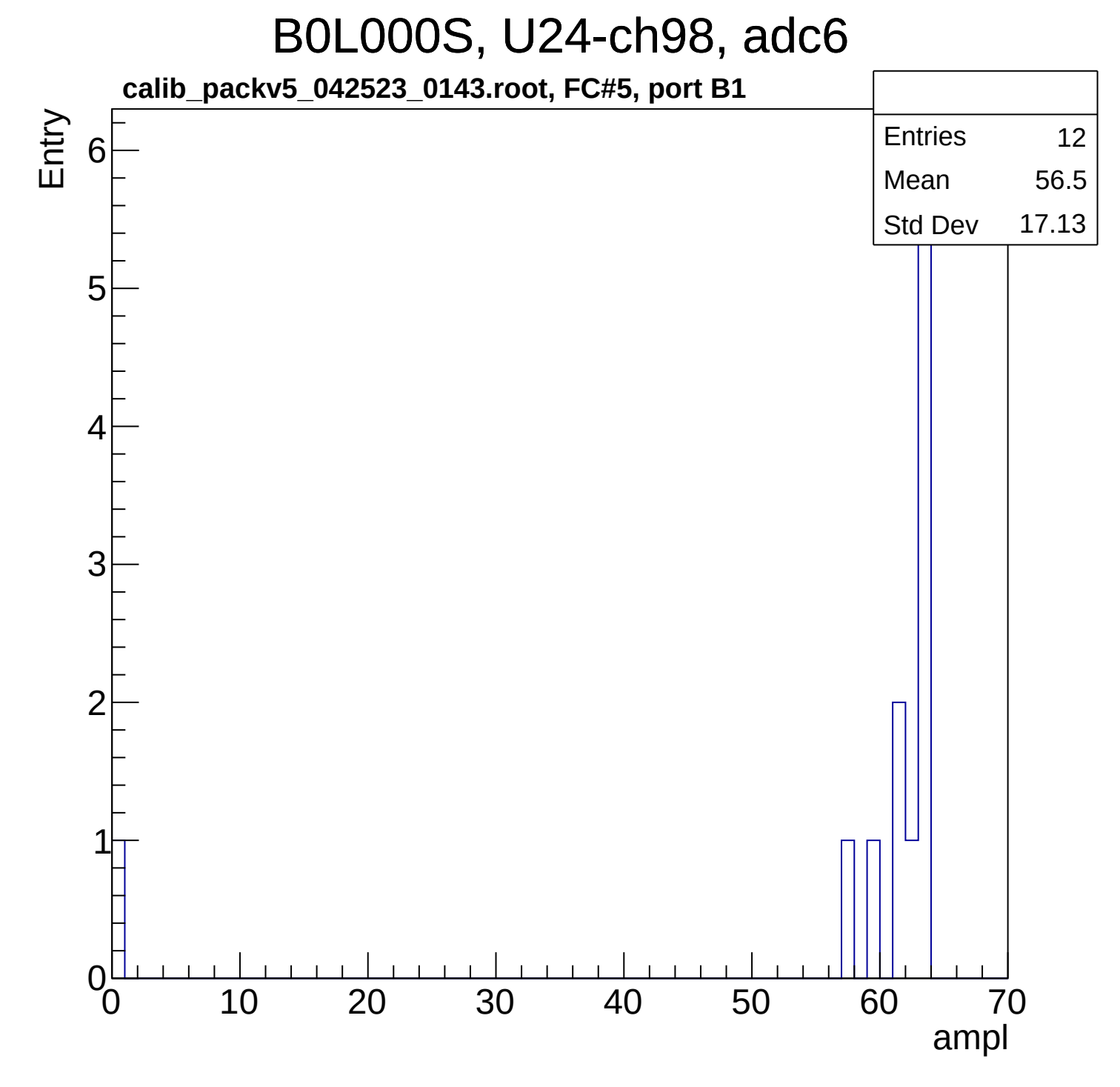
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.5
Std Dev 17.13
Entry
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1
2
3
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10
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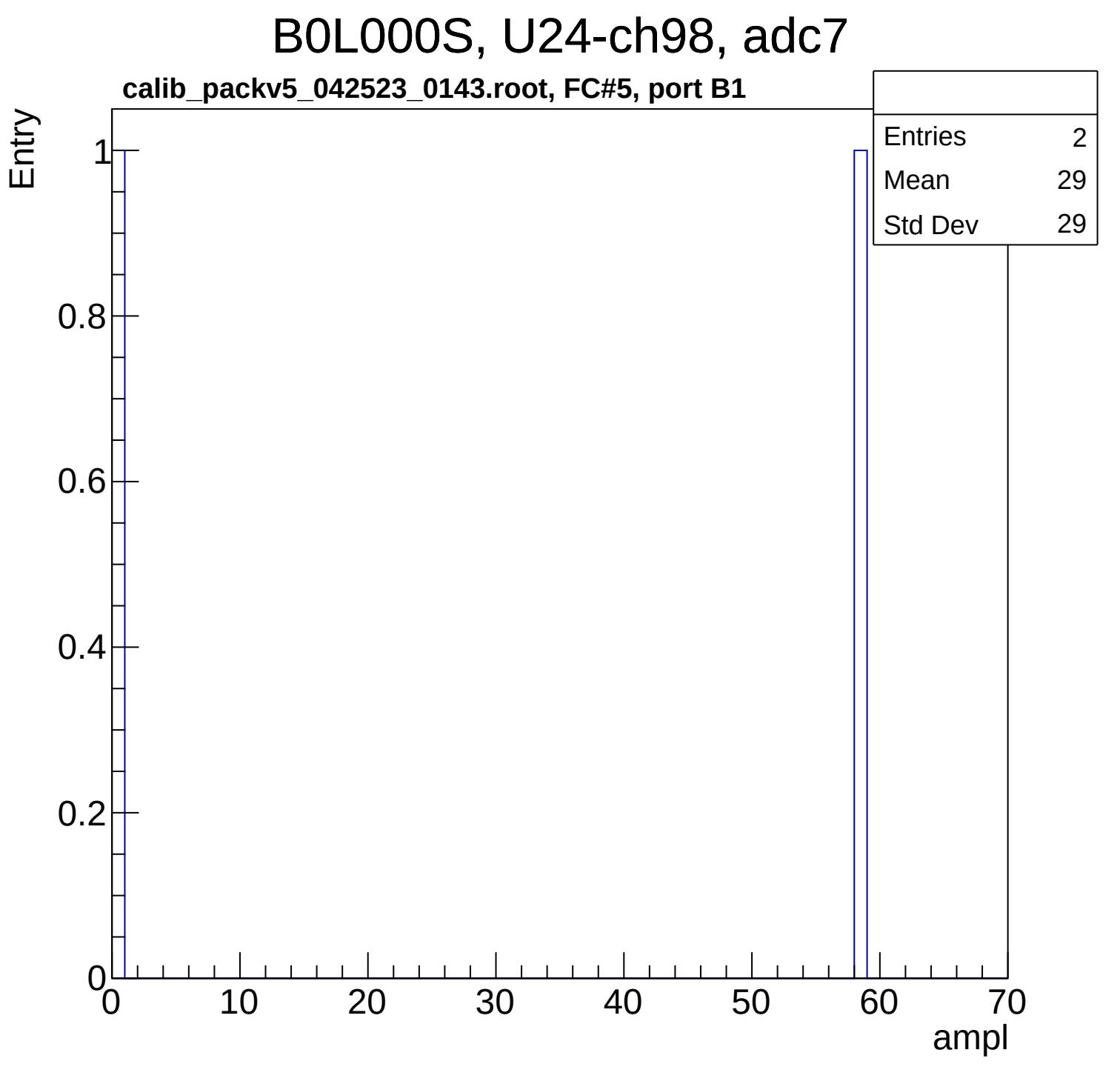
70
ampl


B0L000S, U24-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 29
Std Dev 29
Entry
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0.6
0.4
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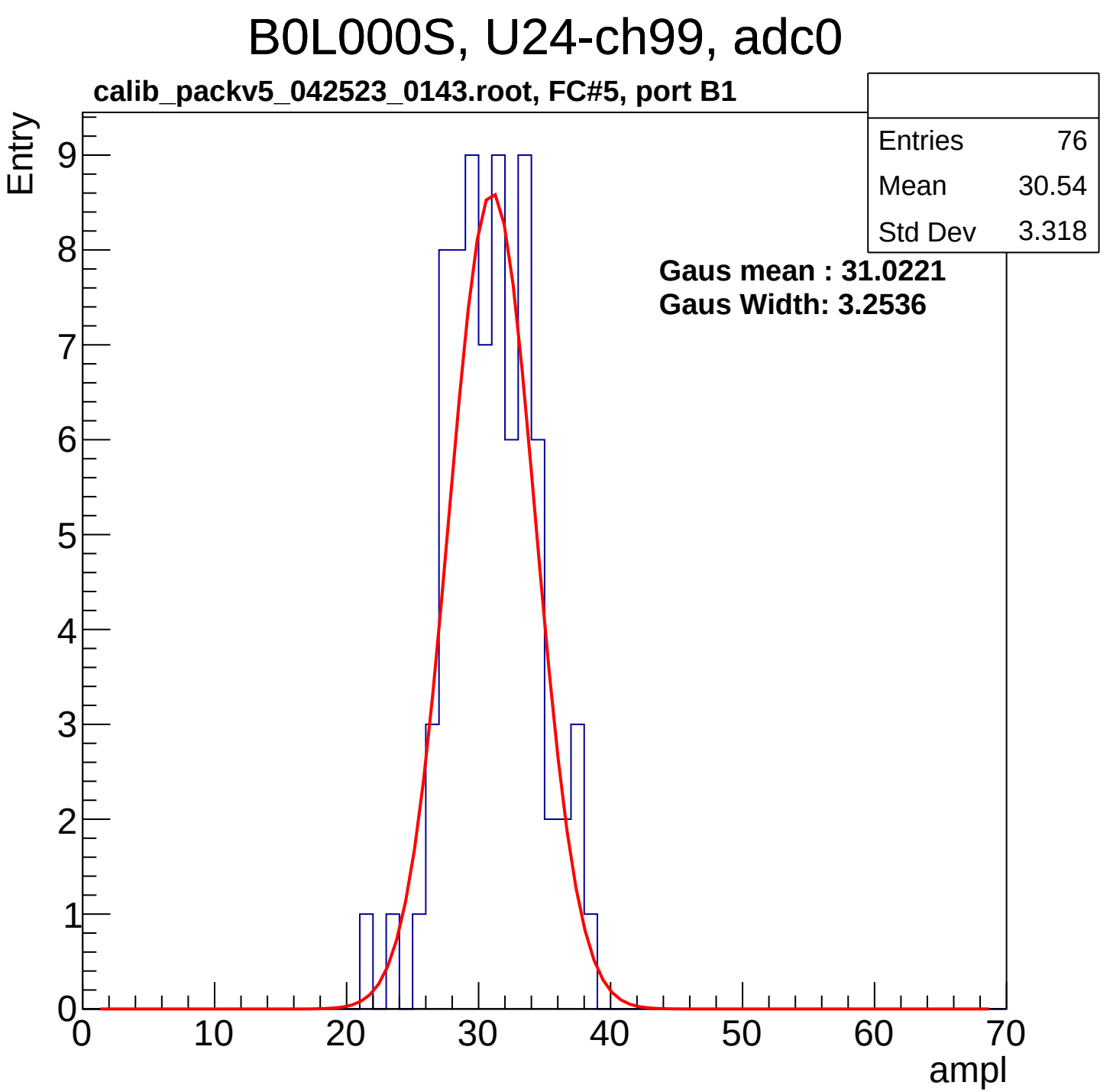
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.54
Std Dev 3.318
Gaus mean : 31.0221
Gaus Width: 3.2536
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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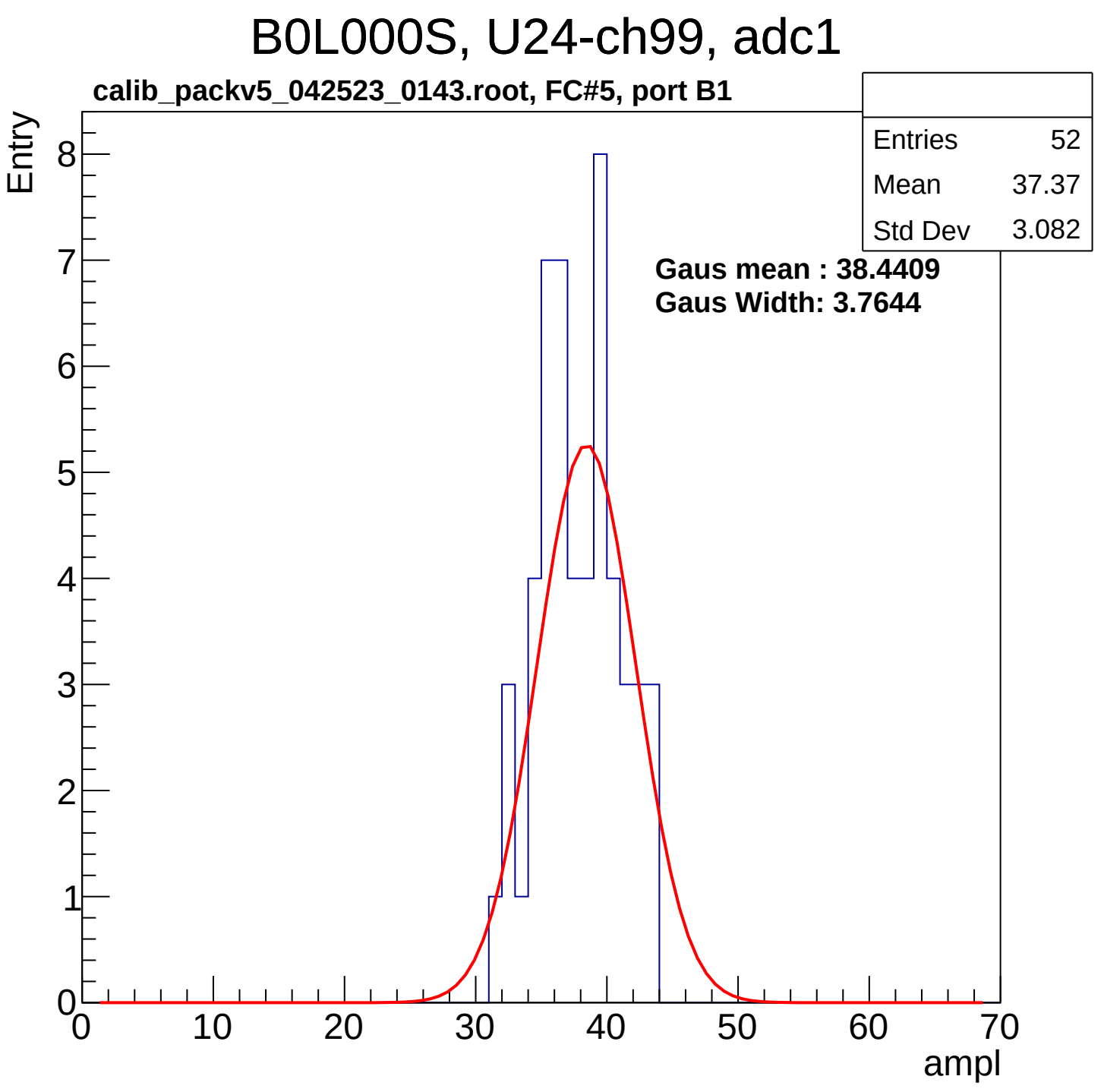
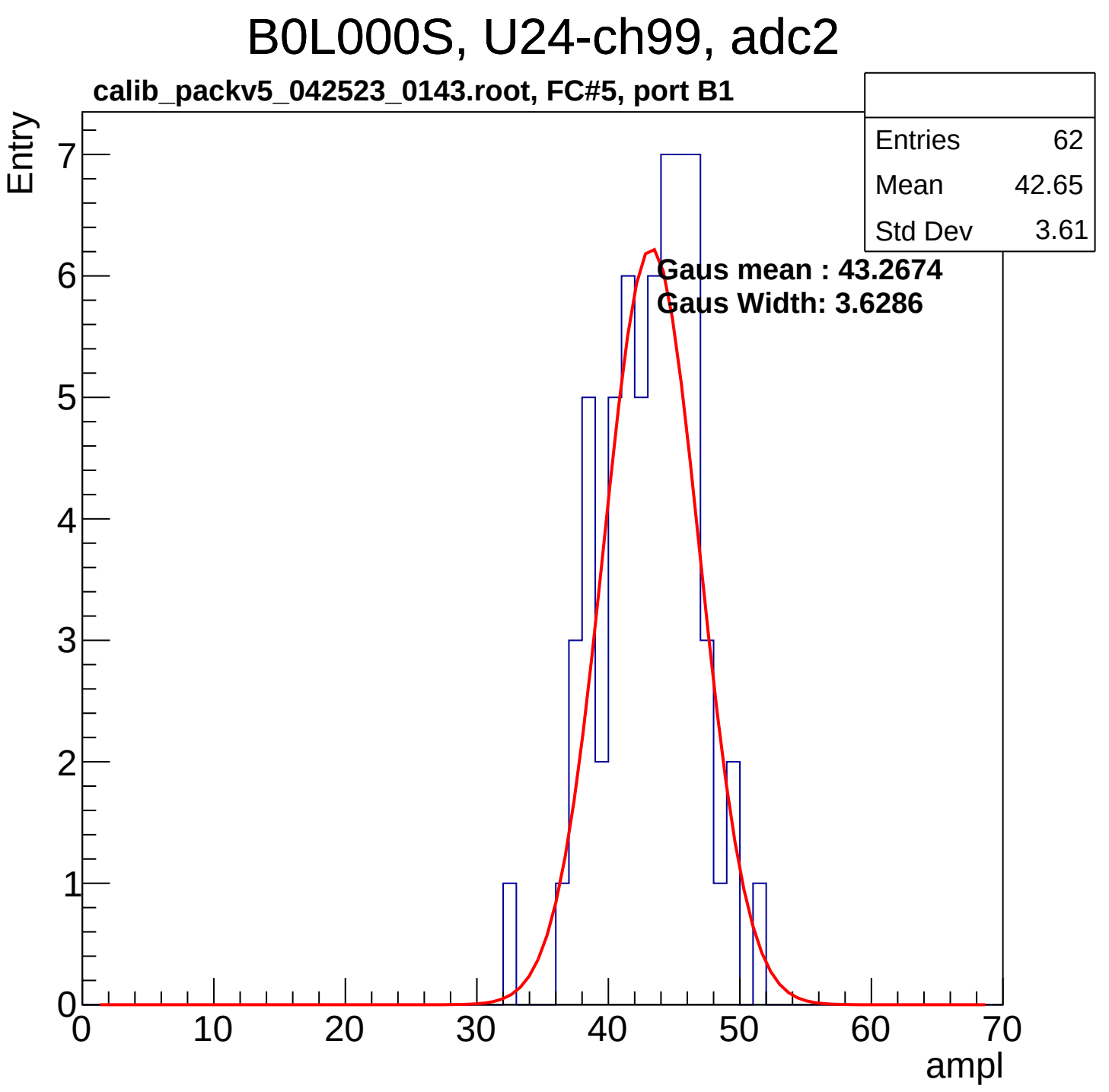
60
70


B0L000S, U24-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 37.37
Std Dev 3.082
Gaus mean : 38.4409
Gaus Width: 3.7644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 42.65
Std Dev 3.61
Gaus mean : 43.2674
Gaus Width: 3.6286
Entry
ampl
0
1
2
3
4
5
6
7
0
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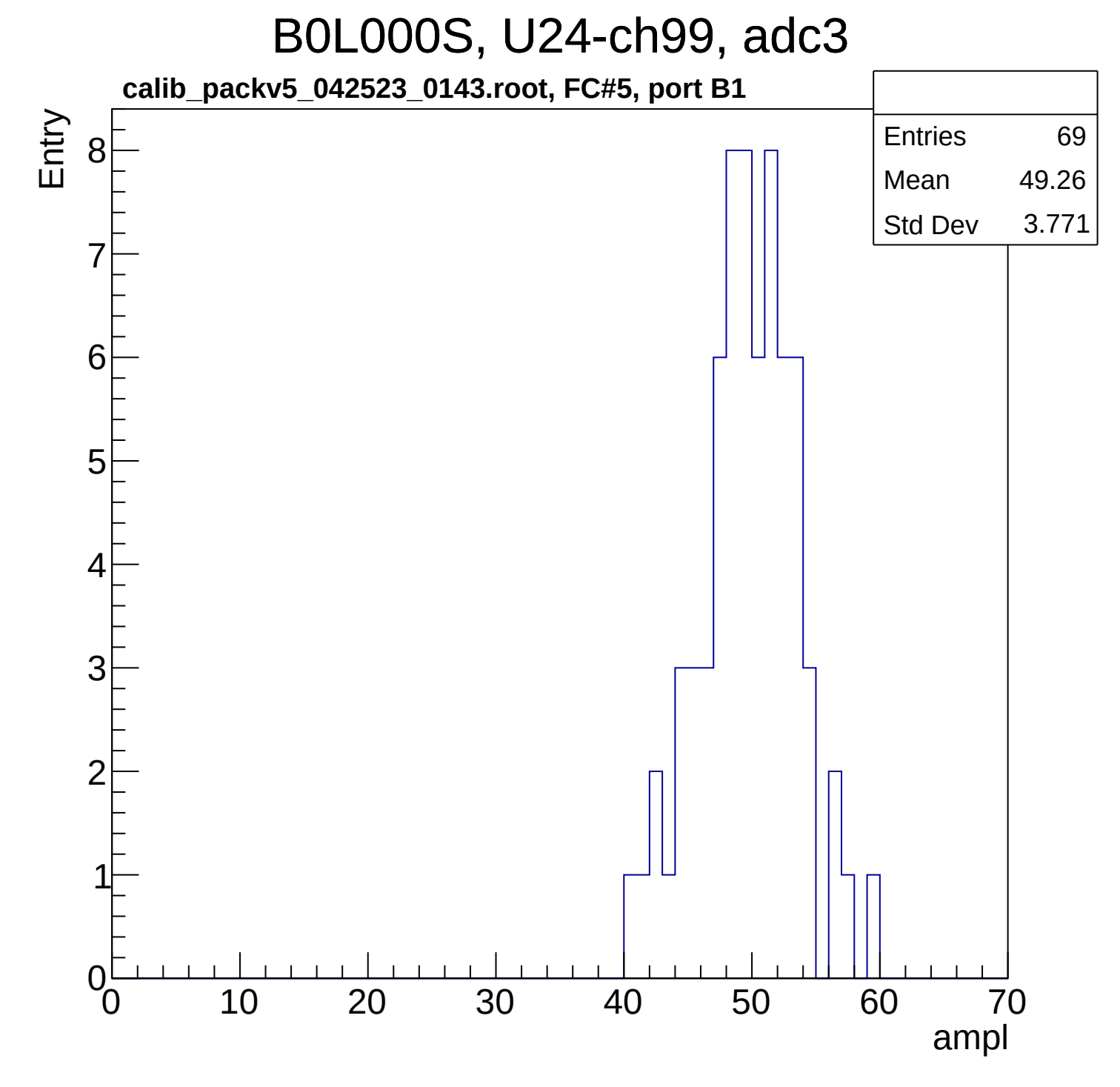
20
30
40
50
60
70


B0L000S, U24-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.26
Std Dev 3.771
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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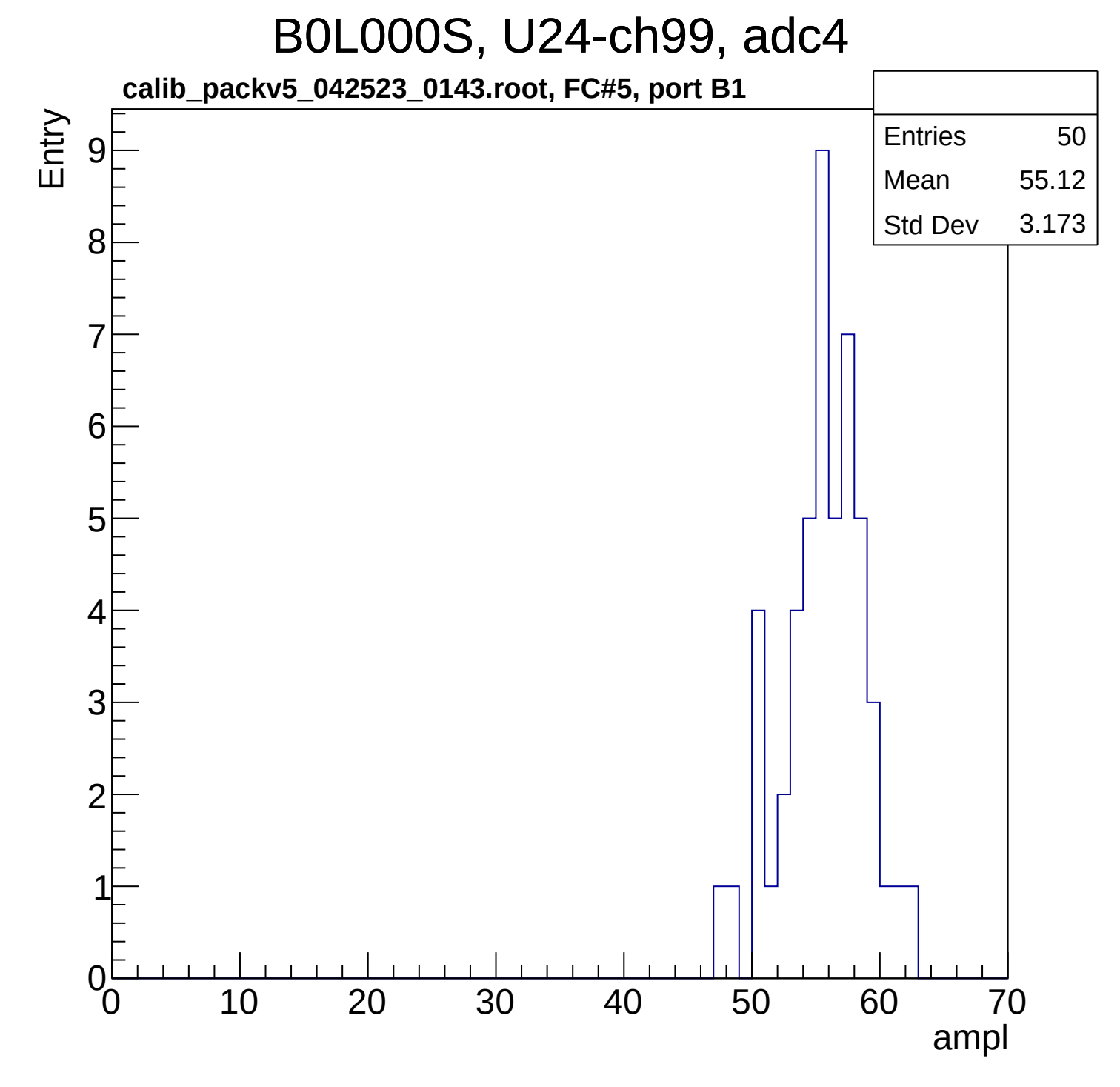
B0L000S, U24-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.12
Std Dev 3.173
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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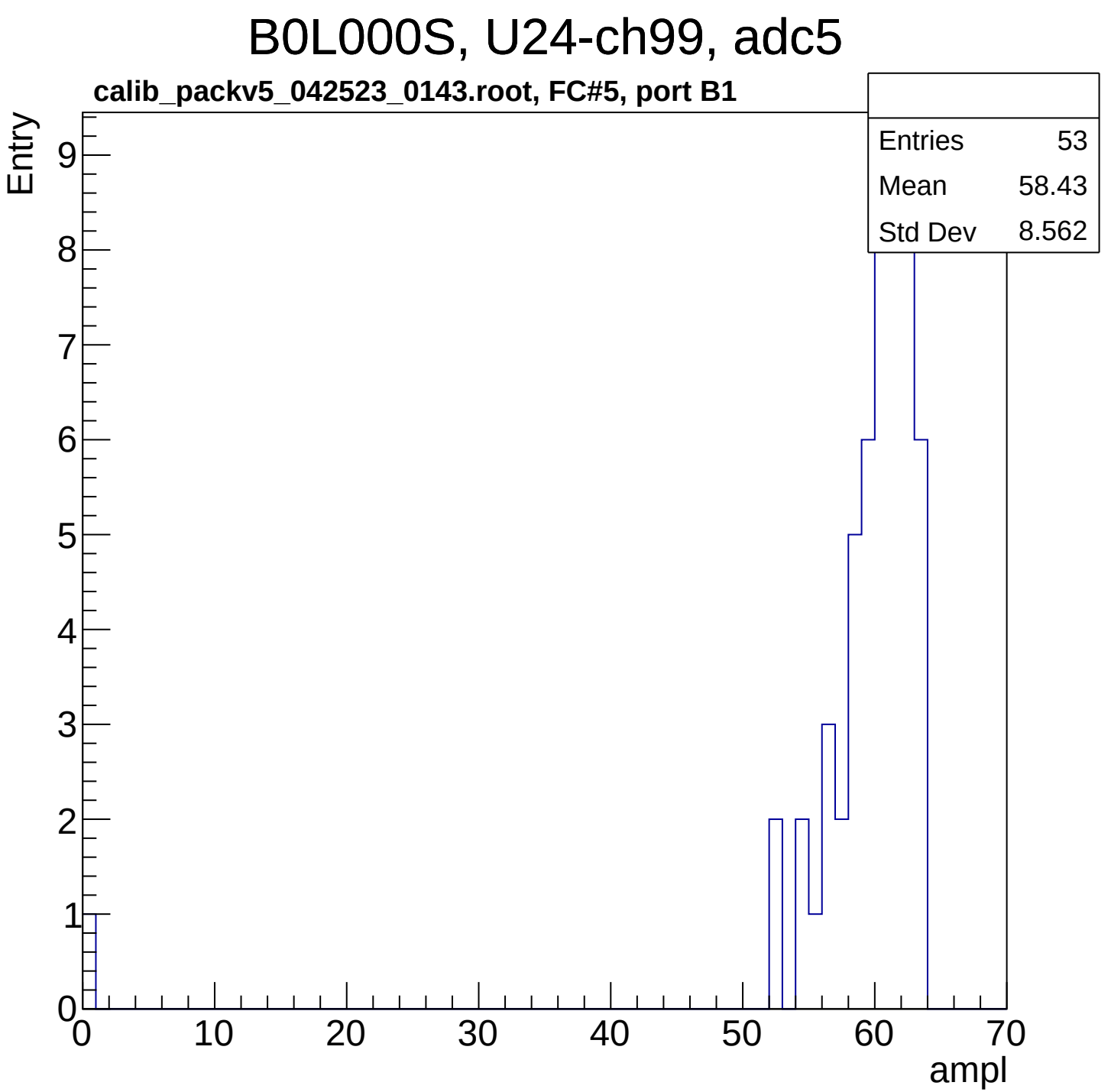
B0L000S, U24-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.43
Std Dev 8.562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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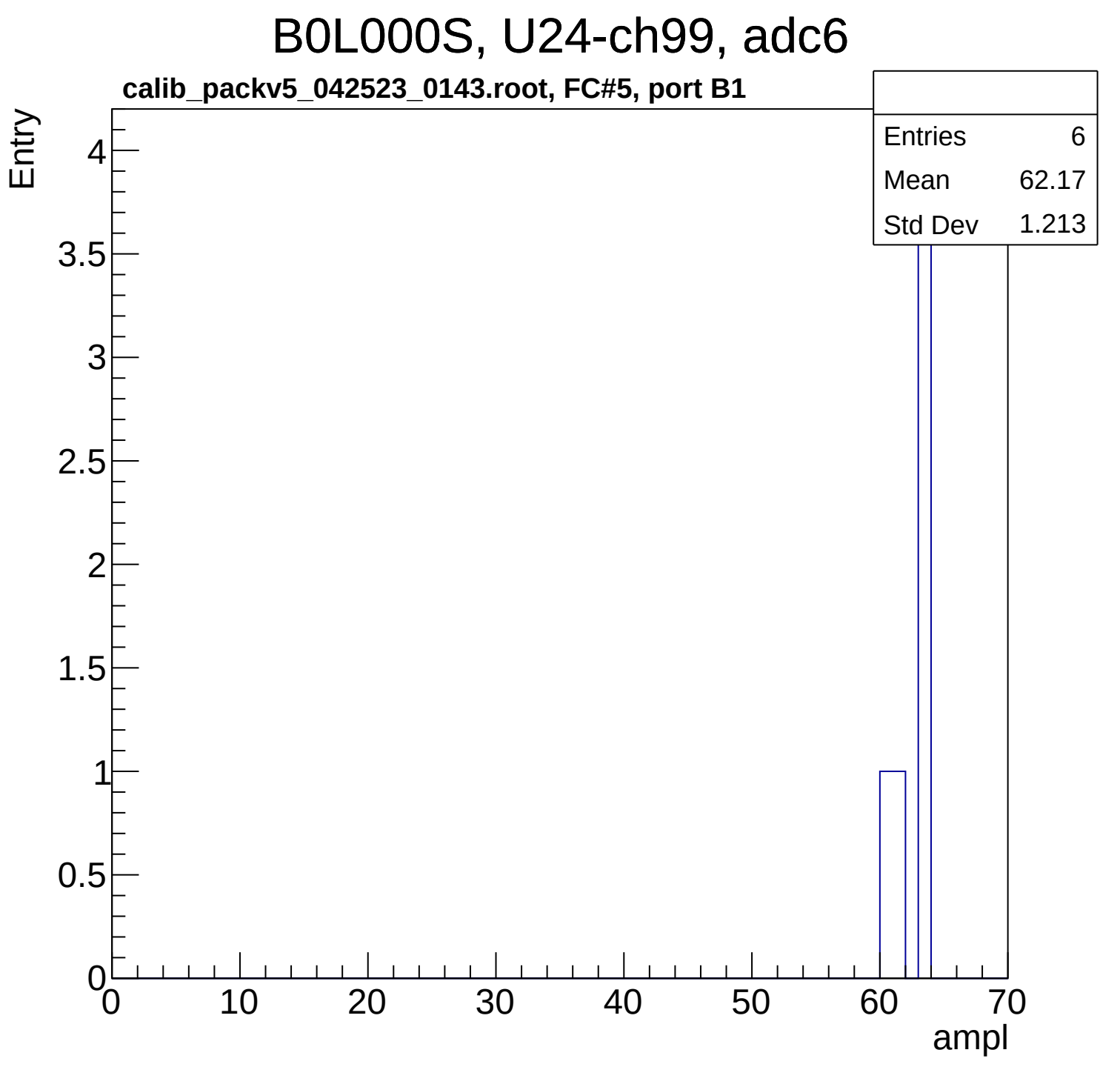
B0L000S, U24-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
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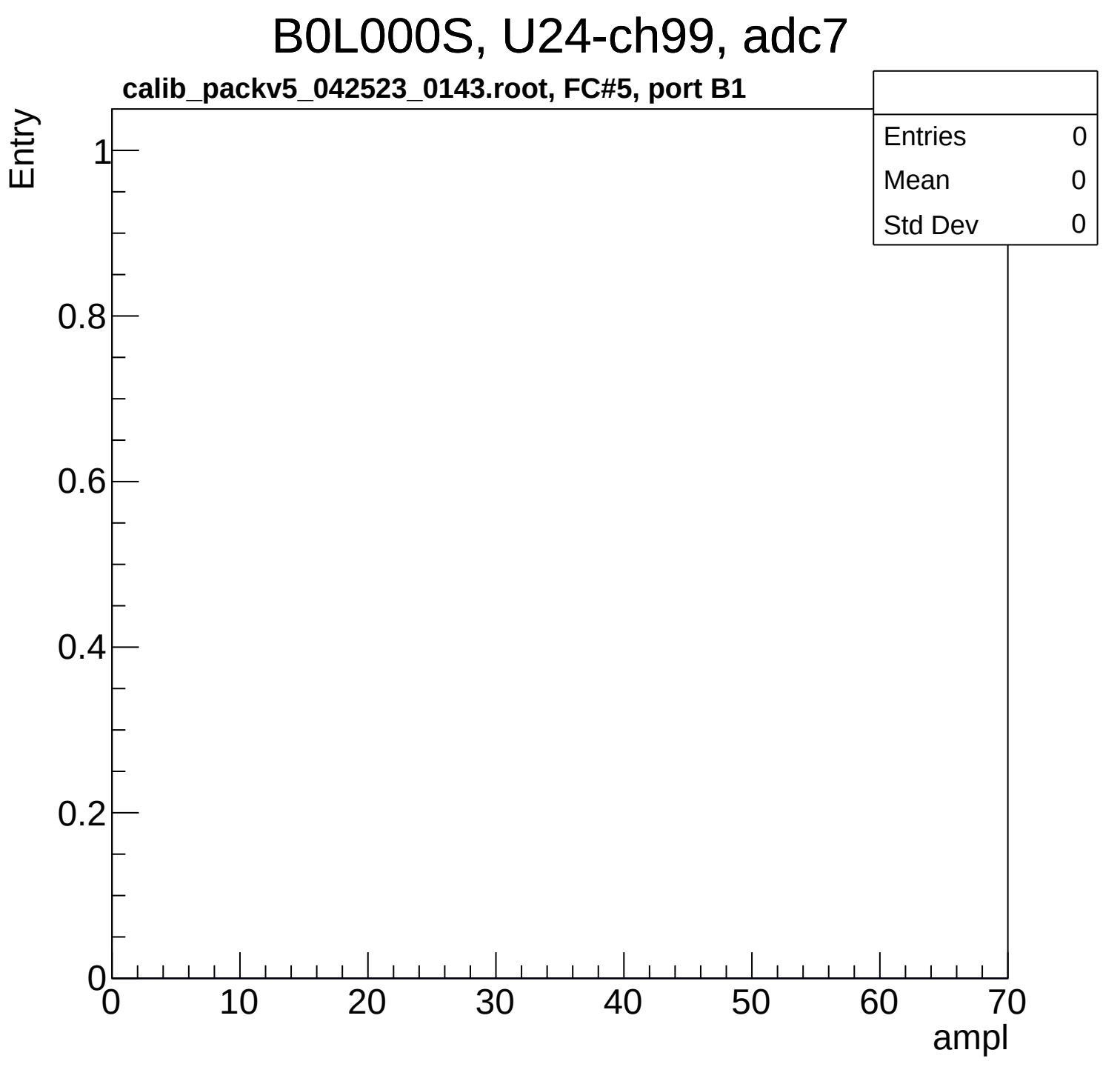
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
0.2
0
0
10
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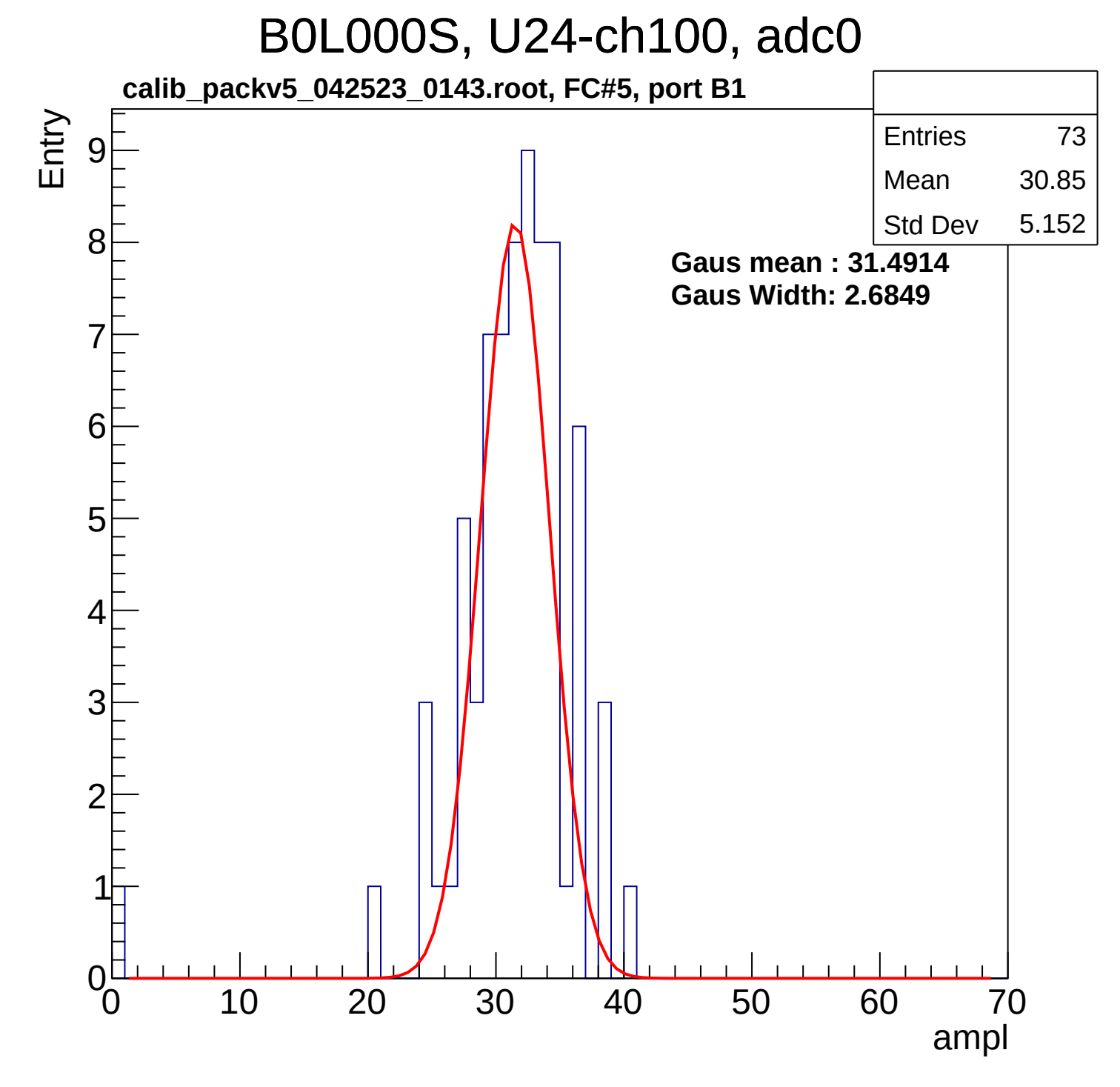
50
60
70
ampl


B0L000S, U24-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 30.85
Std Dev 5.152
Gaus mean : 31.4914
Gaus Width: 2.6849
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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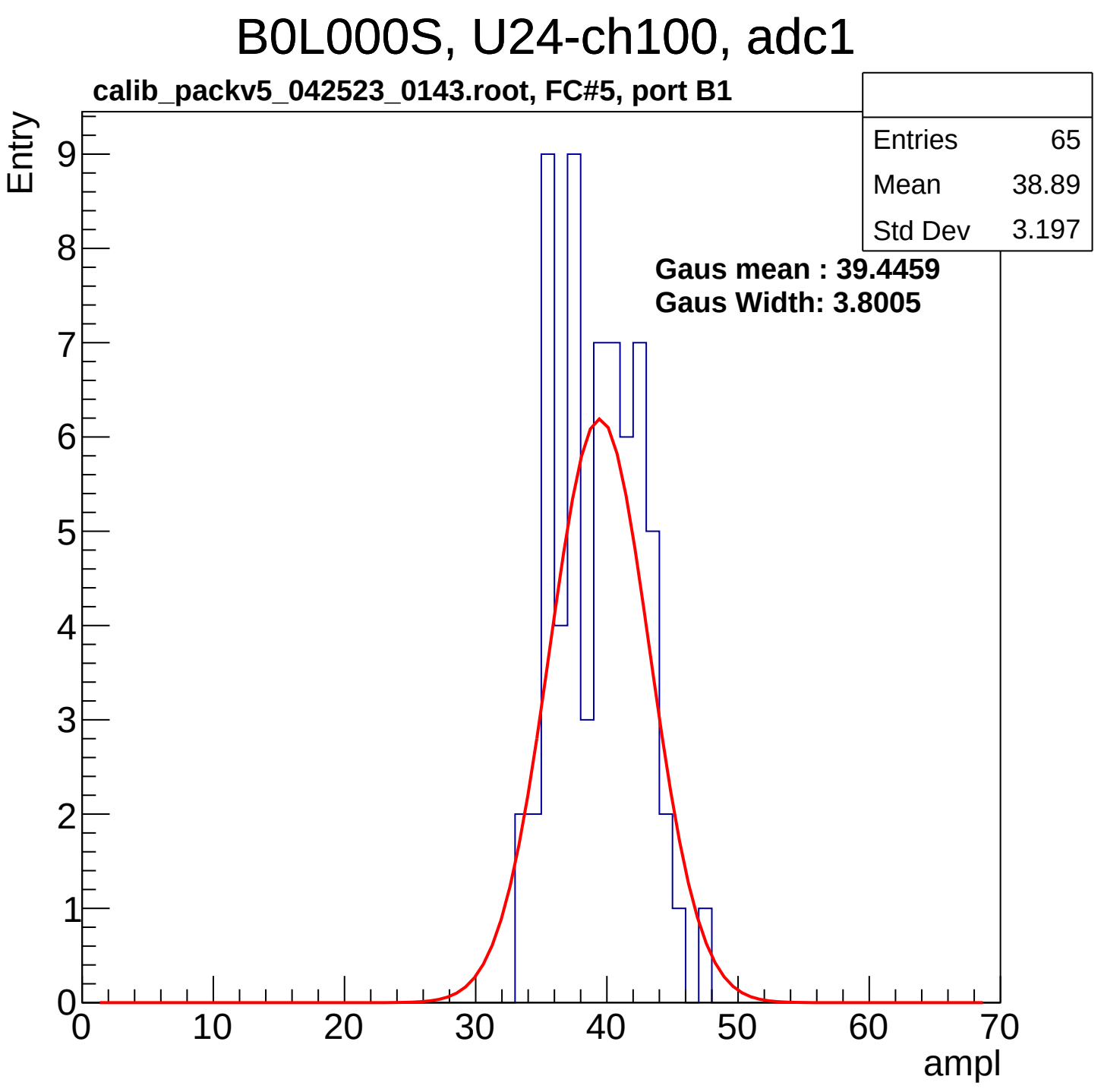
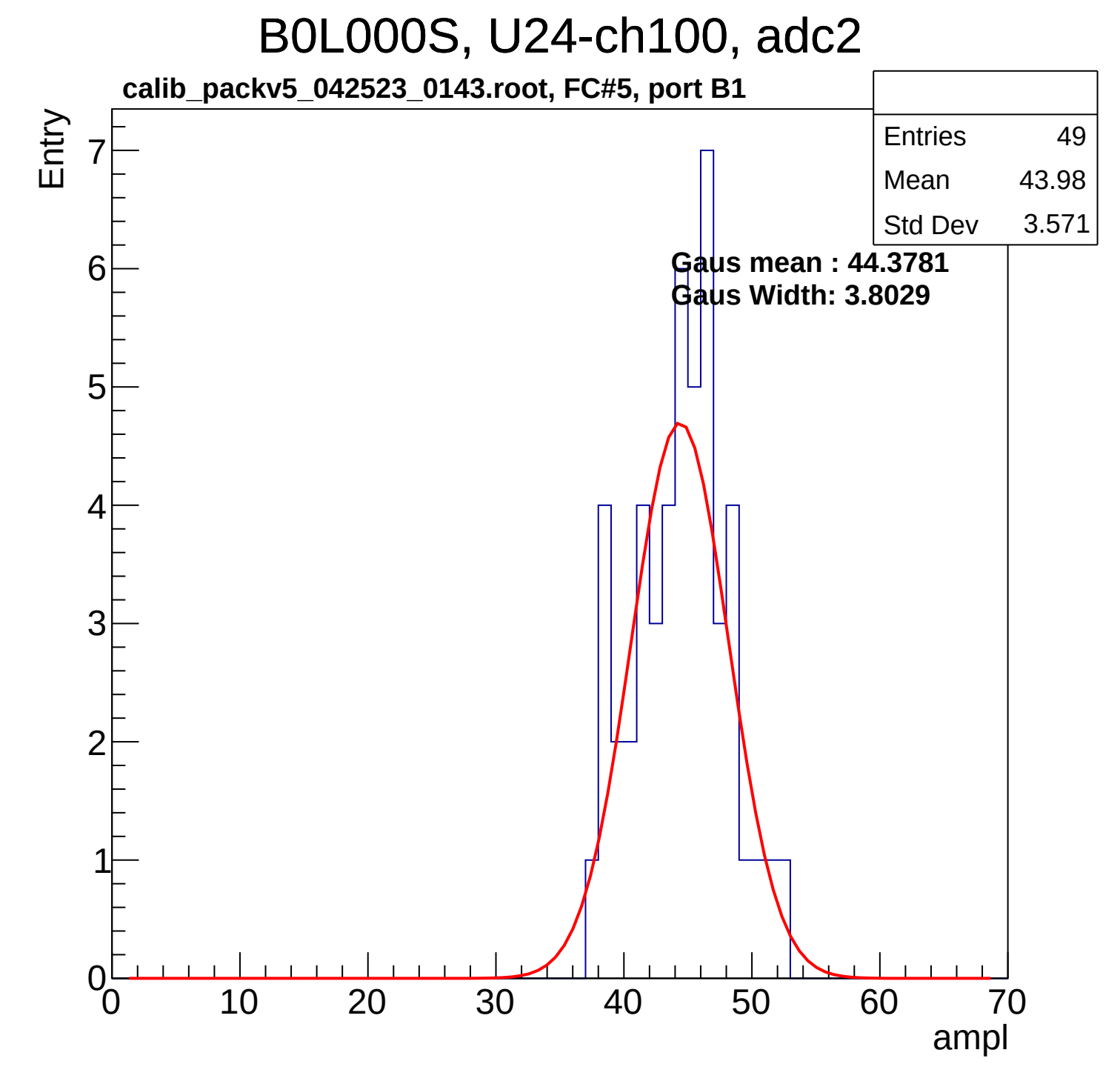
40
50
60
70


B0L000S, U24-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.89
Std Dev 3.197
Gaus mean : 39.4459
Gaus Width: 3.8005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 43.98
Std Dev 3.571
Gaus mean : 44.3781
Gaus Width: 3.8029
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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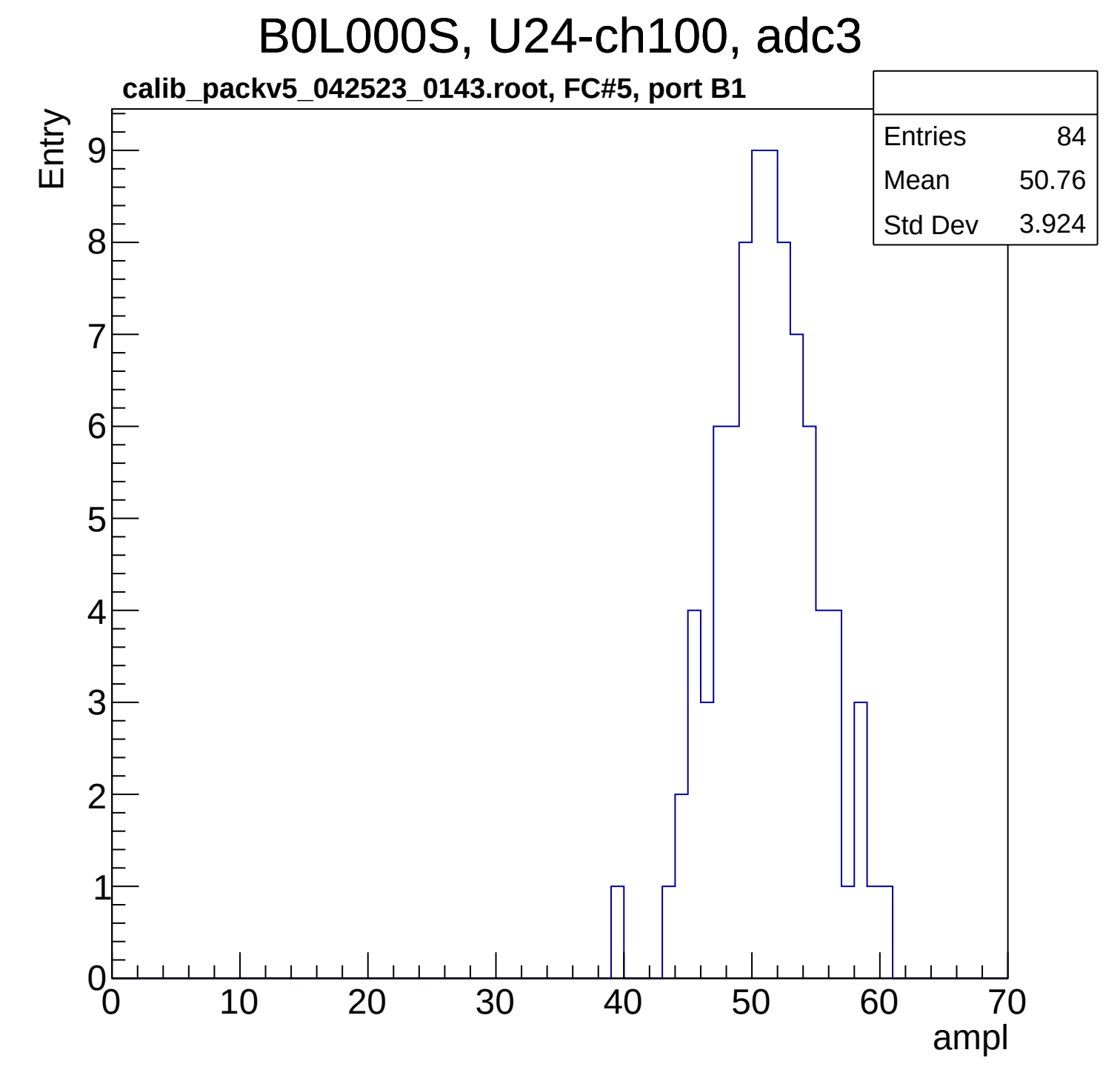
B0L000S, U24-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 50.76
Std Dev 3.924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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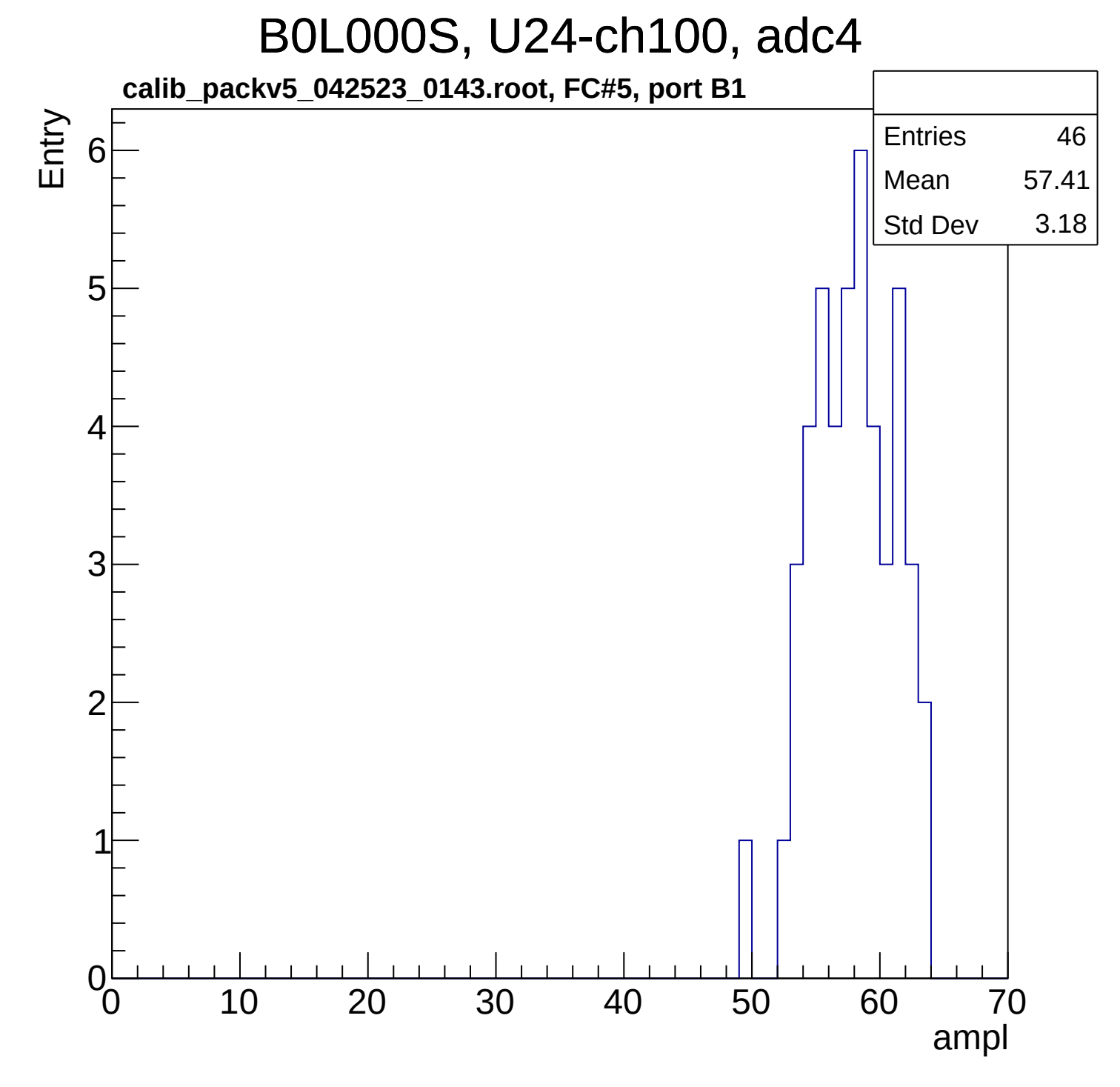
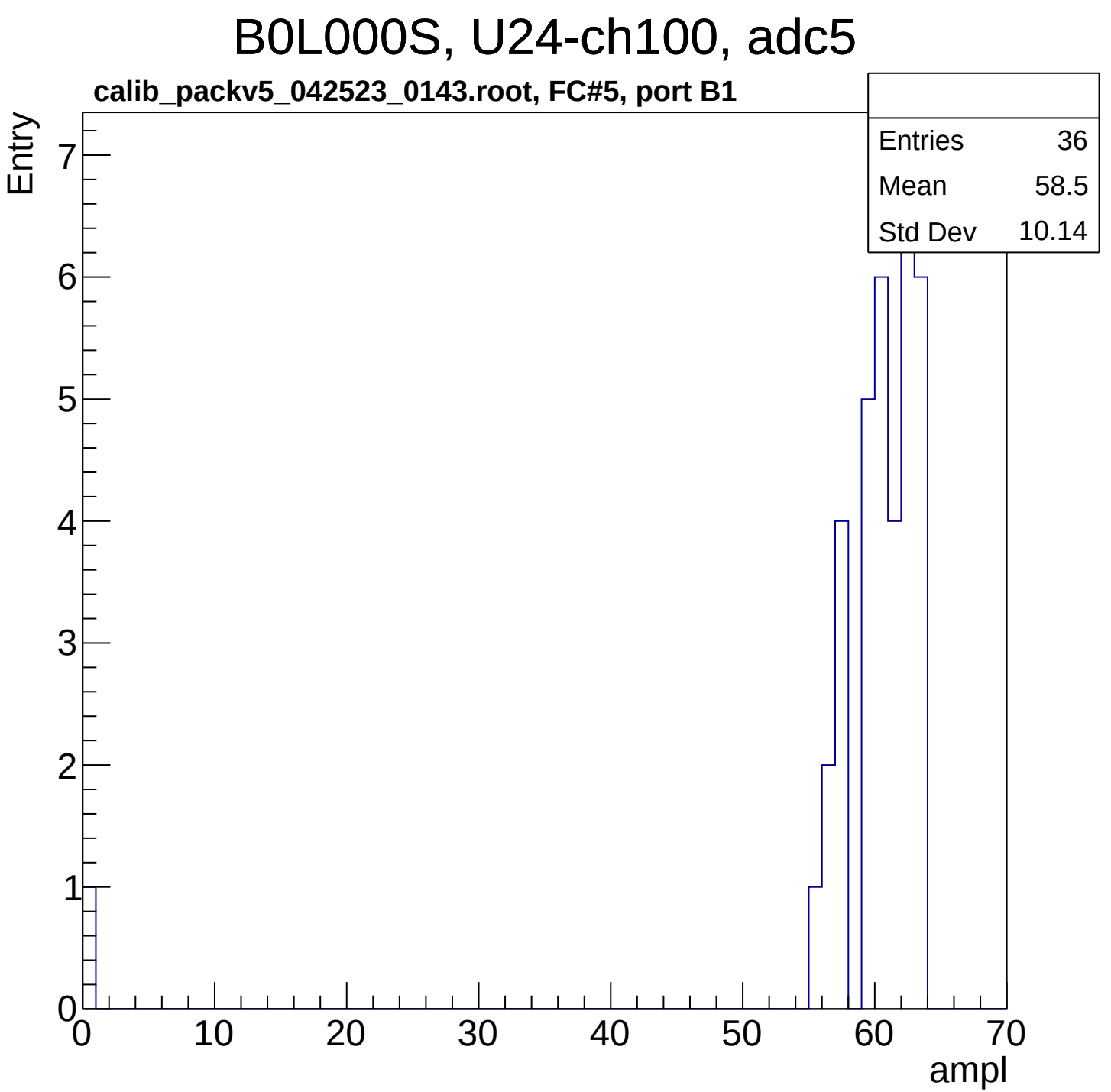
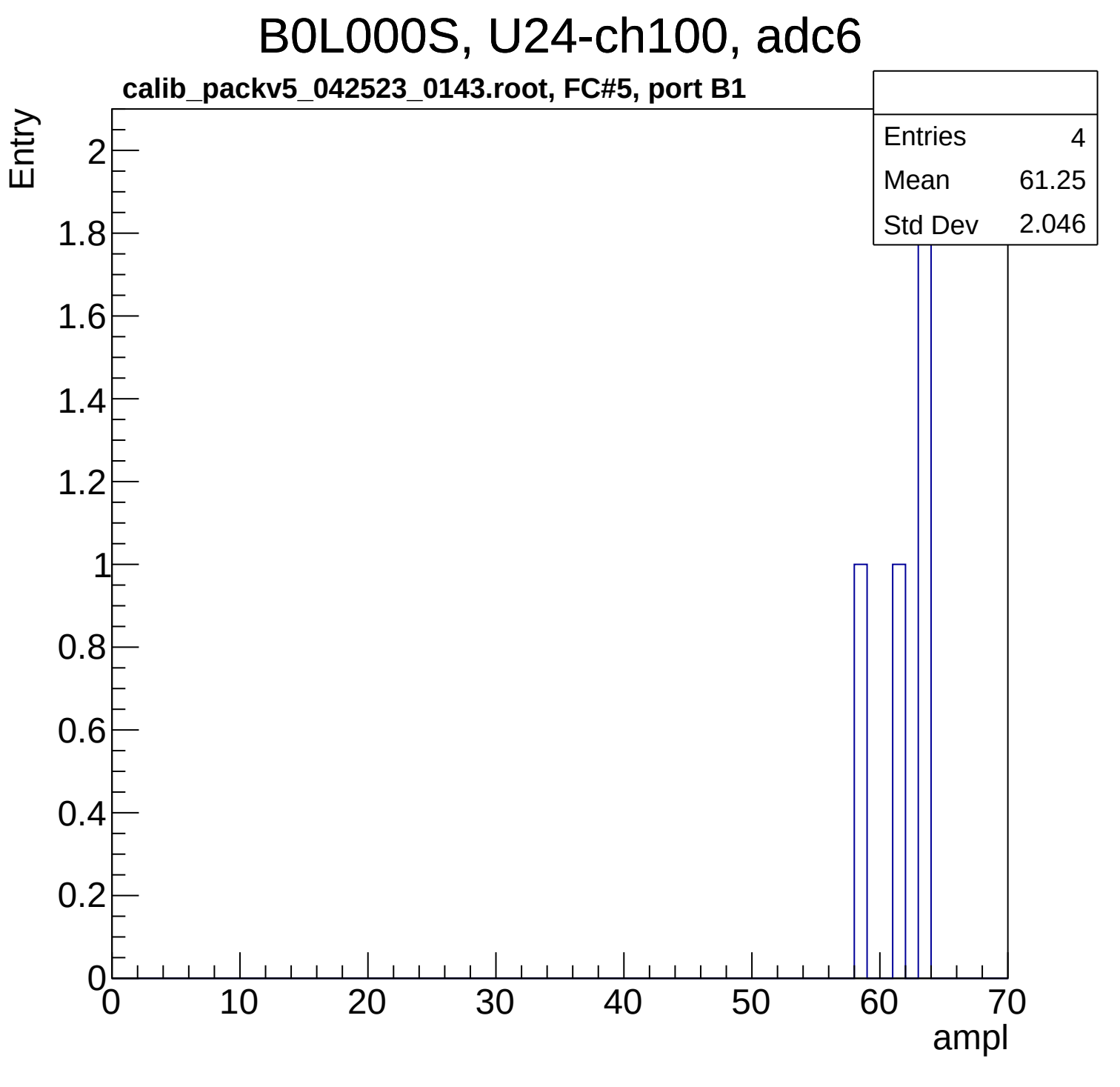
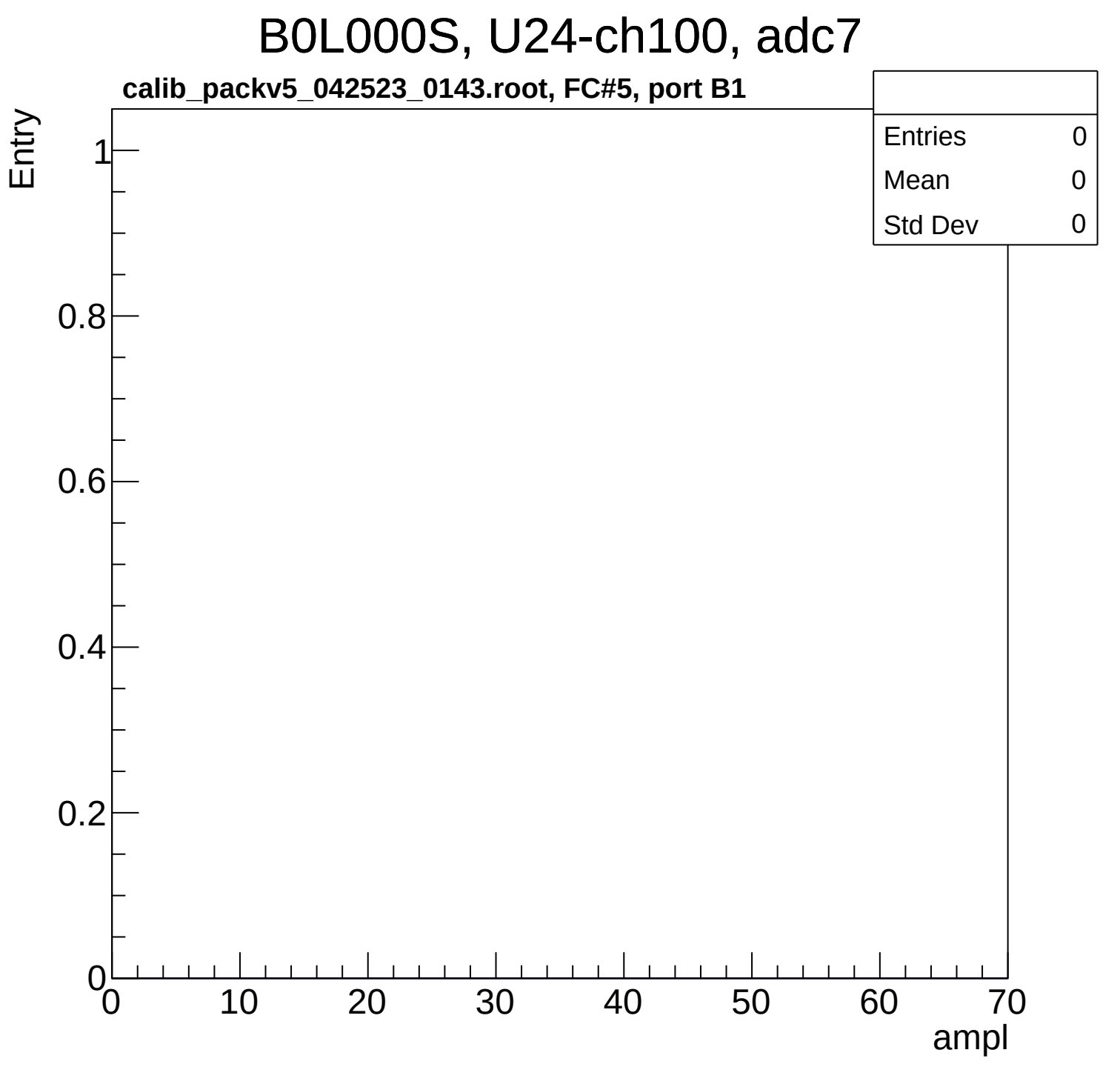
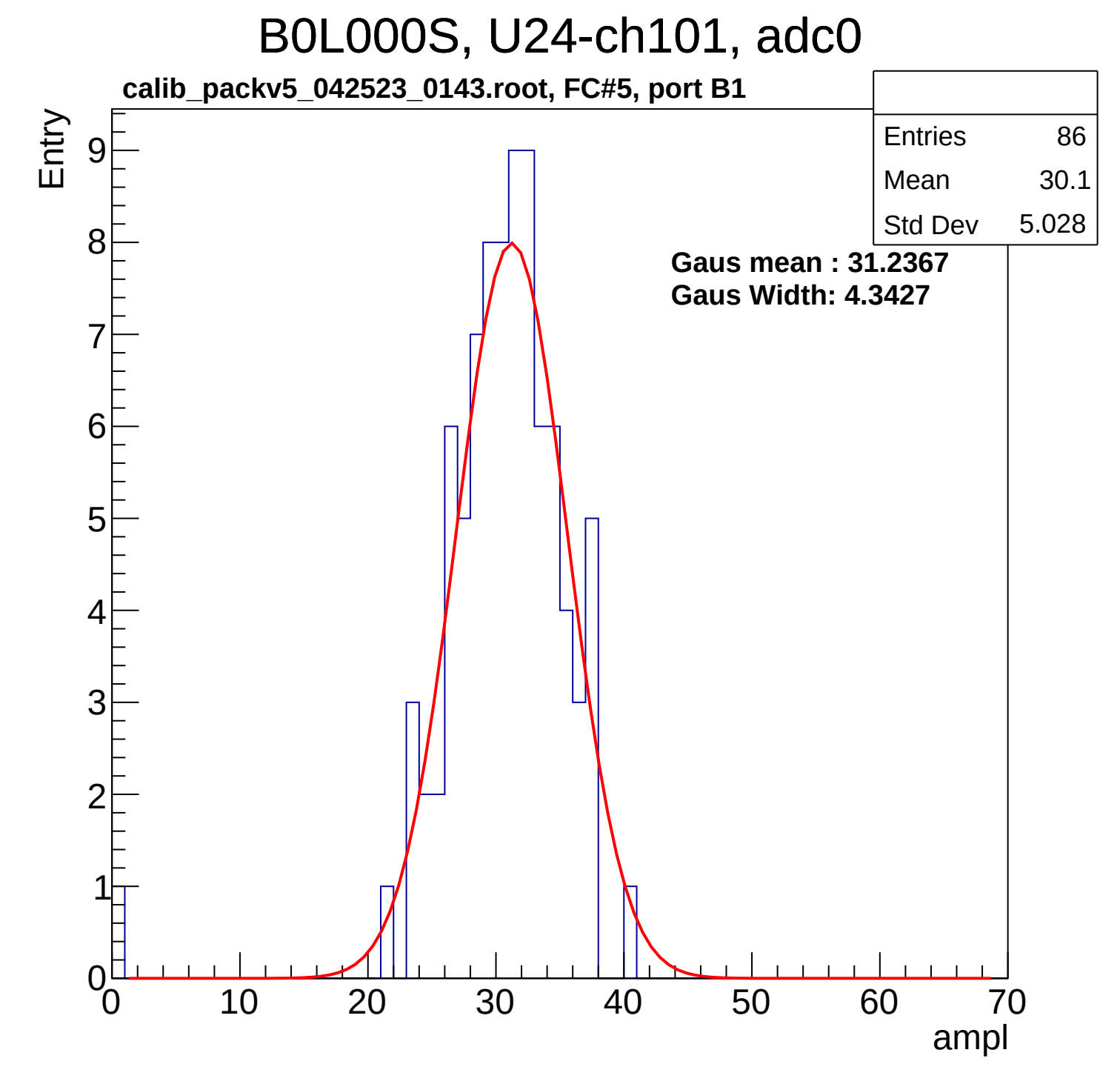
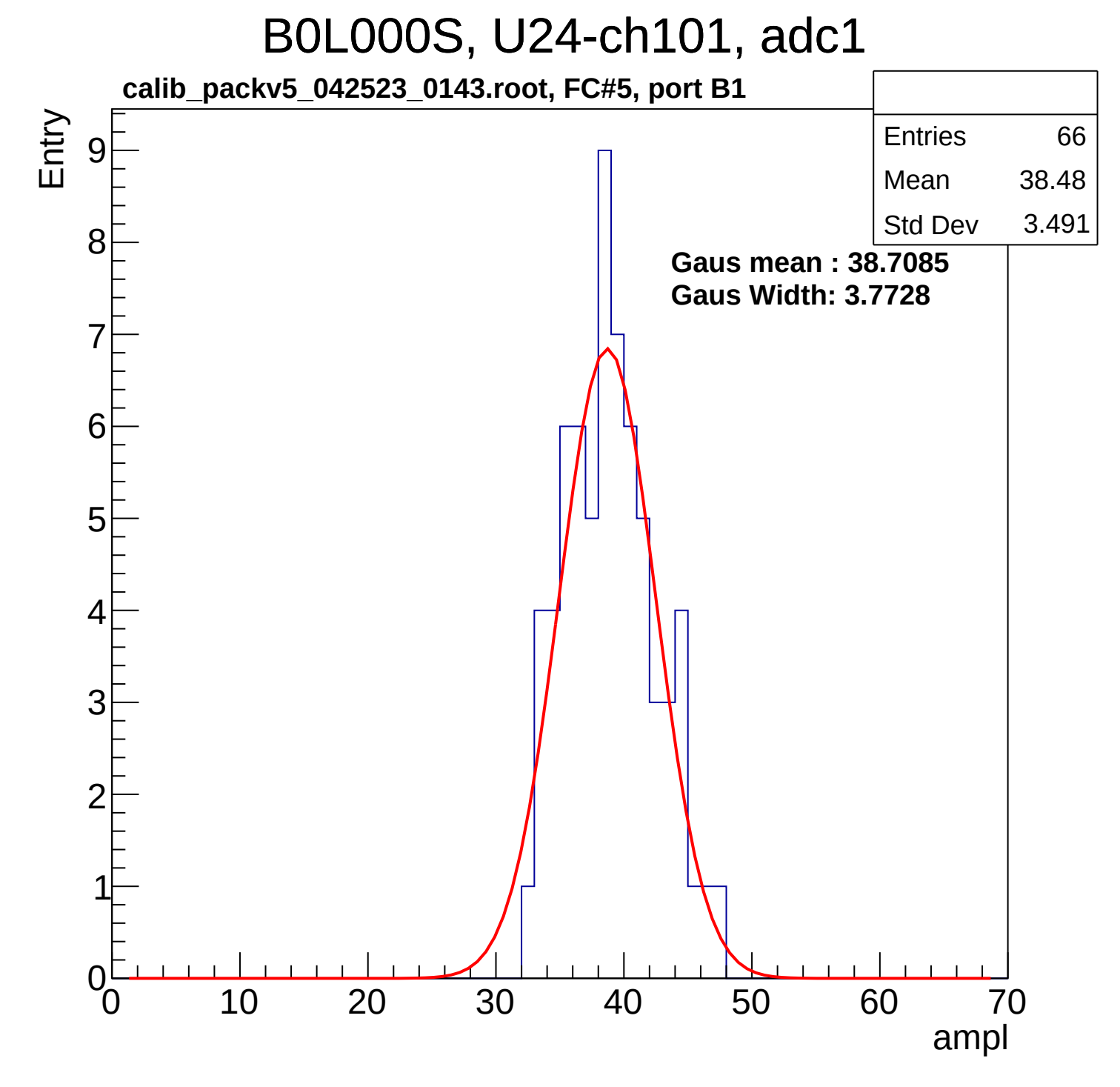

B0L000S, U24-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 57.41
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

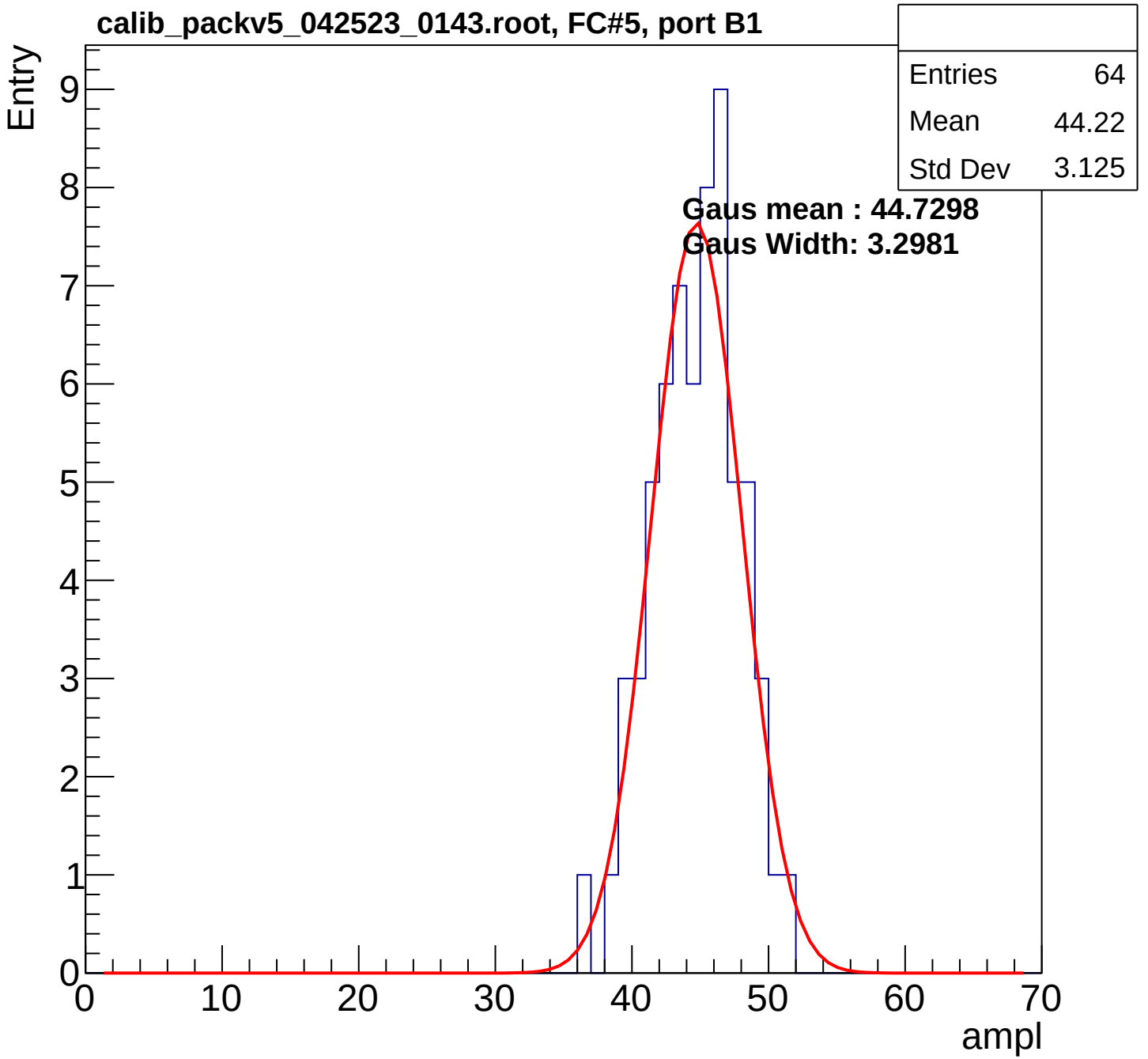

B0L000S, U24-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.5
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.25
Std Dev 2.046
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 30.1
Std Dev 5.028
Gaus mean : 31.2367
Gaus Width: 4.3427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 38.48
Std Dev 3.491
Gaus mean : 38.7085
Gaus Width: 3.7728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.22
Std Dev 3.125
Gaus mean : 44.7298
Gaus Width: 3.2981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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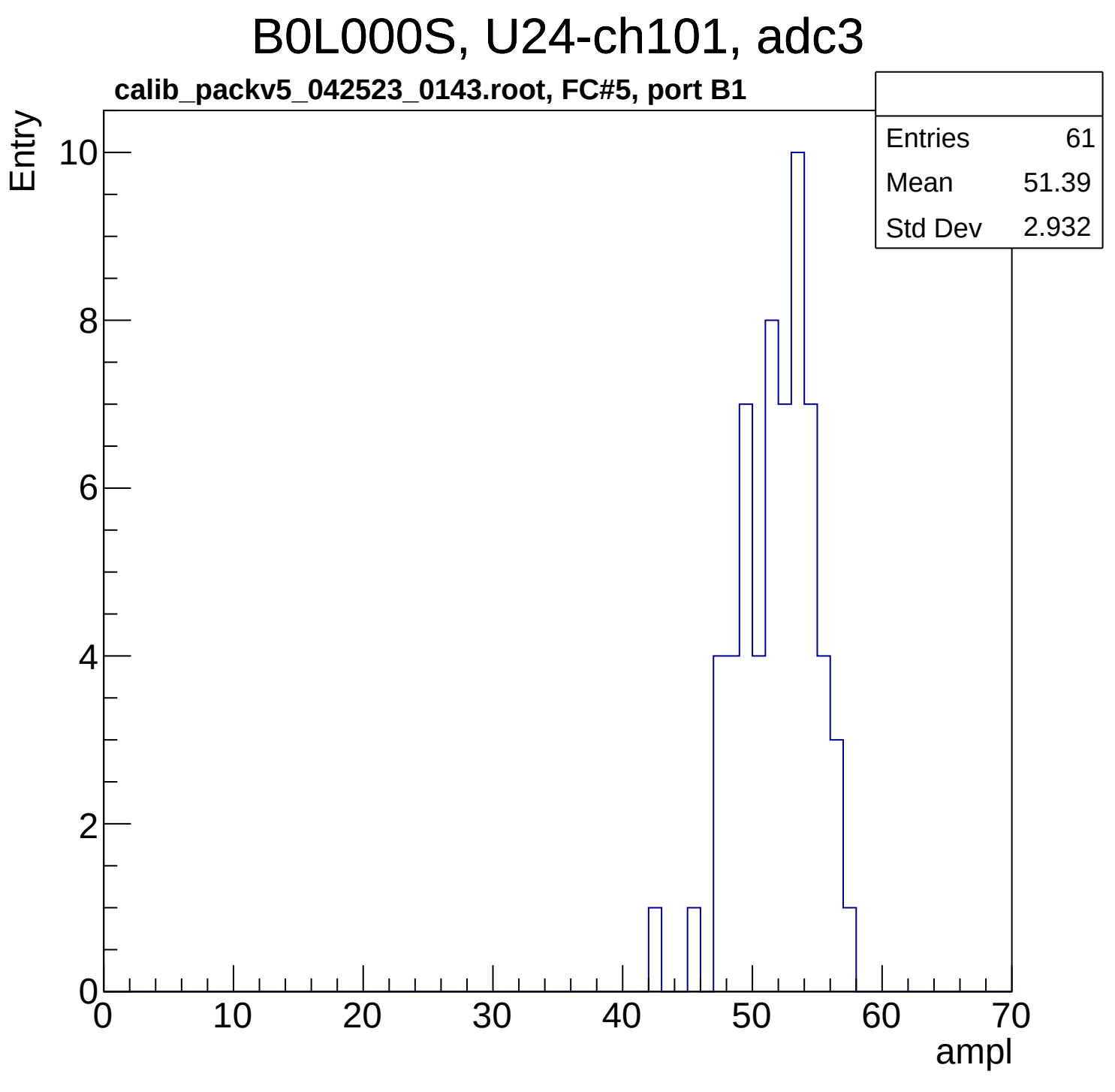
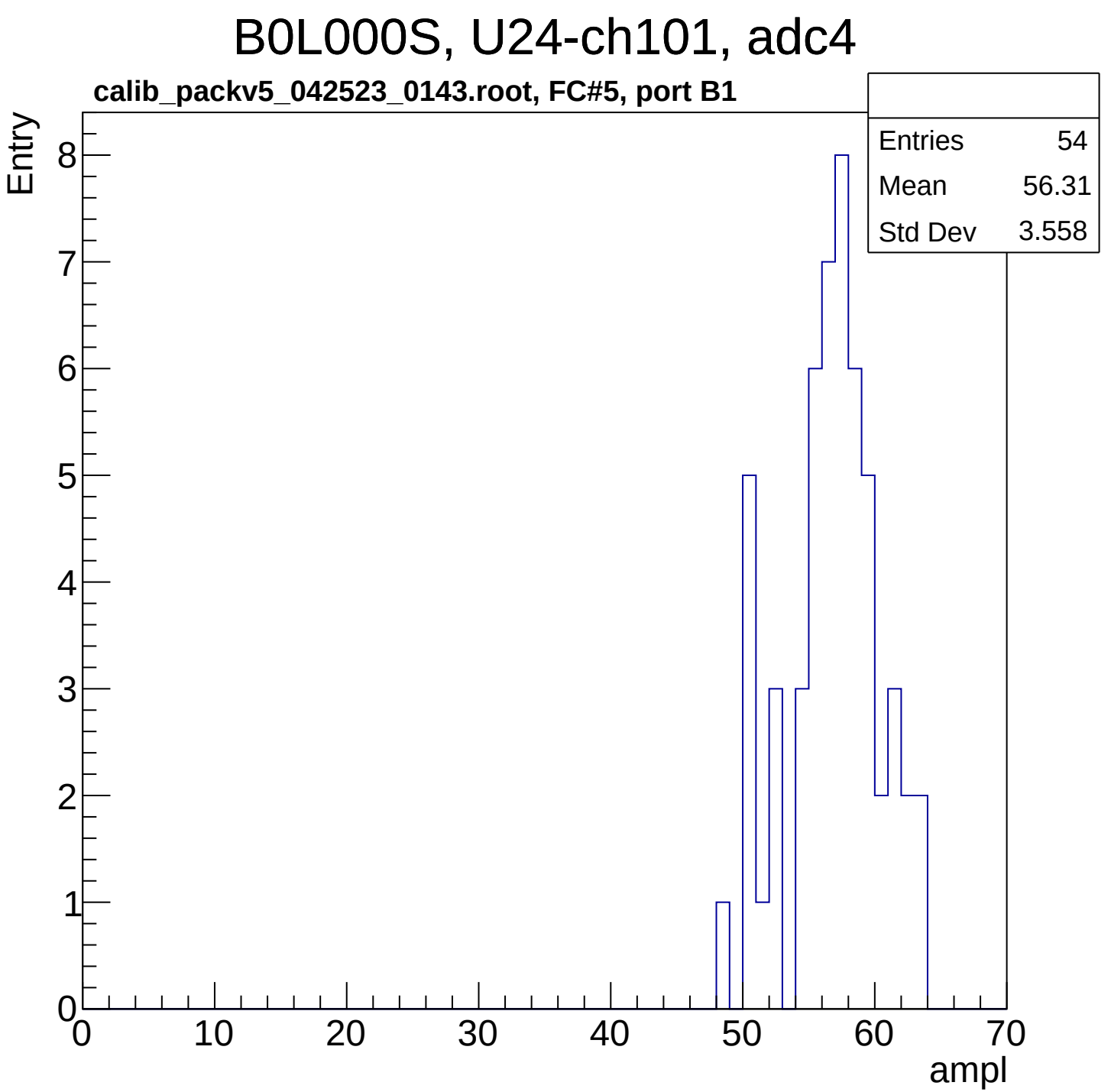

B0L000S, U24-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 51.39
Std Dev 2.932
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.31
Std Dev 3.558
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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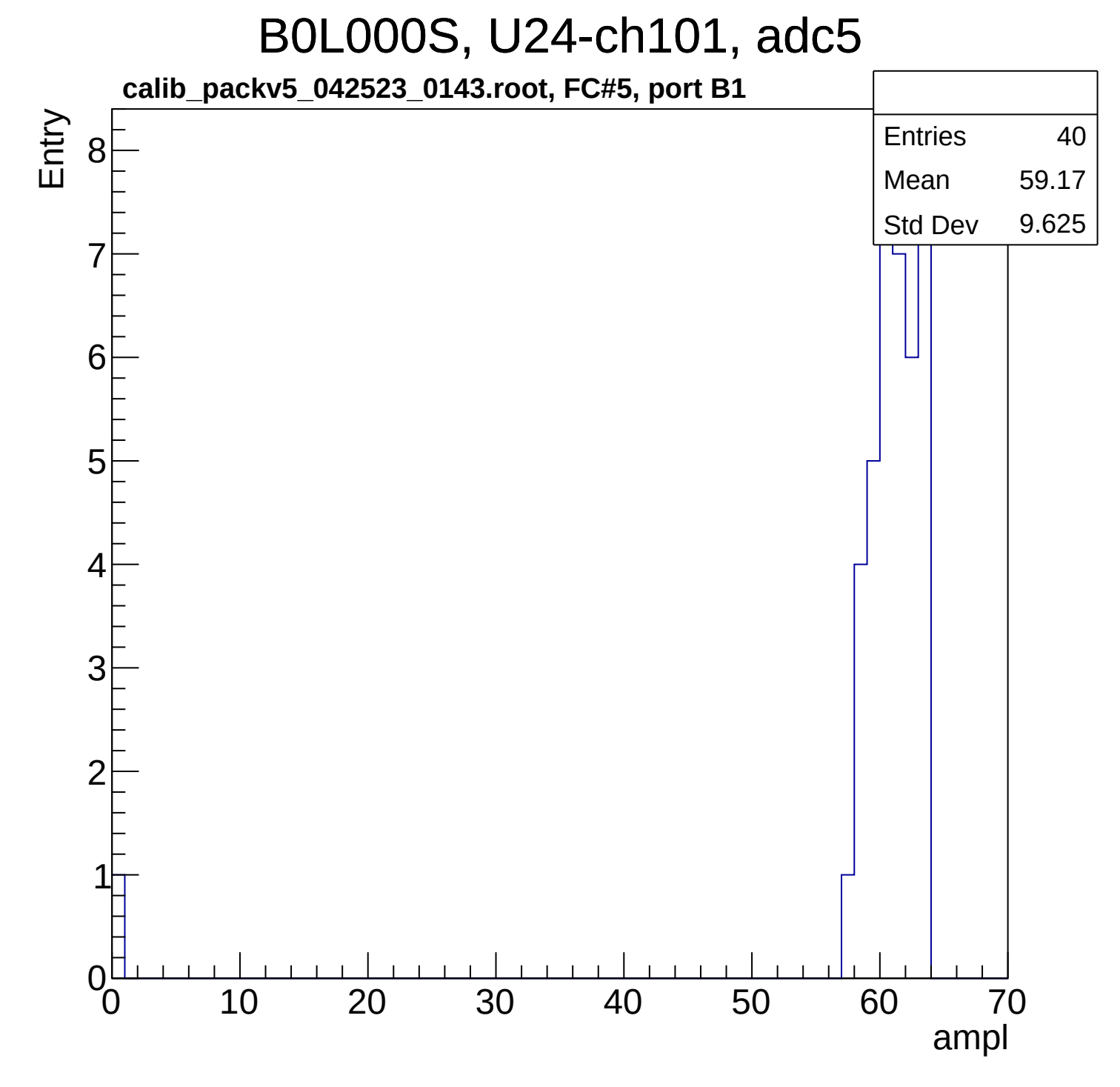
B0L000S, U24-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.17
Std Dev 9.625
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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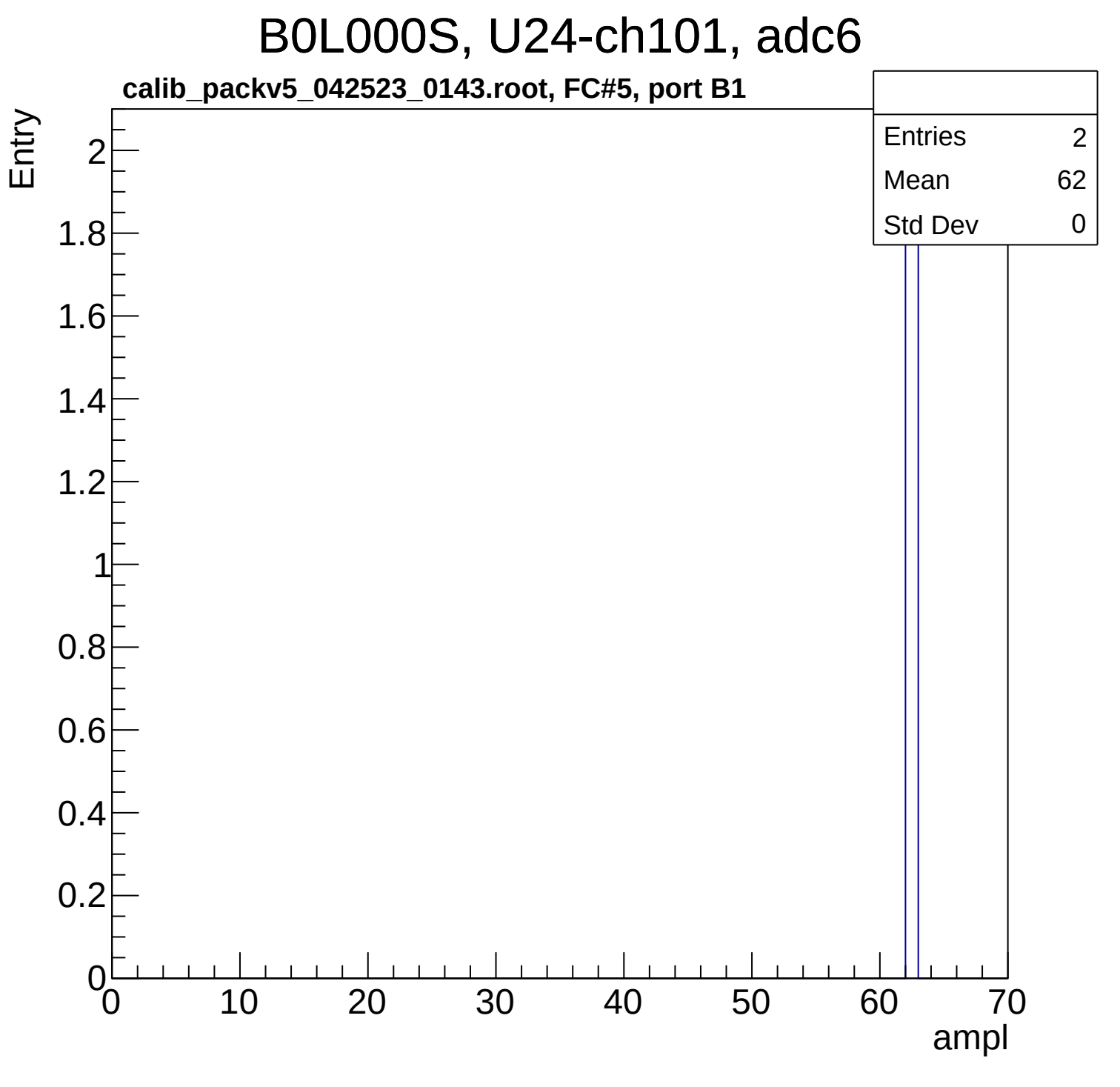
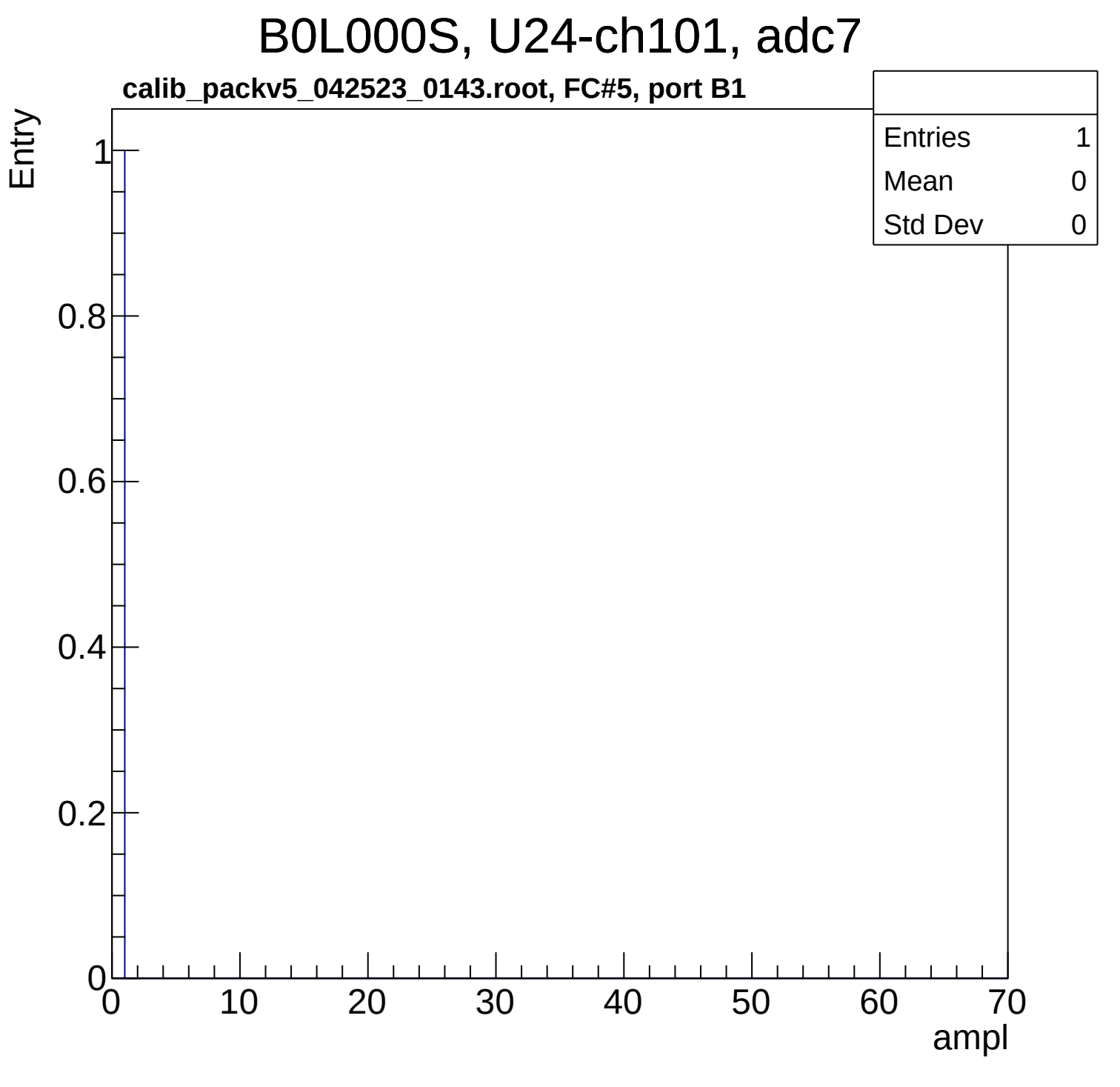
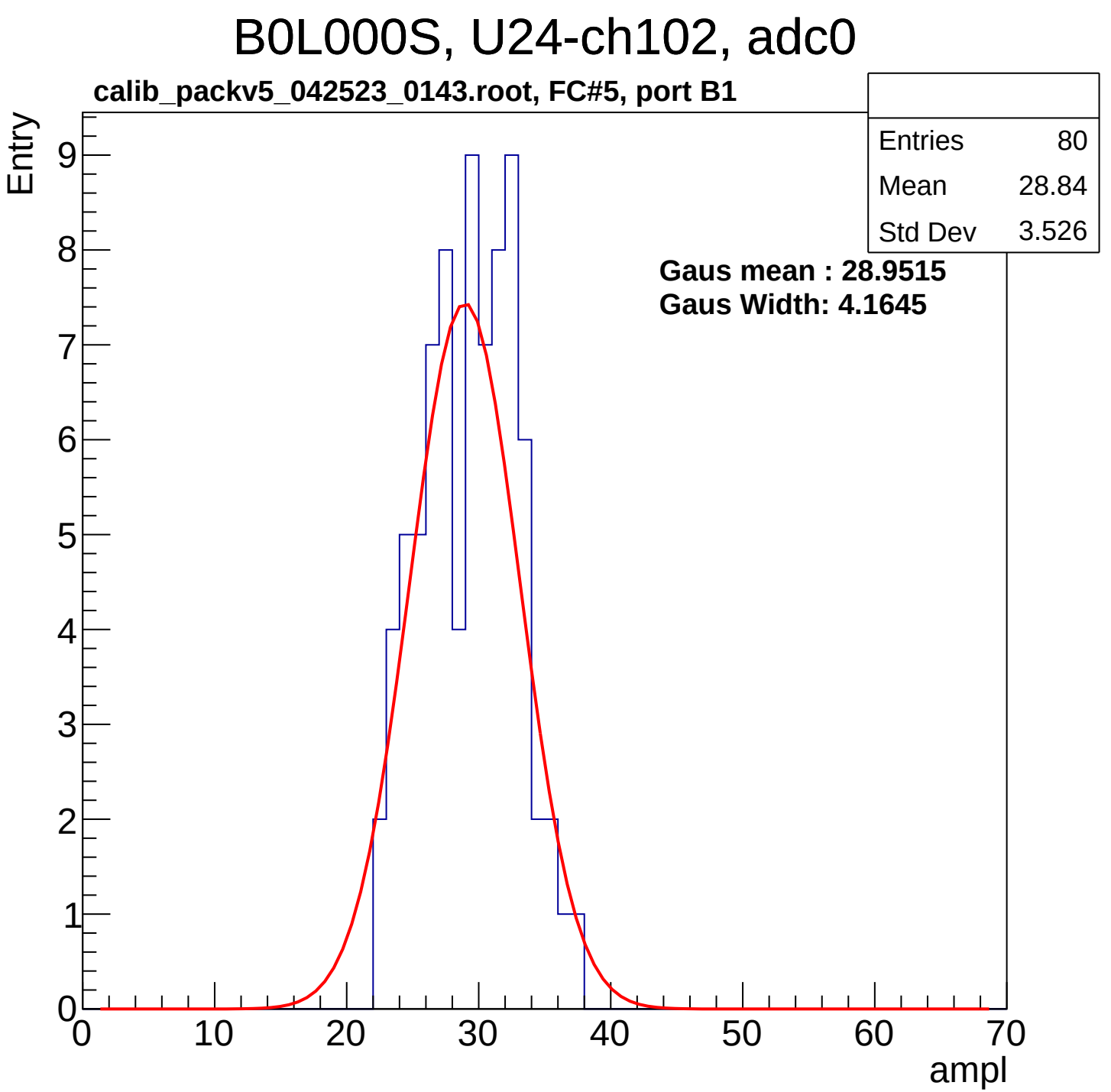

B0L000S, U24-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 28.84
Std Dev 3.526
Gaus mean : 28.9515
Gaus Width: 4.1645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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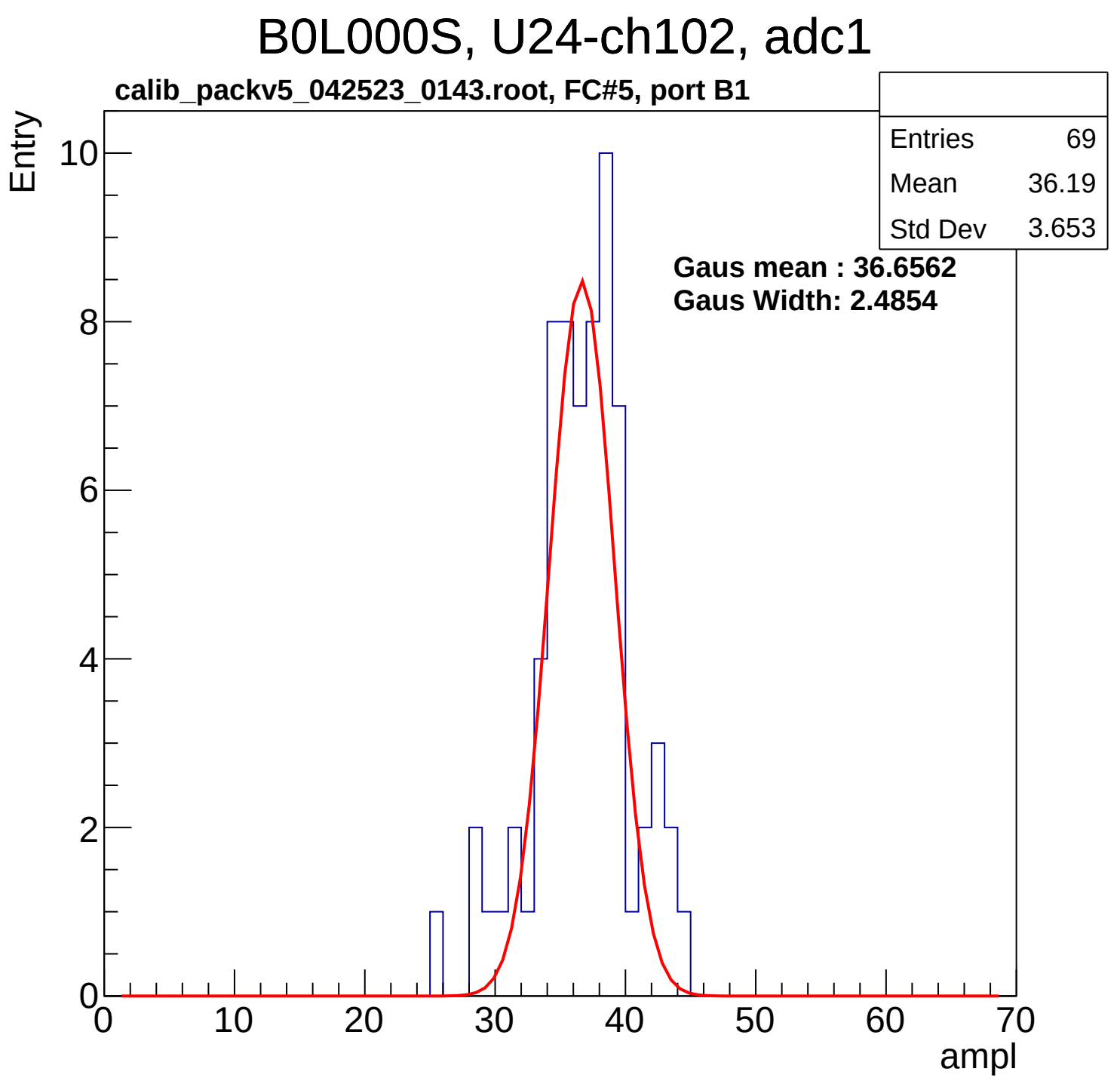
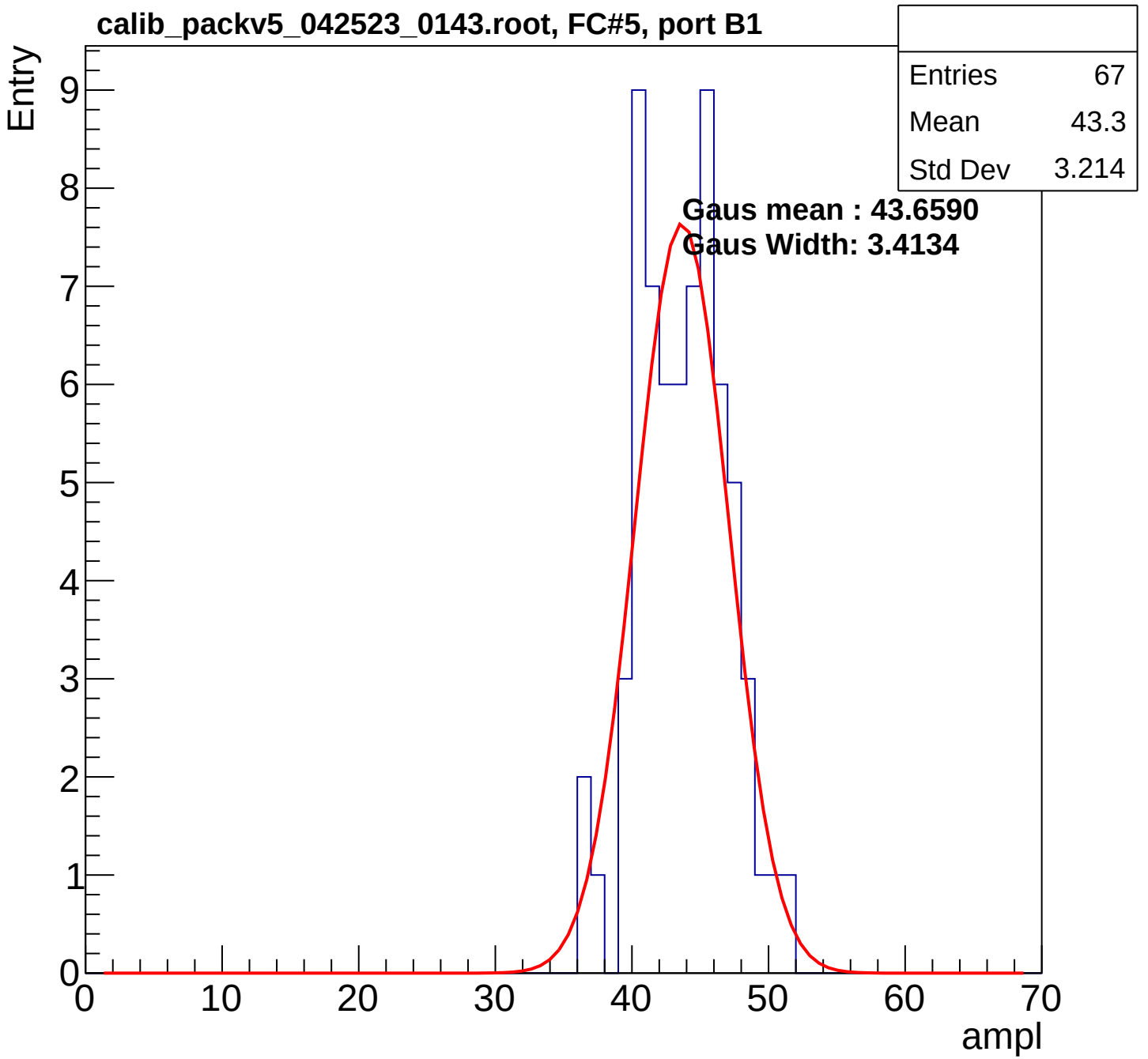
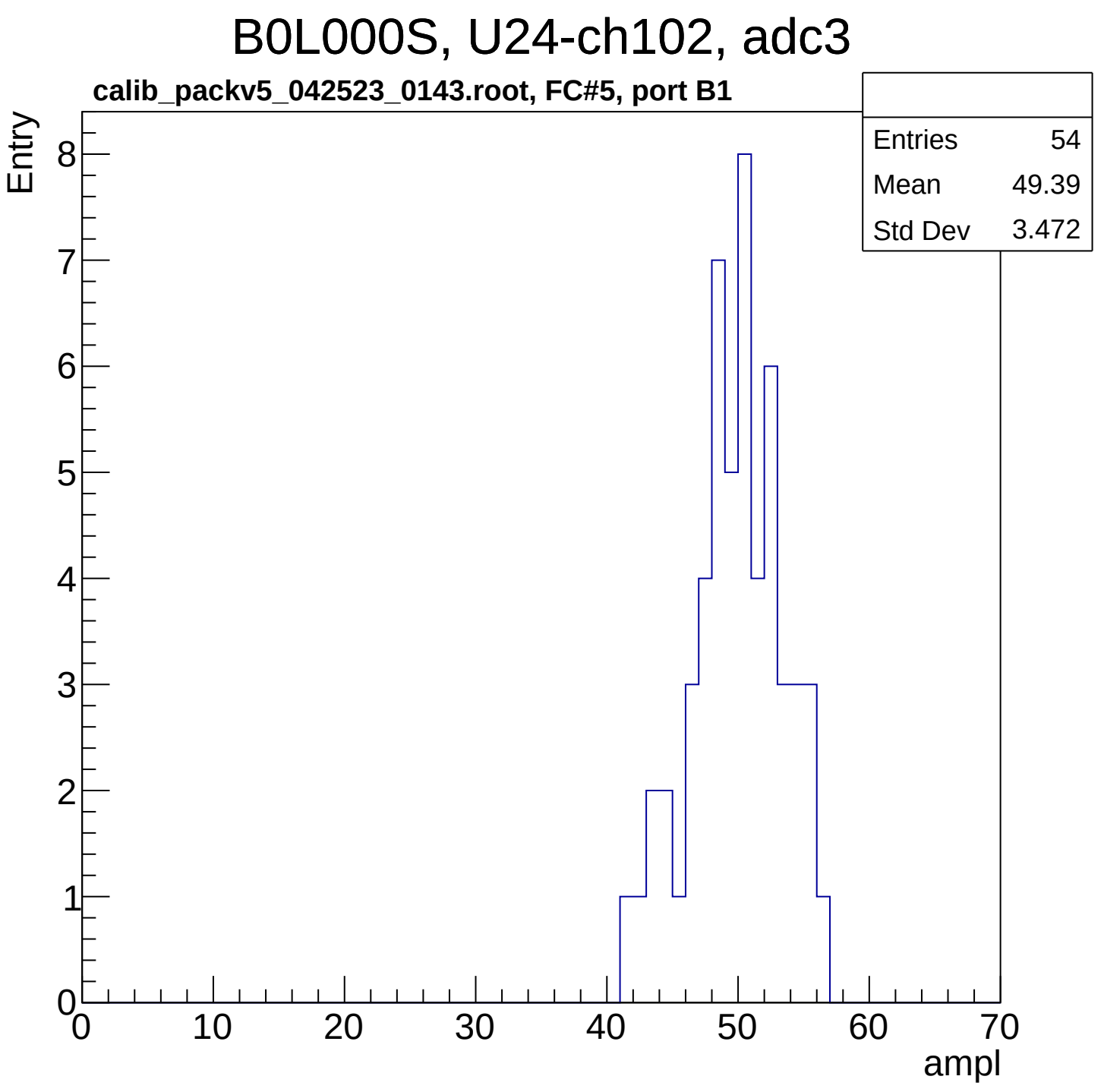
60
70


B0L000S, U24-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.19
Std Dev 3.653
Gaus mean : 36.6562
Gaus Width: 2.4854
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.3
Std Dev 3.214
Gaus mean : 43.6590
Gaus Width: 3.4134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.39
Std Dev 3.472
Entry
ampl
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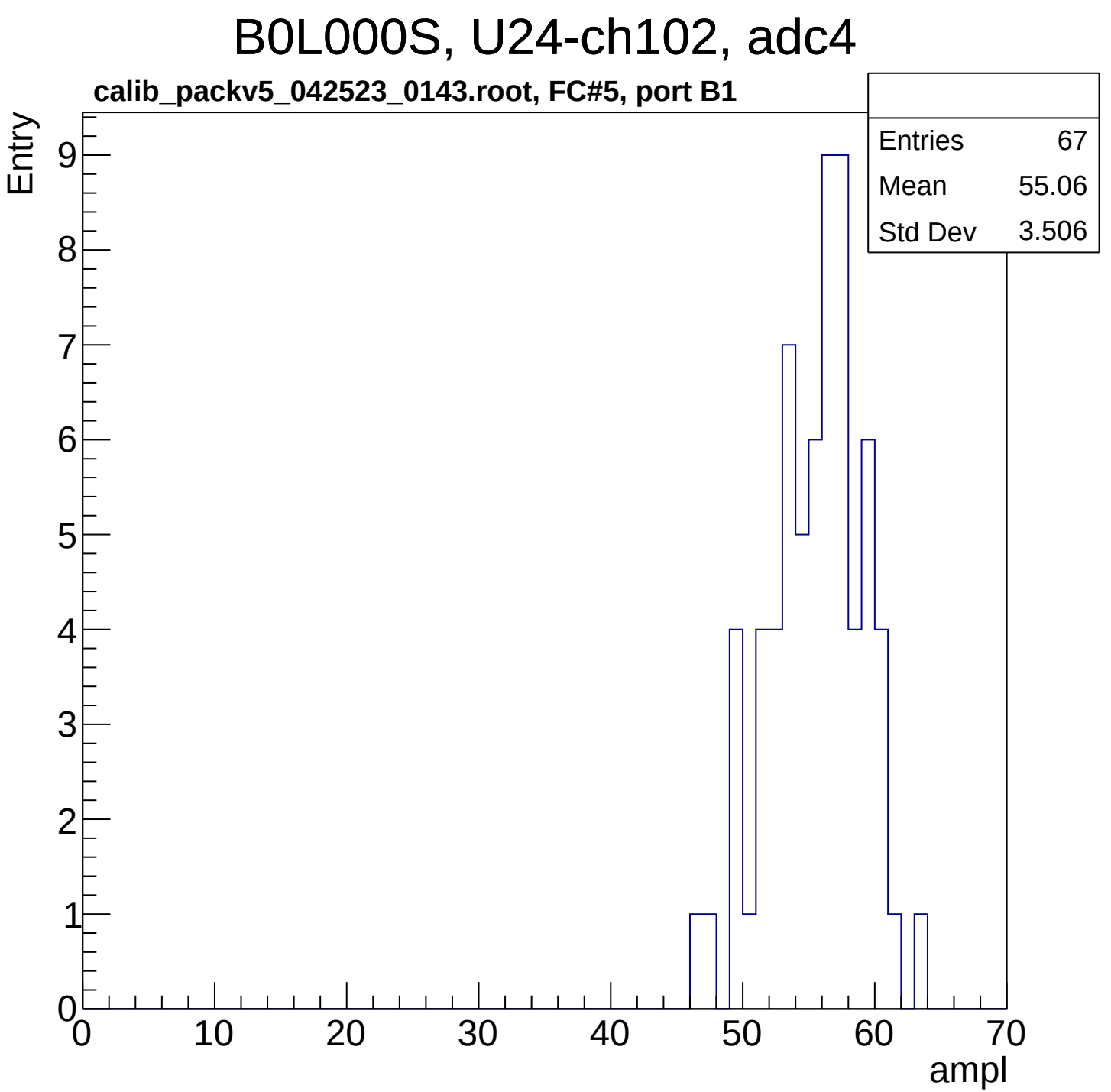
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 55.06
Std Dev 3.506
Entry
ampl
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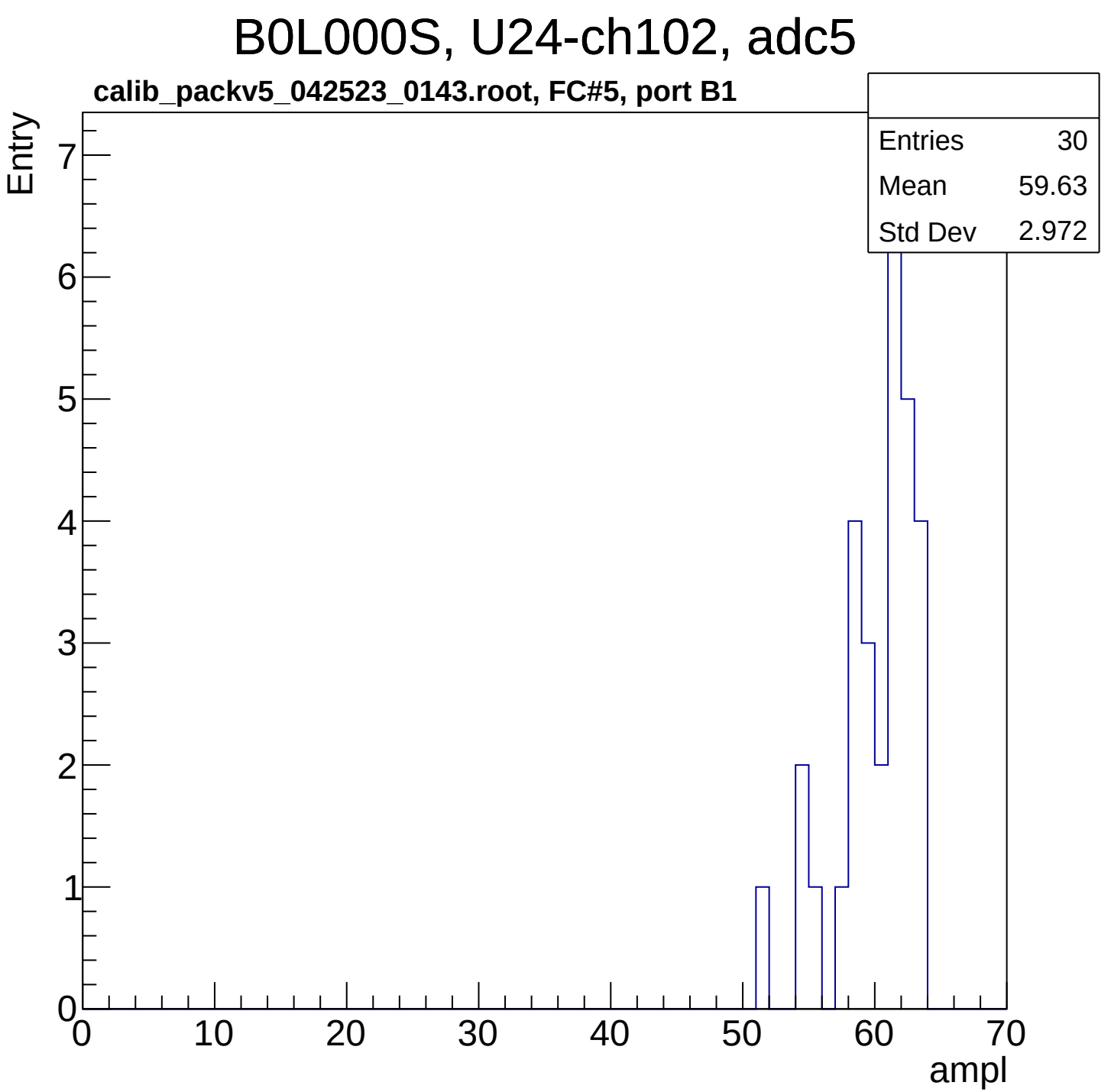
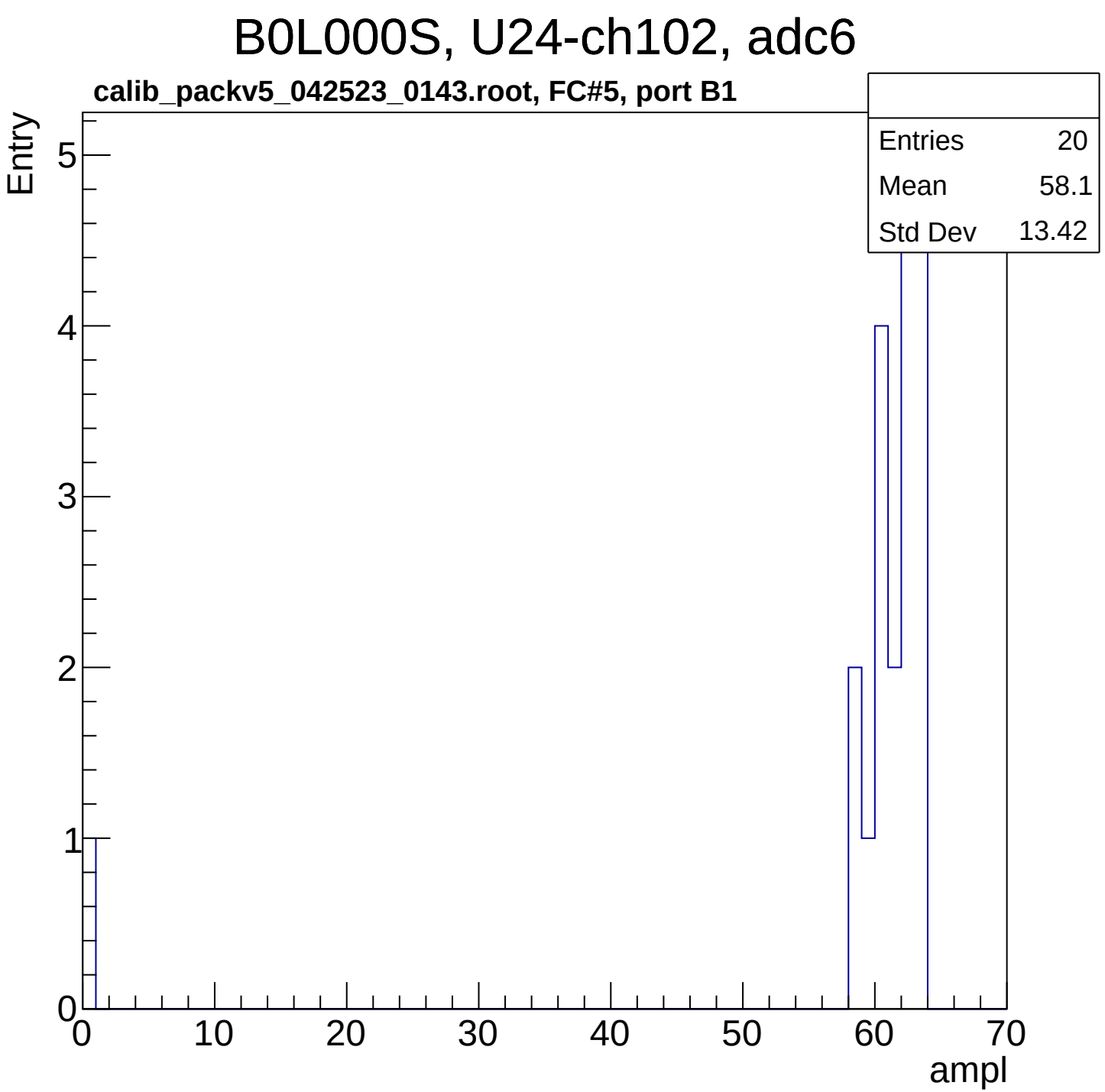
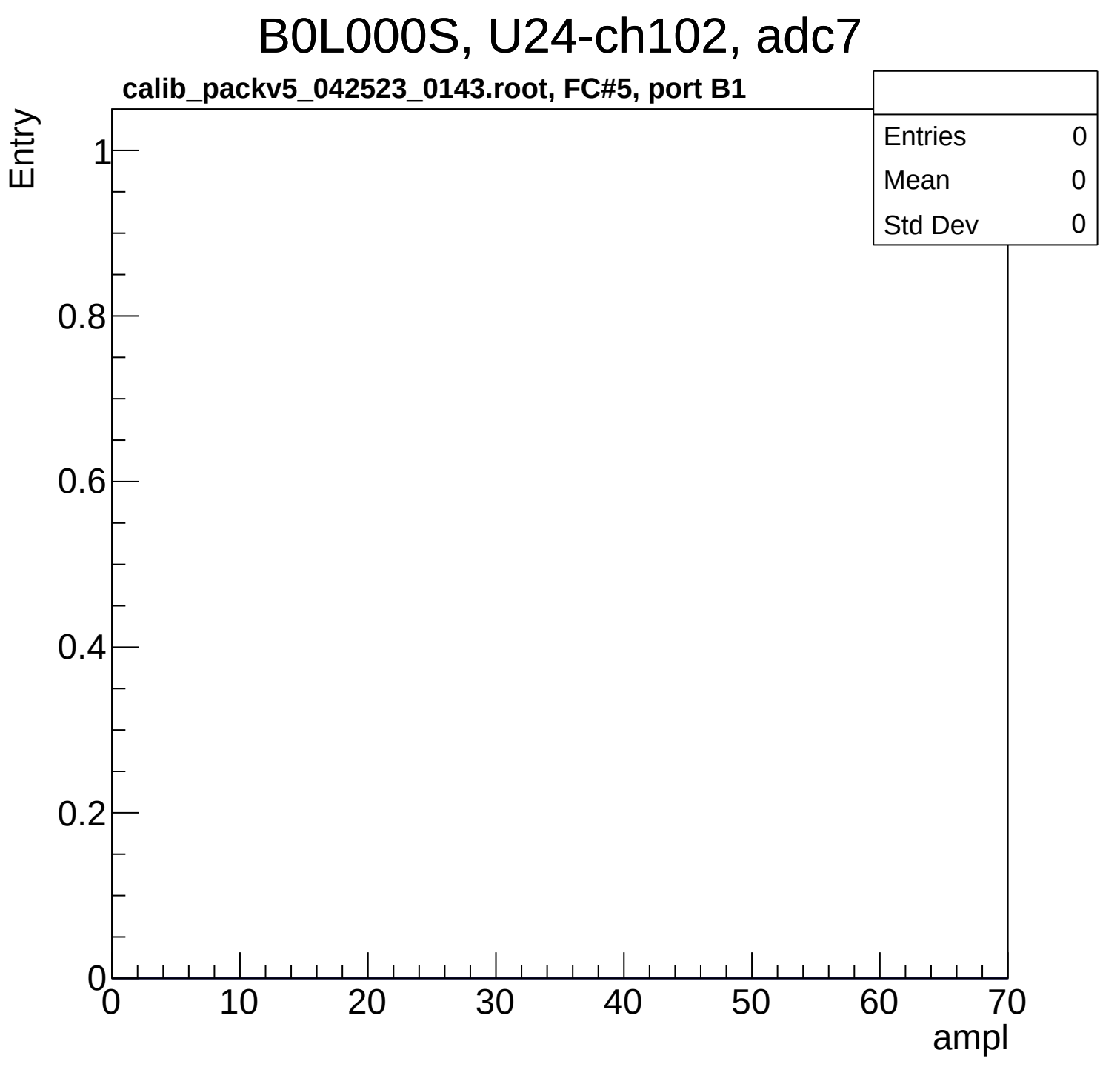
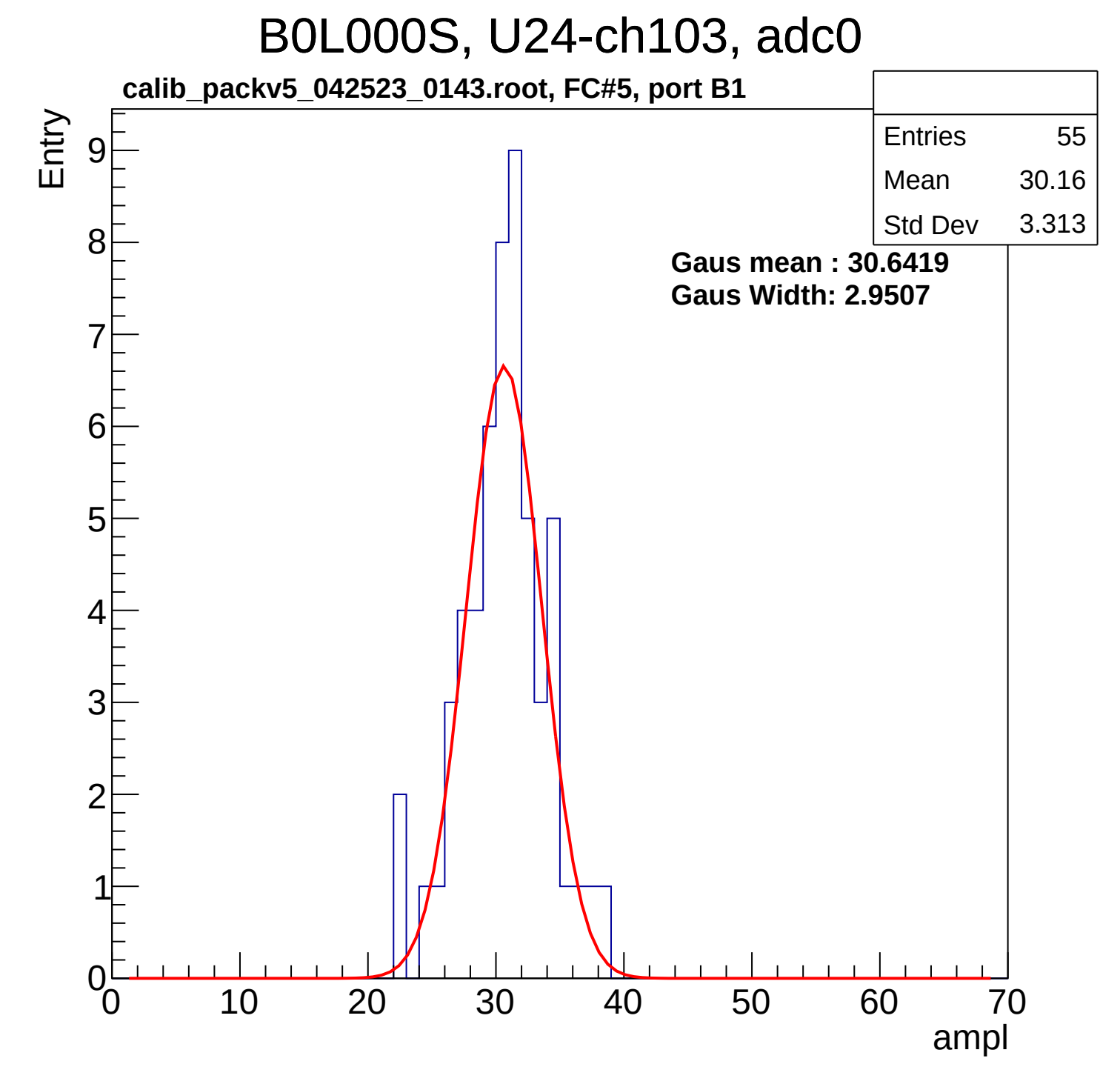
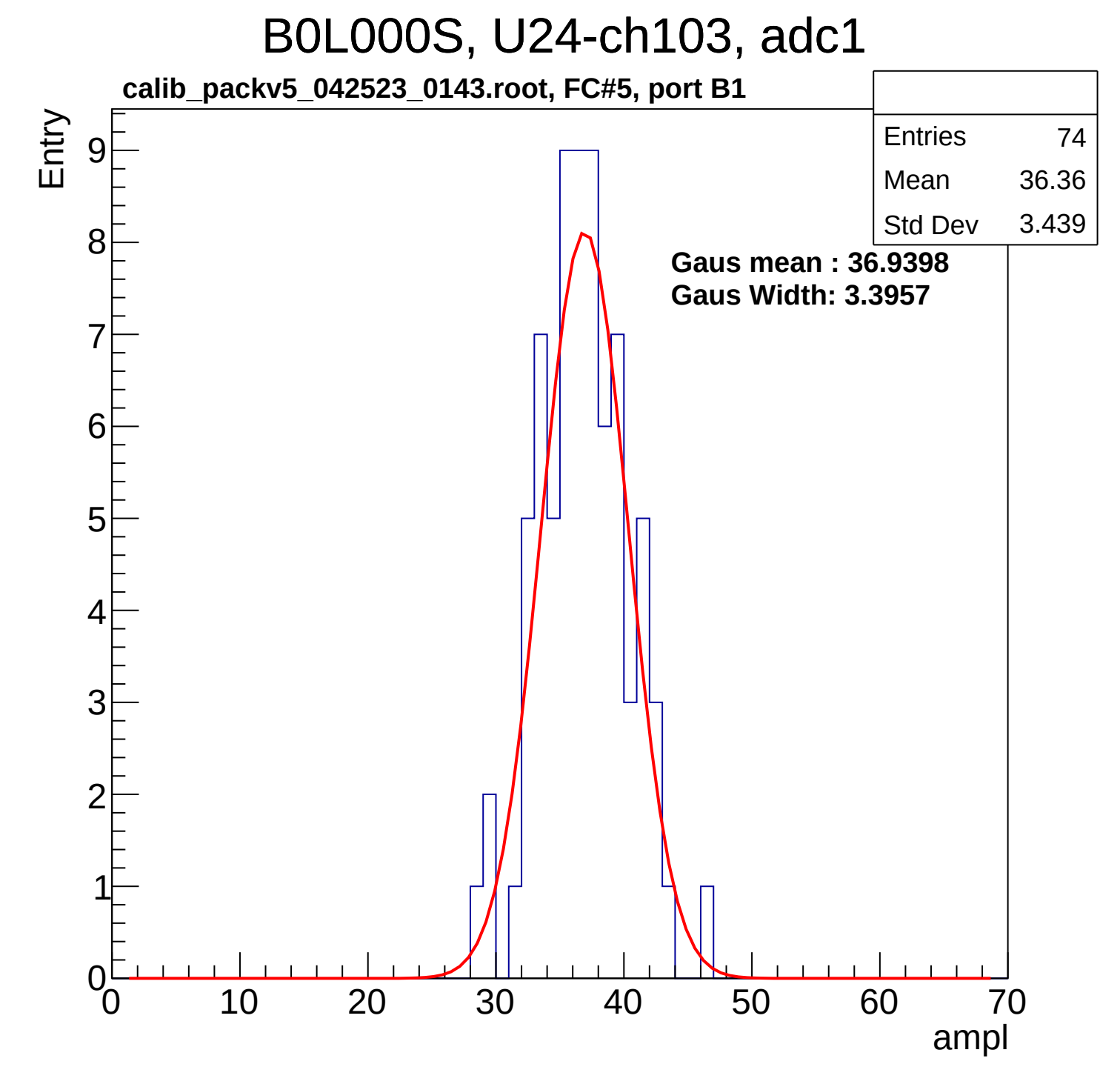
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 59.63
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.1
Std Dev 13.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.16
Std Dev 3.313
Gaus mean : 30.6419
Gaus Width: 2.9507
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.36
Std Dev 3.439
Gaus mean : 36.9398
Gaus Width: 3.3957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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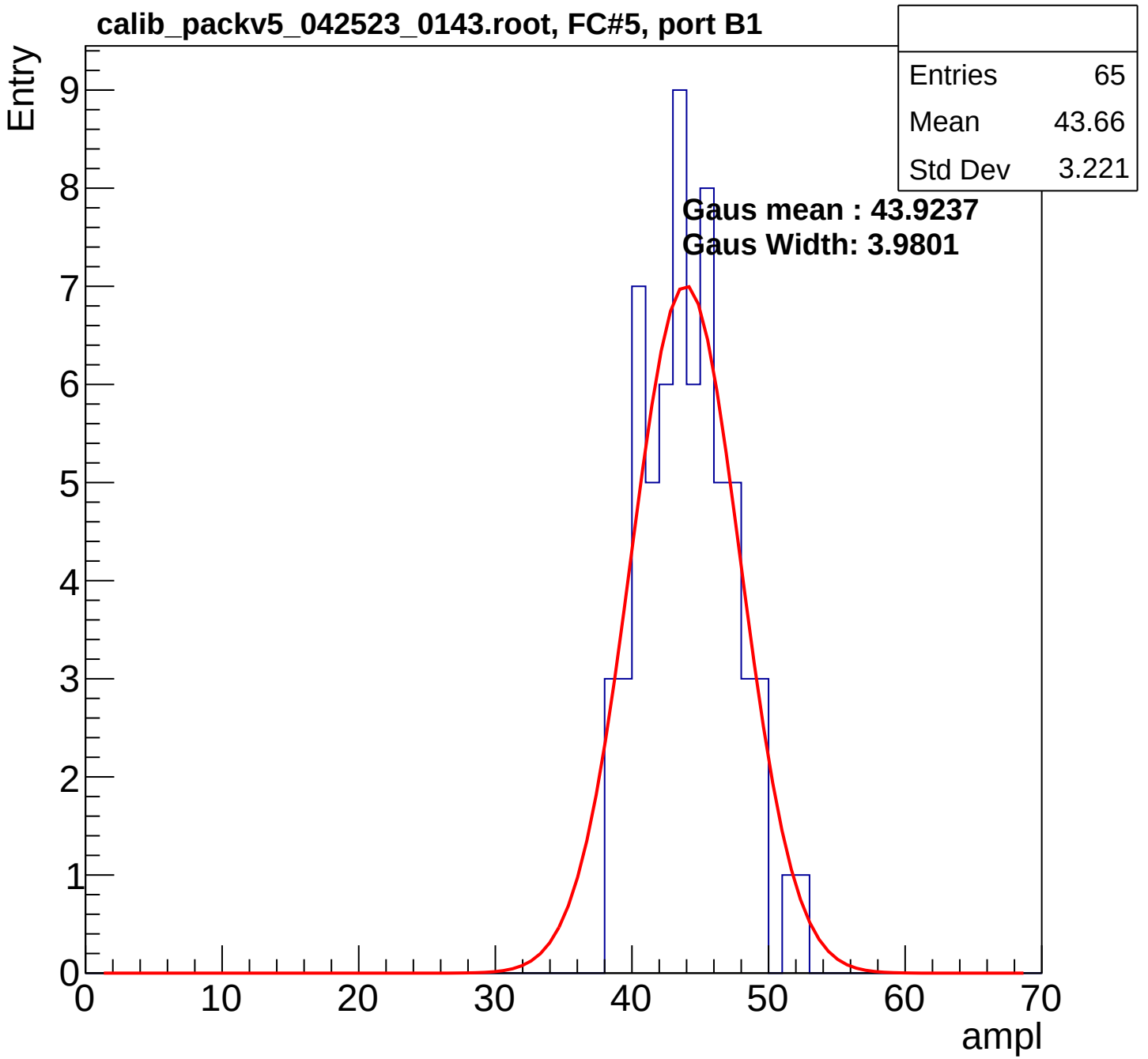
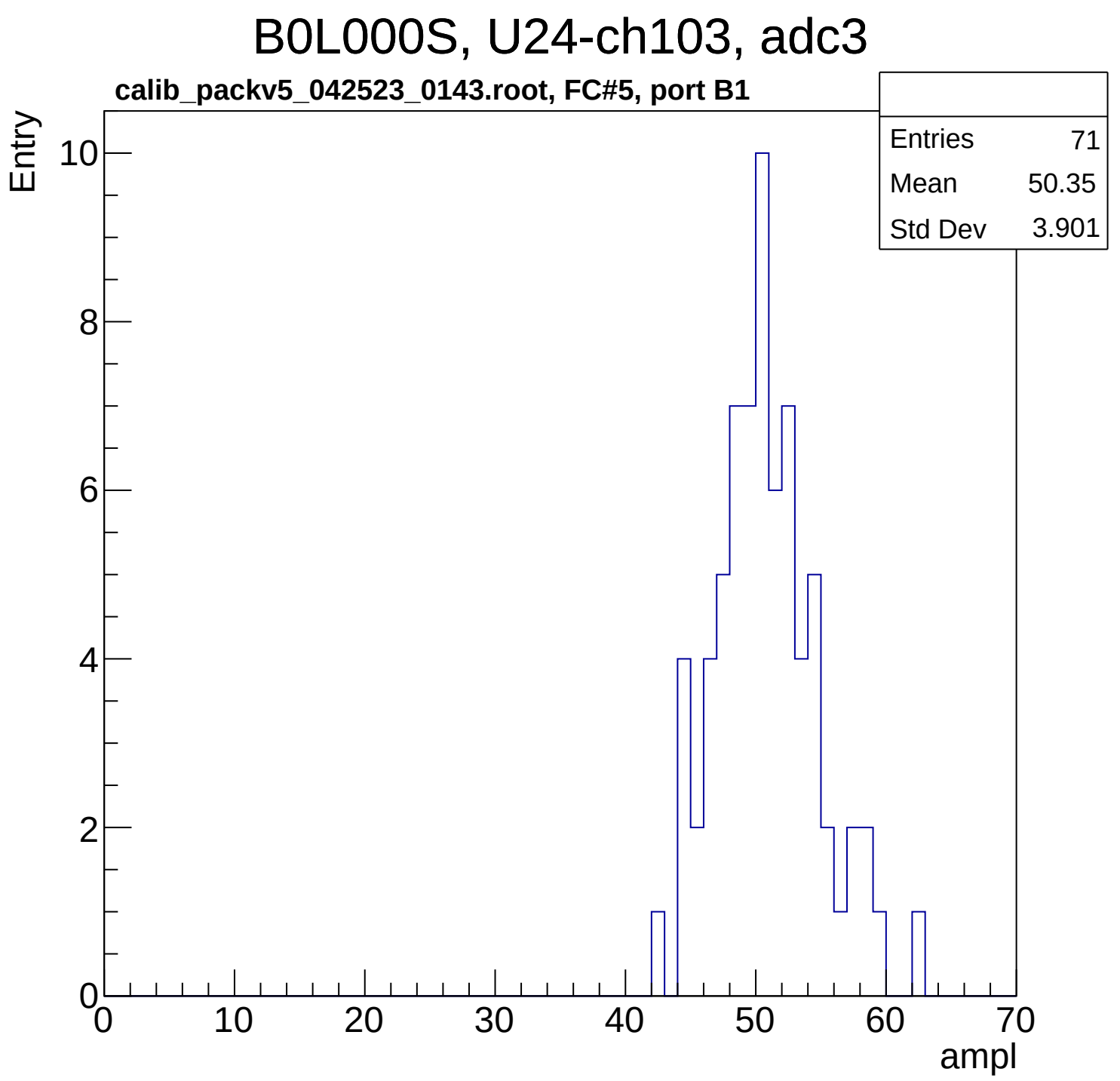

B0L000S, U24-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.66
Std Dev 3.221
Gaus mean : 43.9237
Gaus Width: 3.9801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.35
Std Dev 3.901
Entry
0
2
4
6
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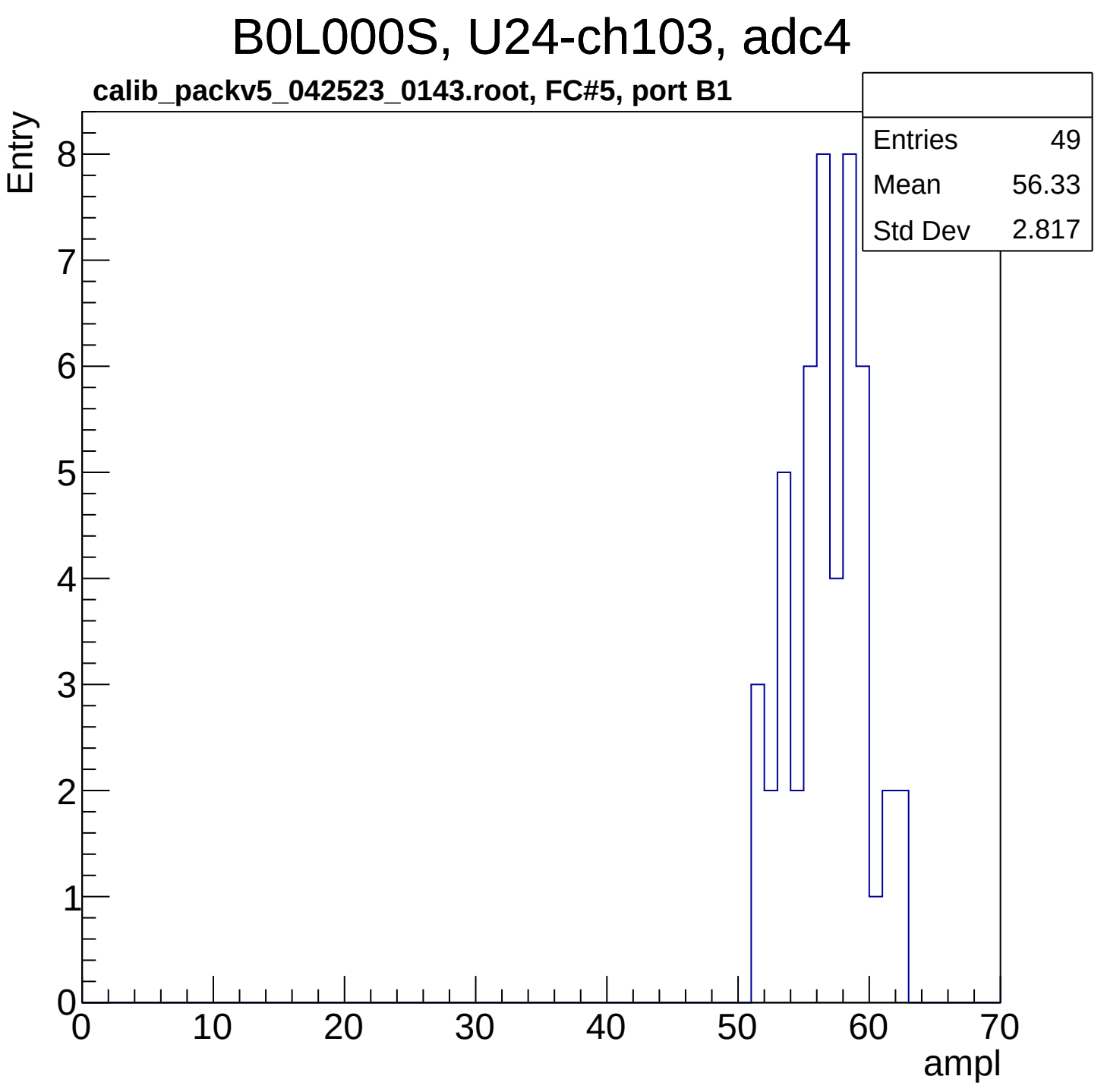
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 56.33
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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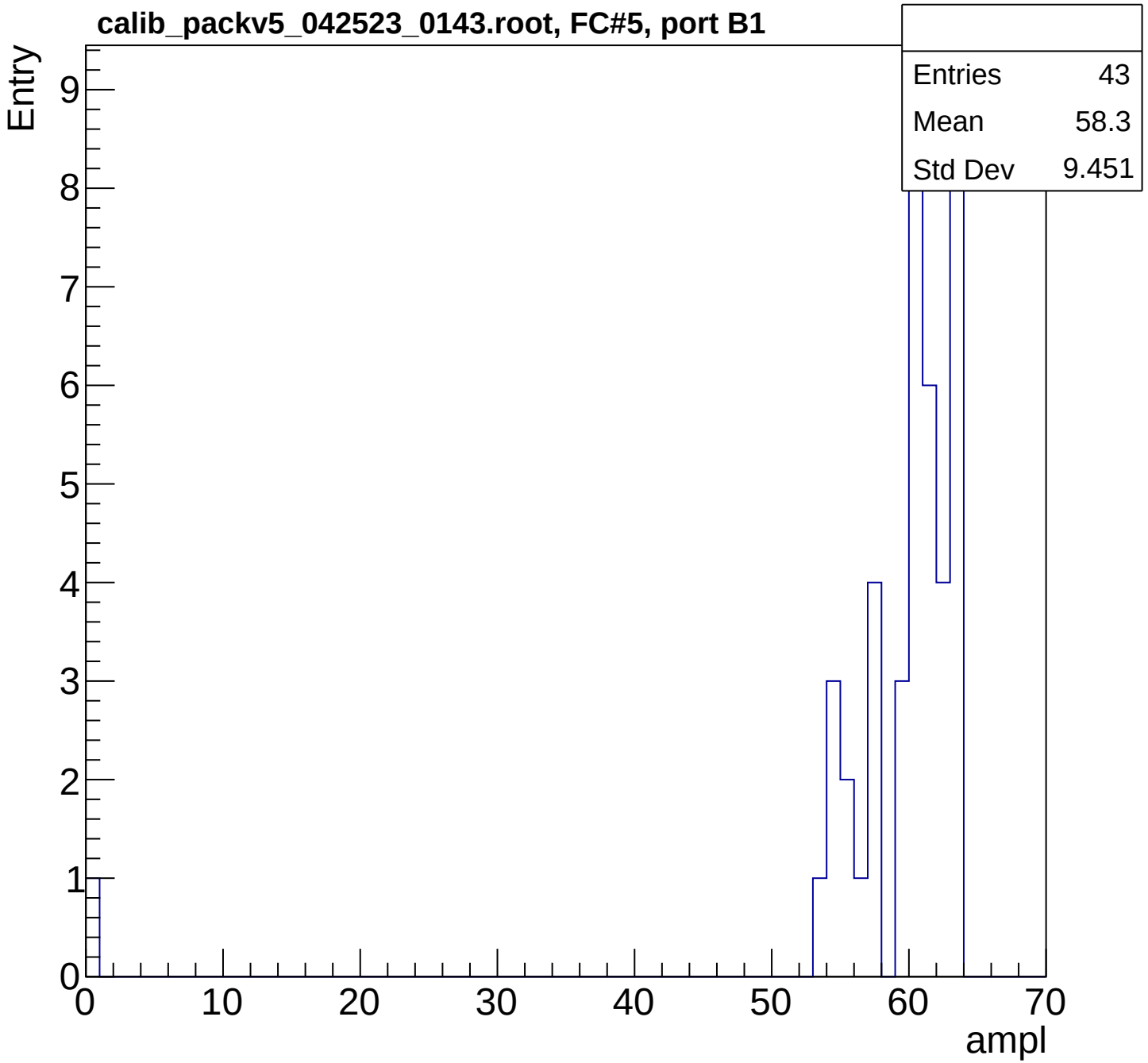
B0L000S, U24-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.3
Std Dev 9.451
Entry
ampl
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2
3
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5
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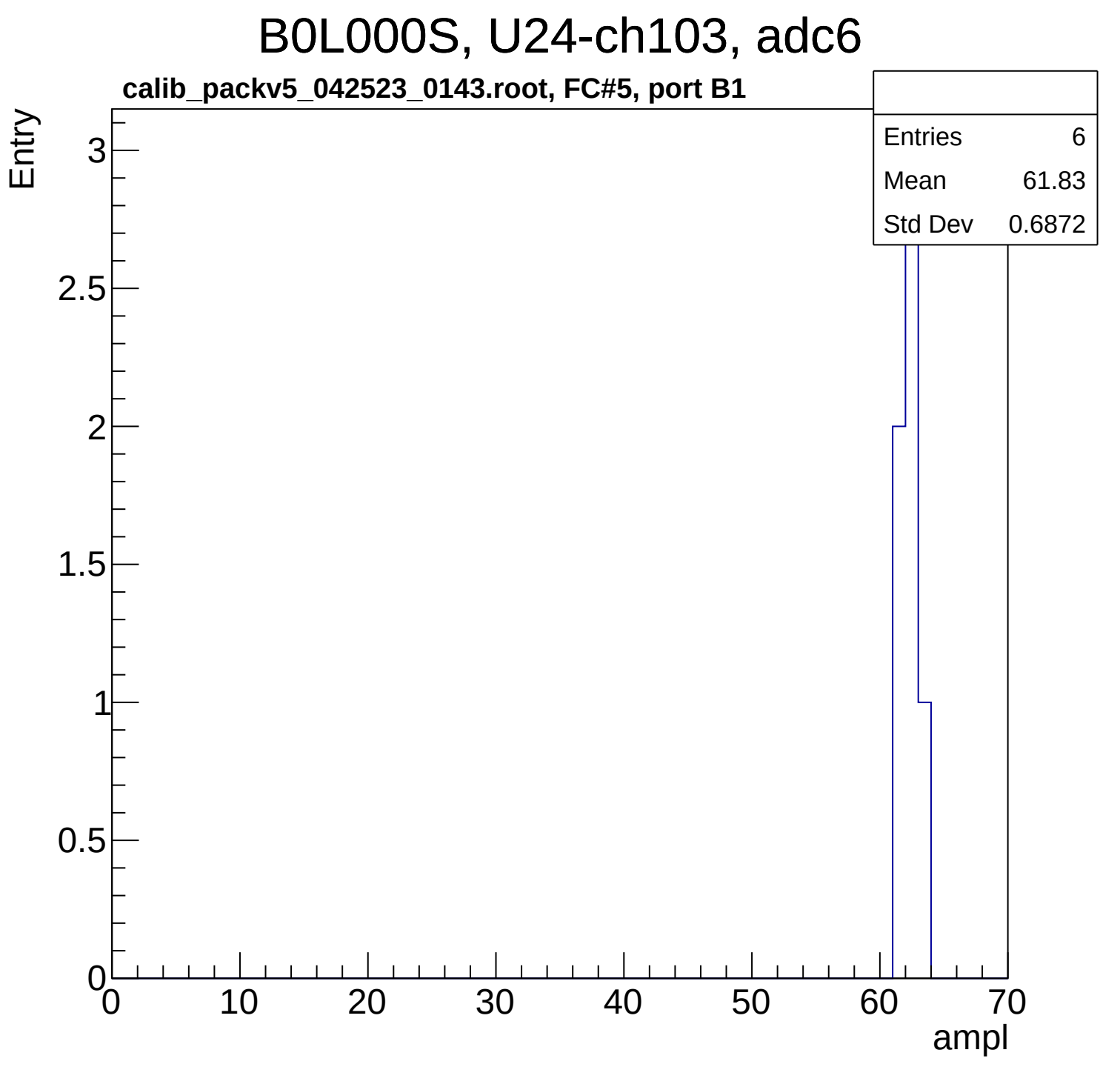
B0L000S, U24-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.83
Std Dev 0.6872
Entry
ampl
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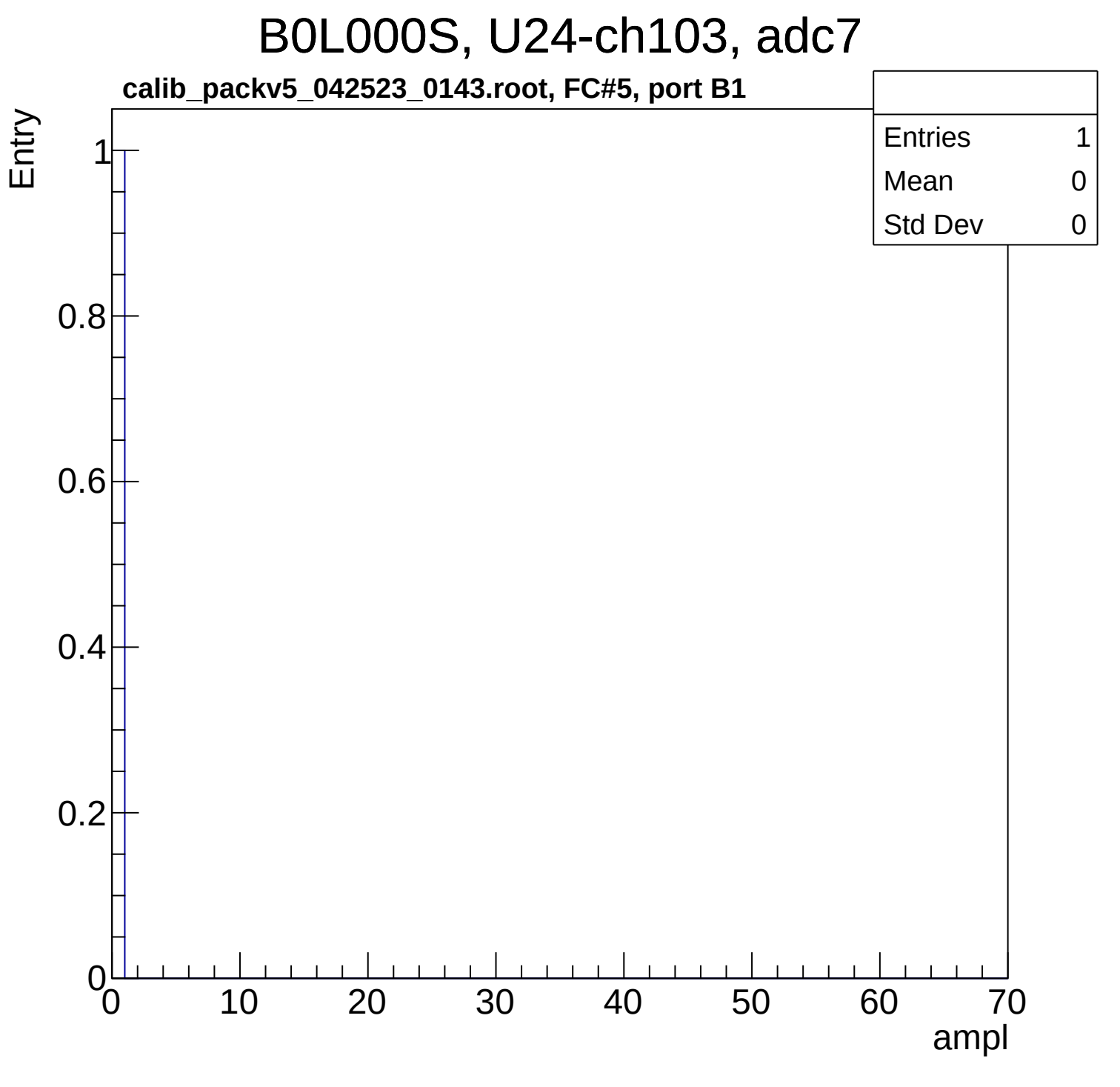
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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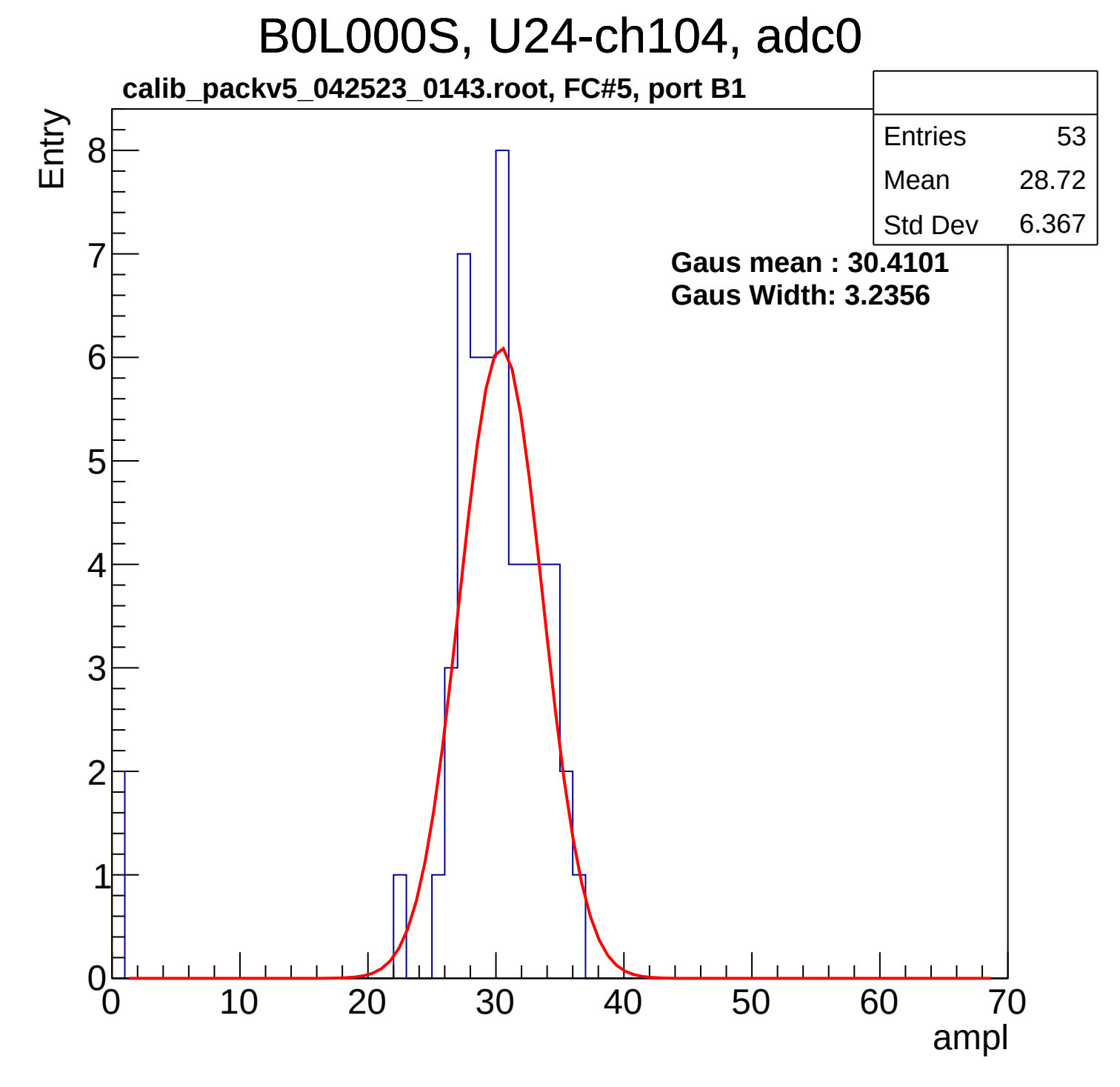
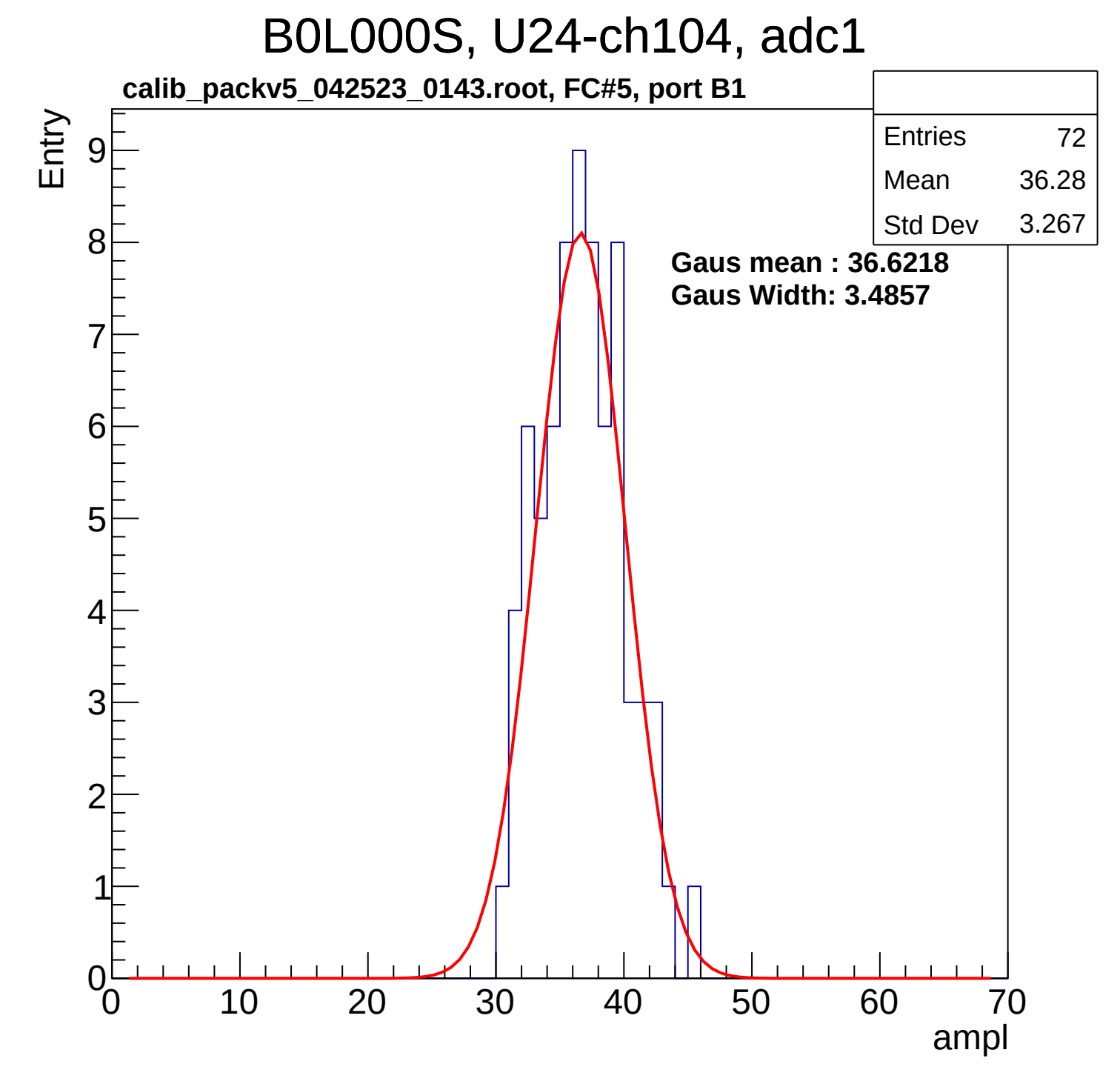
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 28.72
Std Dev 6.367
Gaus mean : 30.4101
Gaus Width: 3.2356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.28
Std Dev 3.267
Gaus mean : 36.6218
Gaus Width: 3.4857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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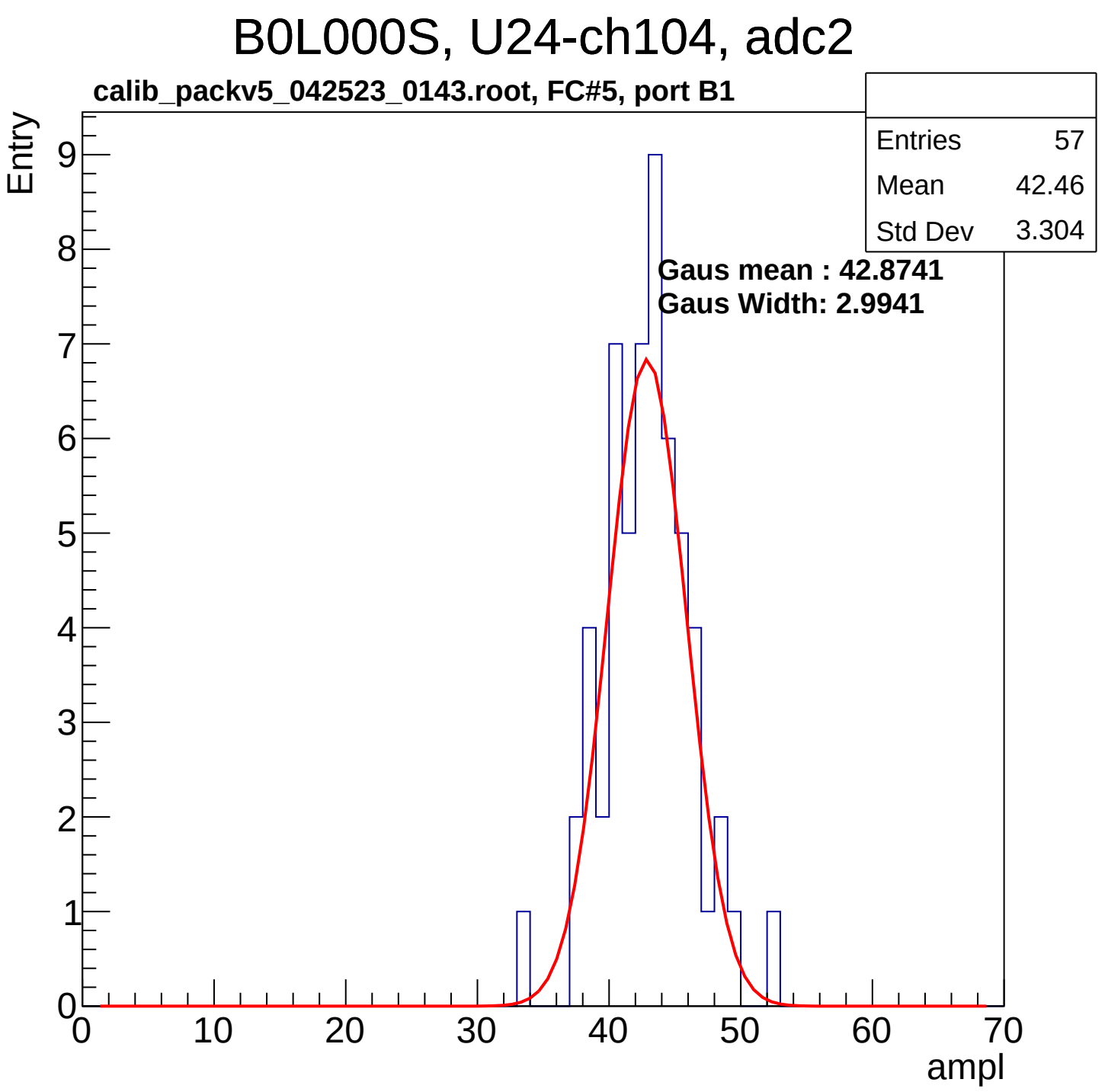
B0L000S, U24-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 42.46
Std Dev 3.304
Gaus mean : 42.8741
Gaus Width: 2.9941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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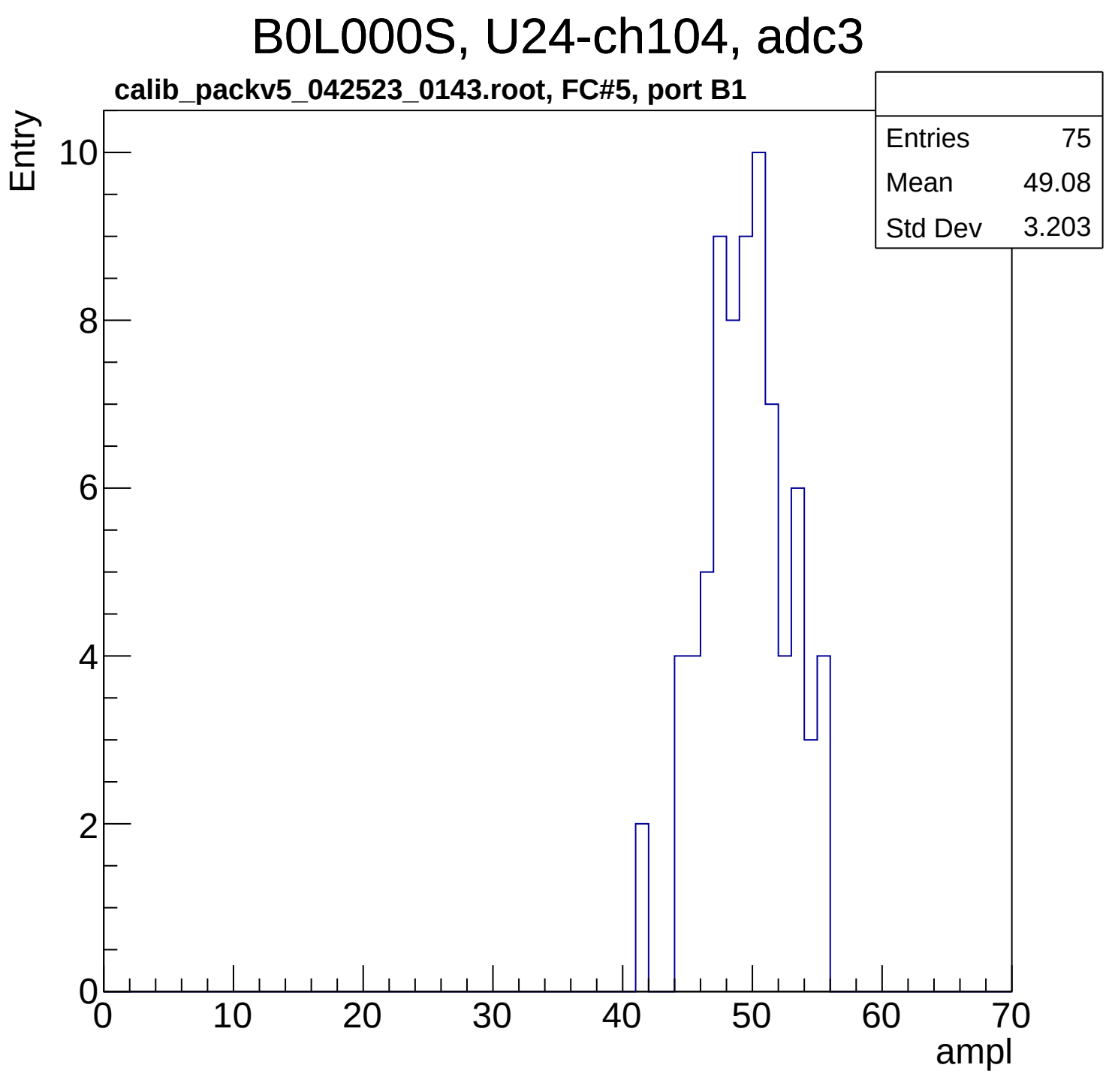
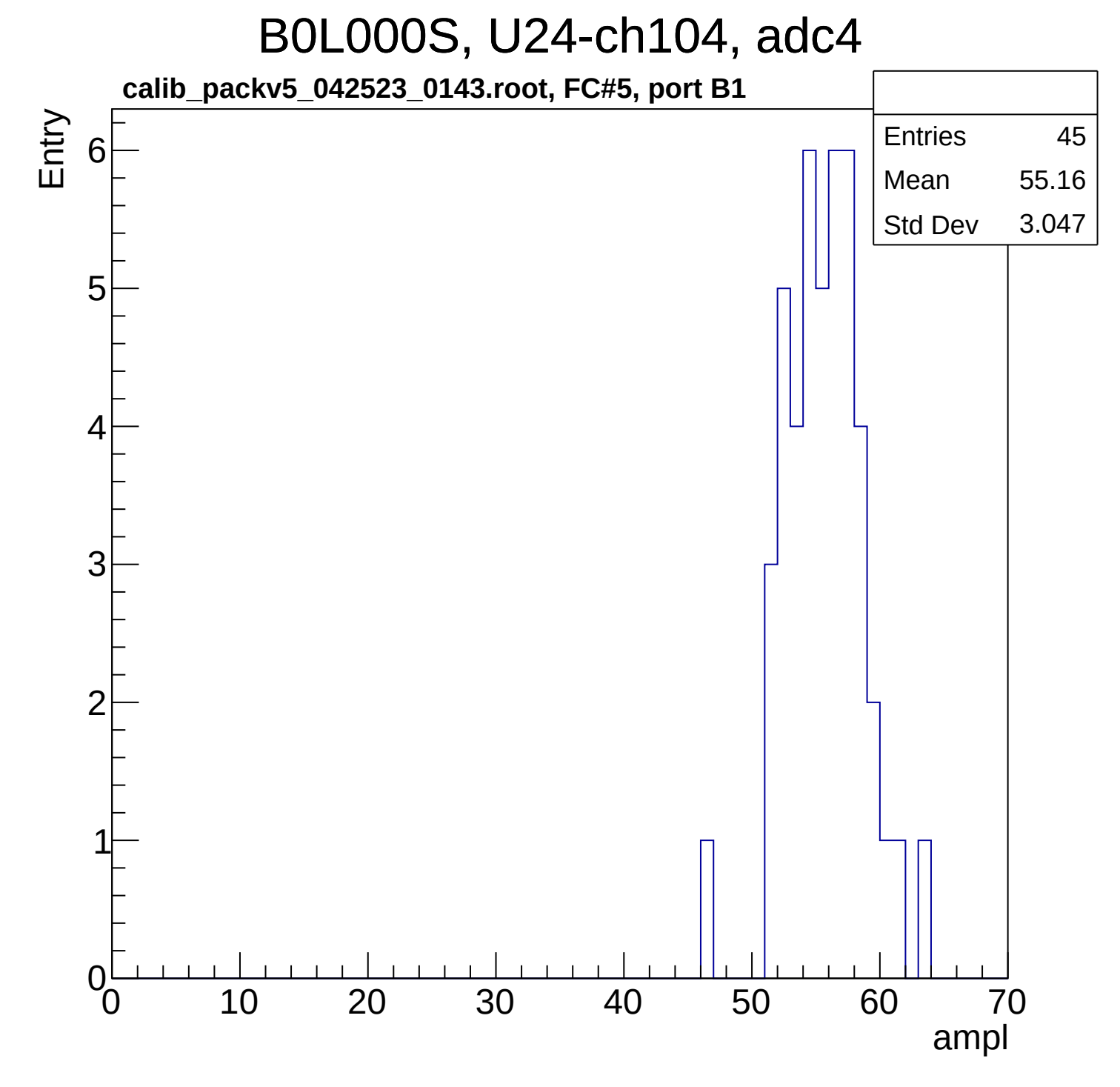
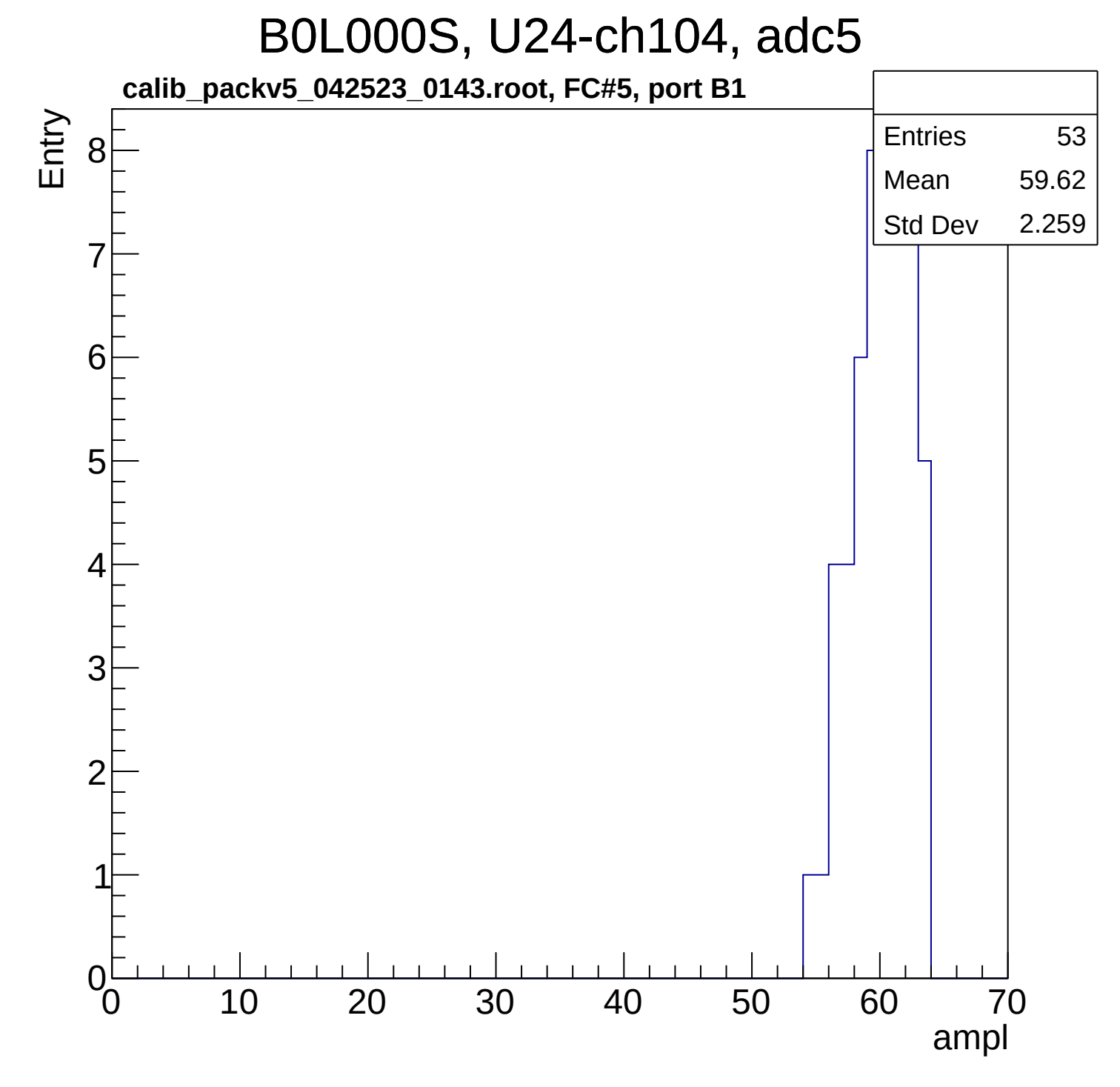
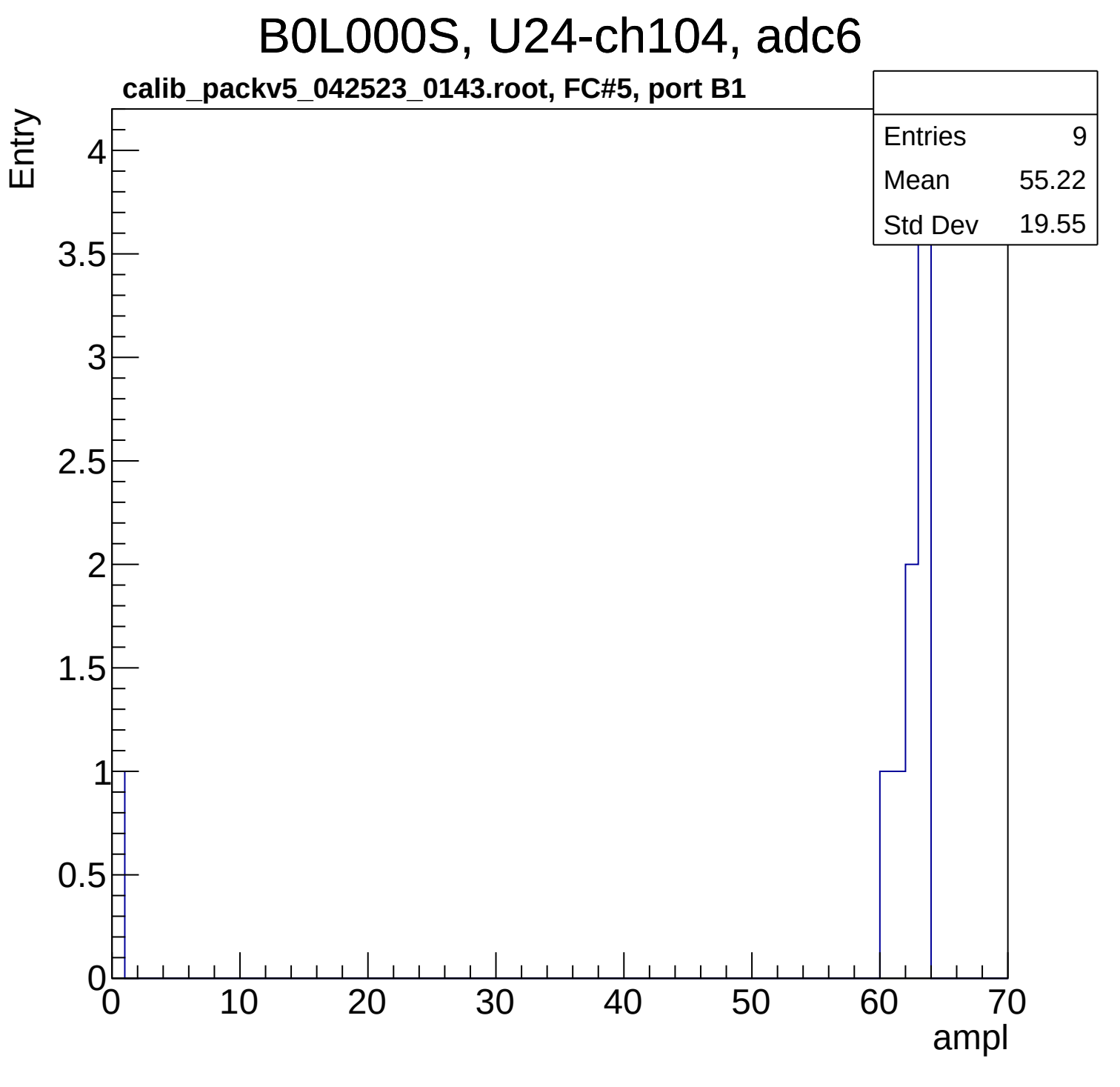
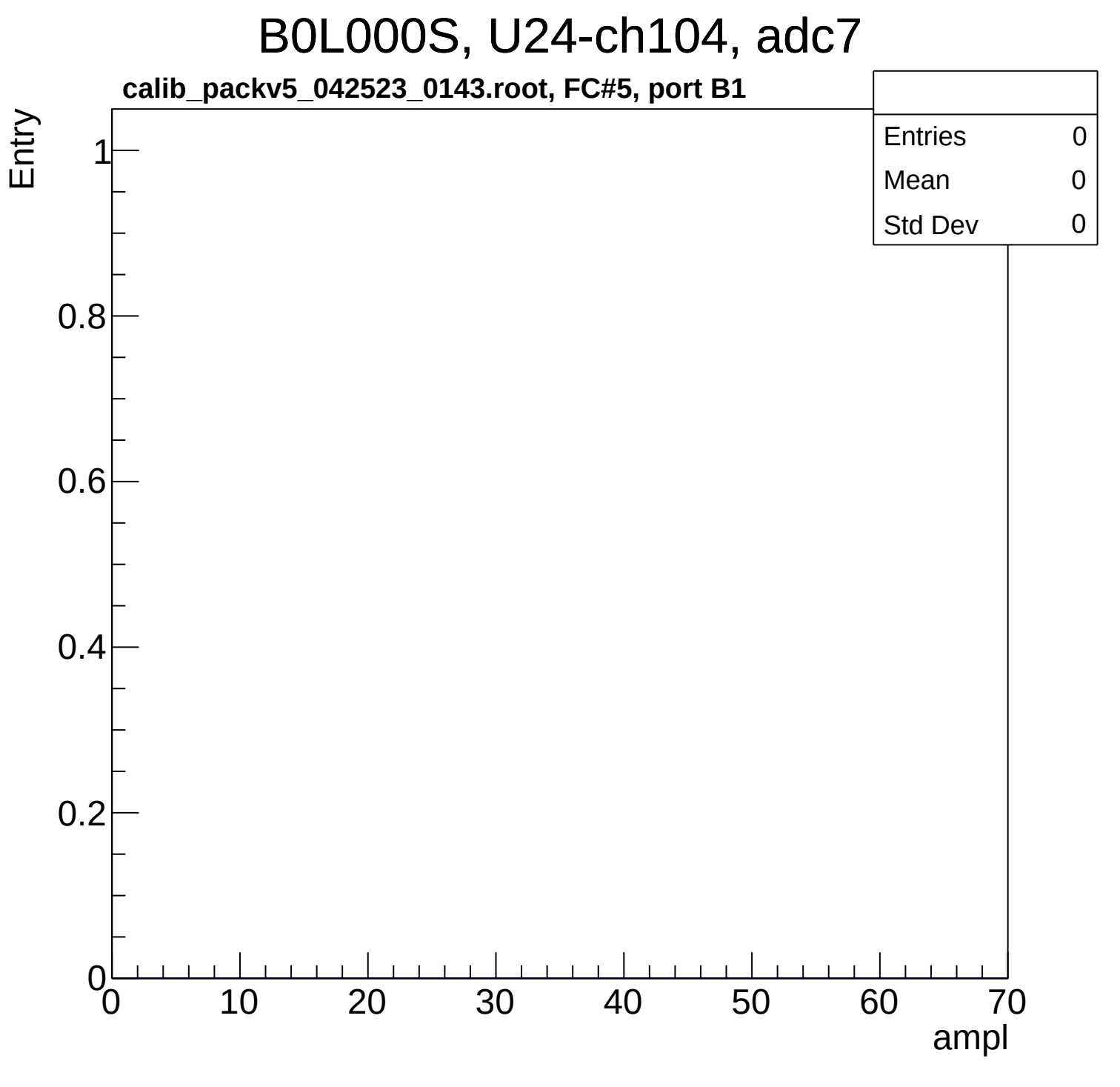
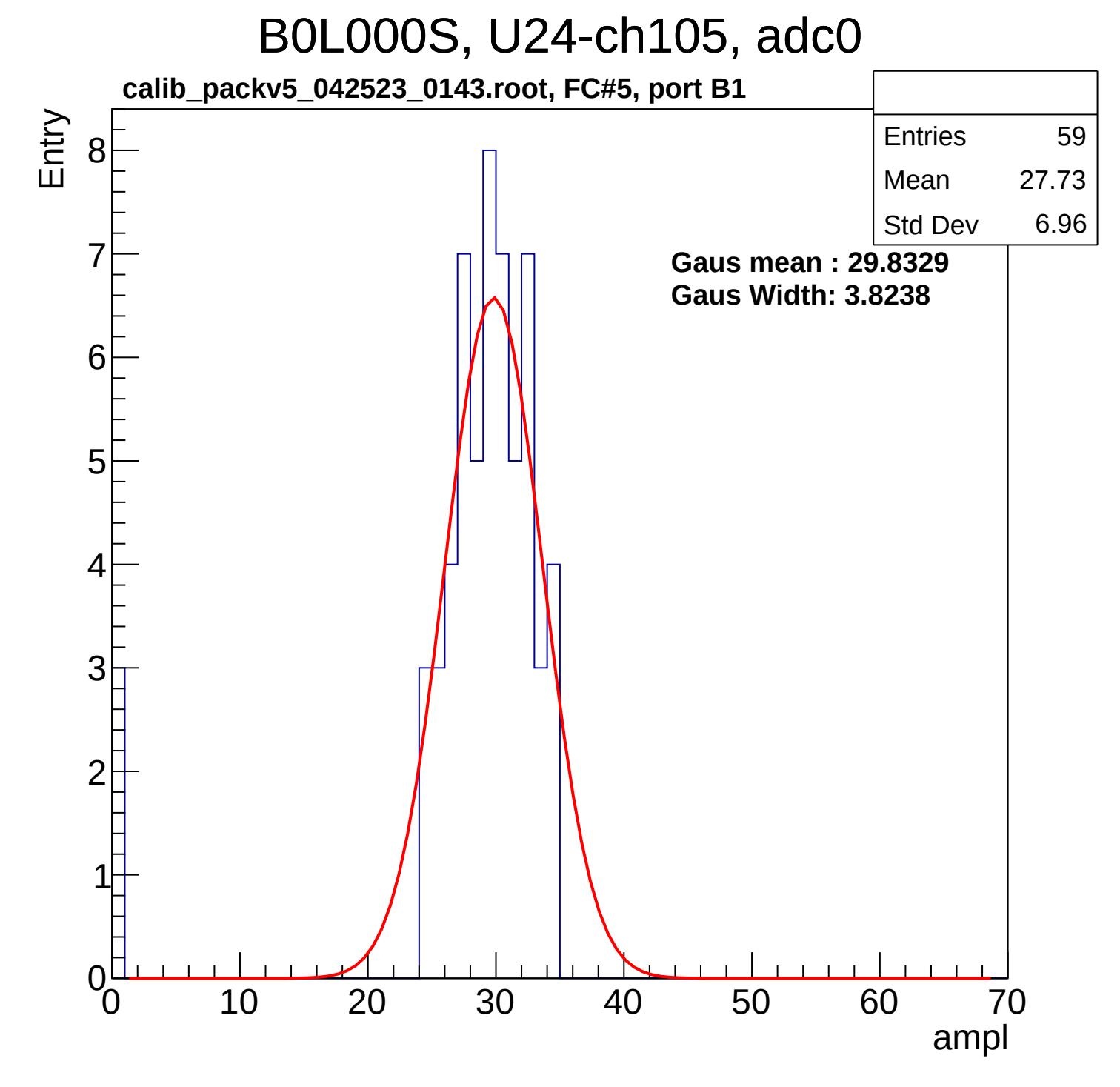

B0L000S, U24-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 49.08
Std Dev 3.203
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

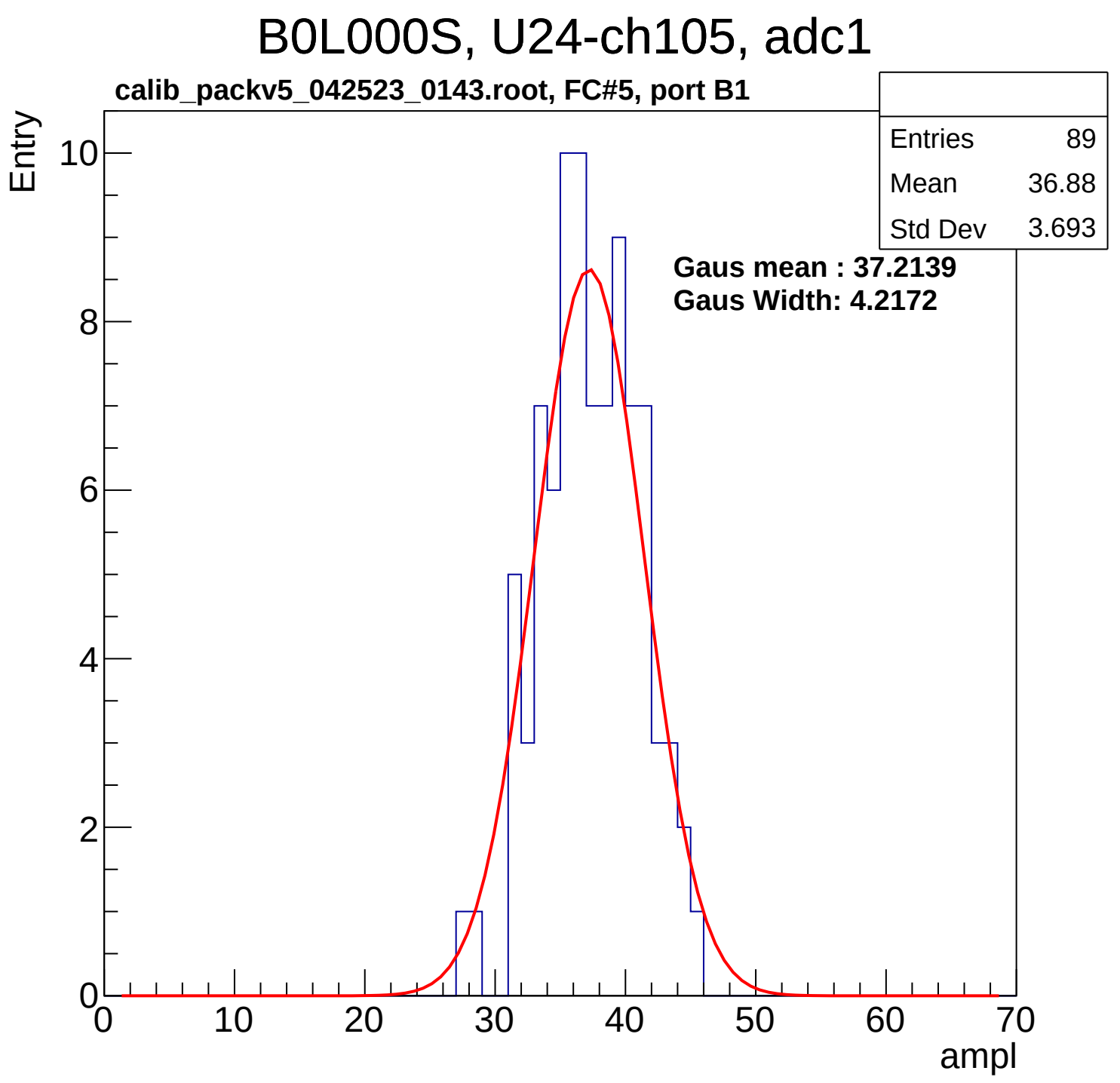

B0L000S, U24-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 55.16
Std Dev 3.047
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

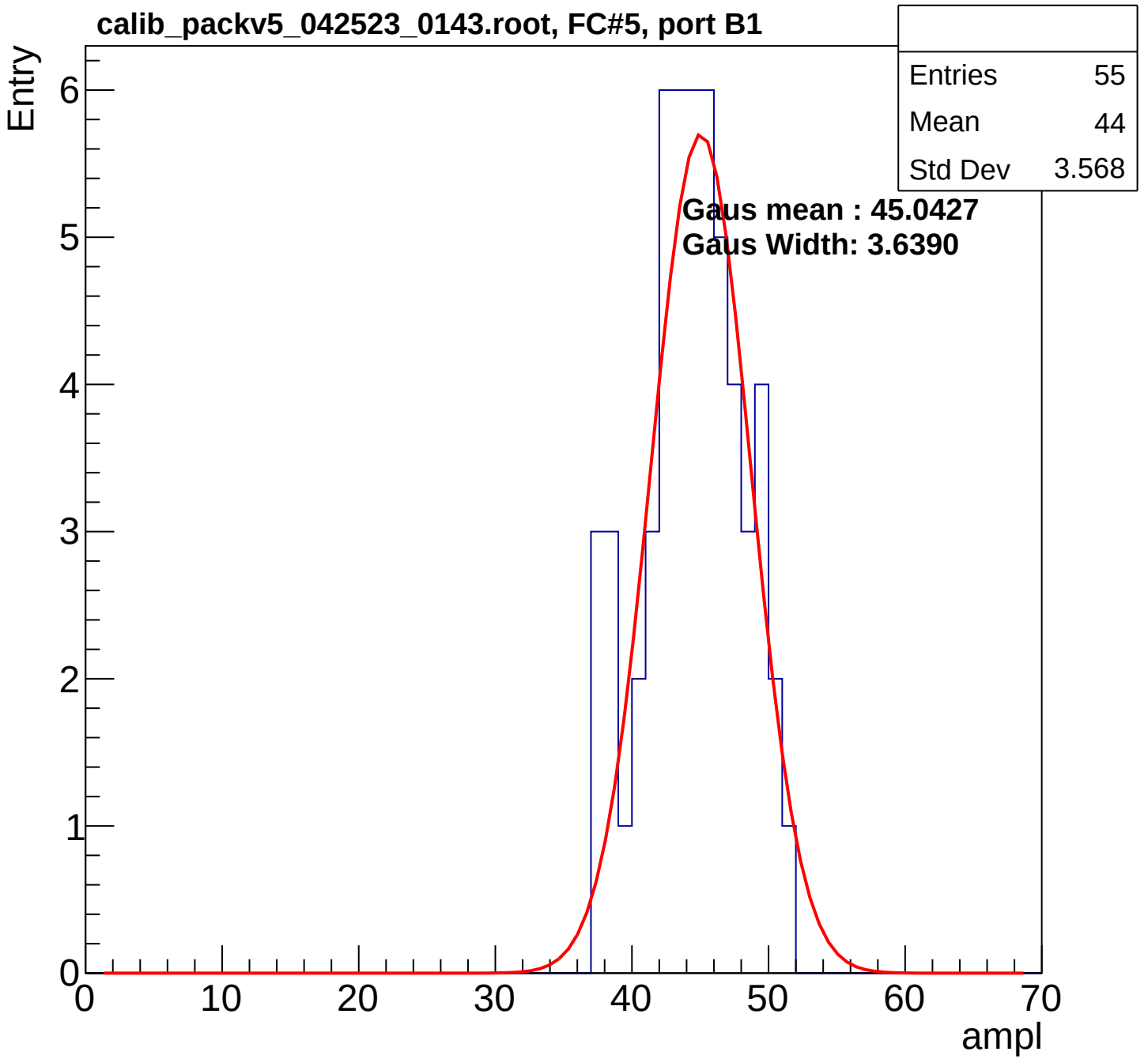

B0L000S, U24-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 59.62
Std Dev 2.259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

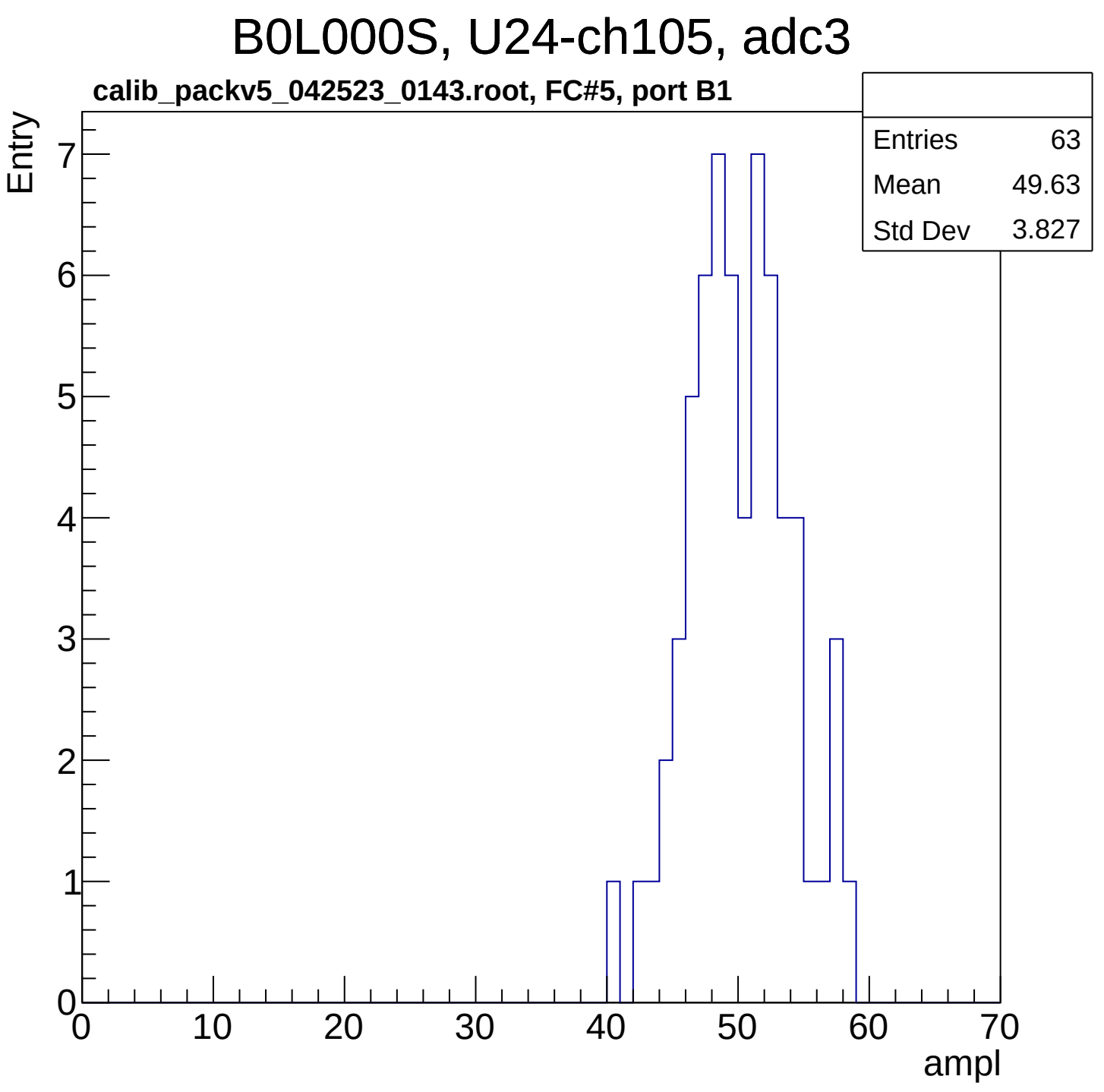

B0L000S, U24-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

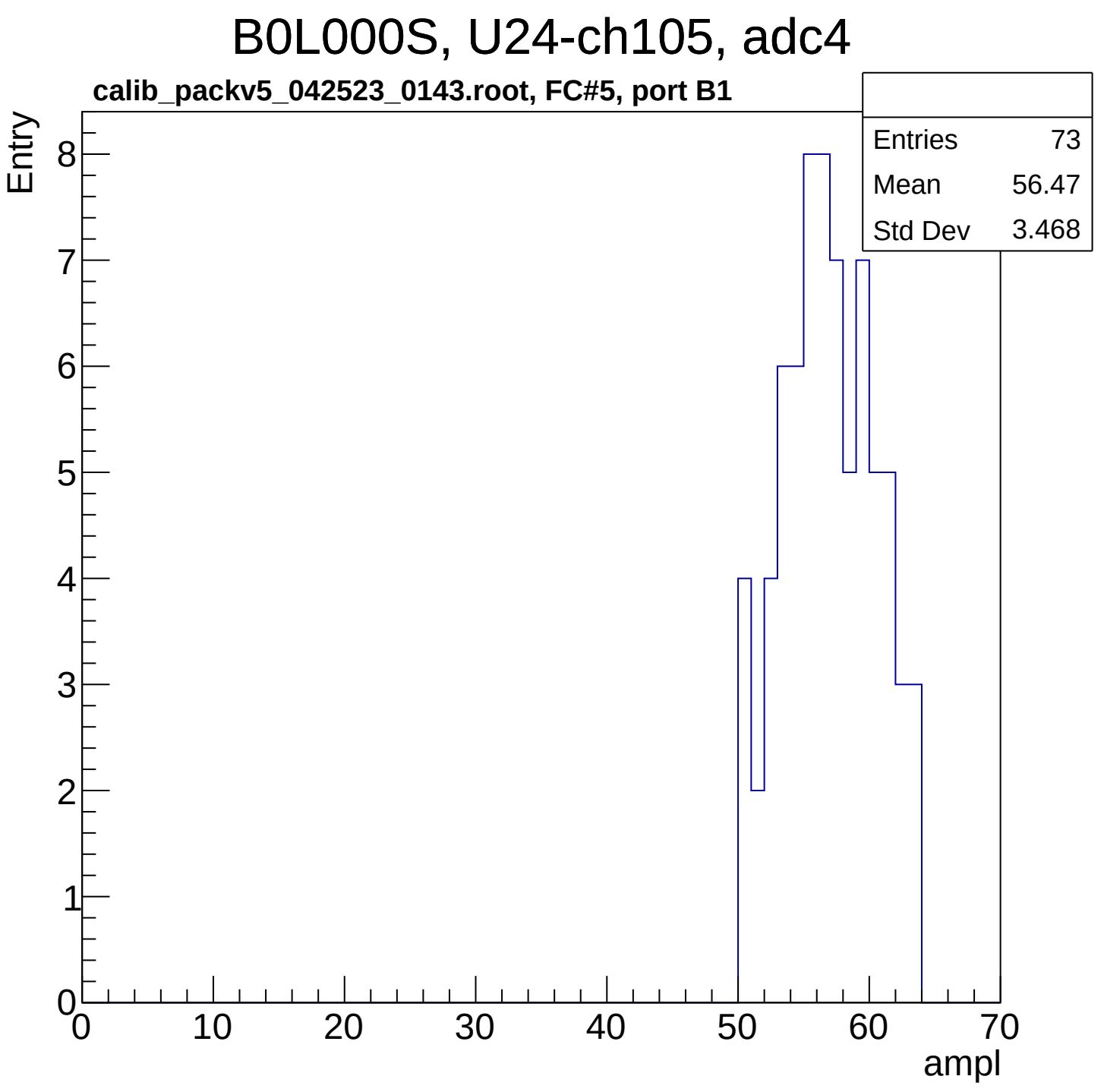

B0L000S, U24-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.73
Std Dev 6.96
Gaus mean : 29.8329
Gaus Width: 3.8238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 89
Mean 36.88
Std Dev 3.693
Gaus mean : 37.2139
Gaus Width: 4.2172
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 44
Std Dev 3.568
Gaus mean : 45.0427
Gaus Width: 3.6390
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.63
Std Dev 3.827
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 56.47
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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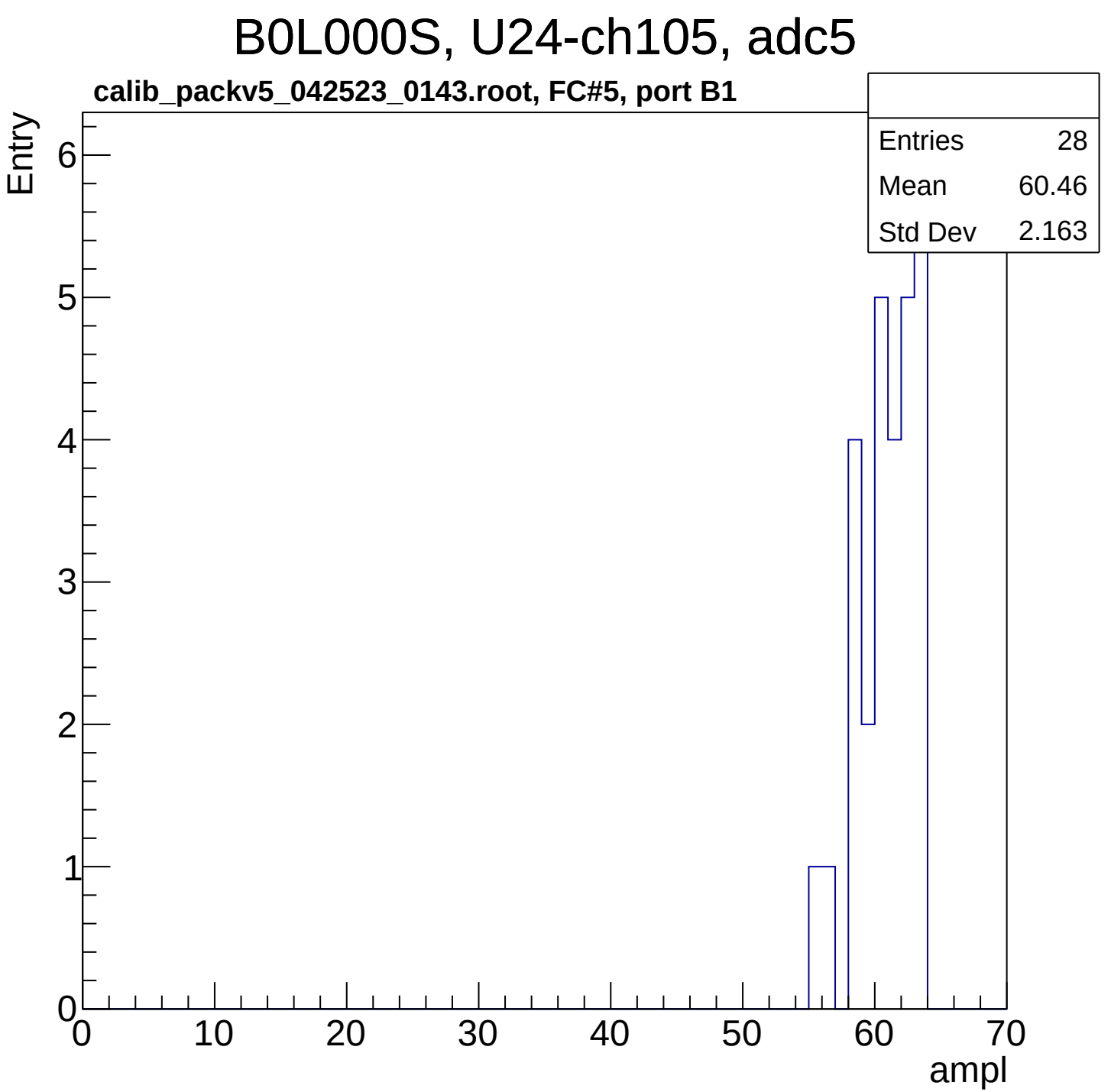
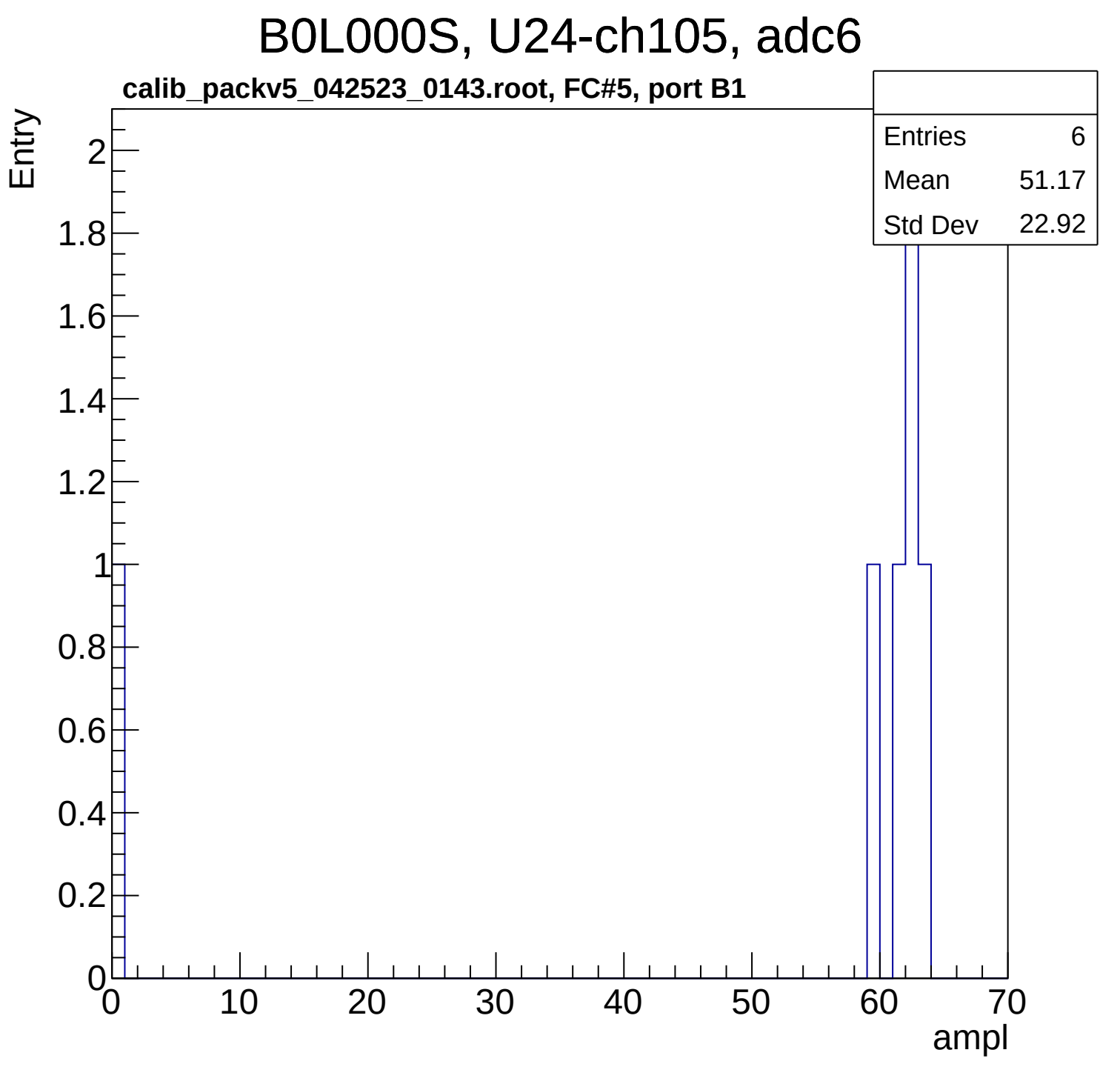
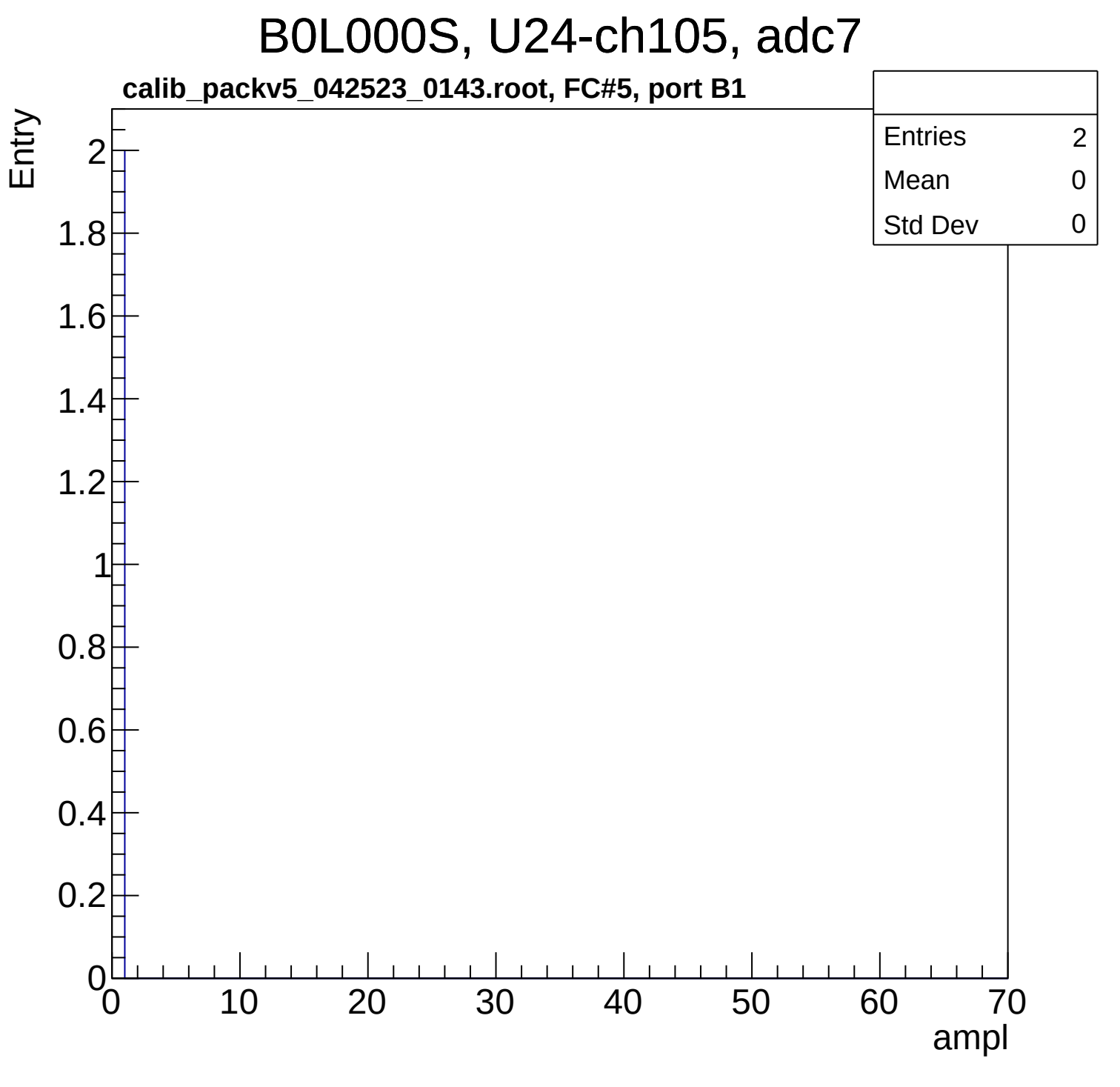
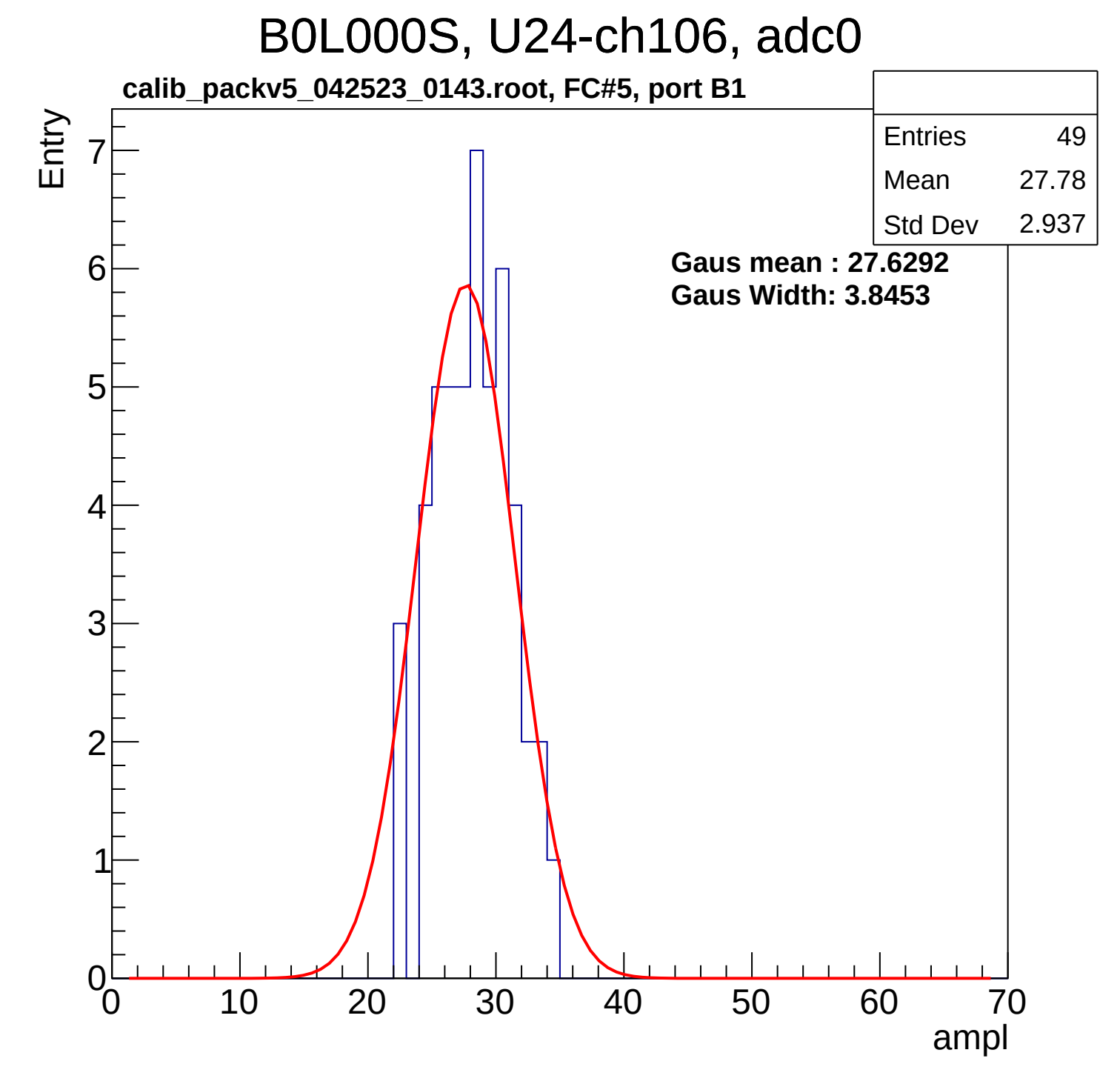
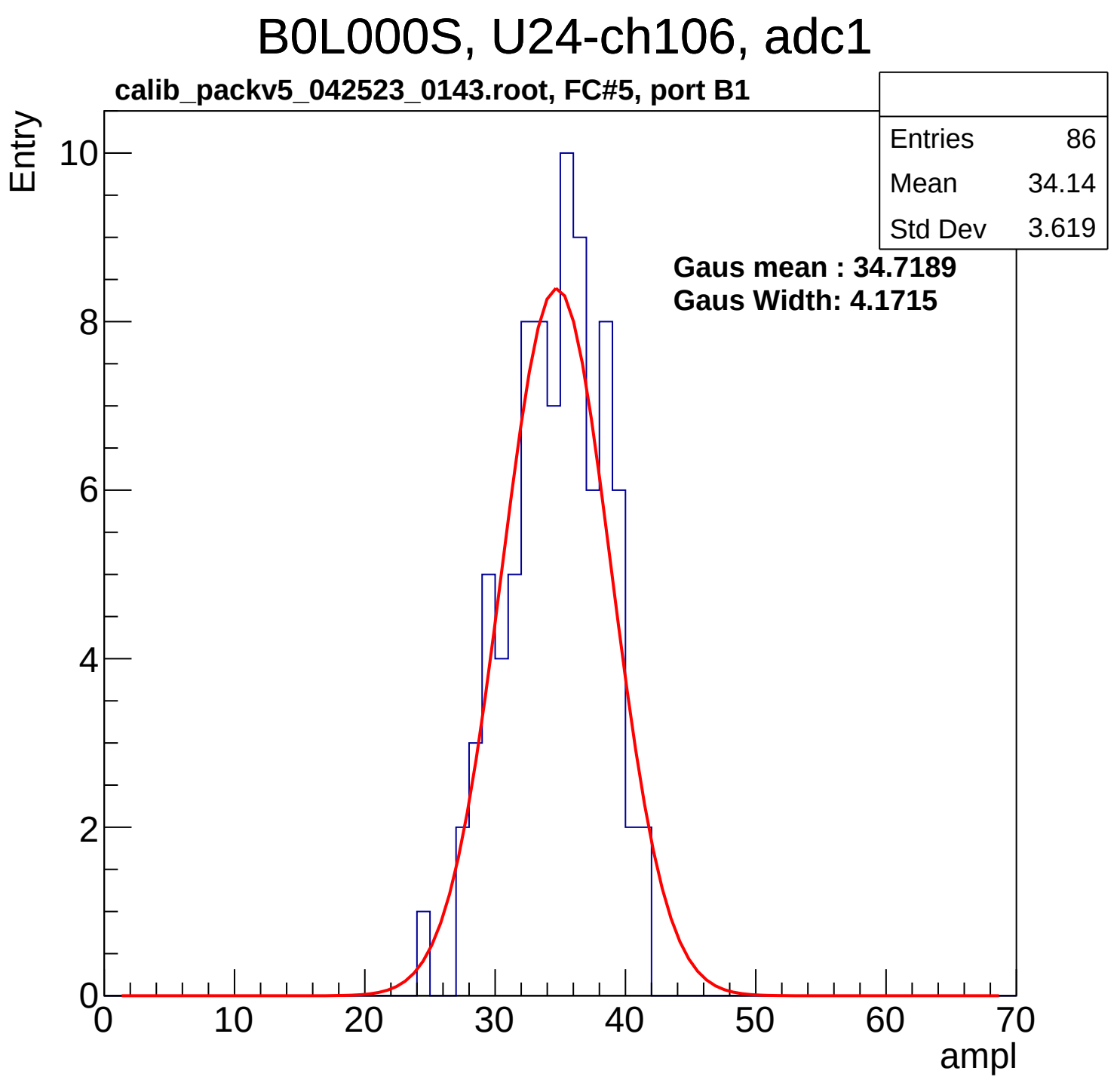
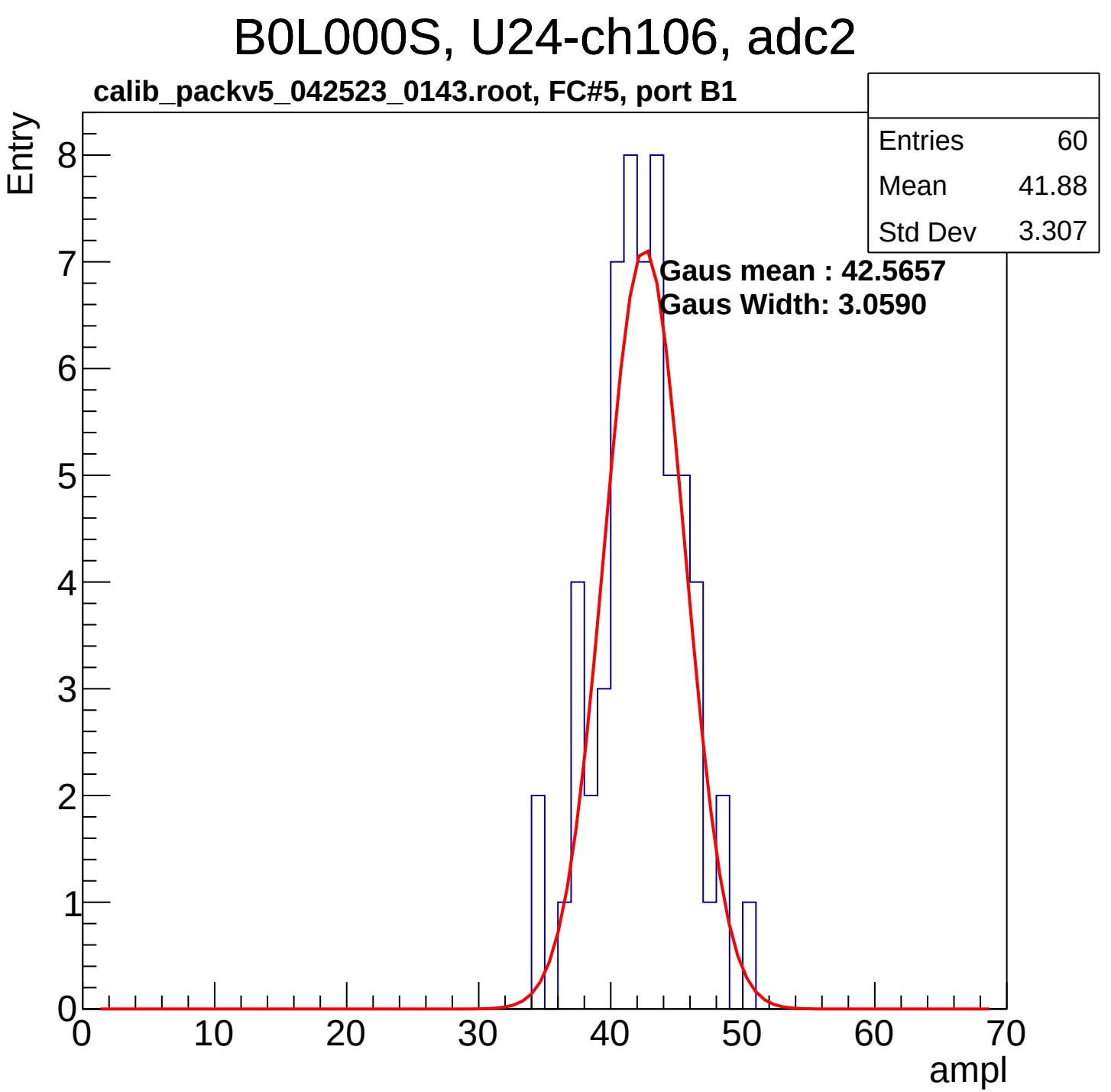

B0L000S, U24-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 60.46
Std Dev 2.163
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 51.17
Std Dev 22.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 27.78
Std Dev 2.937
Gaus mean : 27.6292
Gaus Width: 3.8453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 34.14
Std Dev 3.619
Gaus mean : 34.7189
Gaus Width: 4.1715
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 41.88
Std Dev 3.307
Gaus mean : 42.5657
Gaus Width: 3.0590
Entry
ampl
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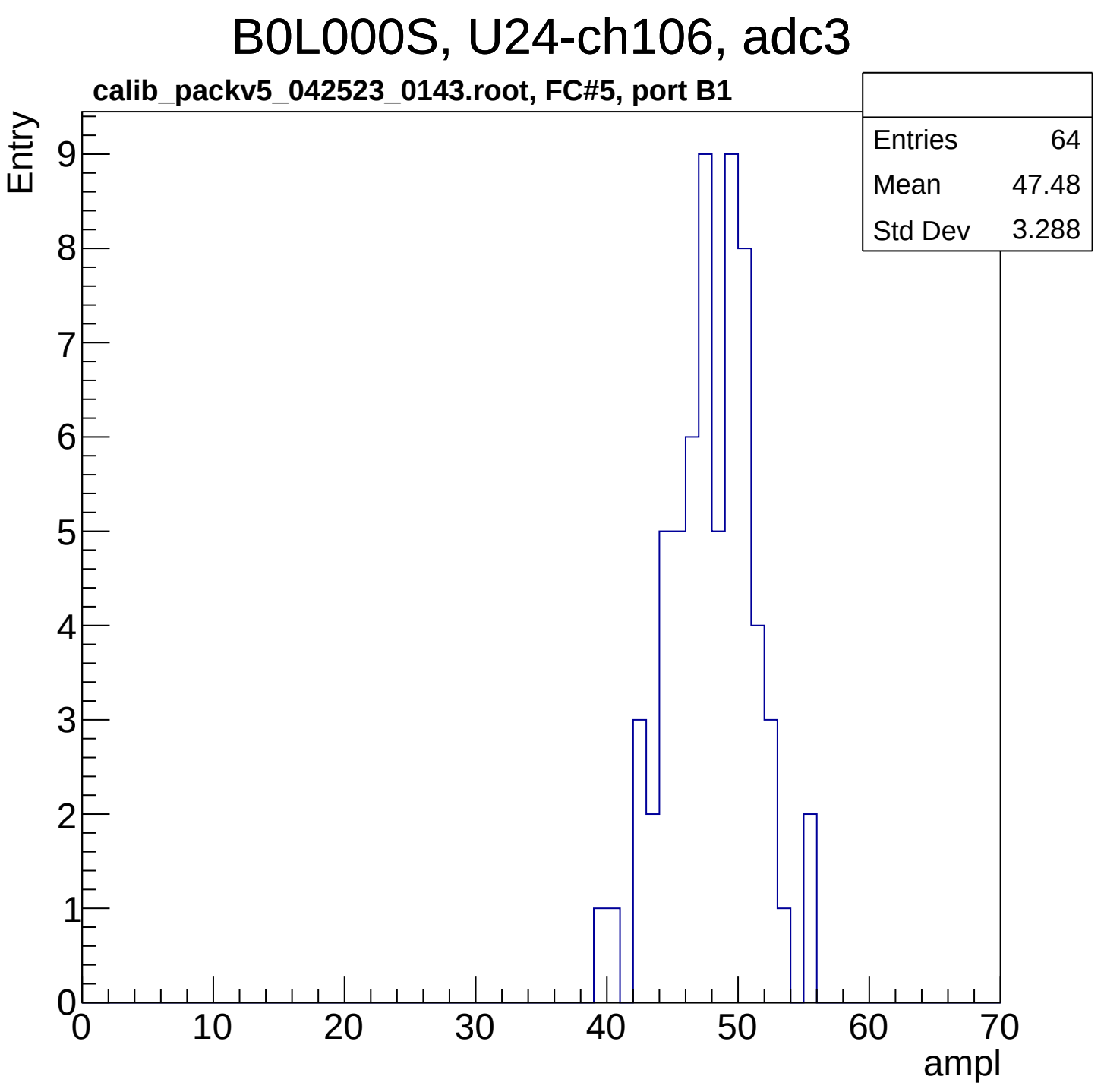
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.48
Std Dev 3.288
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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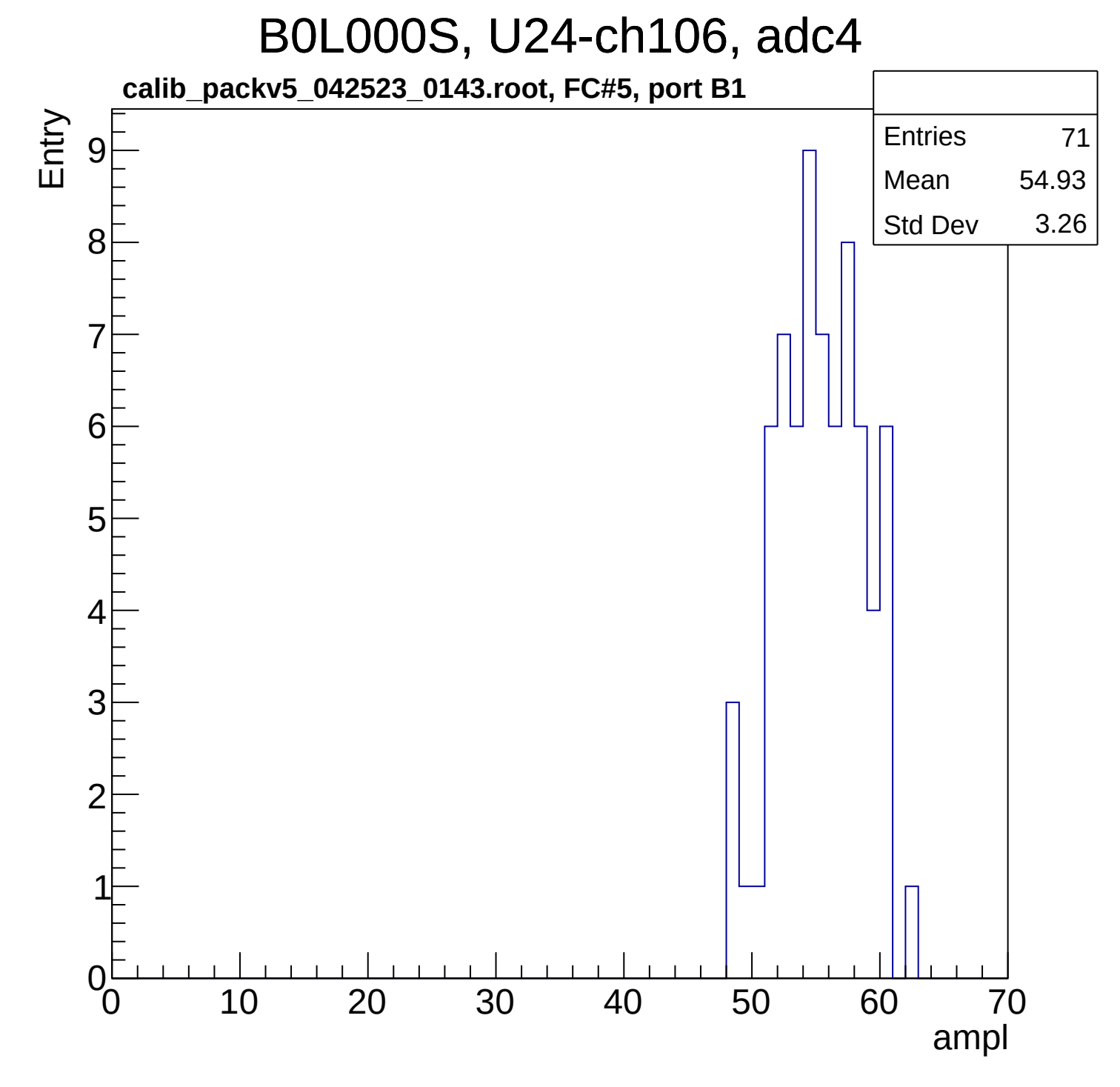
B0L000S, U24-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.93
Std Dev 3.26
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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60
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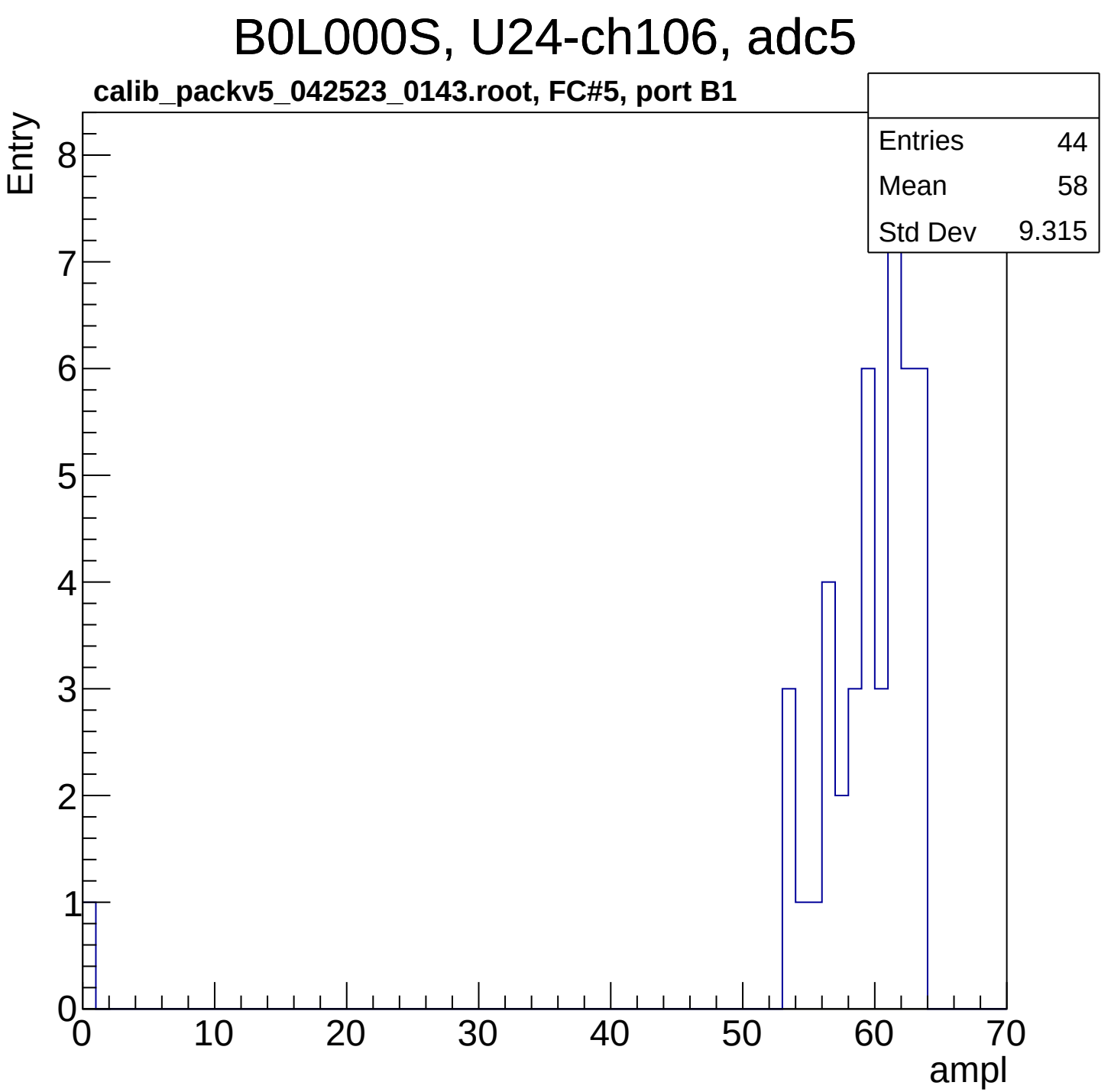
B0L000S, U24-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58
Std Dev 9.315
Entry
ampl
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1
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5
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0
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60
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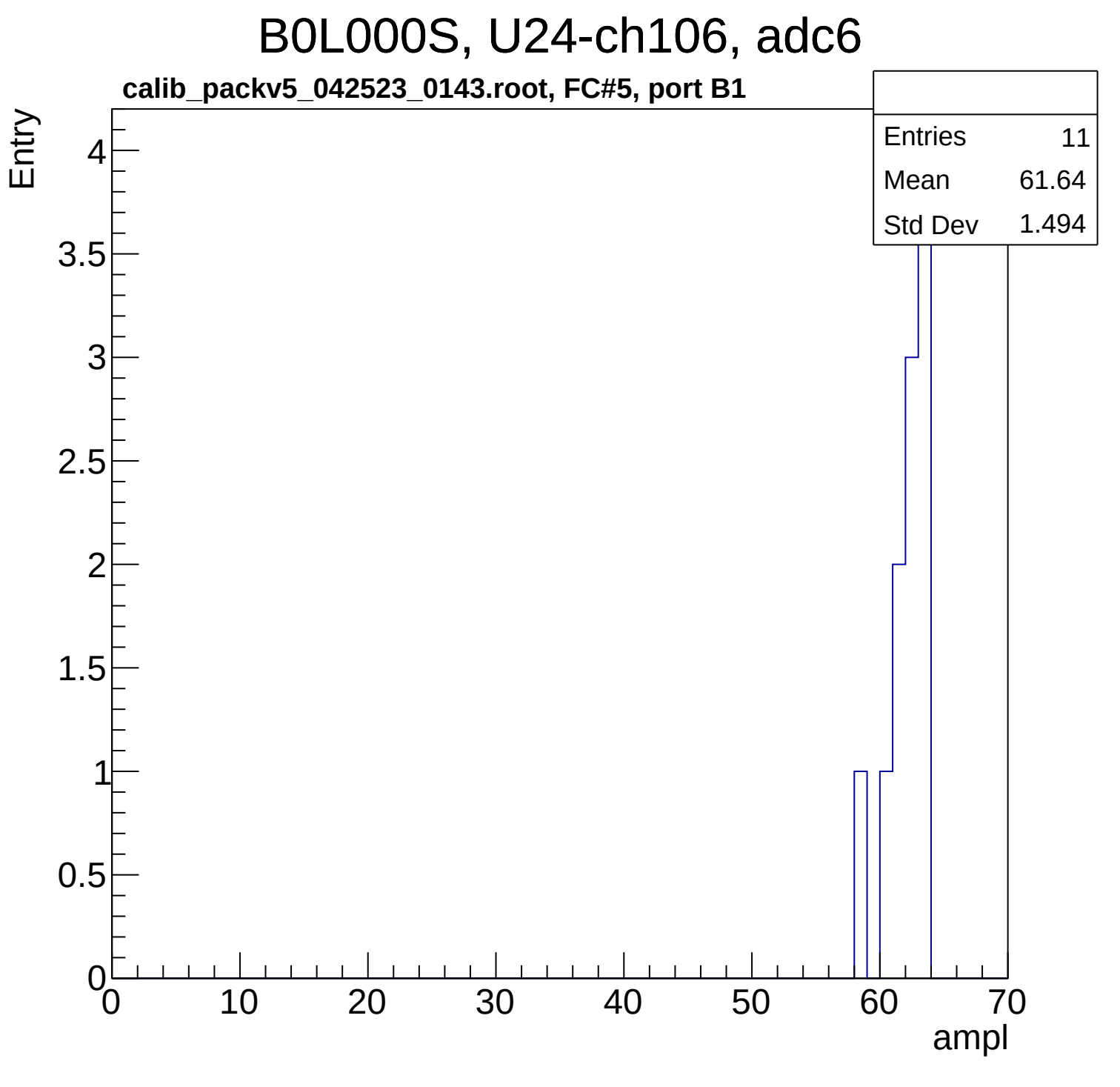
B0L000S, U24-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.64
Std Dev 1.494
Entry
ampl
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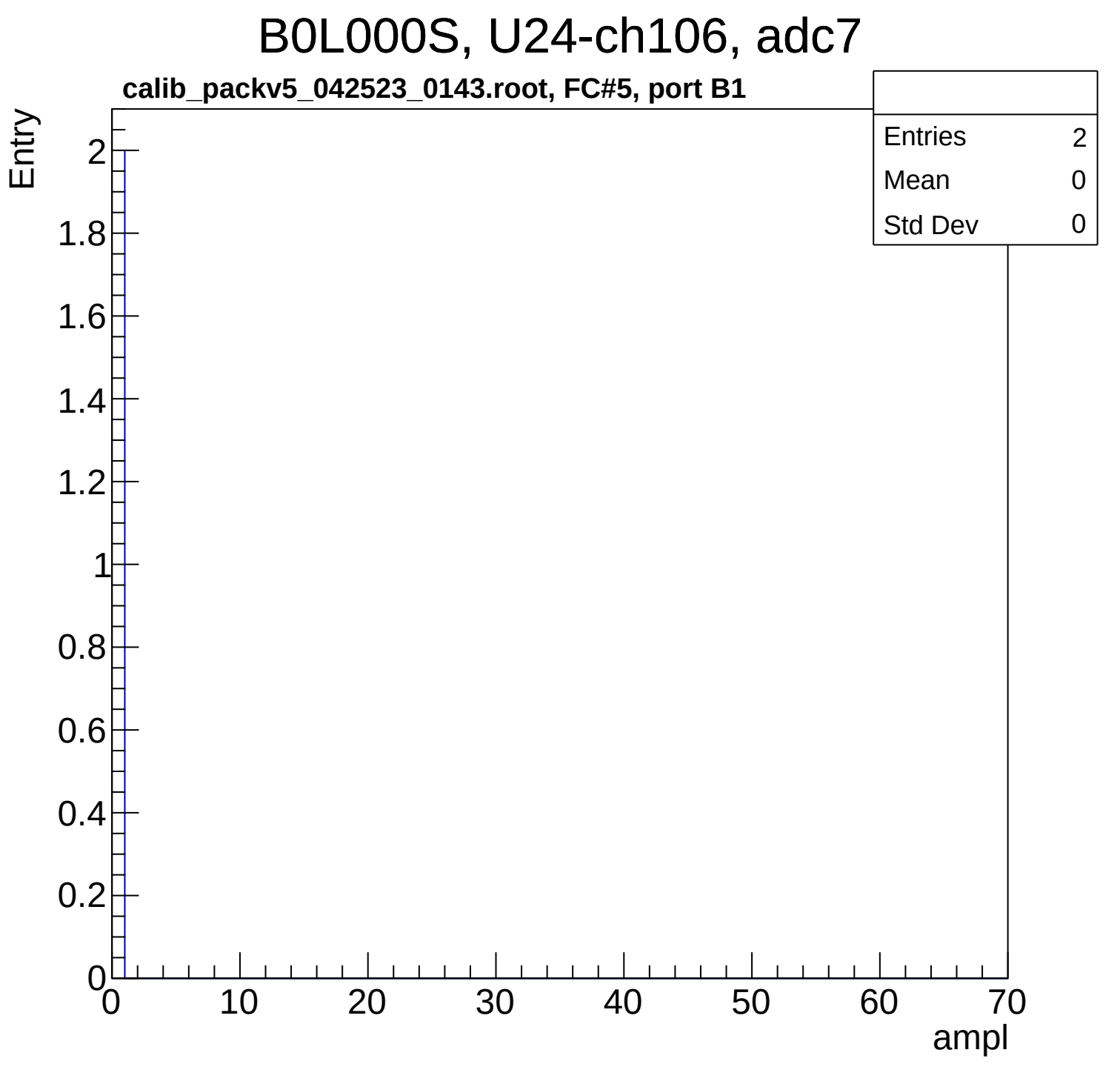
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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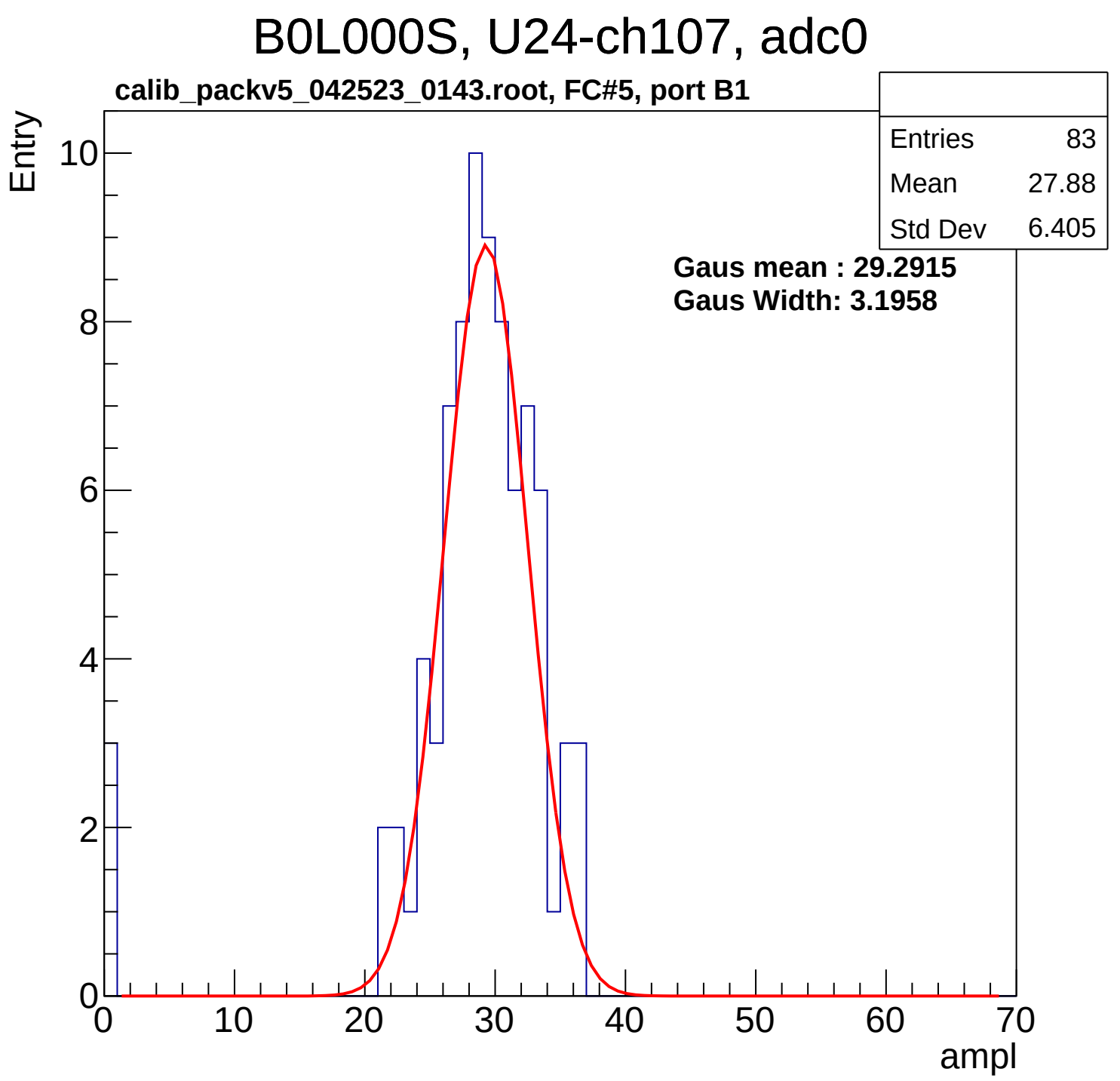
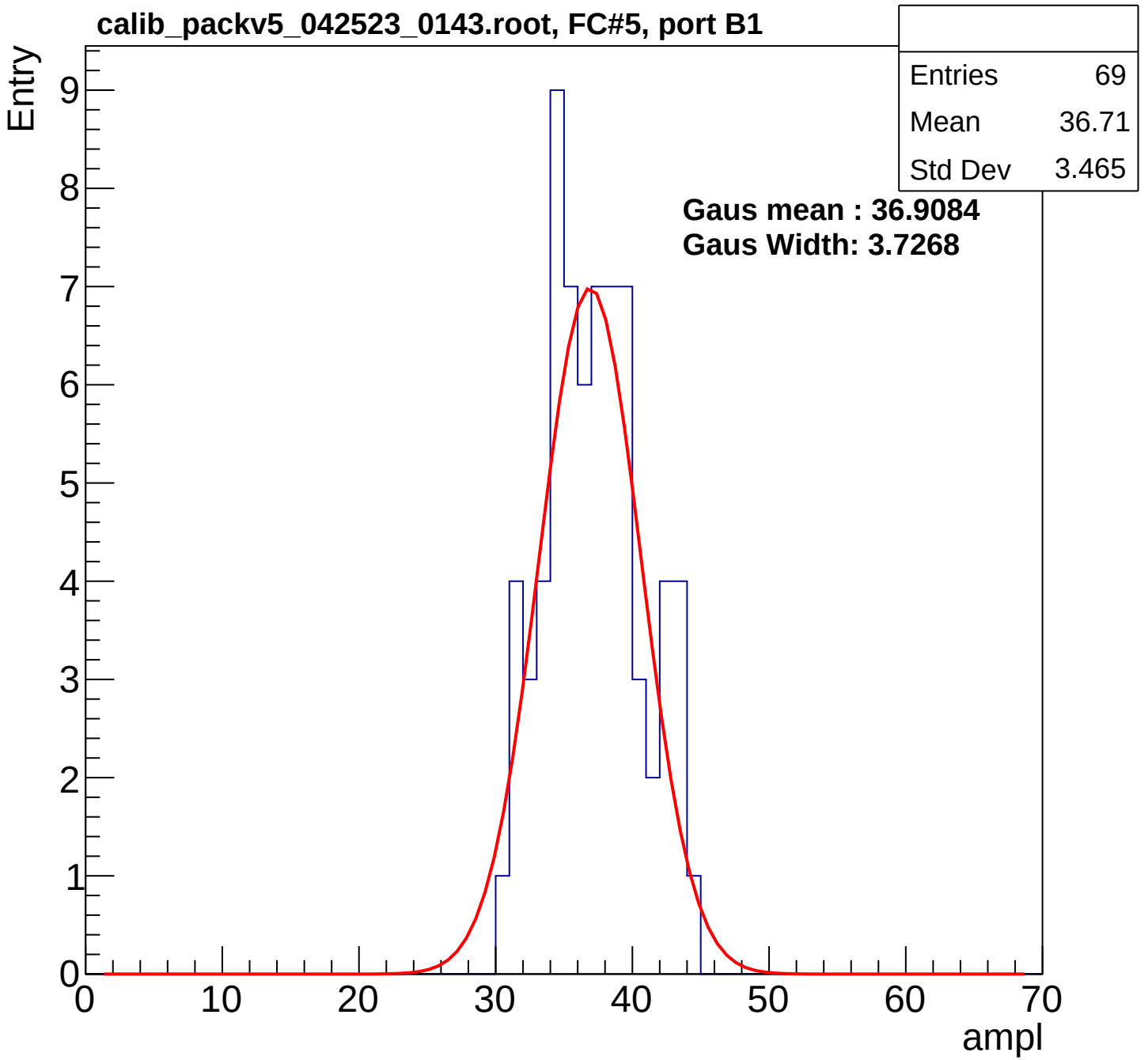

B0L000S, U24-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 27.88
Std Dev 6.405
Gaus mean : 29.2915
Gaus Width: 3.1958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.71
Std Dev 3.465
Gaus mean : 36.9084
Gaus Width: 3.7268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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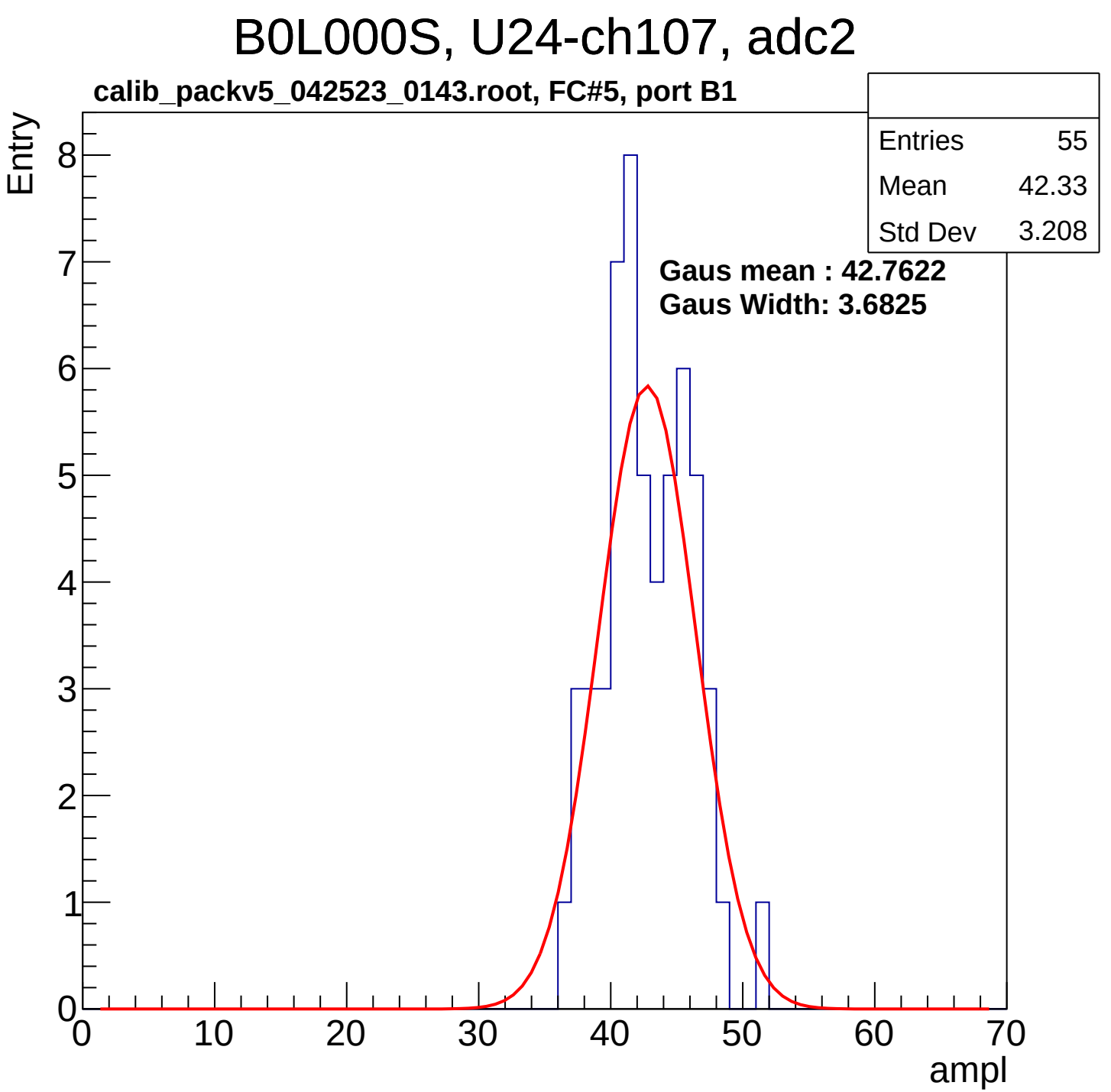
B0L000S, U24-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.33
Std Dev 3.208
Gaus mean : 42.7622
Gaus Width: 3.6825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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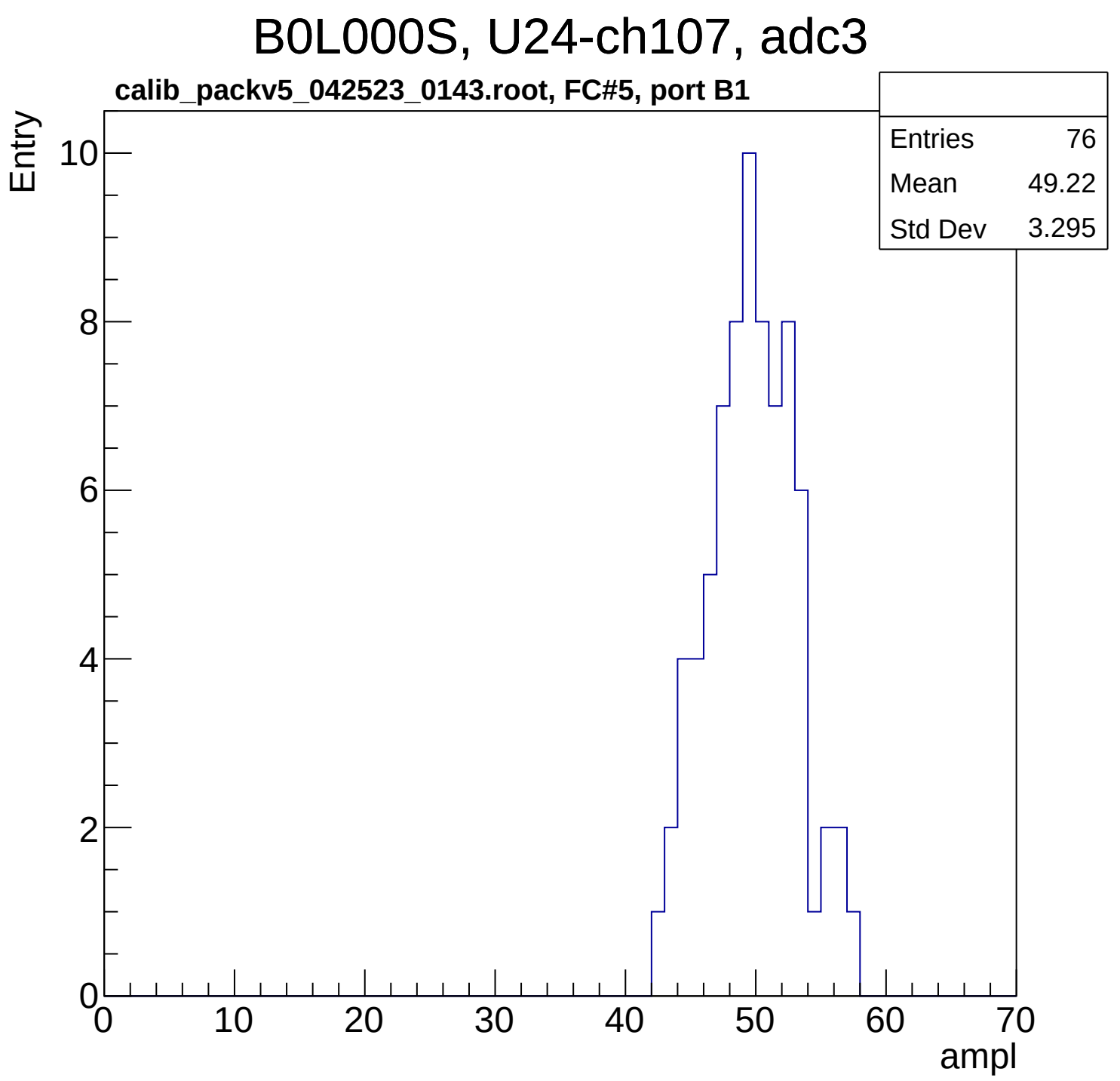
40
50
60
70


B0L000S, U24-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 49.22
Std Dev 3.295
Entry
ampl
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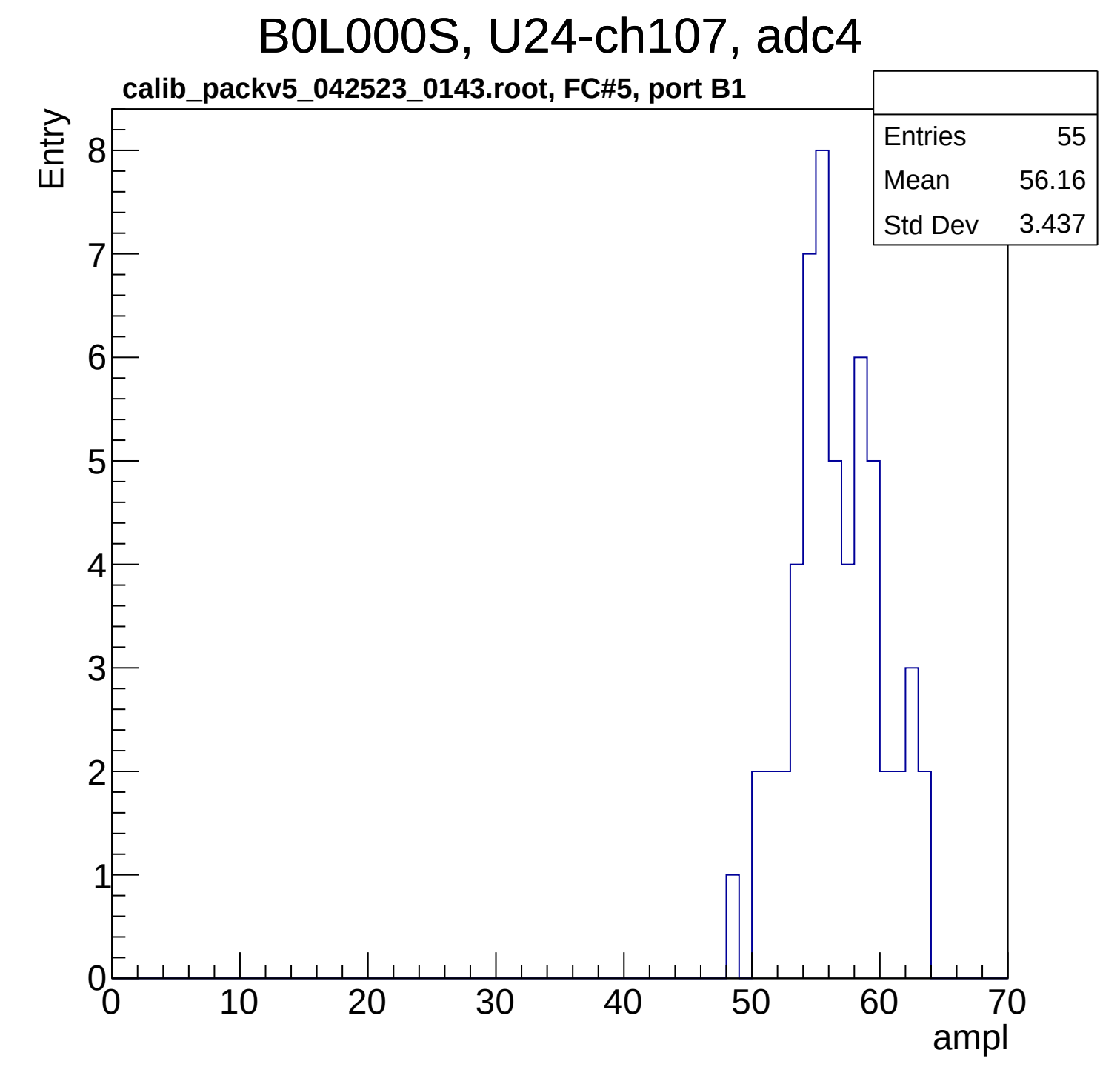
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 56.16
Std Dev 3.437
Entry
ampl
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2
3
4
5
6
7
8
0
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50
60
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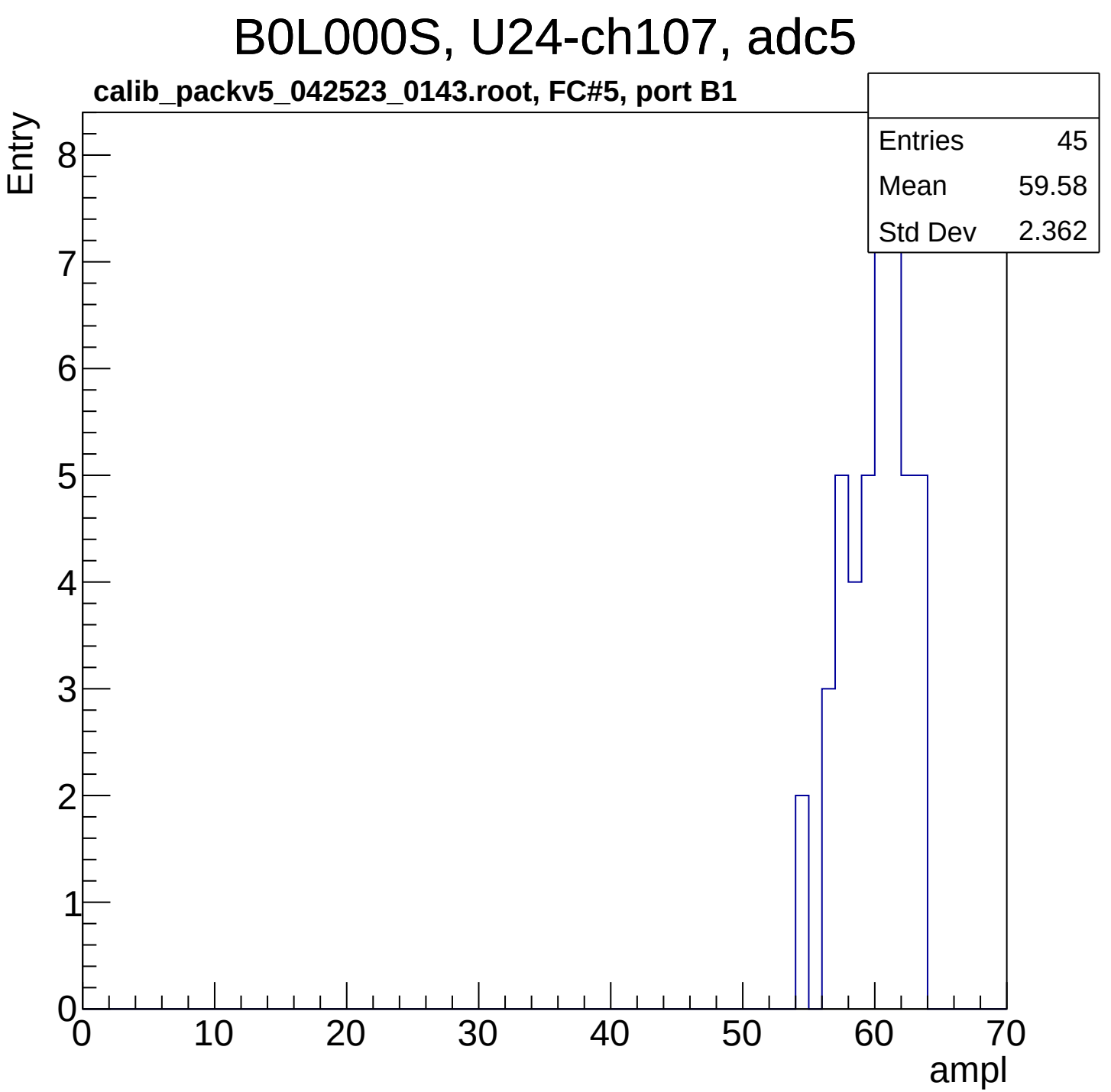
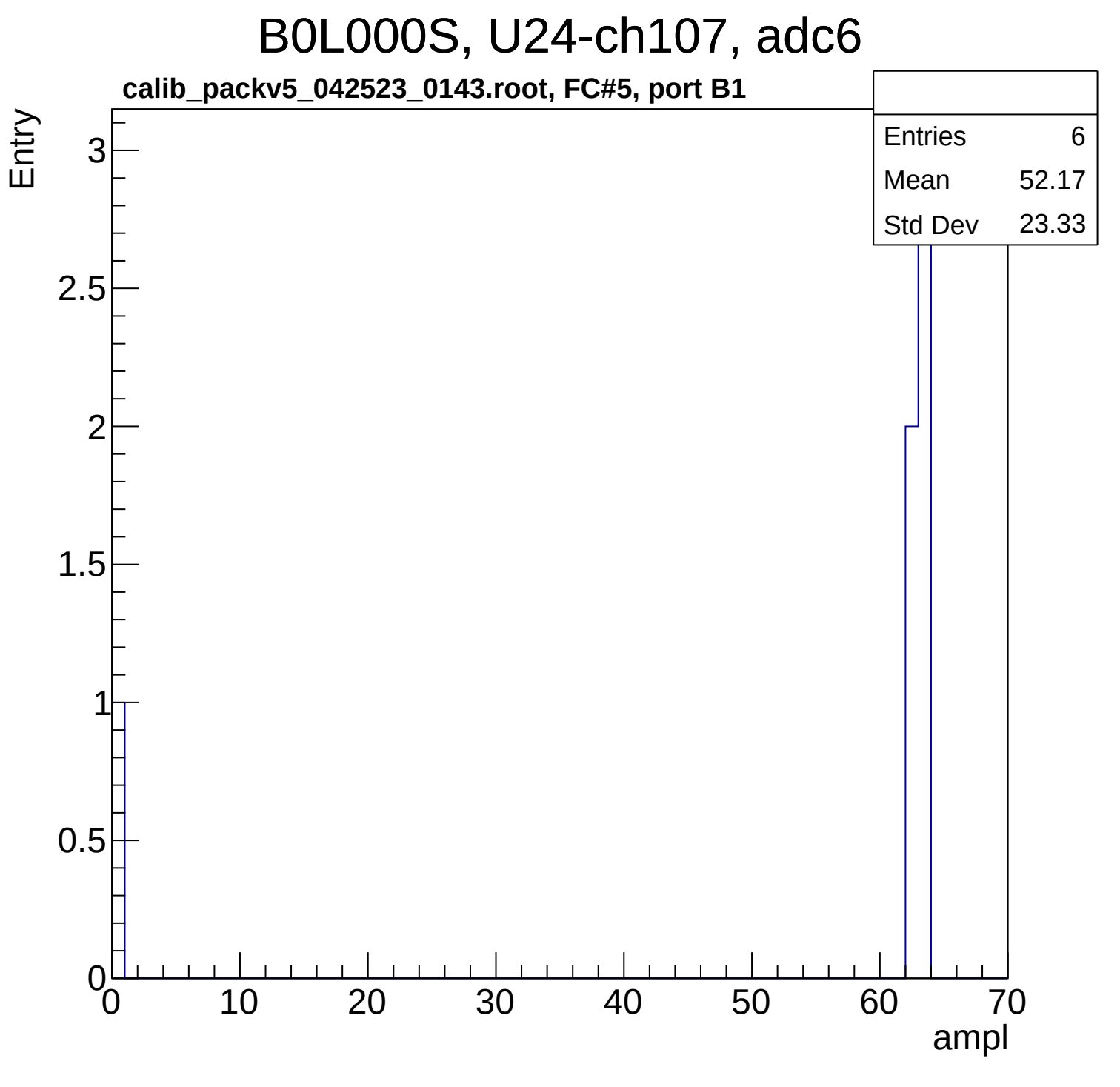

B0L000S, U24-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.58
Std Dev 2.362
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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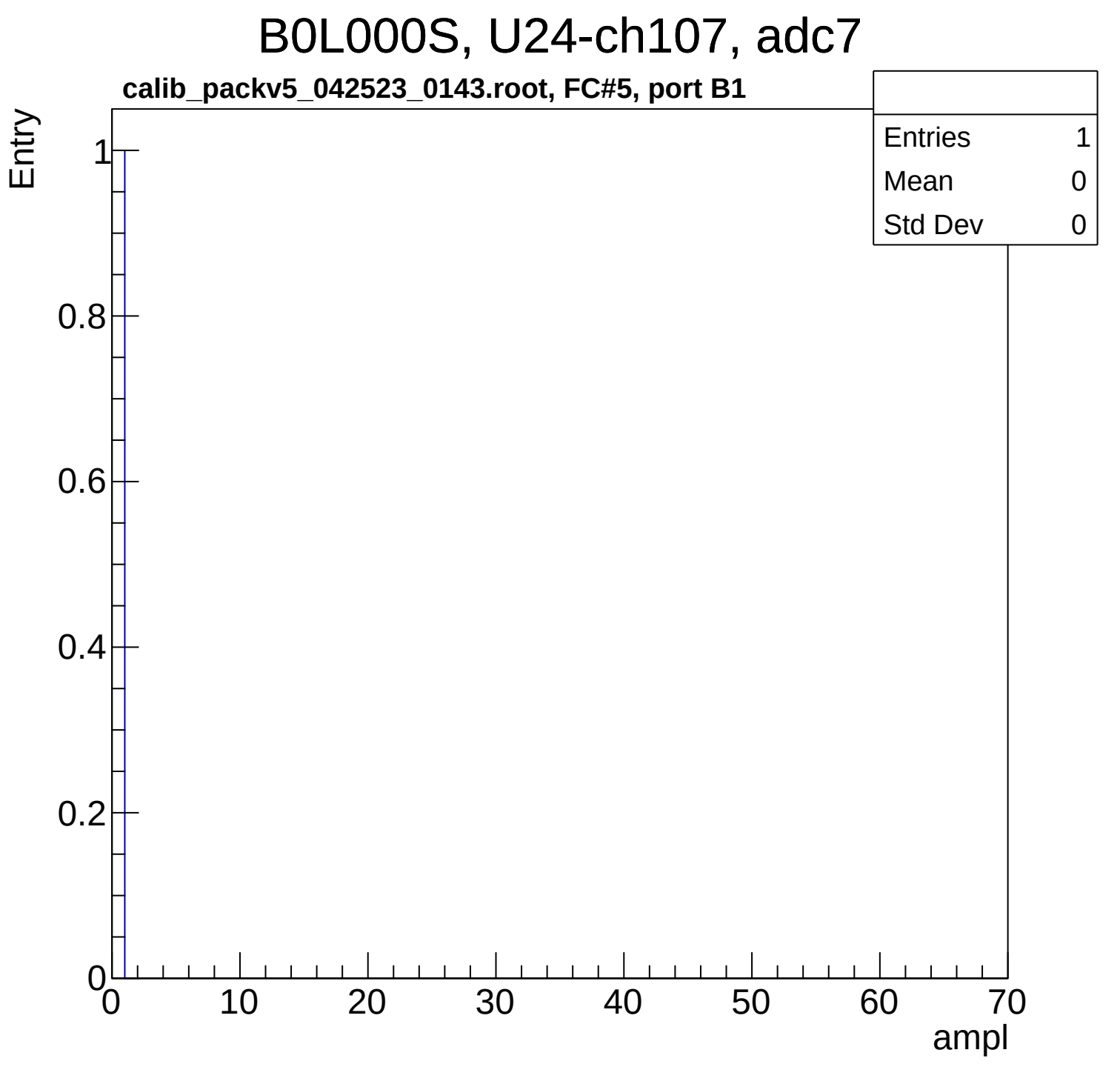
B0L000S, U24-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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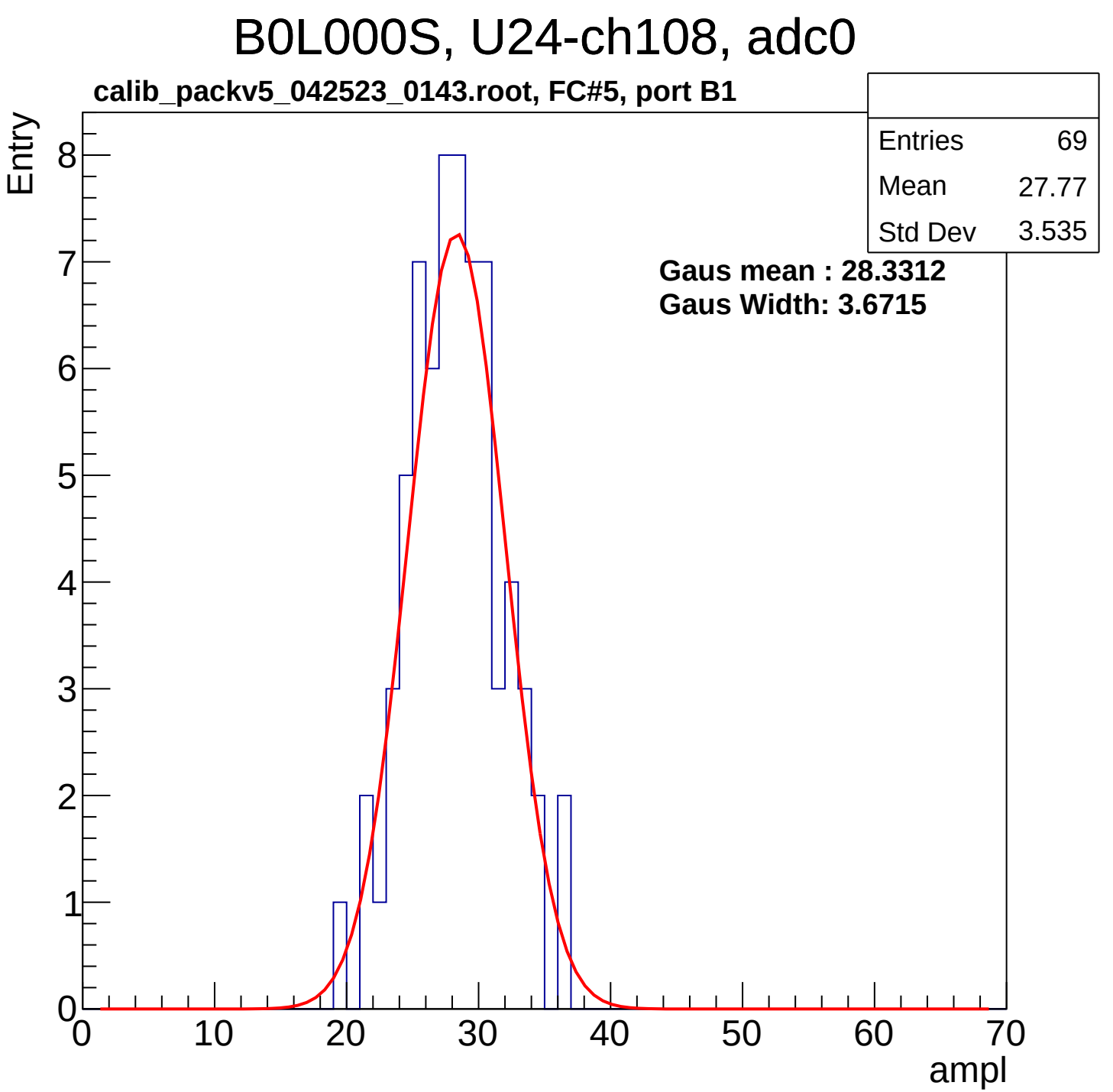
ampl


B0L000S, U24-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 27.77
Std Dev 3.535
Gaus mean : 28.3312
Gaus Width: 3.6715
Entry
ampl
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1
2
3
4
5
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0
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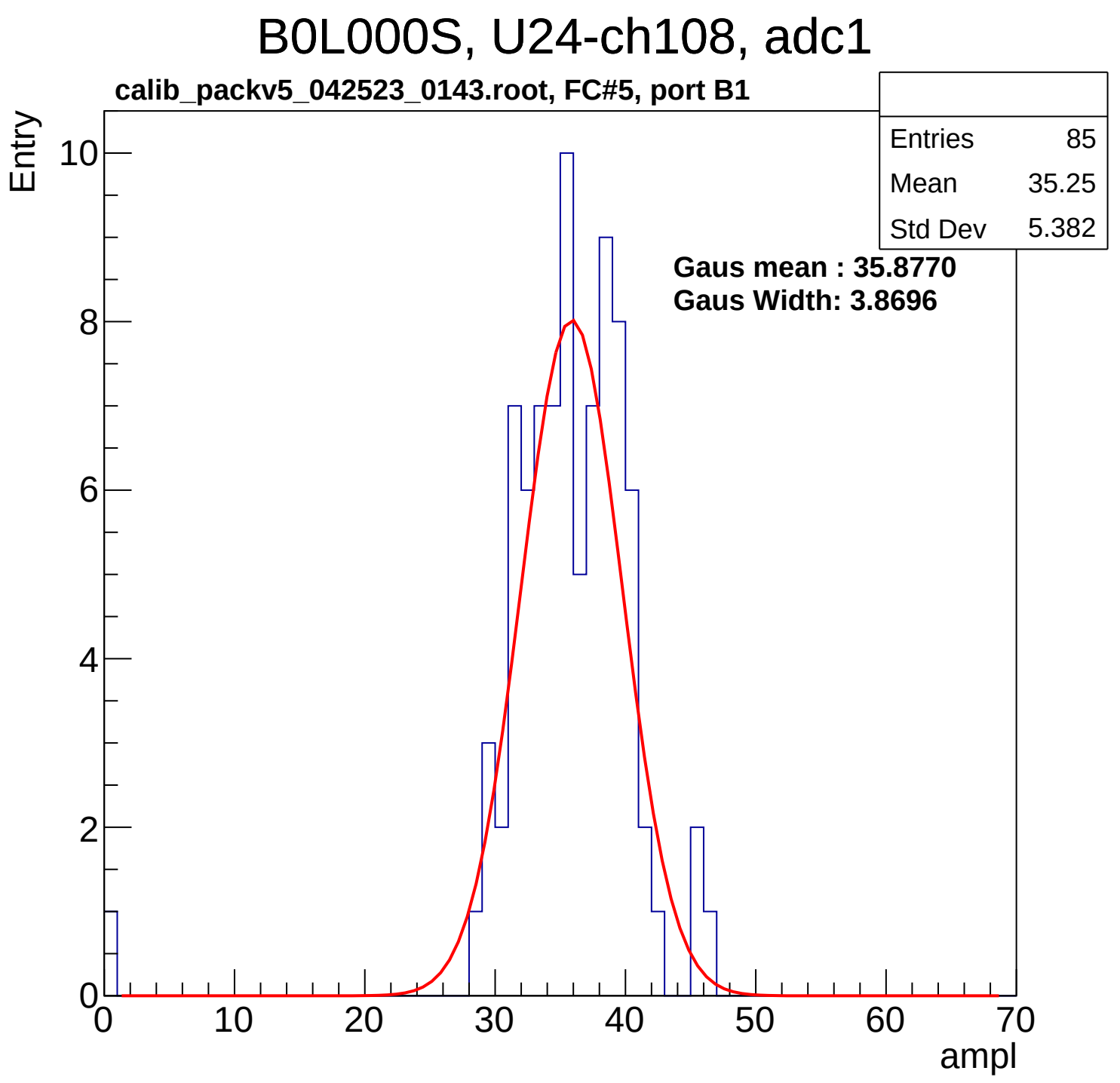
B0L000S, U24-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 35.25
Std Dev 5.382
Gaus mean : 35.8770
Gaus Width: 3.8696
Entry
ampl
0
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4
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0
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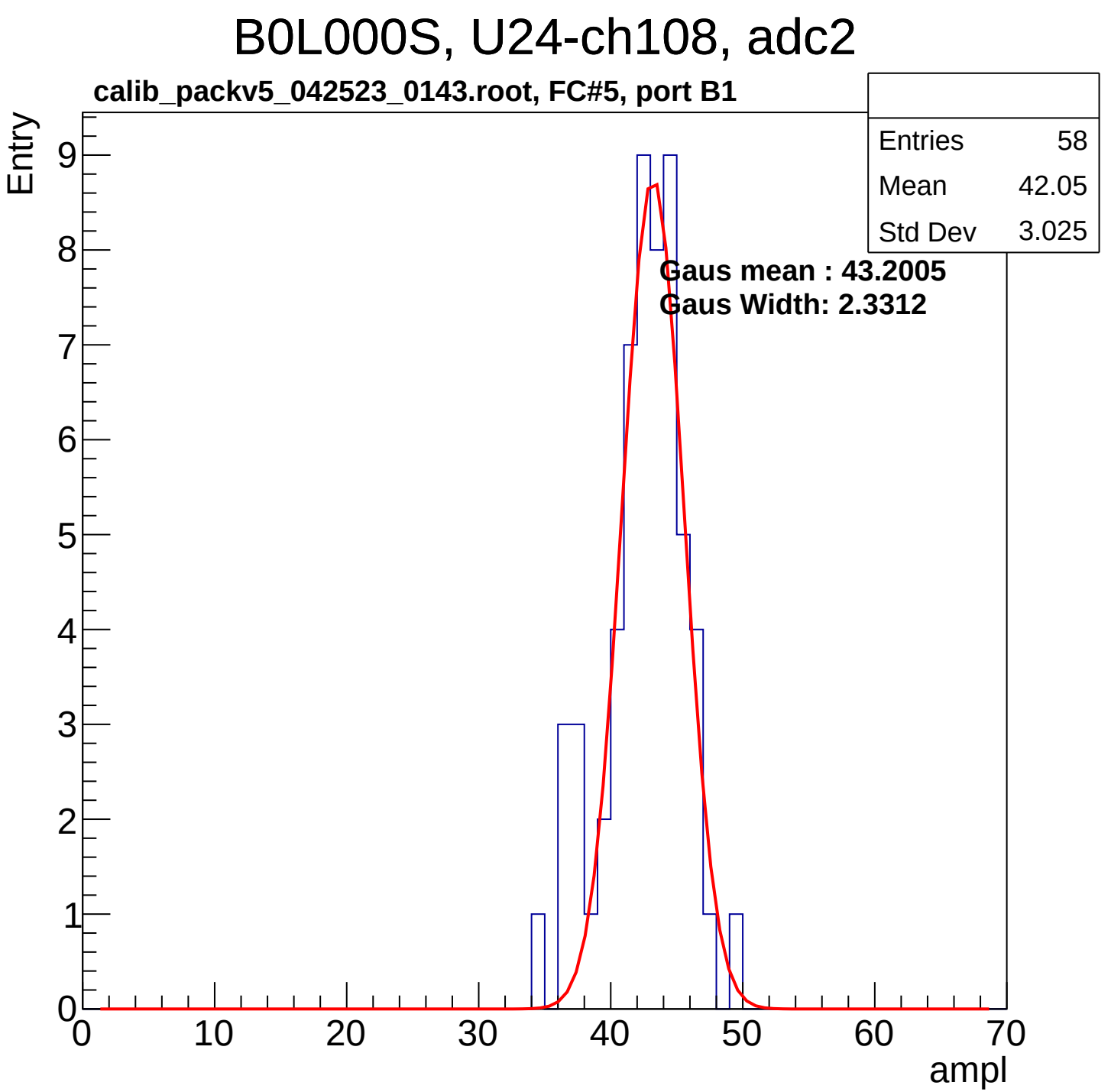
B0L000S, U24-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.05
Std Dev 3.025
Gaus mean : 43.2005
Gaus Width: 2.3312
Entry
ampl
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3
4
5
6
7
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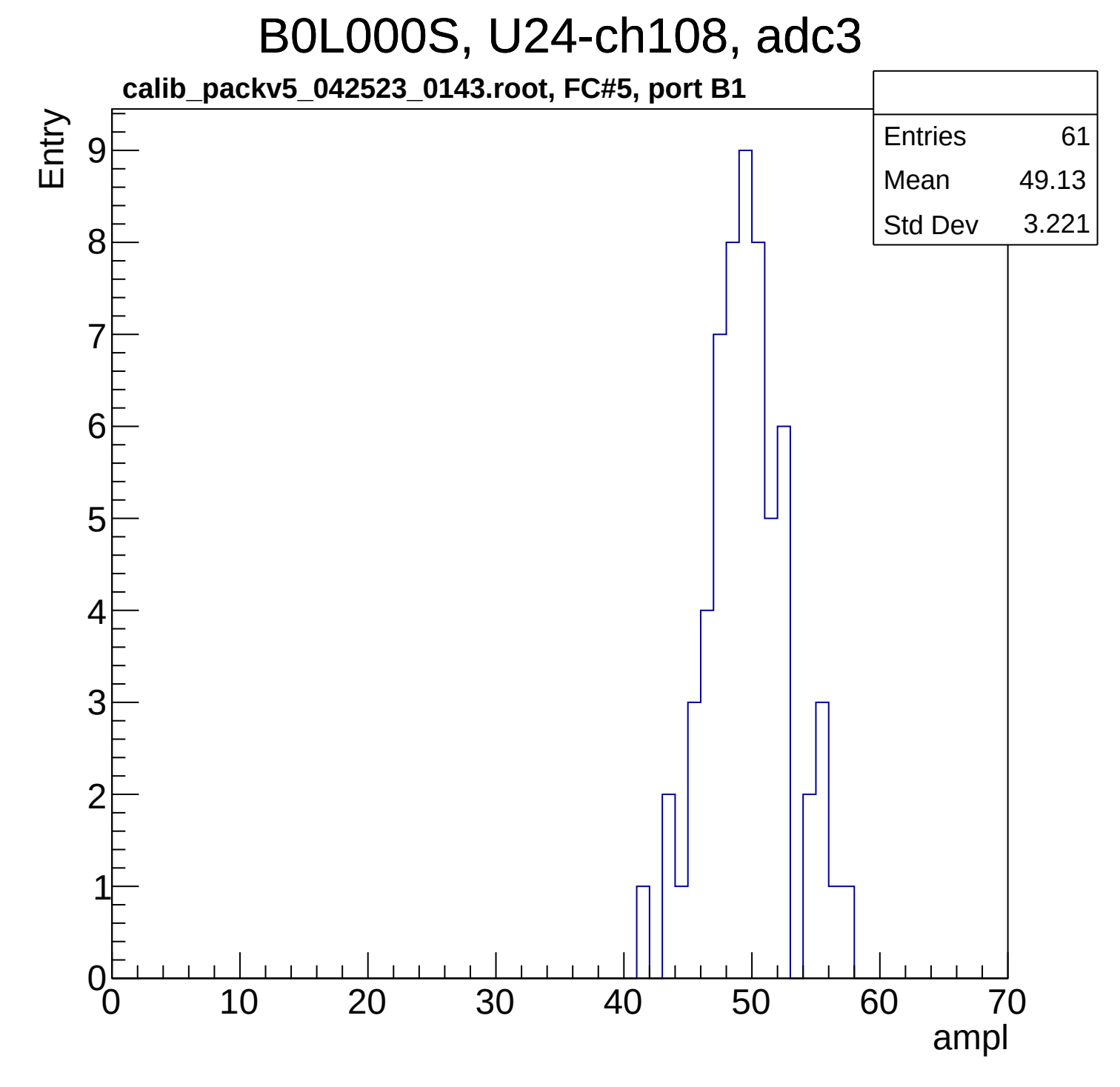
B0L000S, U24-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.13
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
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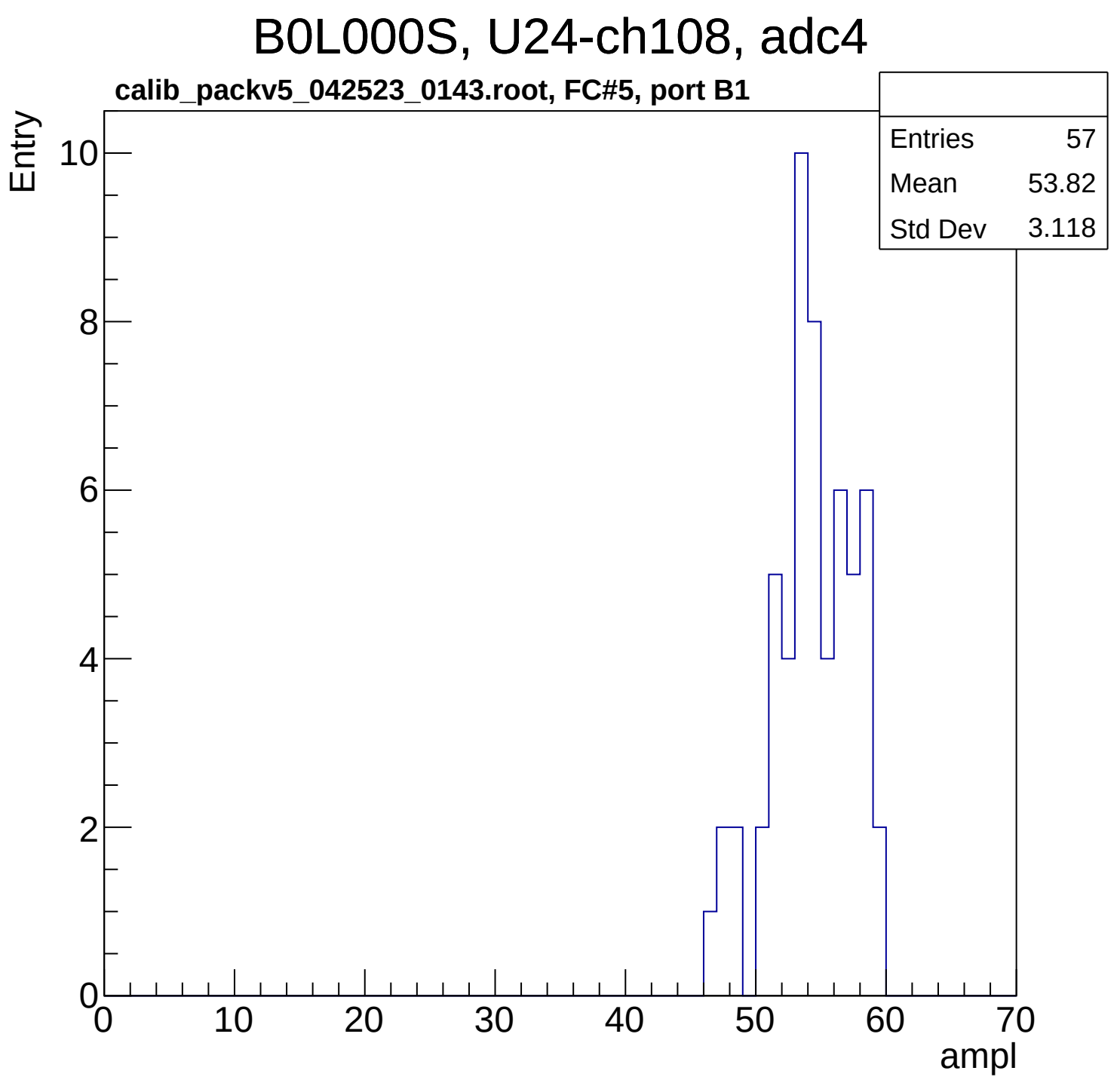
B0L000S, U24-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.82
Std Dev 3.118
Entry
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2
4
6
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10
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10
20
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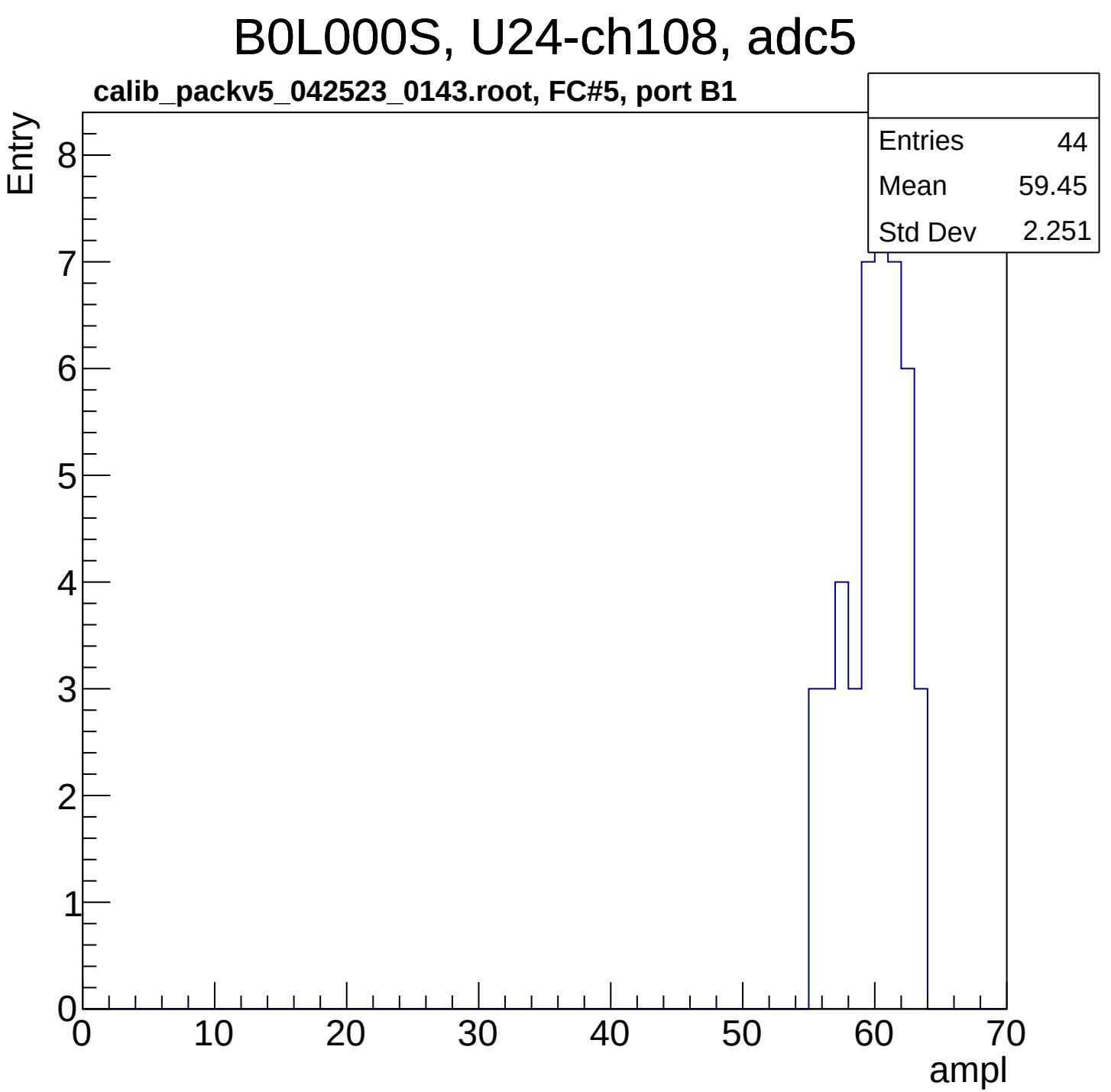
50
60
70
ampl


B0L000S, U24-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 59.45
Std Dev 2.251
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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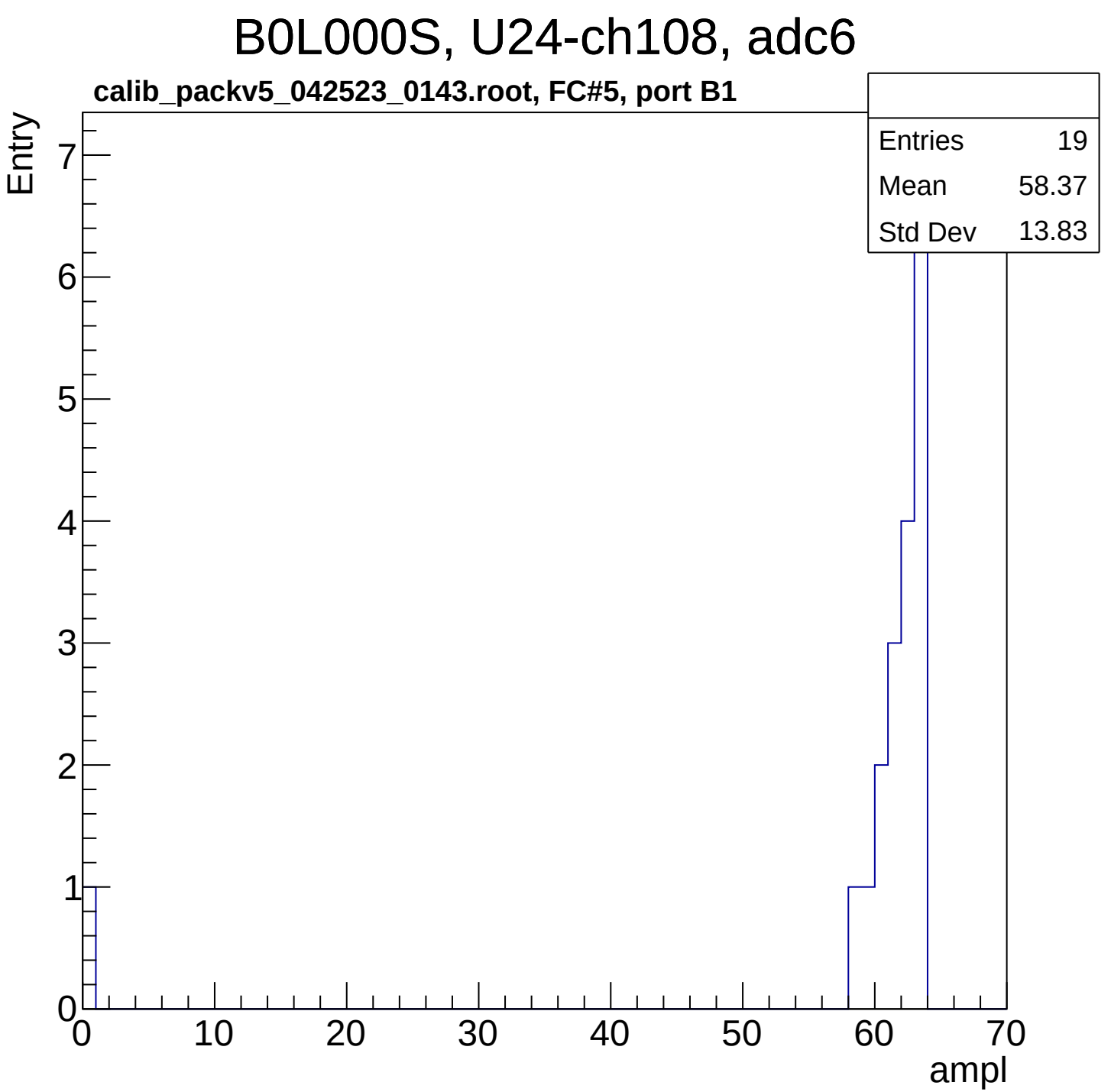
B0L000S, U24-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.37
Std Dev 13.83
Entry
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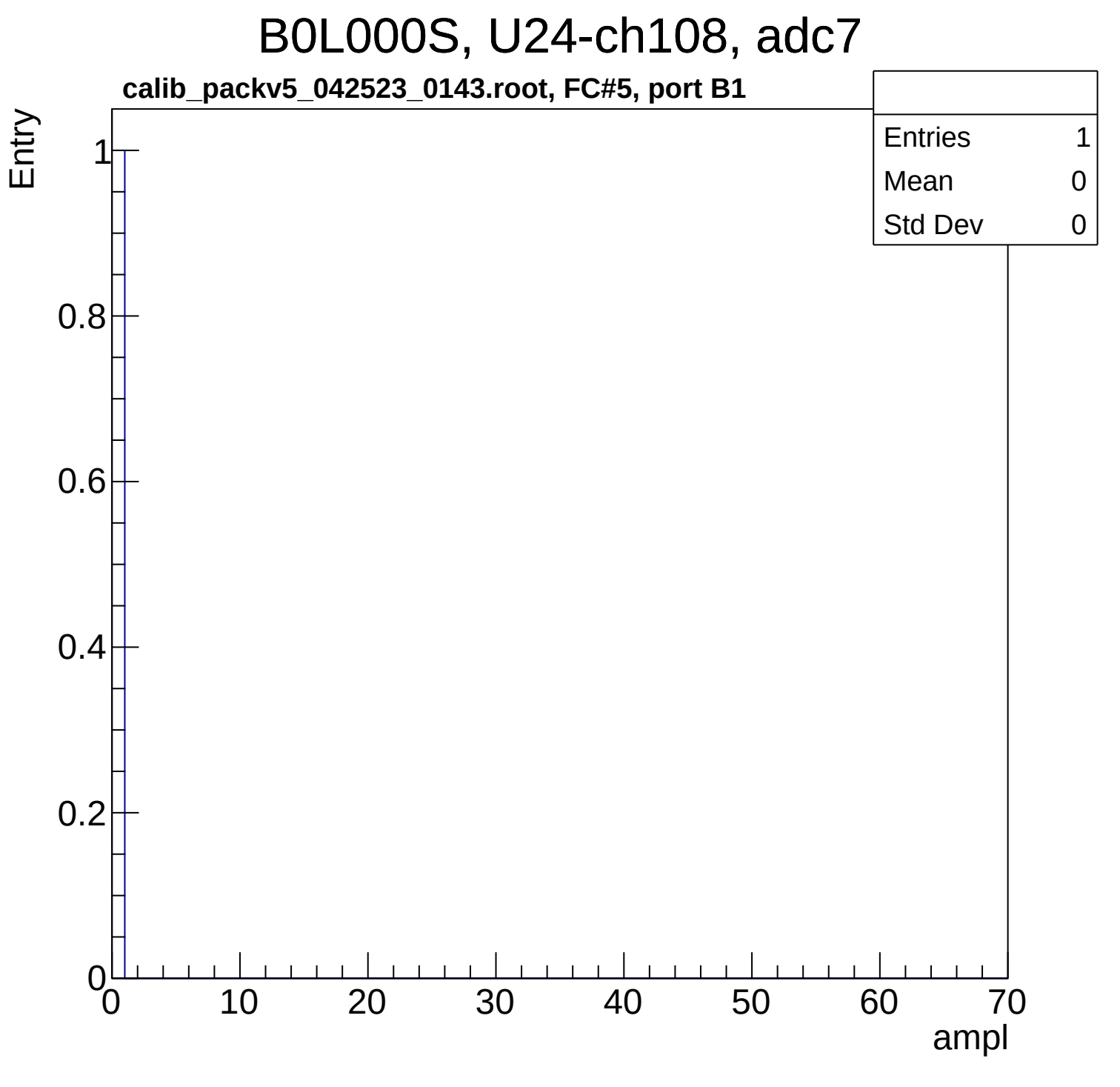
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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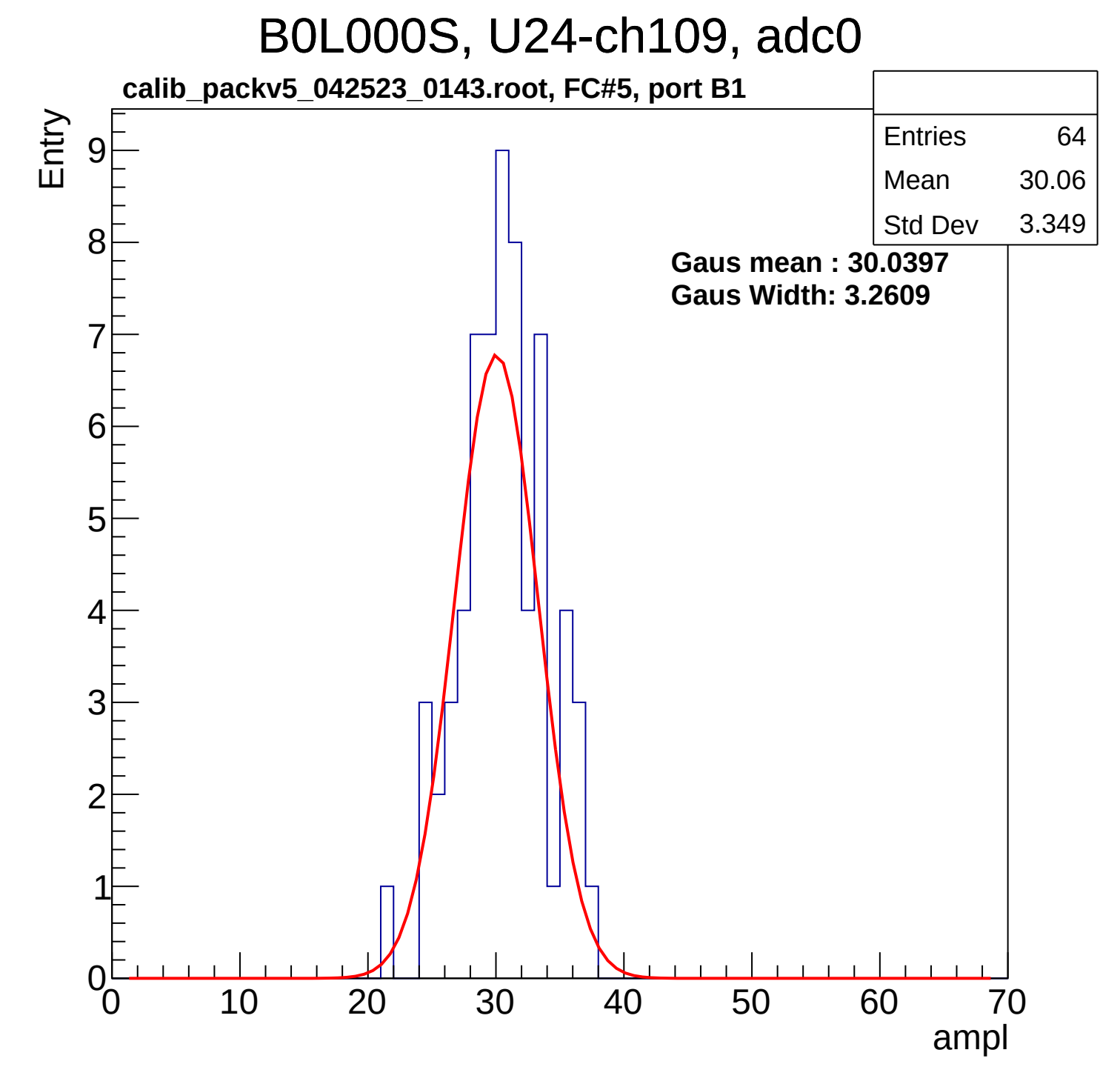
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 30.06
Std Dev 3.349
Gaus mean : 30.0397
Gaus Width: 3.2609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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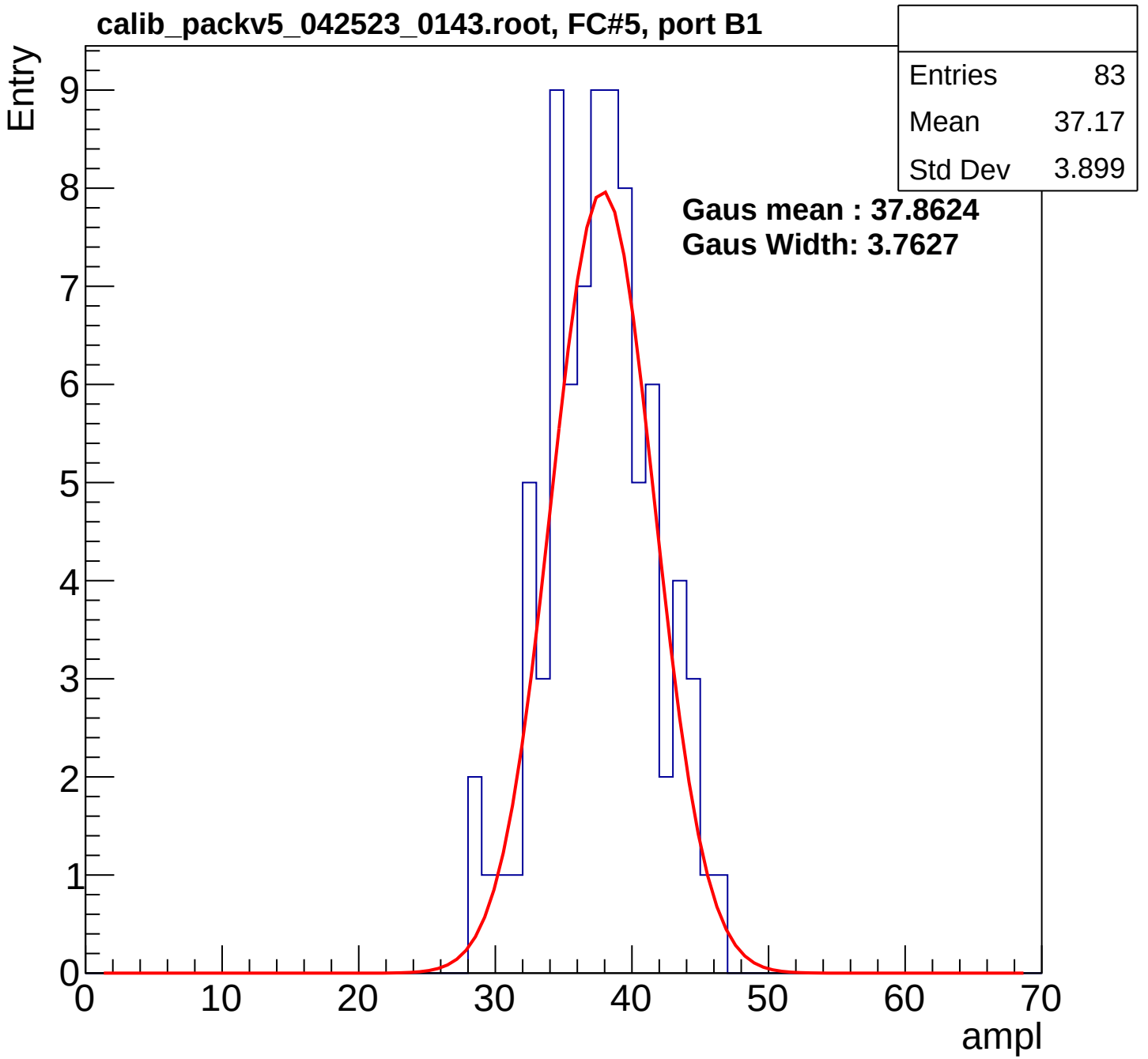
B0L000S, U24-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 37.17
Std Dev 3.899
Gaus mean : 37.8624
Gaus Width: 3.7627
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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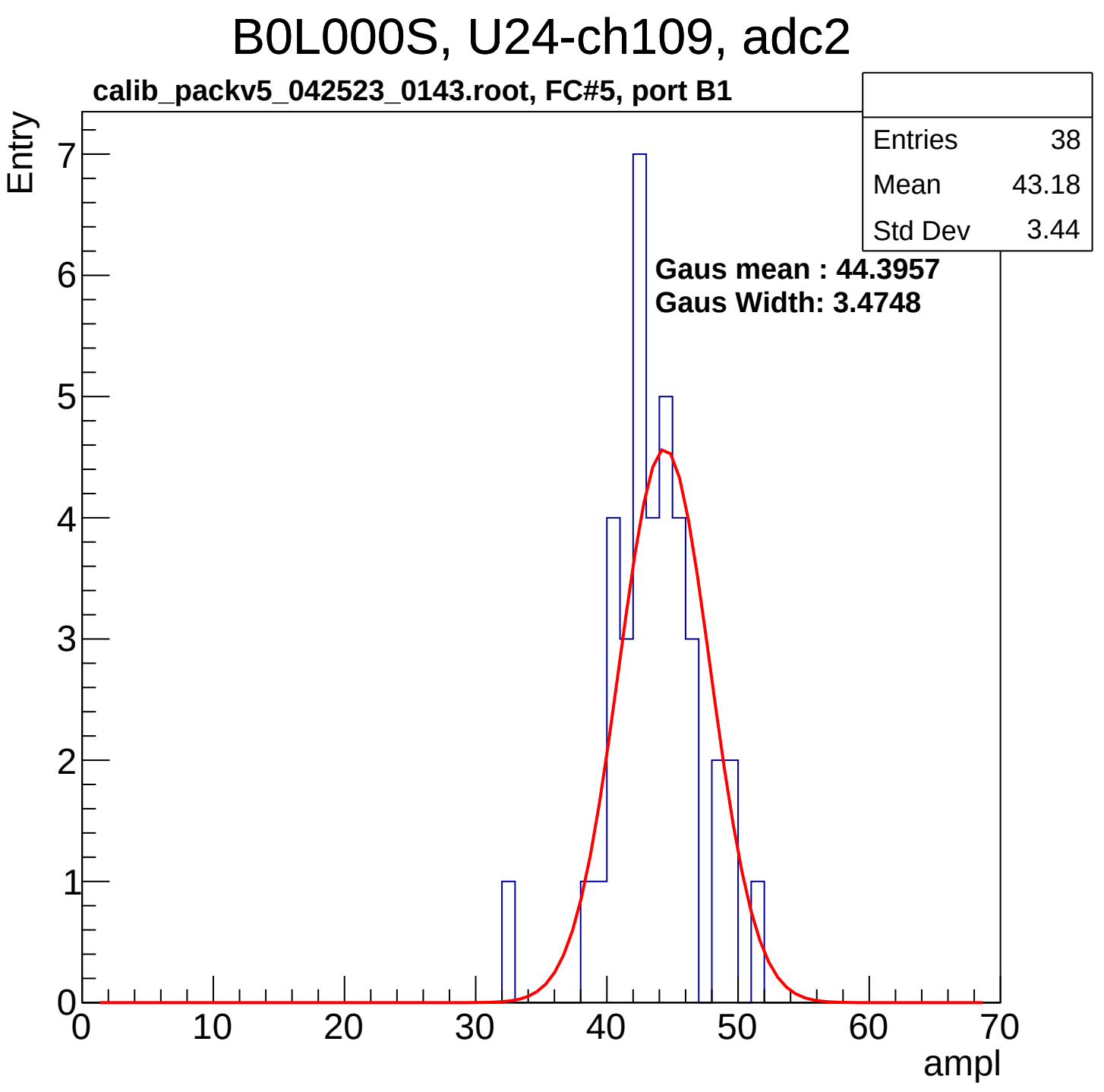
B0L000S, U24-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 43.18
Std Dev 3.44
Gaus mean : 44.3957
Gaus Width: 3.4748
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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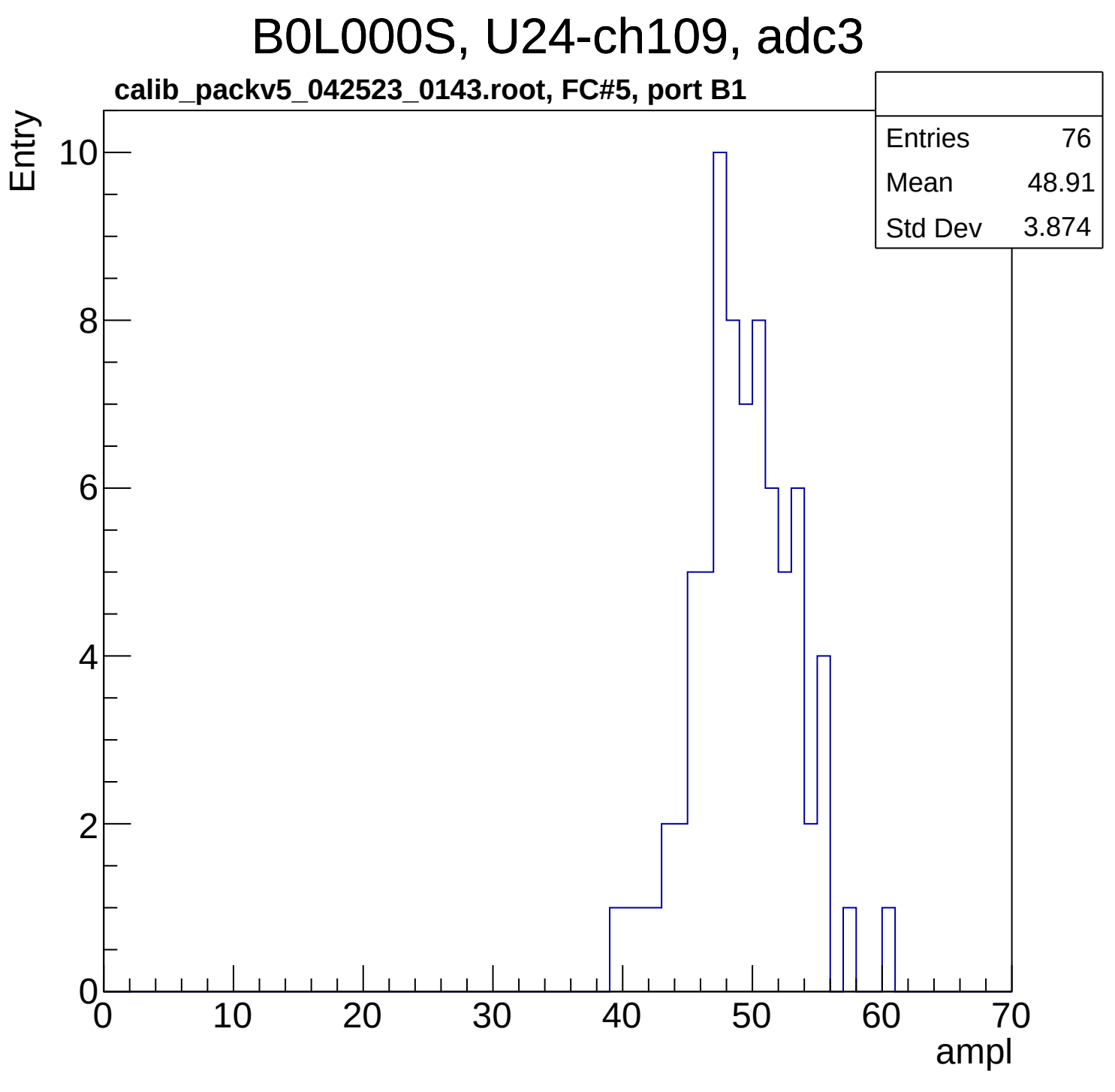
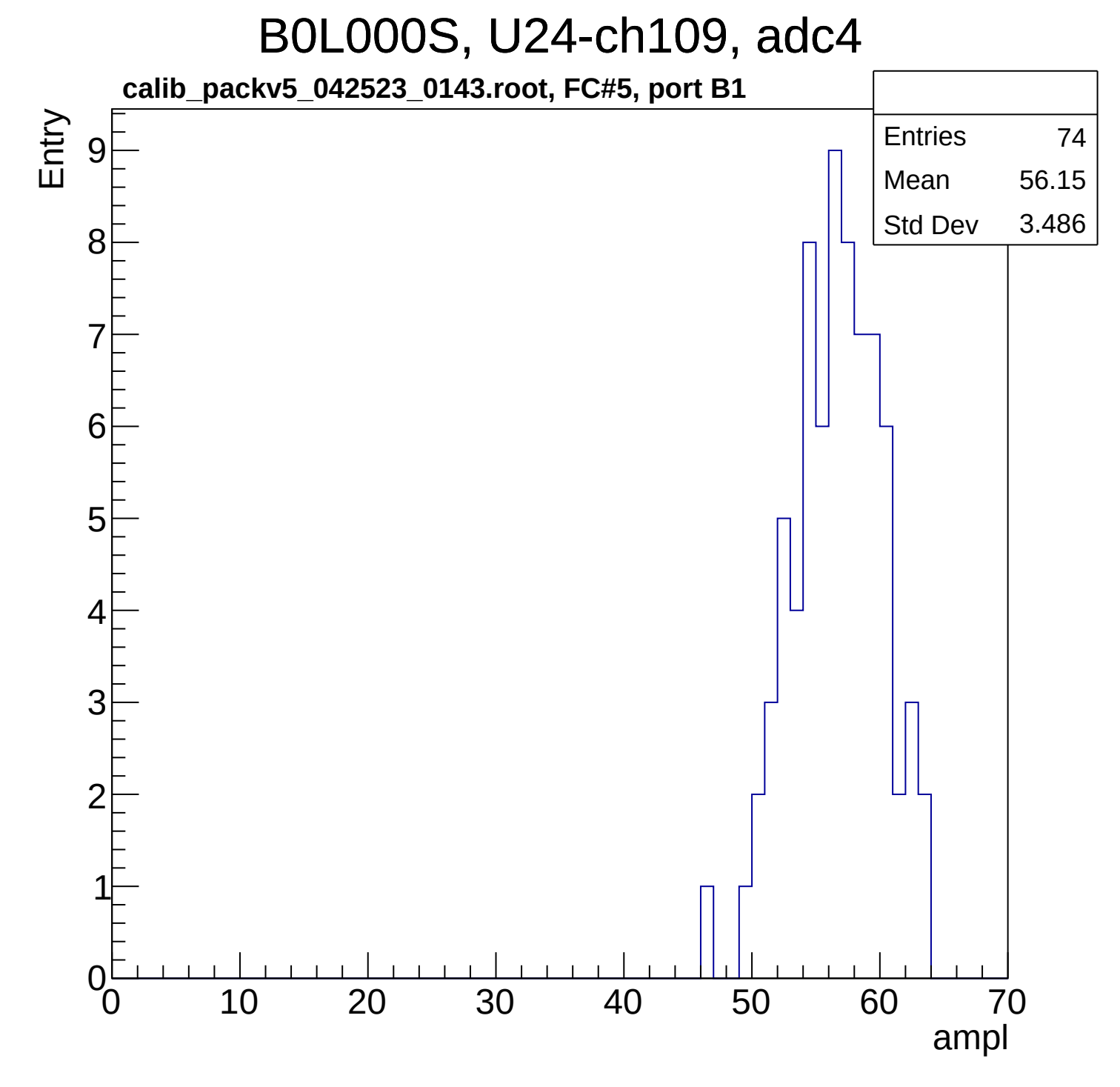

B0L000S, U24-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 48.91
Std Dev 3.874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 56.15
Std Dev 3.486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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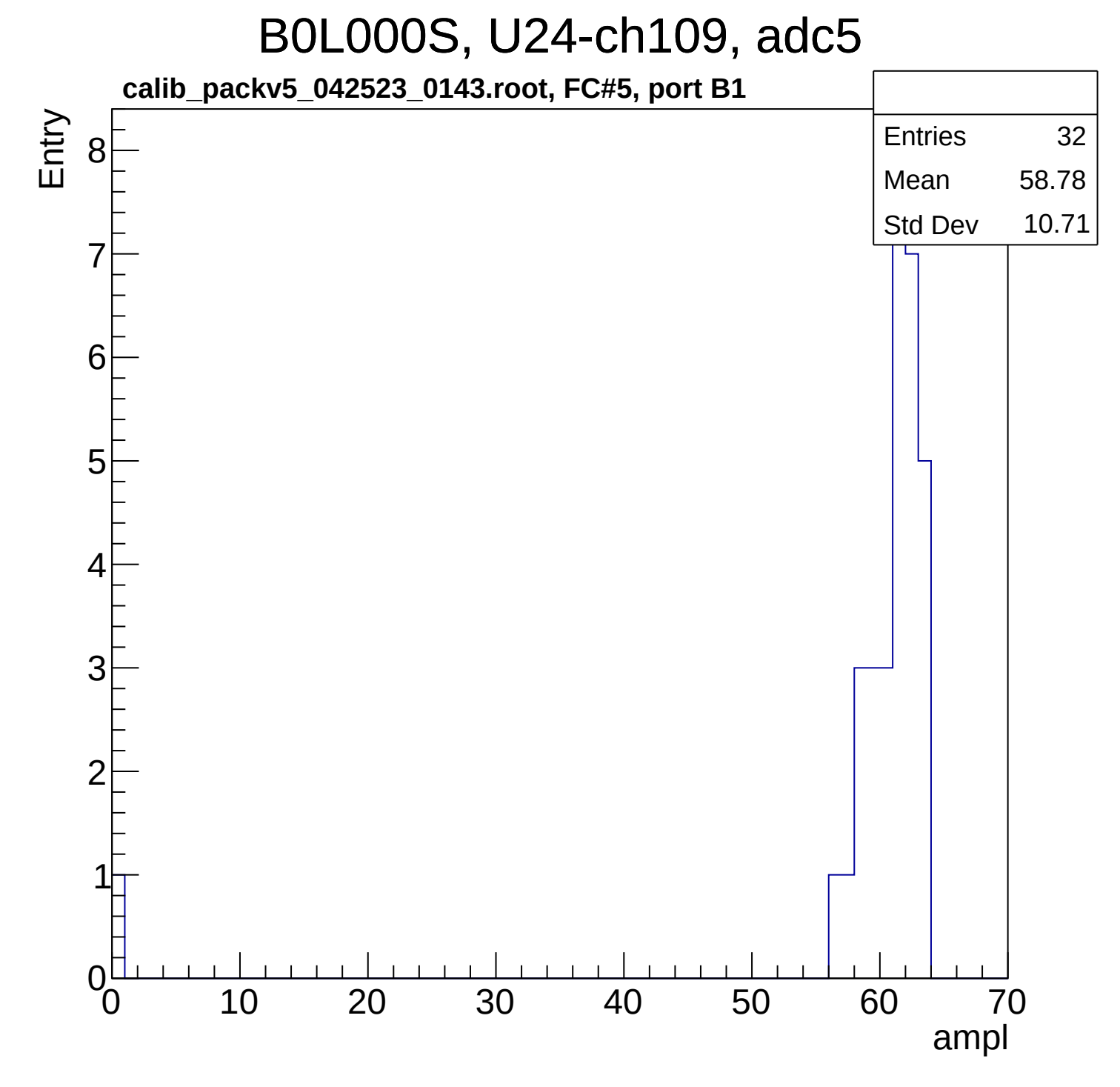
B0L000S, U24-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 58.78
Std Dev 10.71
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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60
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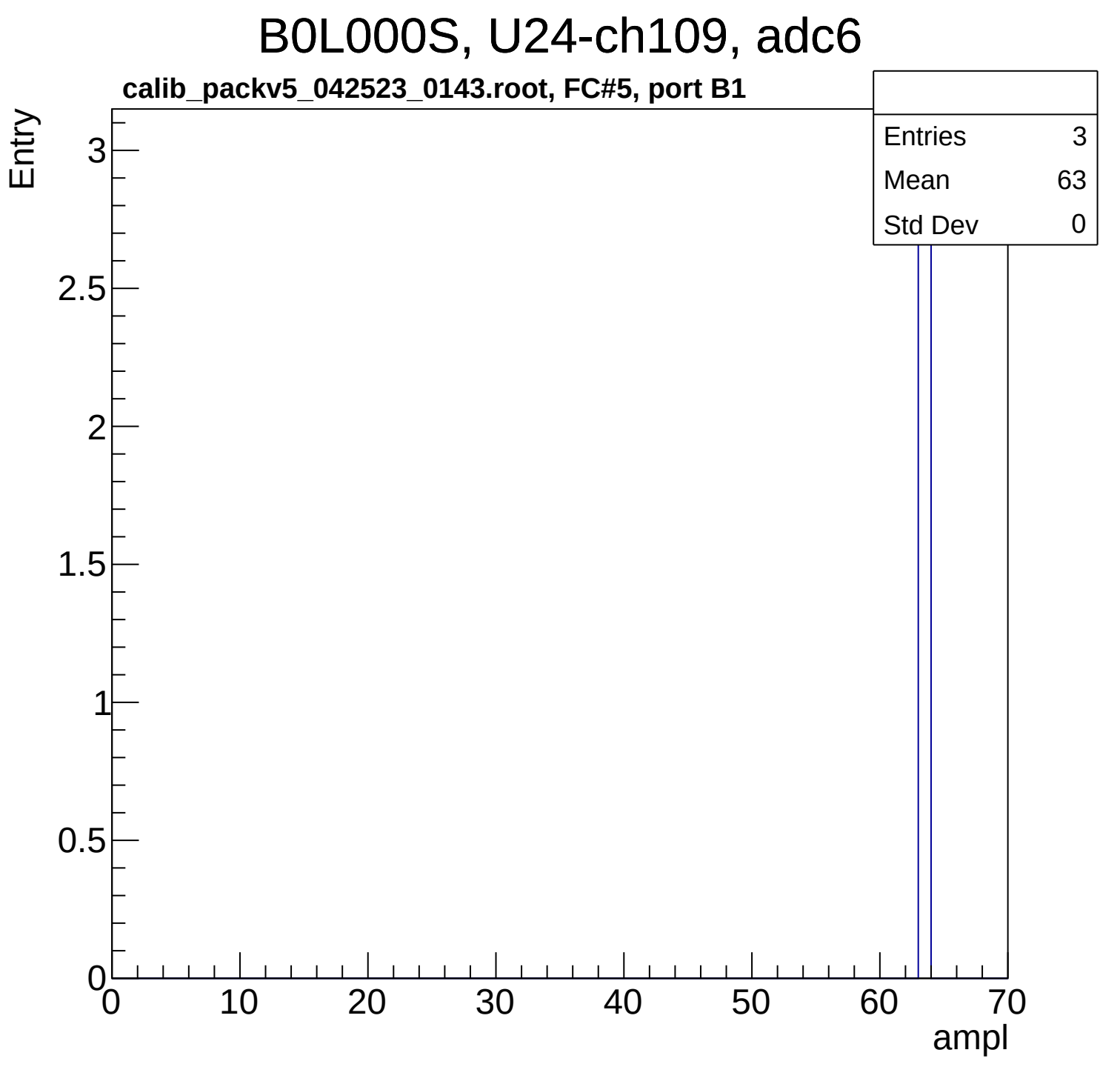
B0L000S, U24-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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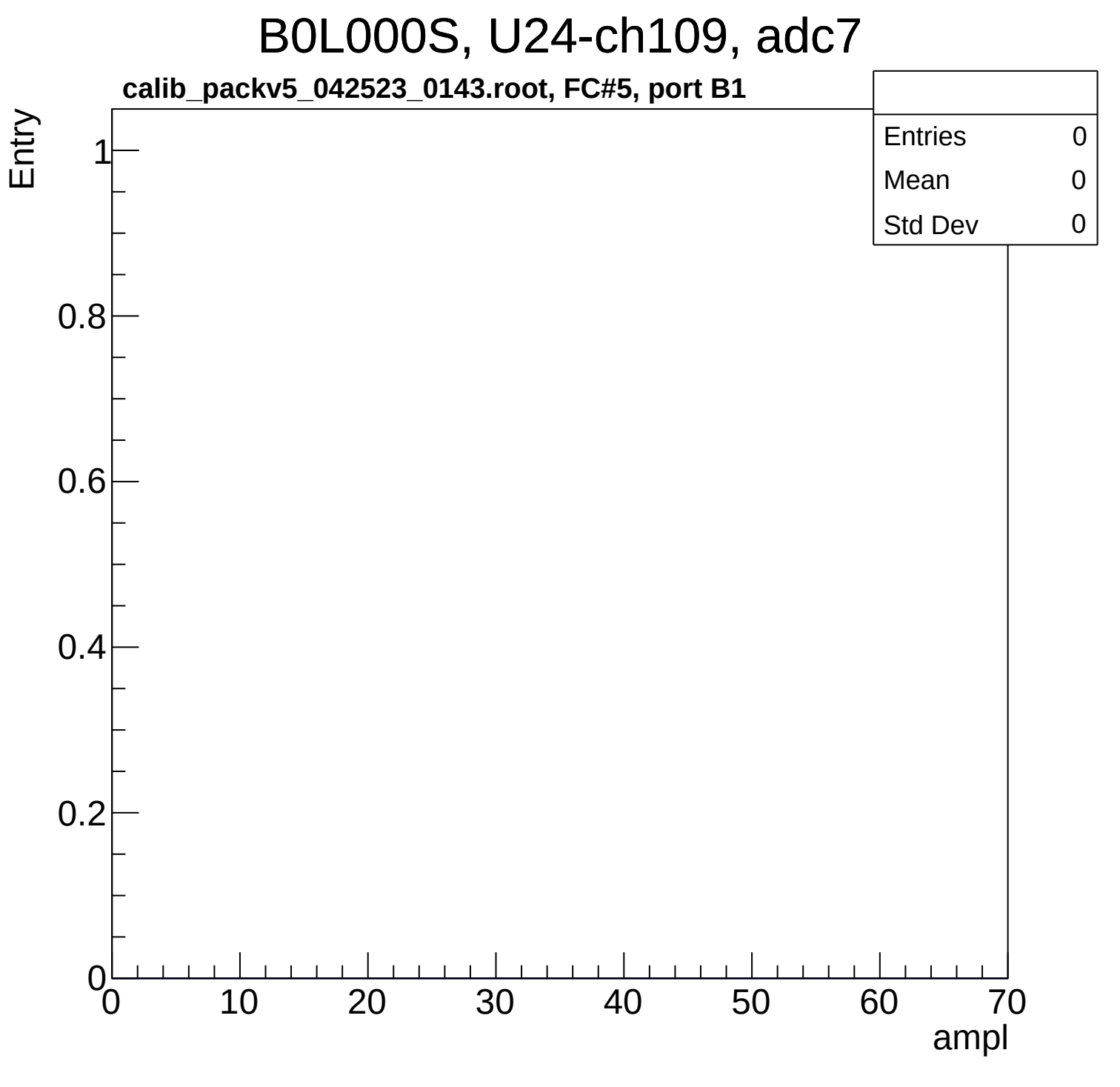
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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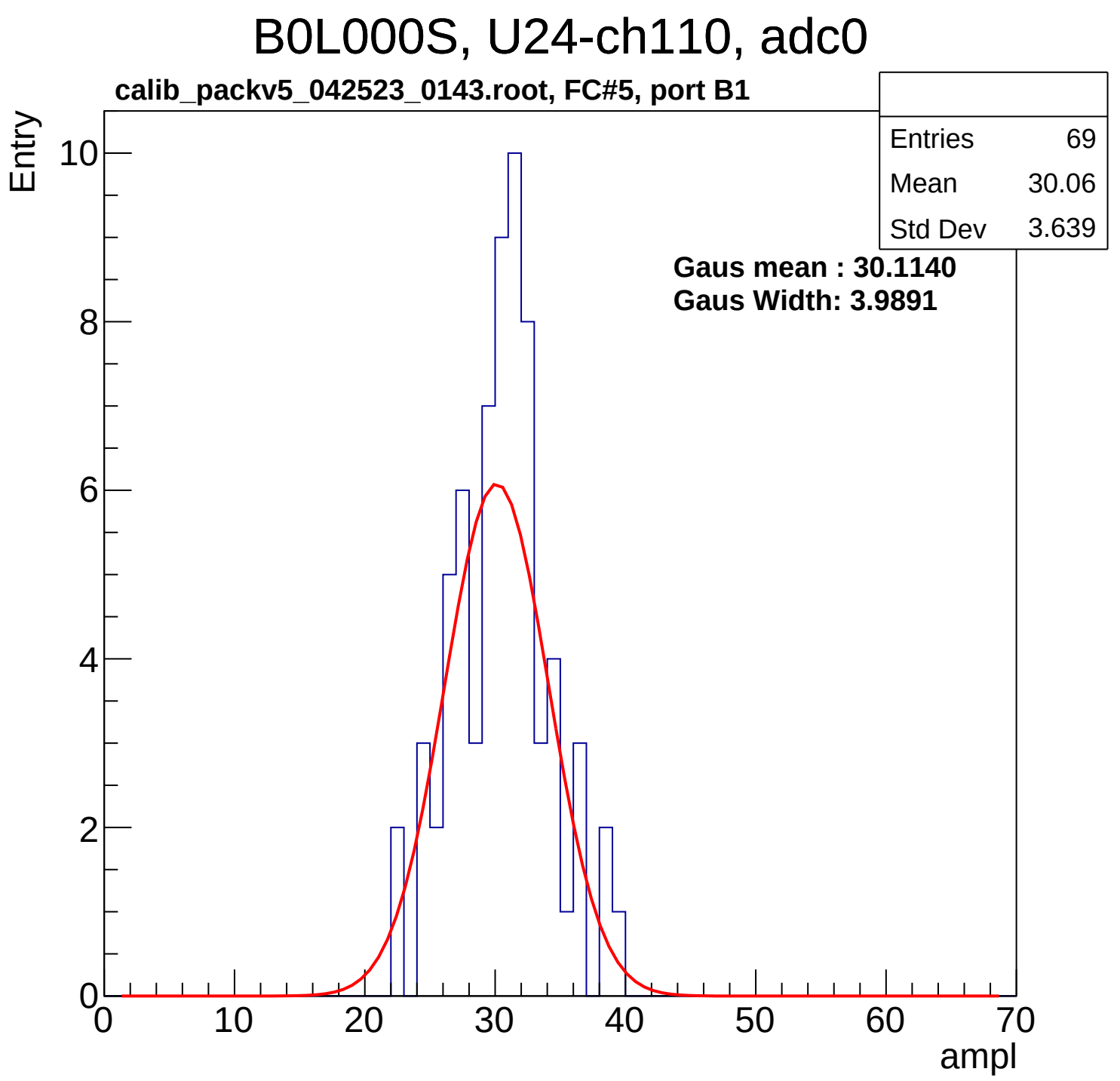
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 30.06
Std Dev 3.639
Gaus mean : 30.1140
Gaus Width: 3.9891
Entry
ampl
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4
6
8
10
0
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20
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40
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60
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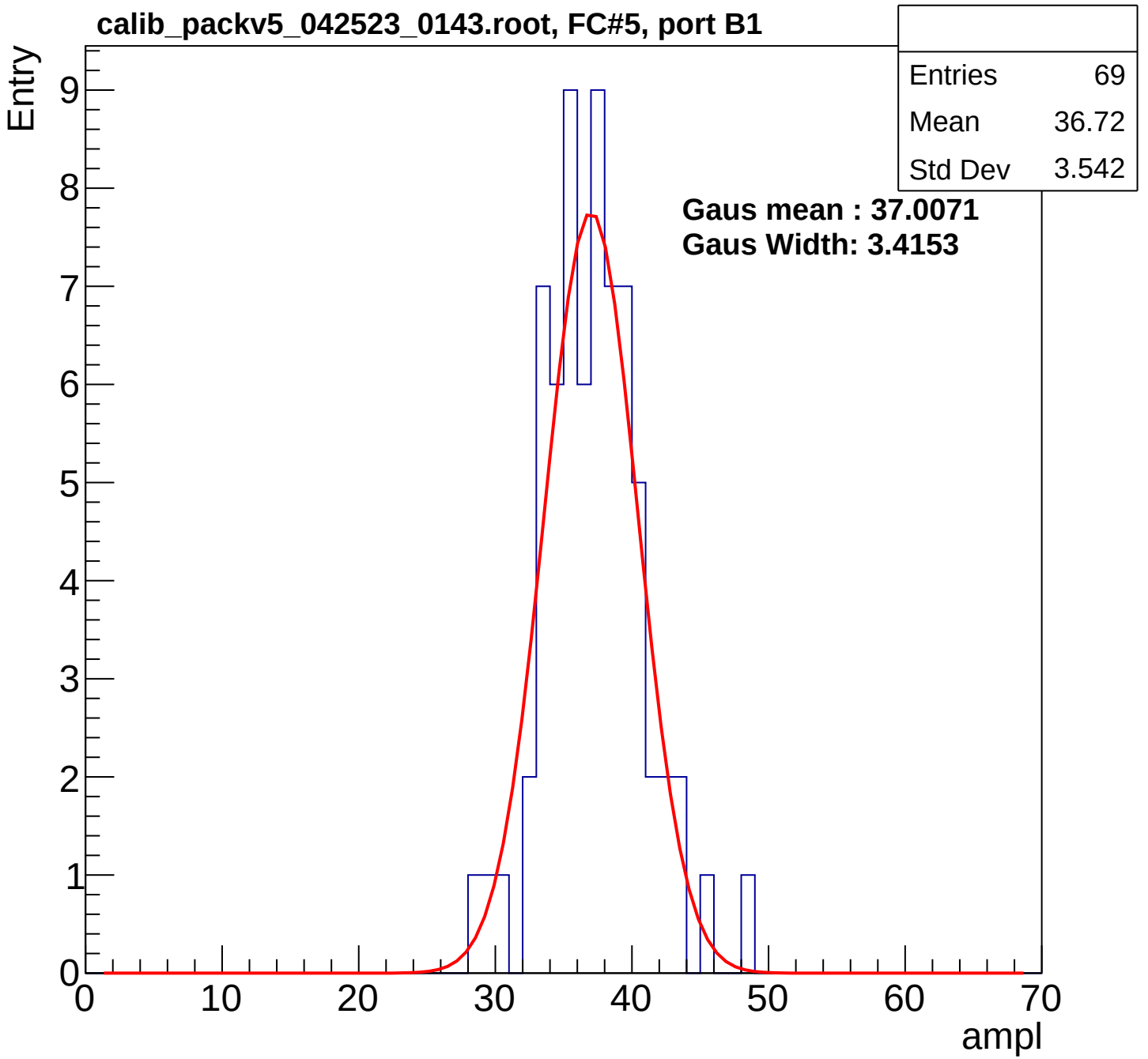
B0L000S, U24-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 36.72
Std Dev 3.542
Gaus mean : 37.0071
Gaus Width: 3.4153
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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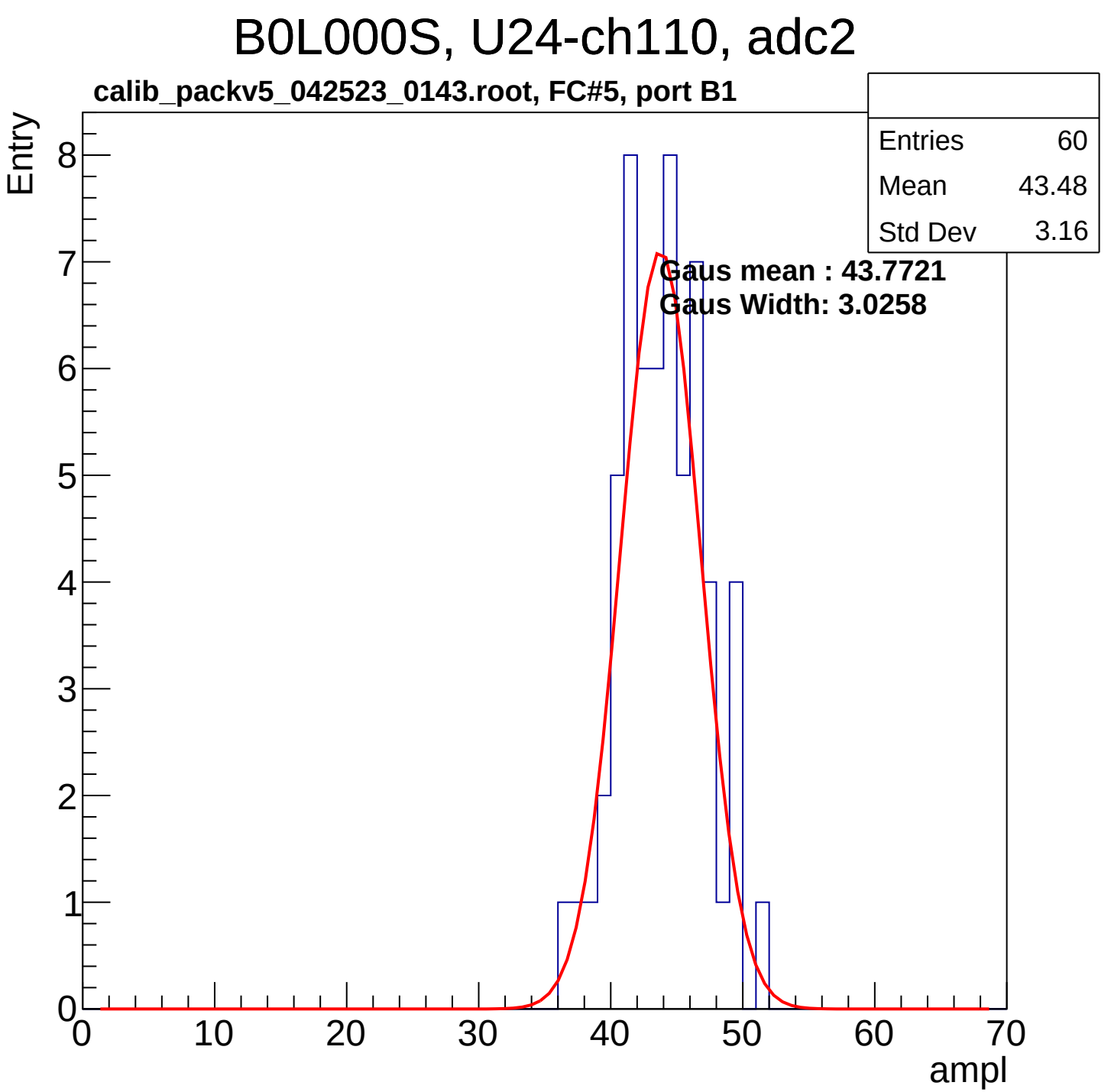
B0L000S, U24-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.48
Std Dev 3.16
Gaus mean : 43.7721
Gaus Width: 3.0258
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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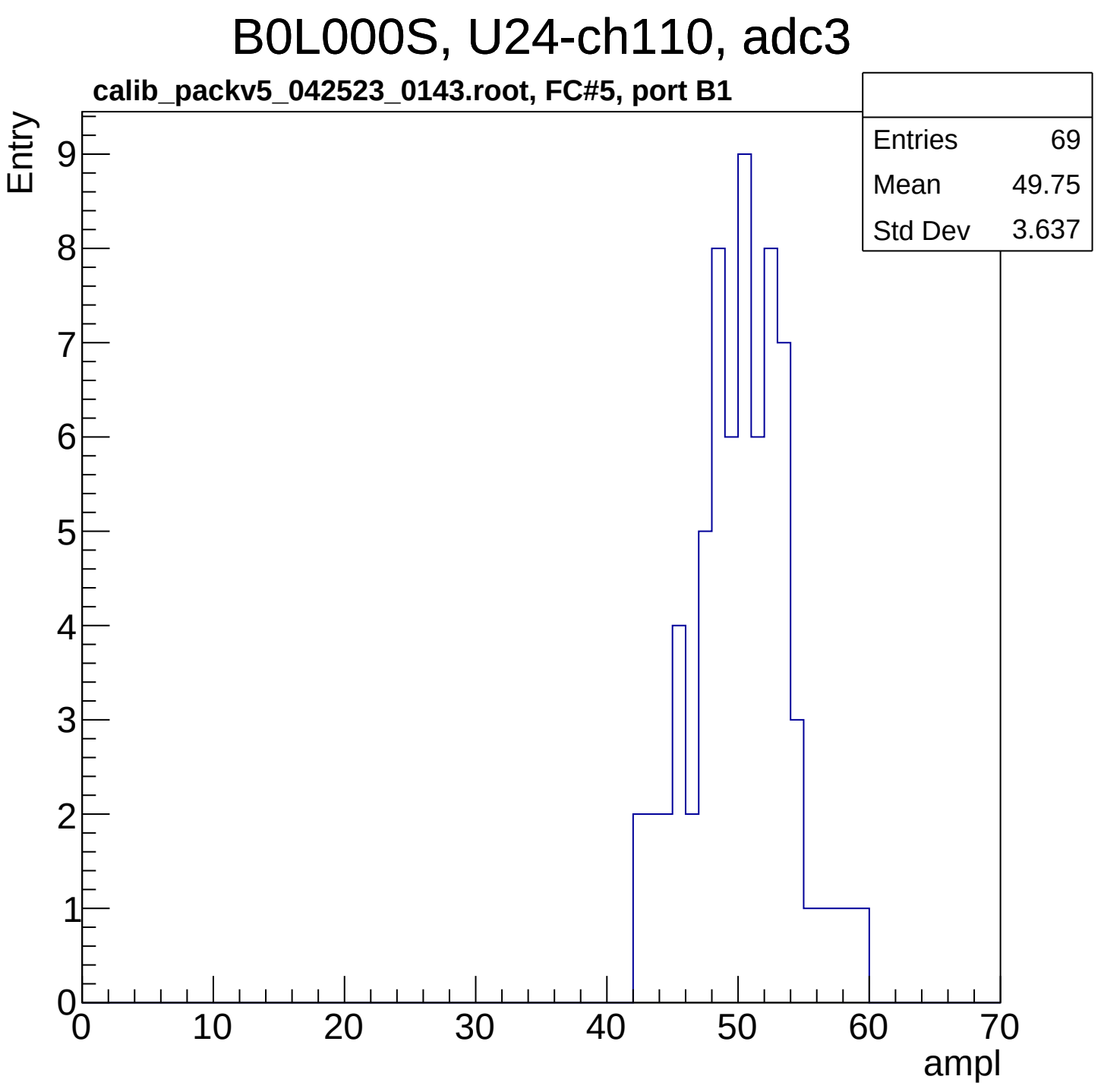
B0L000S, U24-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.75
Std Dev 3.637
Entry
ampl
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6
7
8
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0
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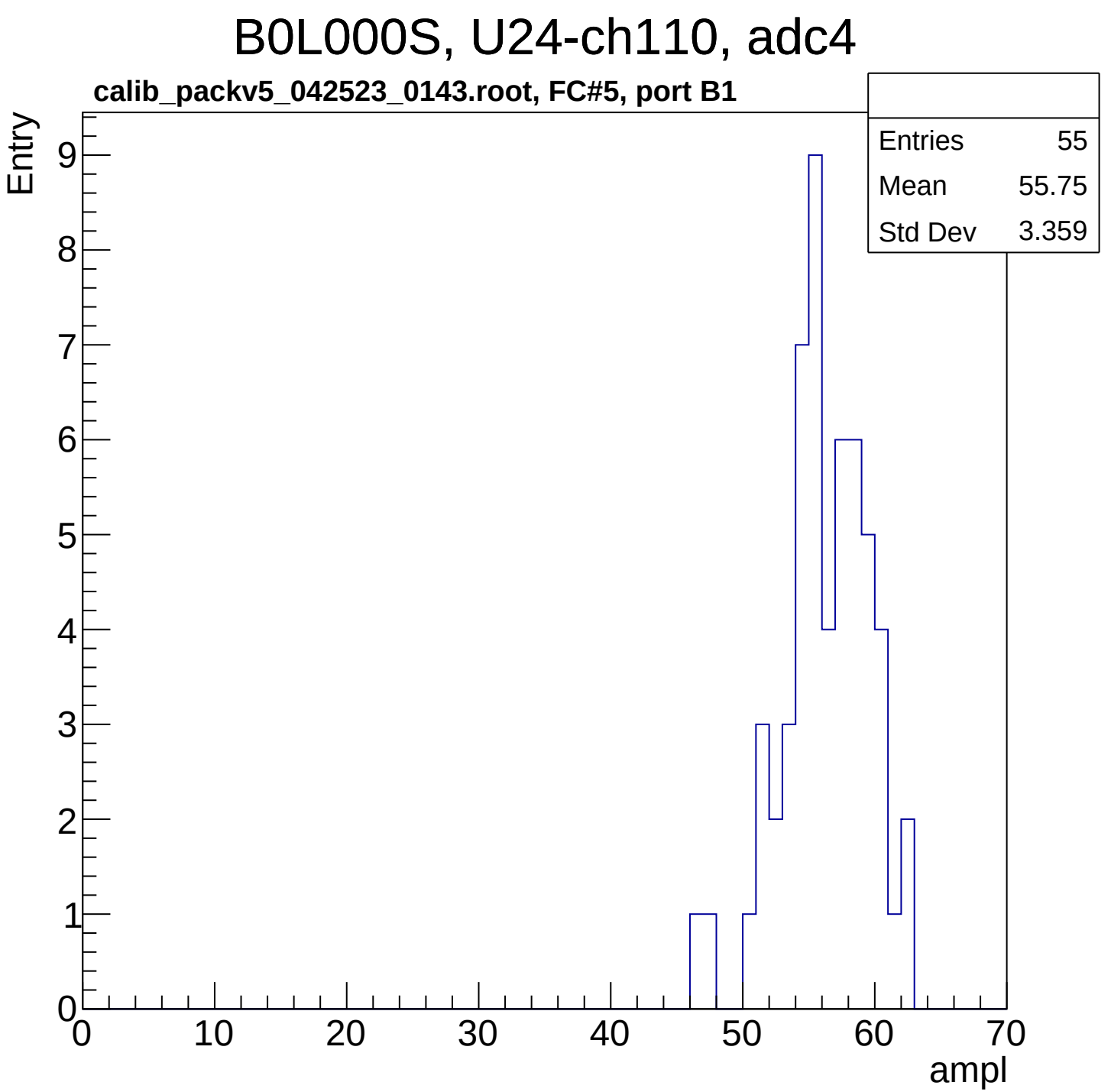
B0L000S, U24-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.75
Std Dev 3.359
Entry
ampl
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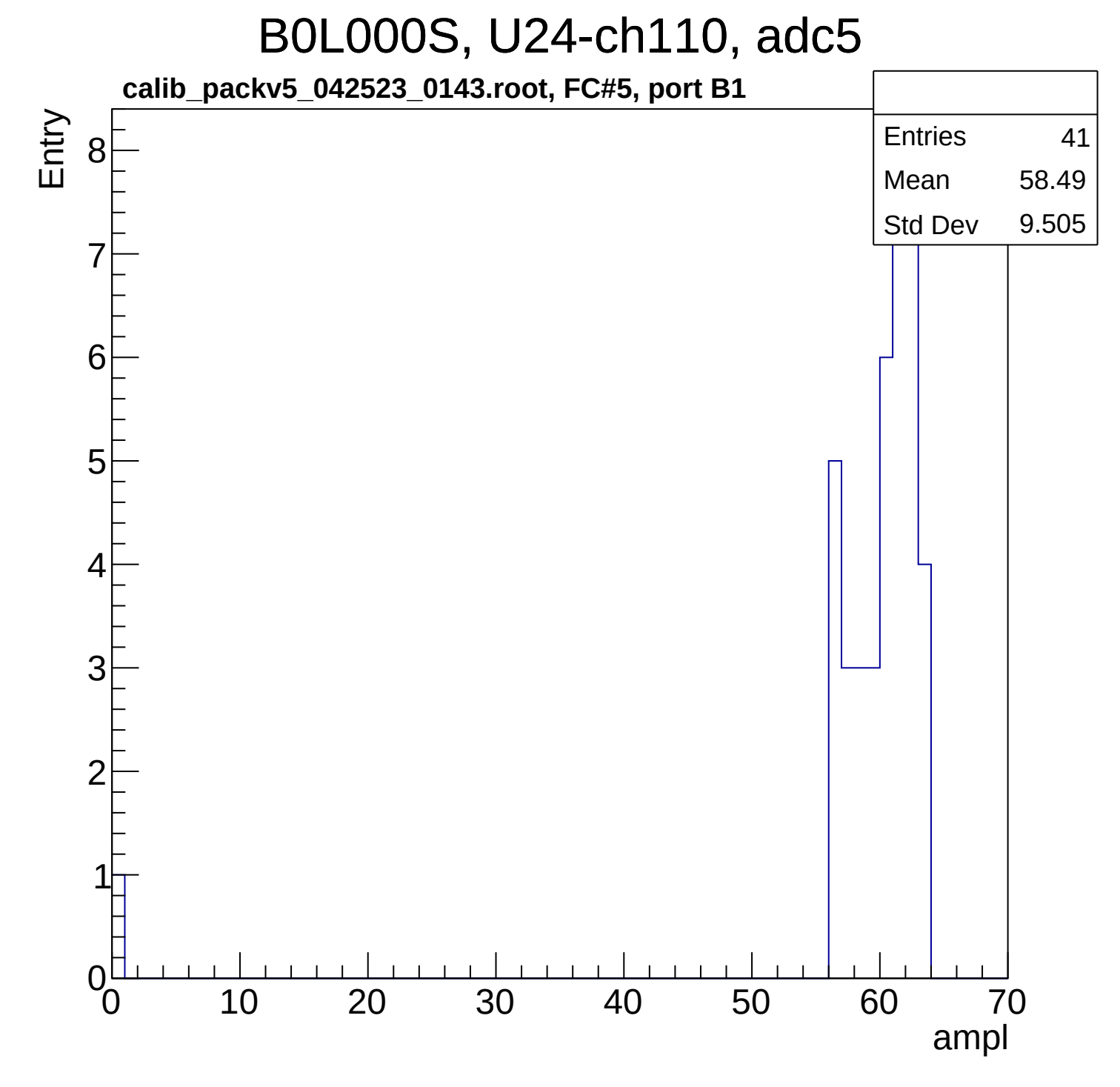
B0L000S, U24-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.49
Std Dev 9.505
Entry
ampl
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3
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5
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7
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0
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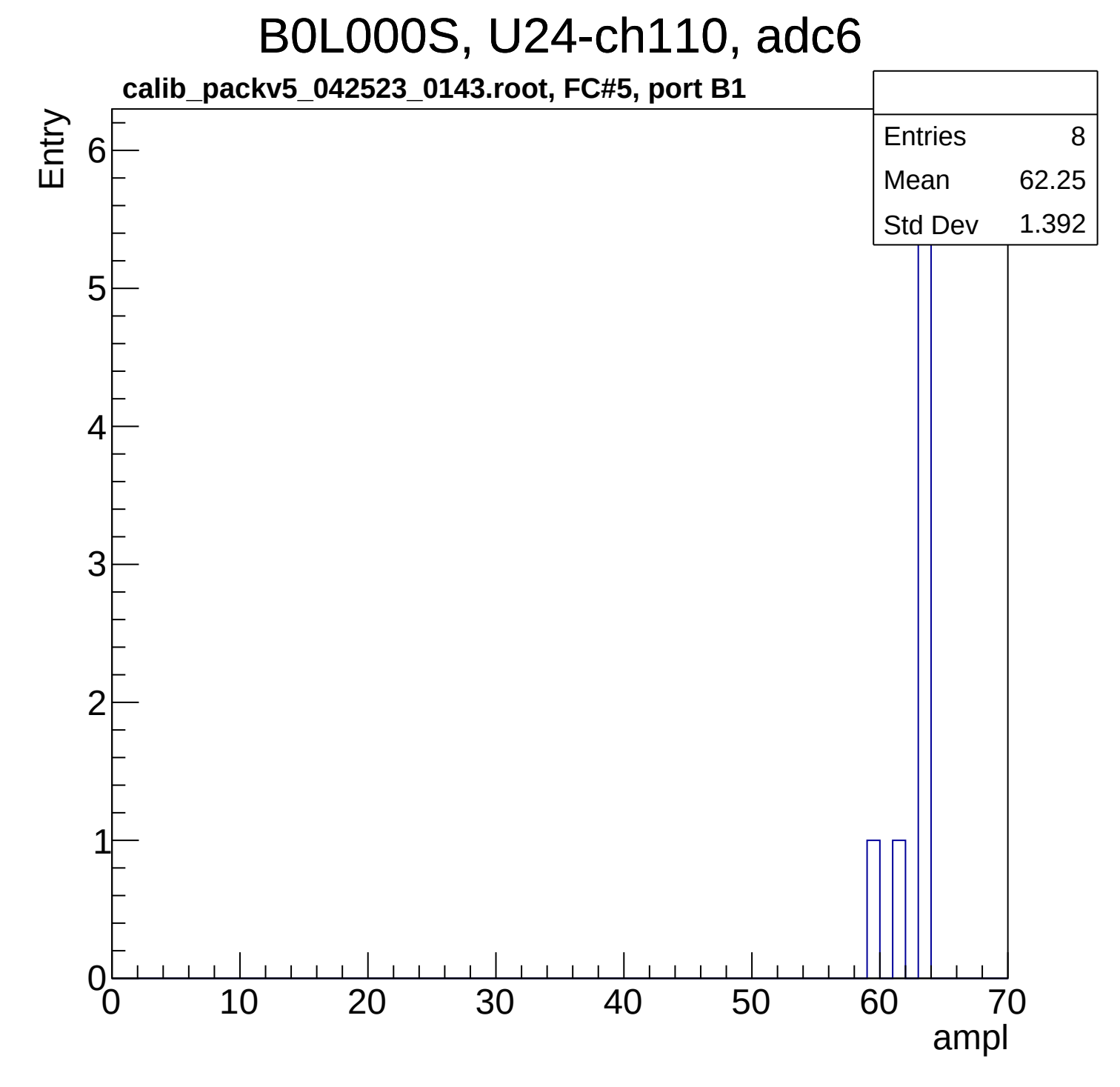
60
70


B0L000S, U24-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.25
Std Dev 1.392
Entry
ampl
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3
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0
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60
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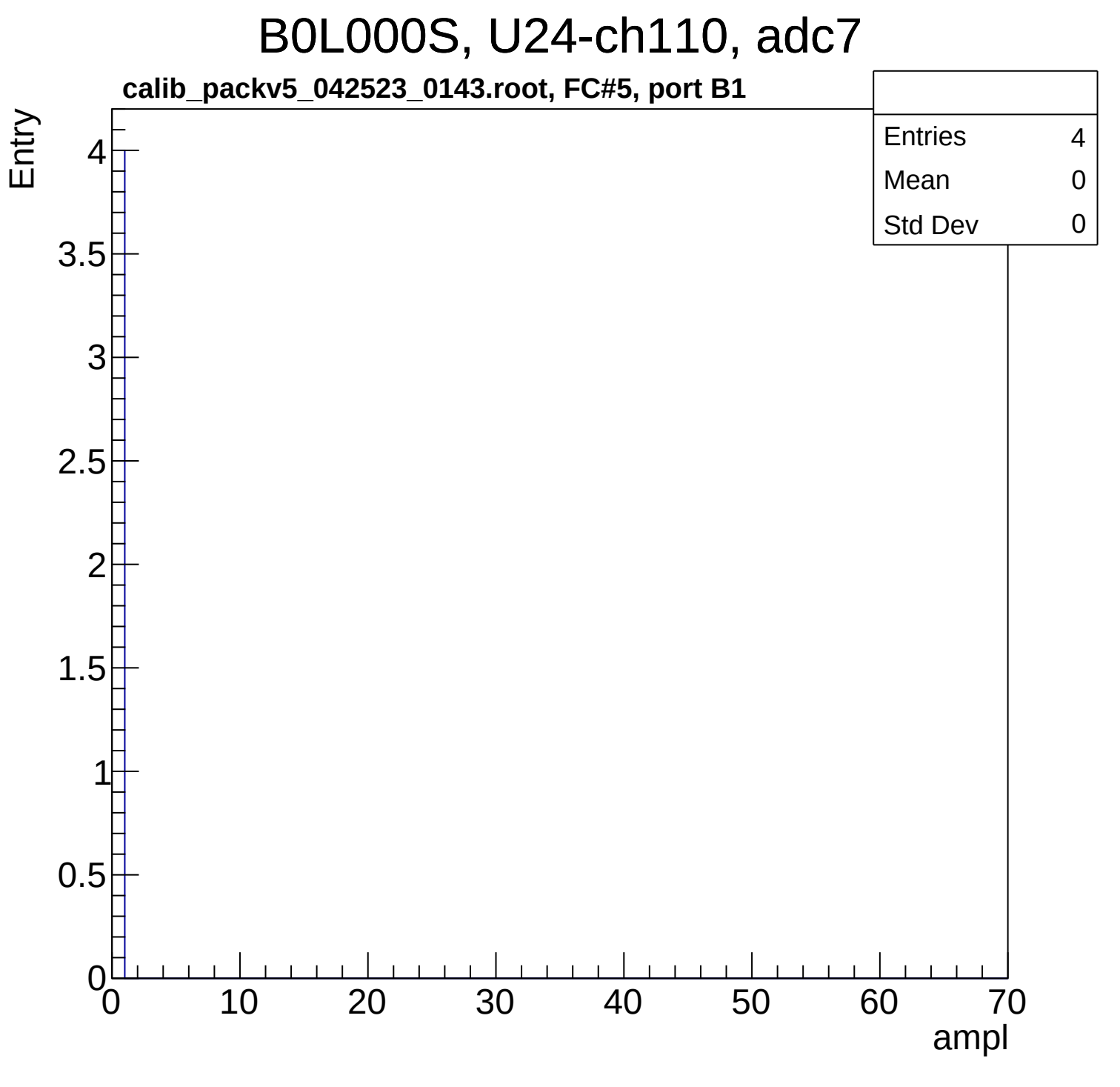
B0L000S, U24-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
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3.5
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20
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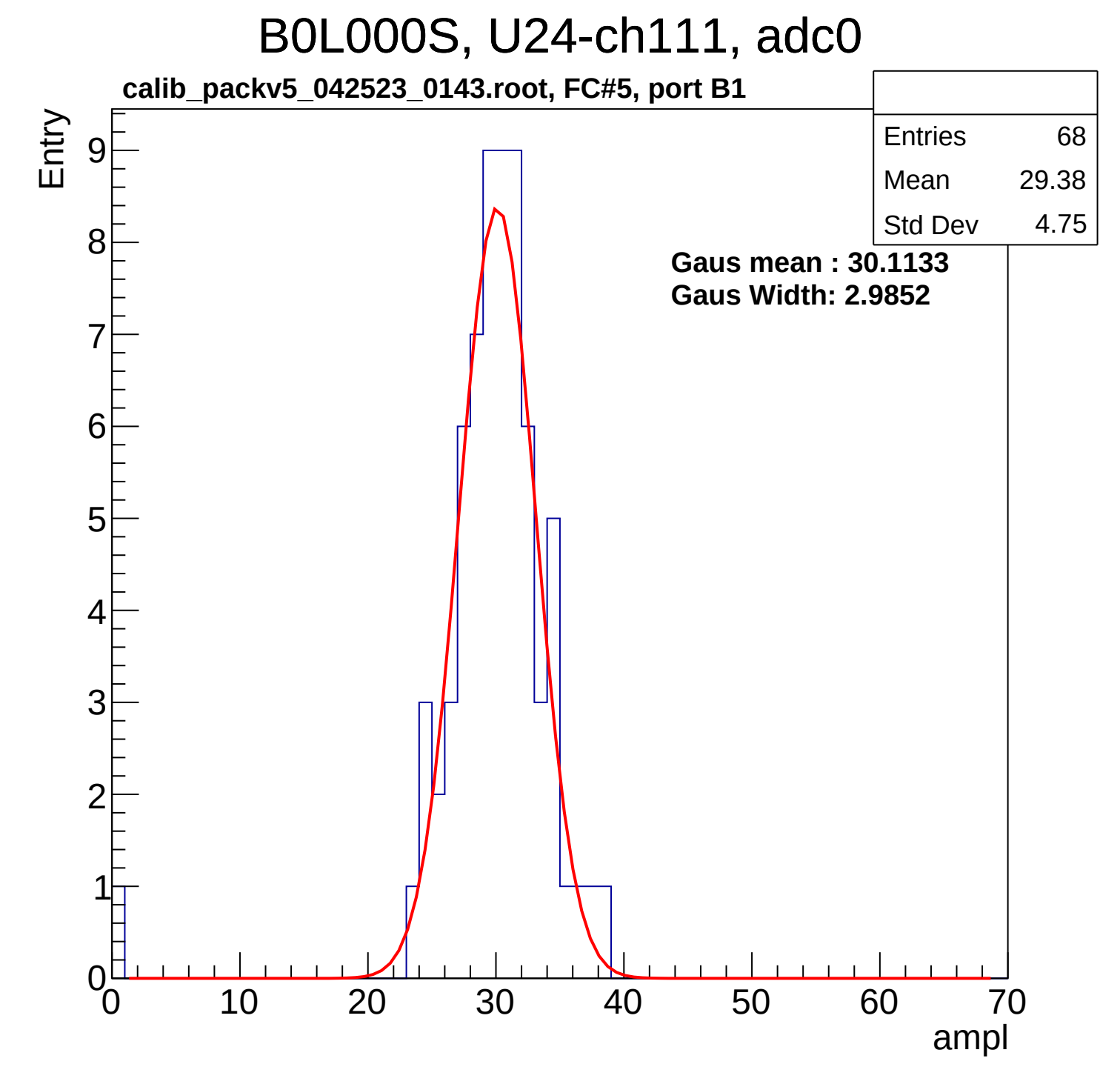
60
70
ampl


B0L000S, U24-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.38
Std Dev 4.75
Gaus mean : 30.1133
Gaus Width: 2.9852
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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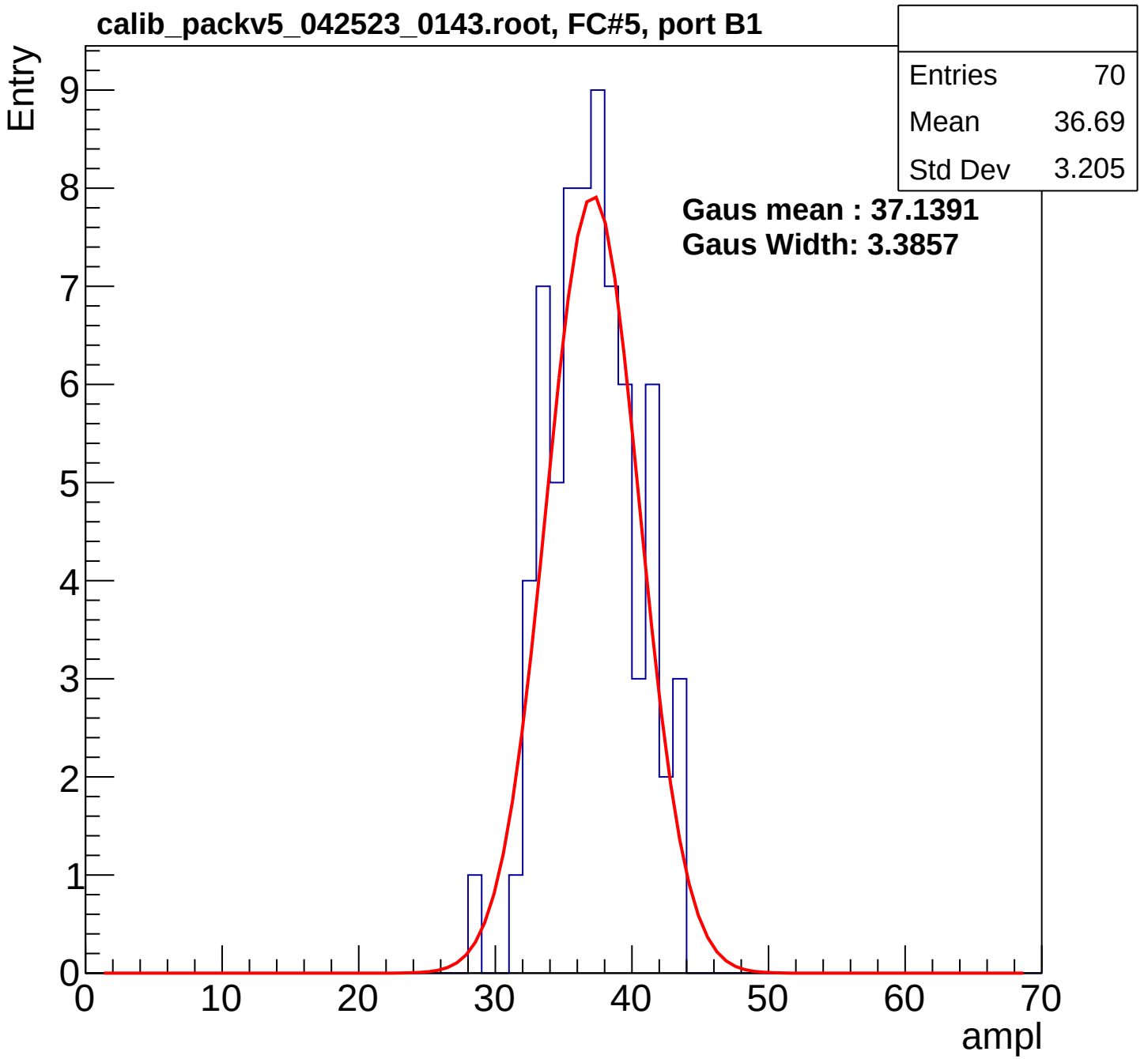
B0L000S, U24-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.69
Std Dev 3.205
Gaus mean : 37.1391
Gaus Width: 3.3857
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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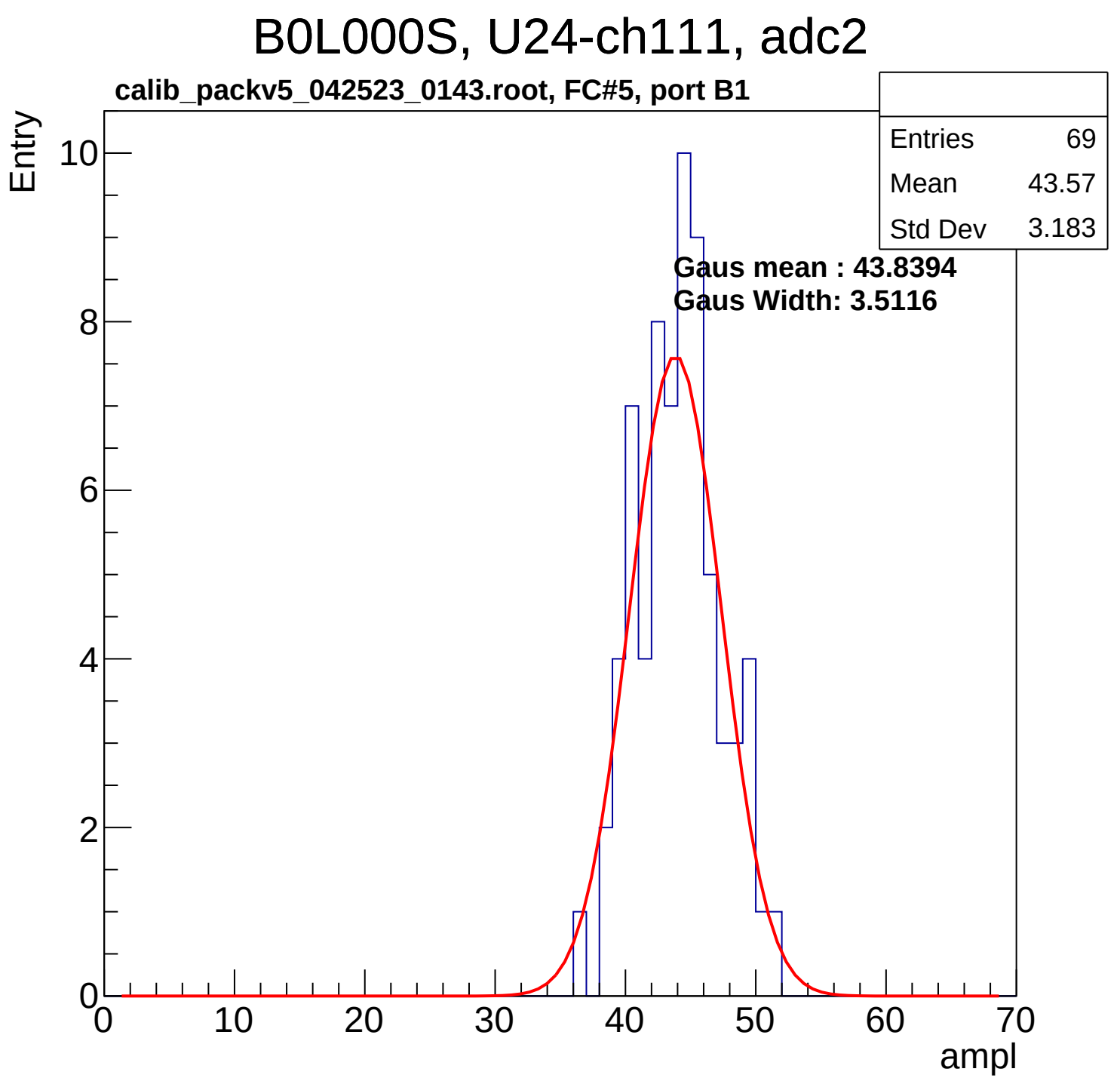
B0L000S, U24-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.57
Std Dev 3.183
Gaus mean : 43.8394
Gaus Width: 3.5116
Entry
ampl
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4
6
8
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0
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60
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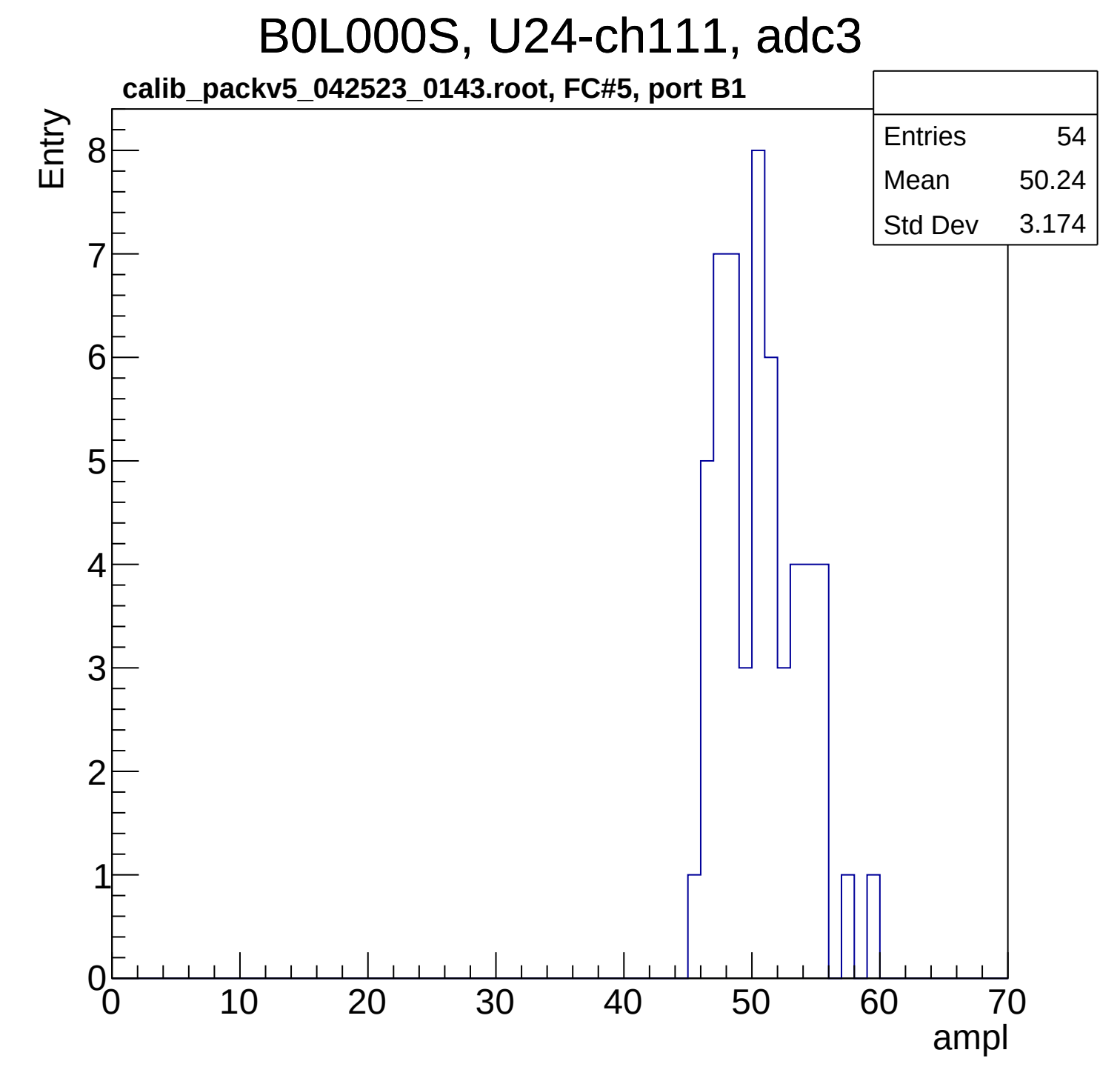
B0L000S, U24-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.24
Std Dev 3.174
Entry
ampl
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3
4
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6
7
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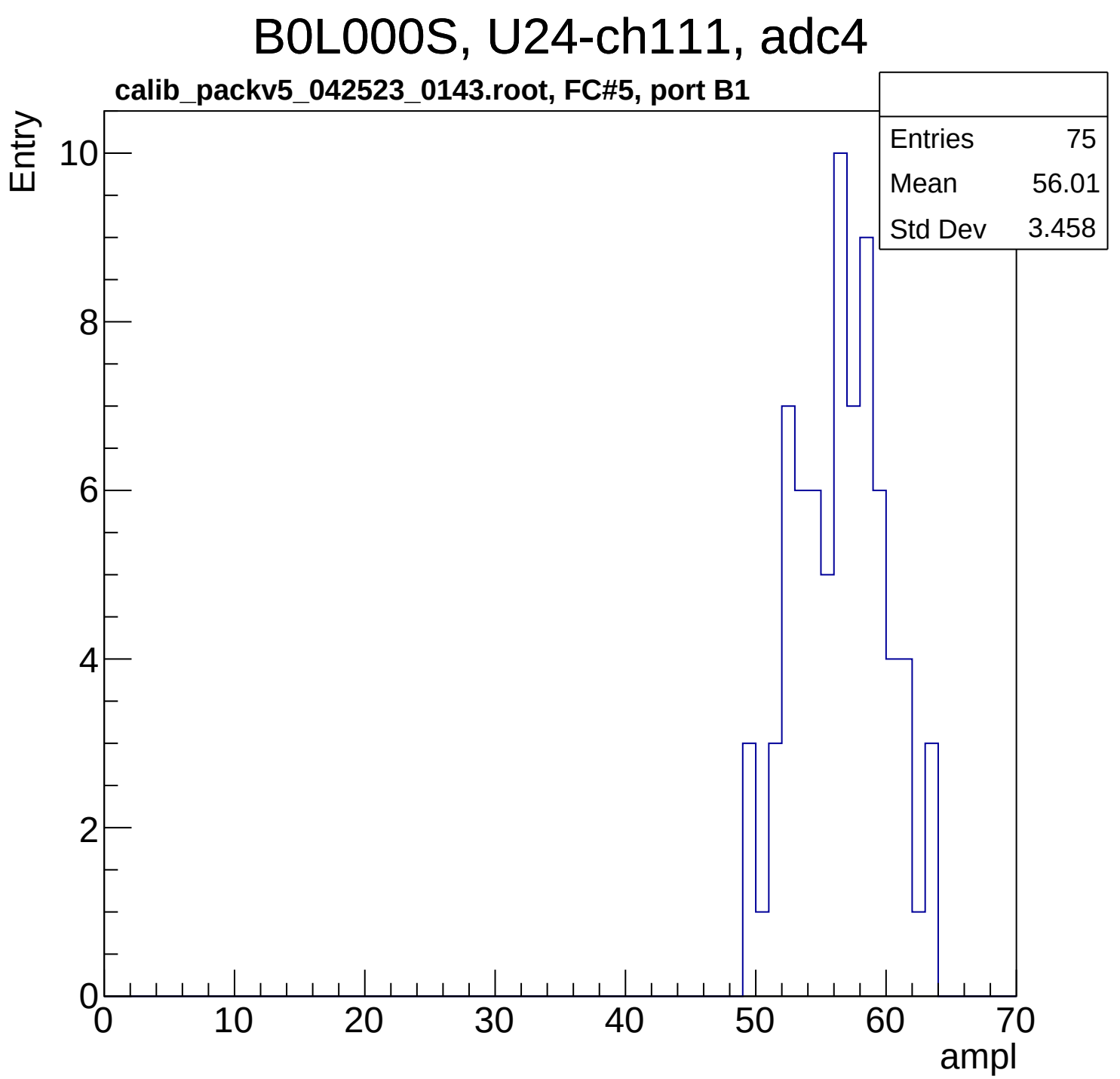
B0L000S, U24-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 56.01
Std Dev 3.458
Entry
ampl
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8
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40
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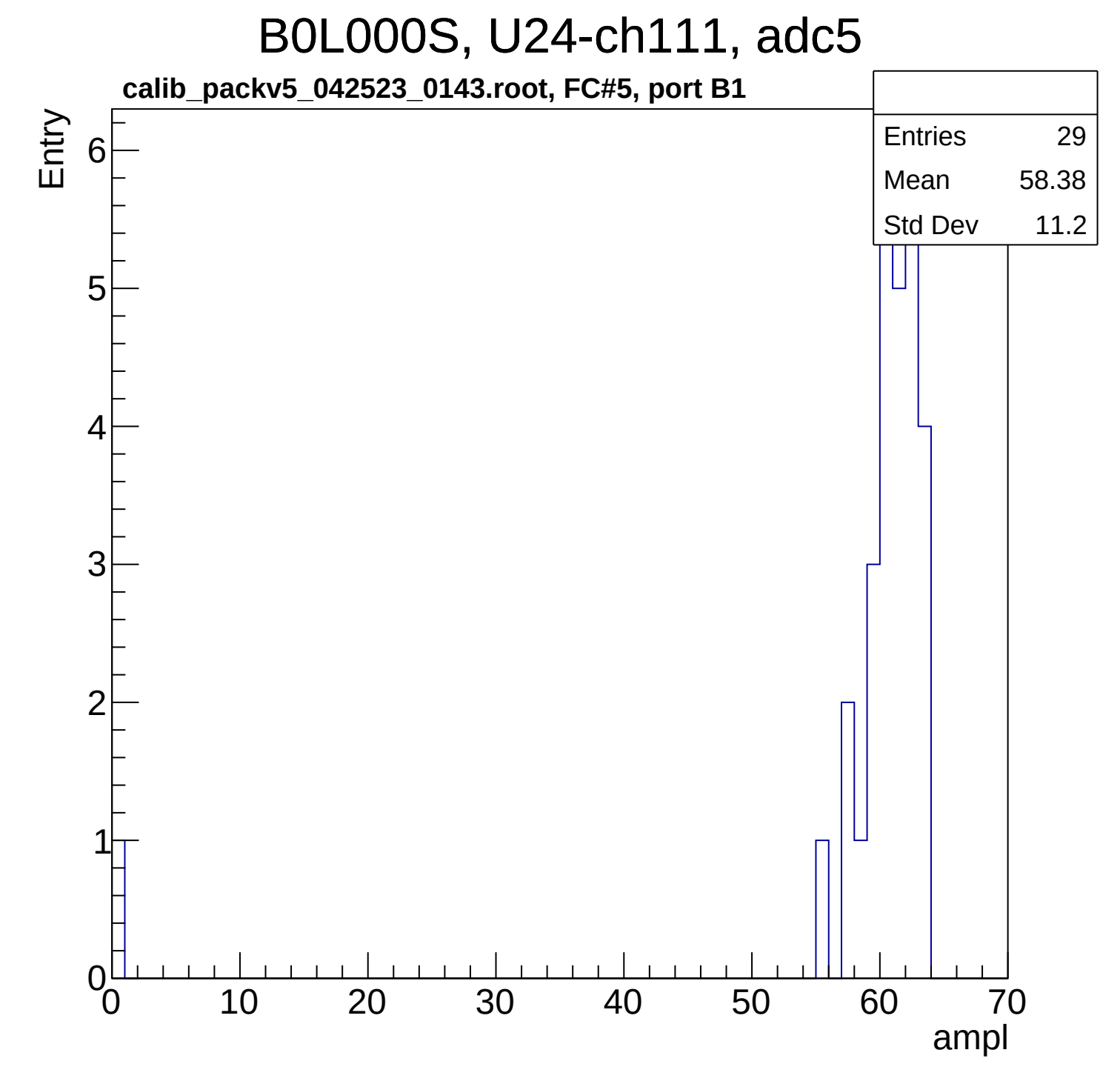
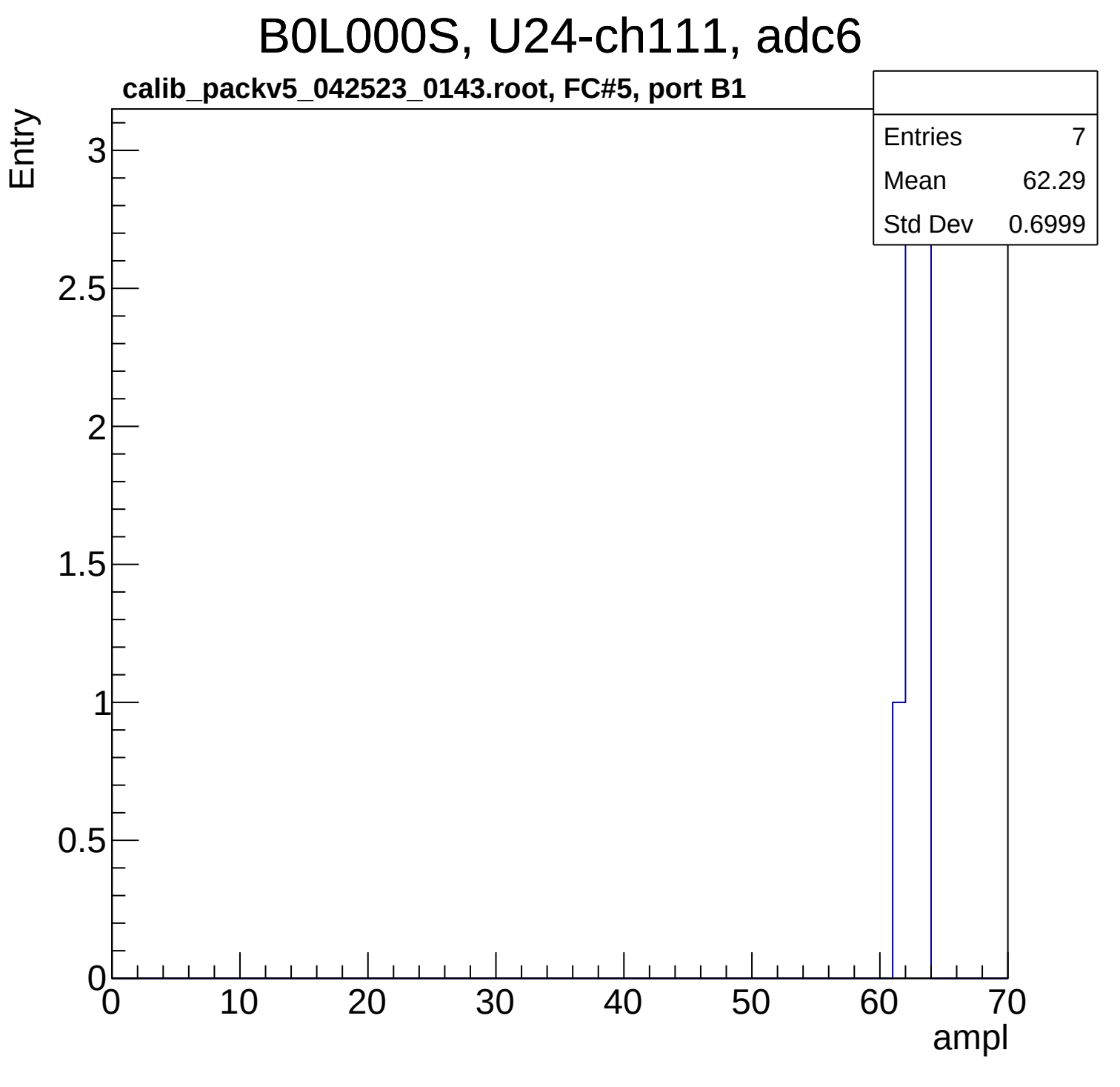
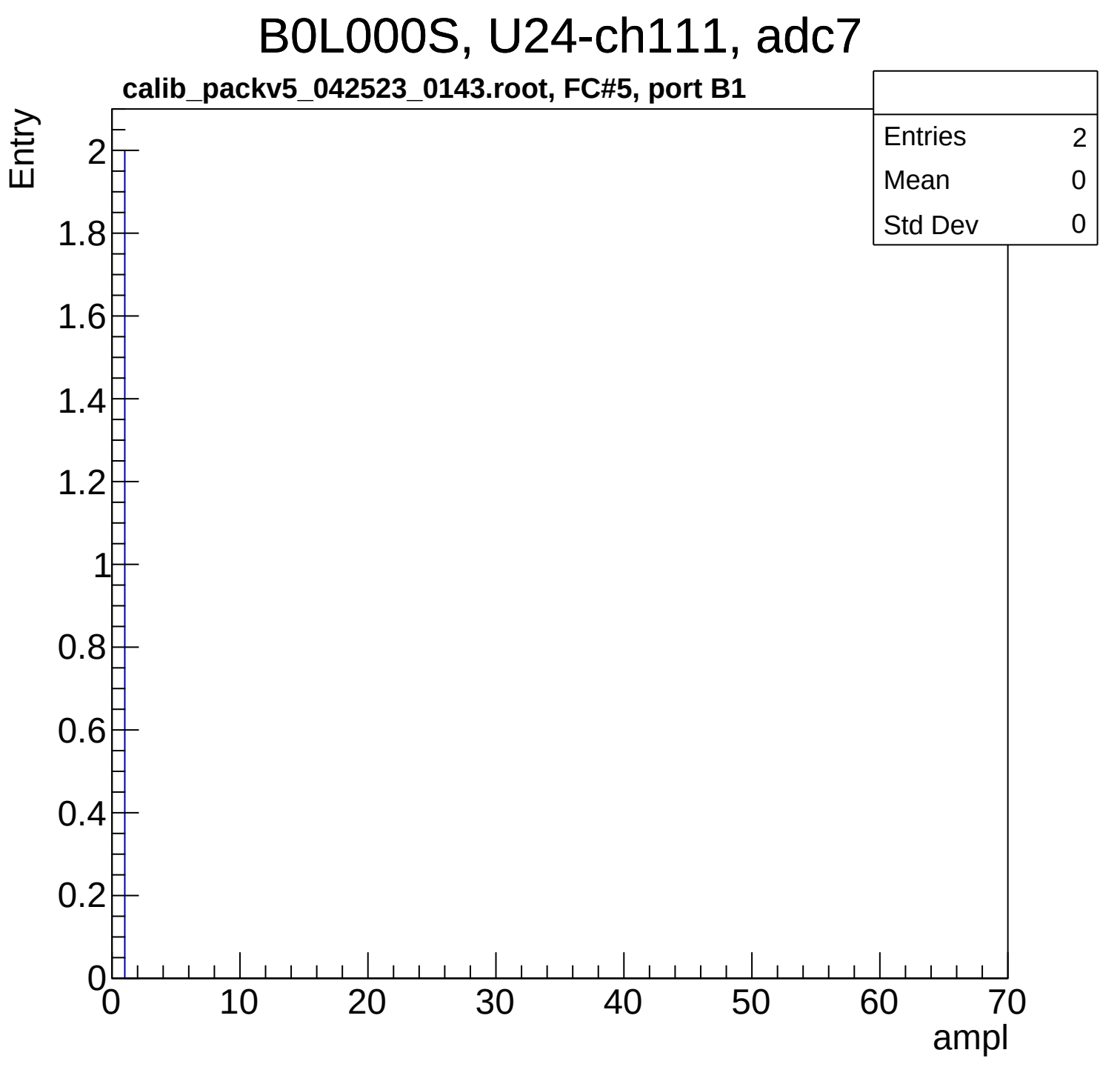

B0L000S, U24-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58.38
Std Dev 11.2
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
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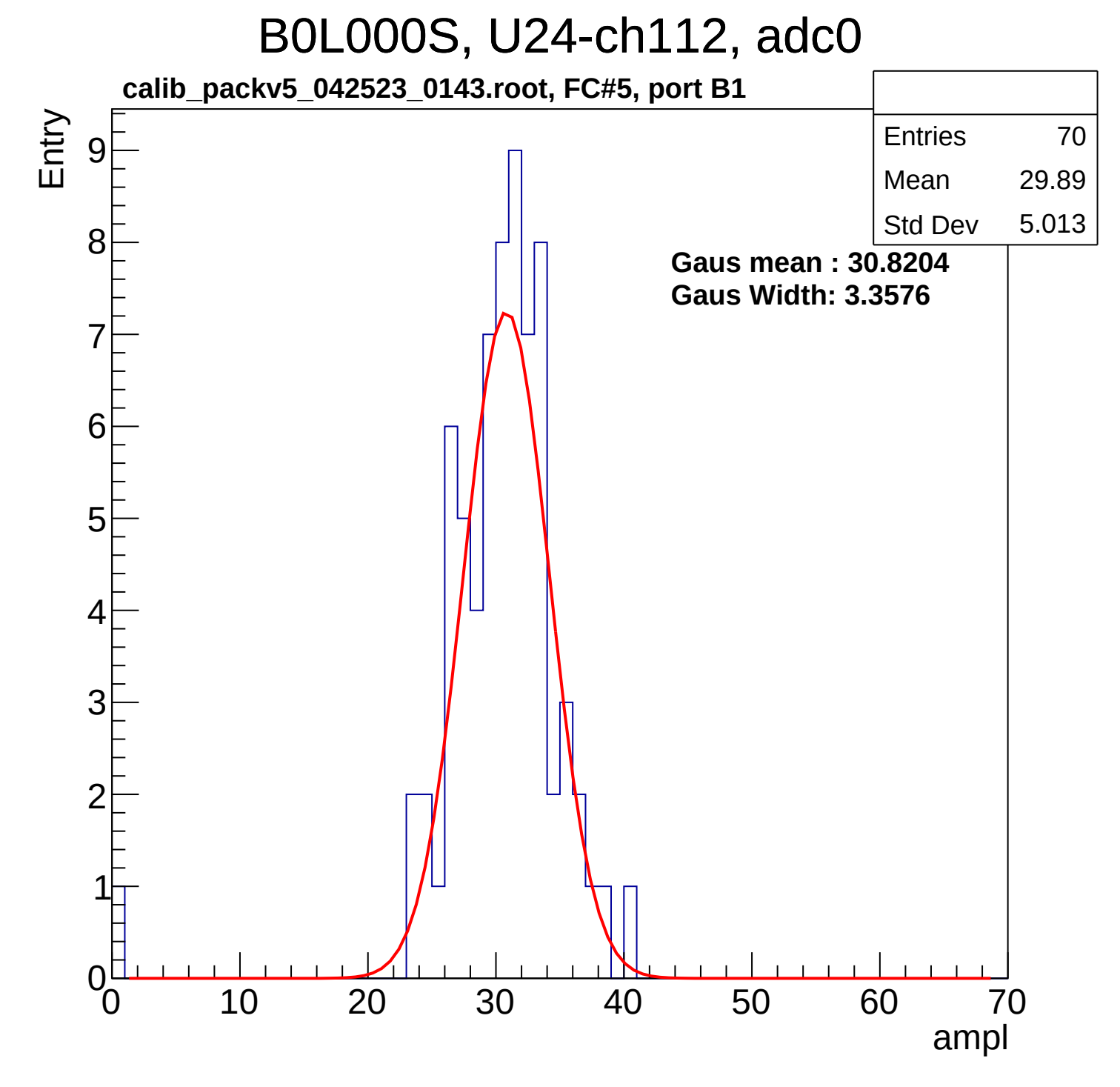
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.89
Std Dev 5.013
Gaus mean : 30.8204
Gaus Width: 3.3576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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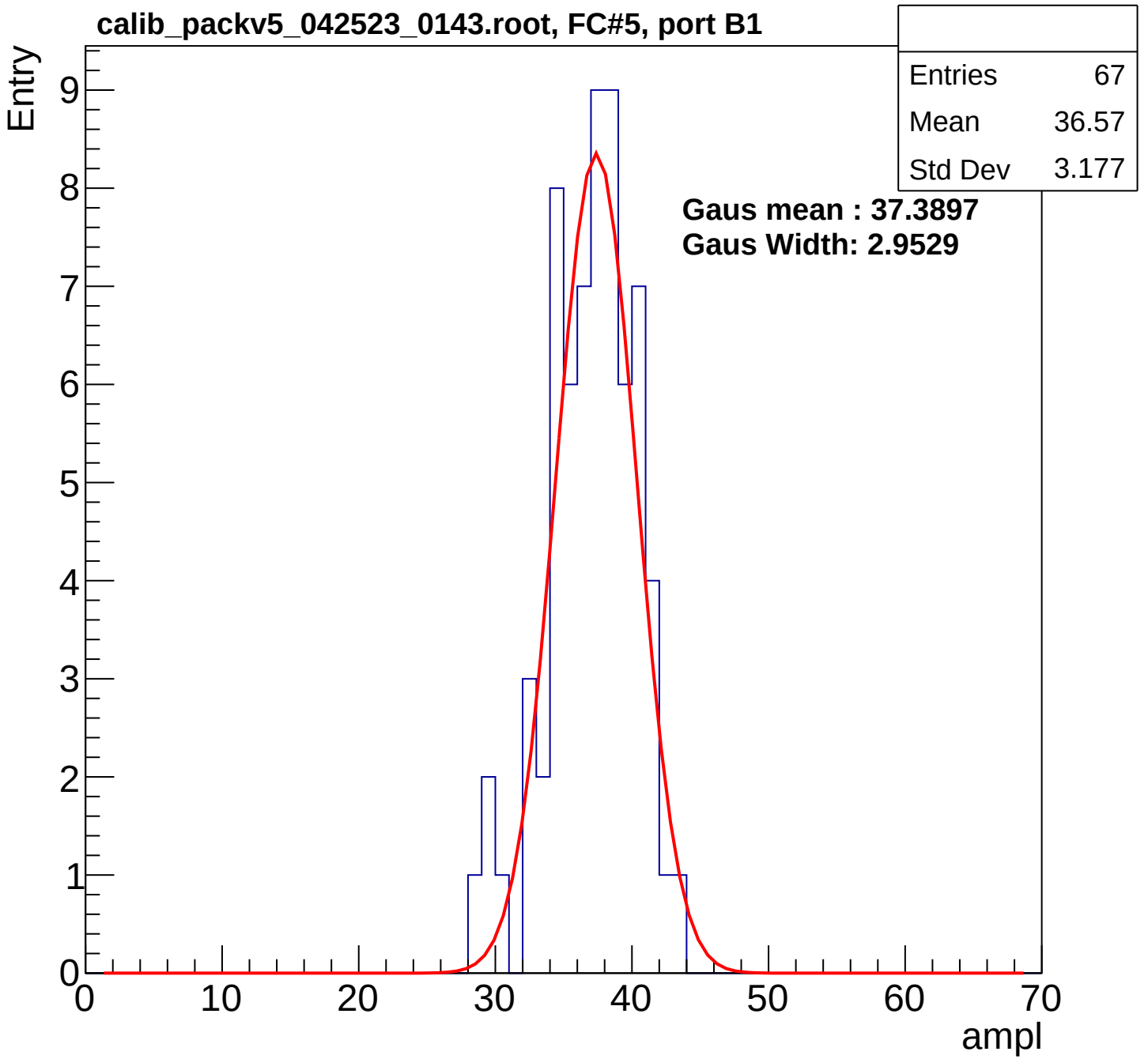
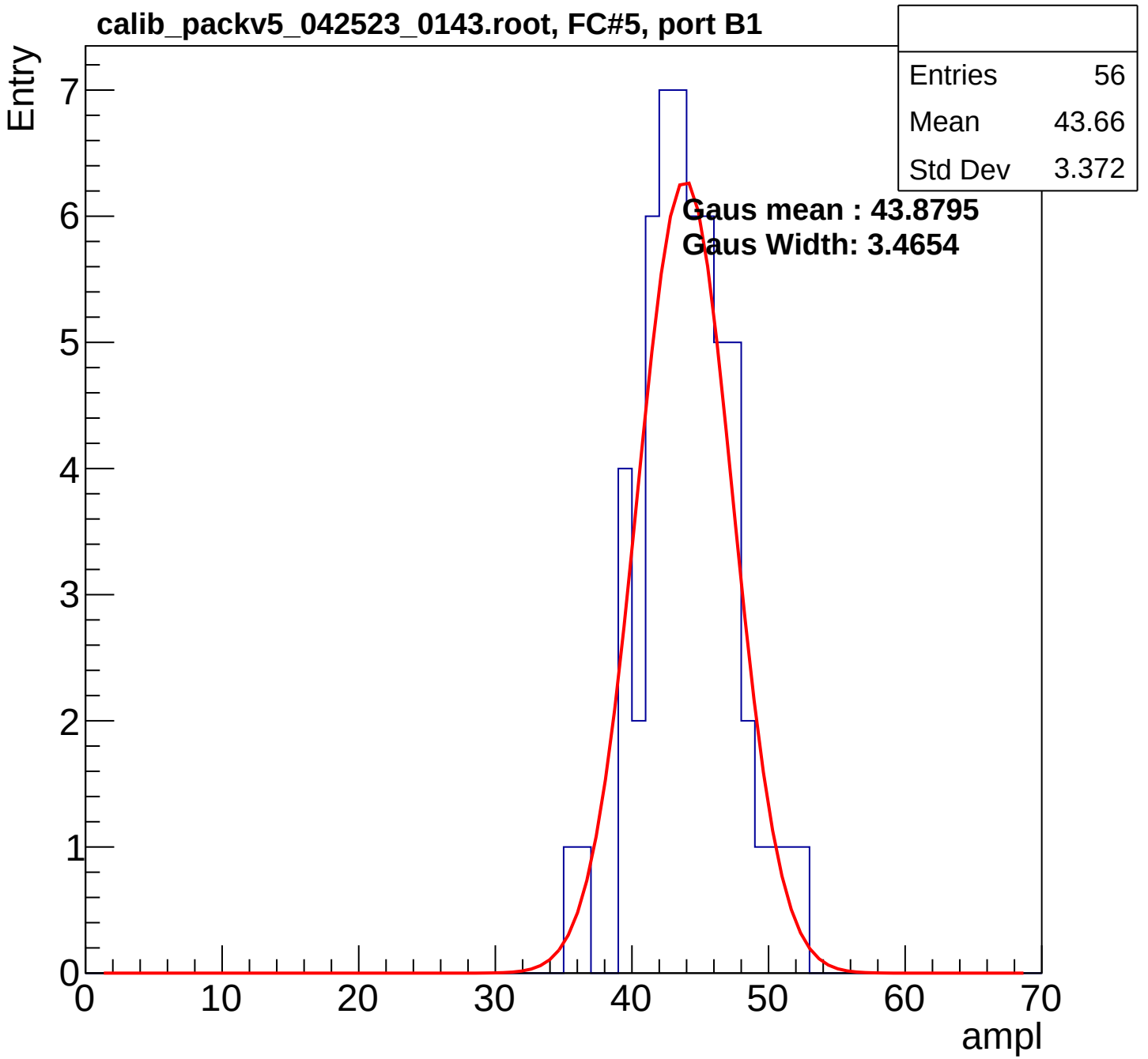
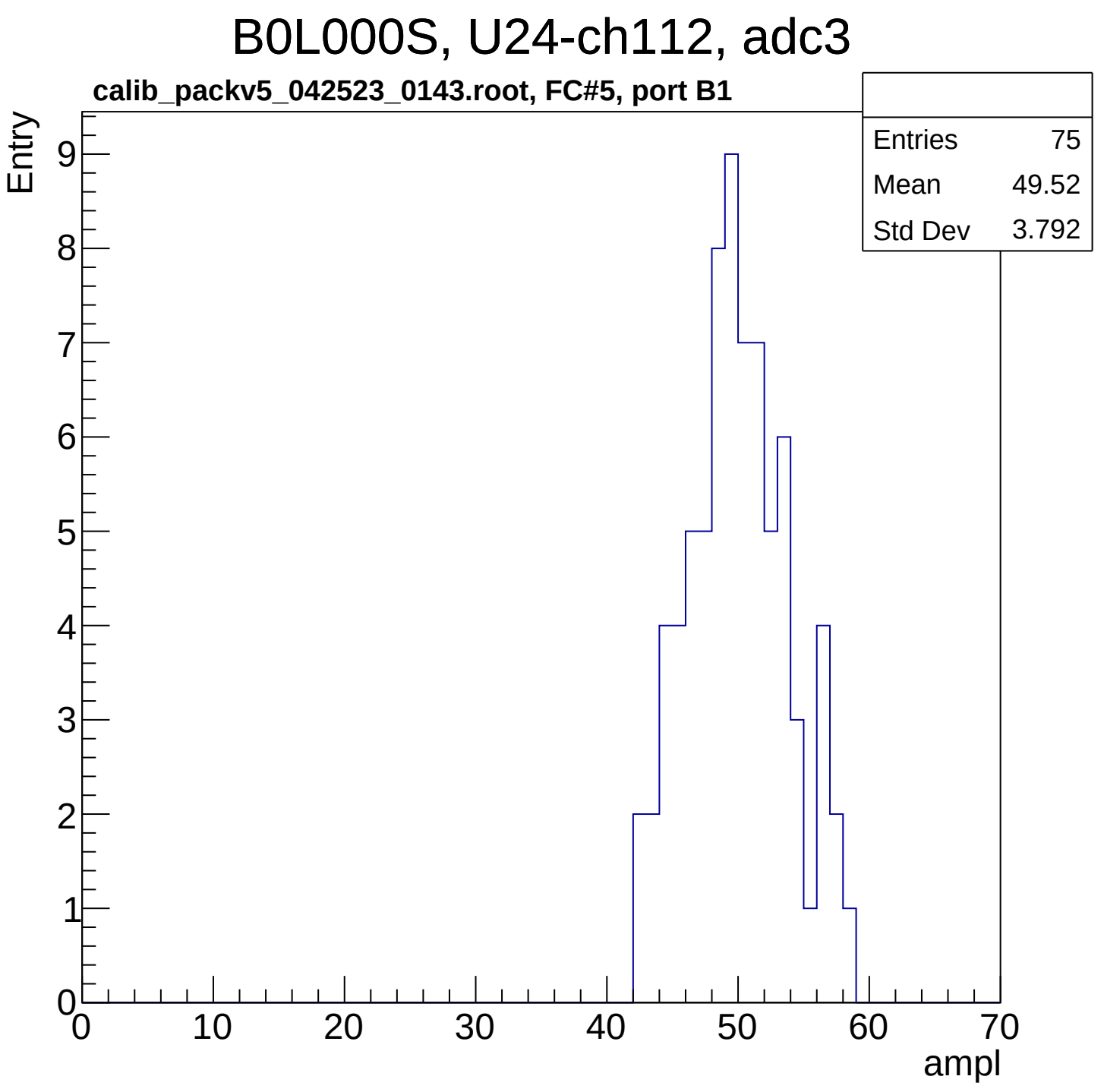

B0L000S, U24-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.57
Std Dev 3.177
Gaus mean : 37.3897
Gaus Width: 2.9529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.66
Std Dev 3.372
Gaus mean : 43.8795
Gaus Width: 3.4654
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 49.52
Std Dev 3.792
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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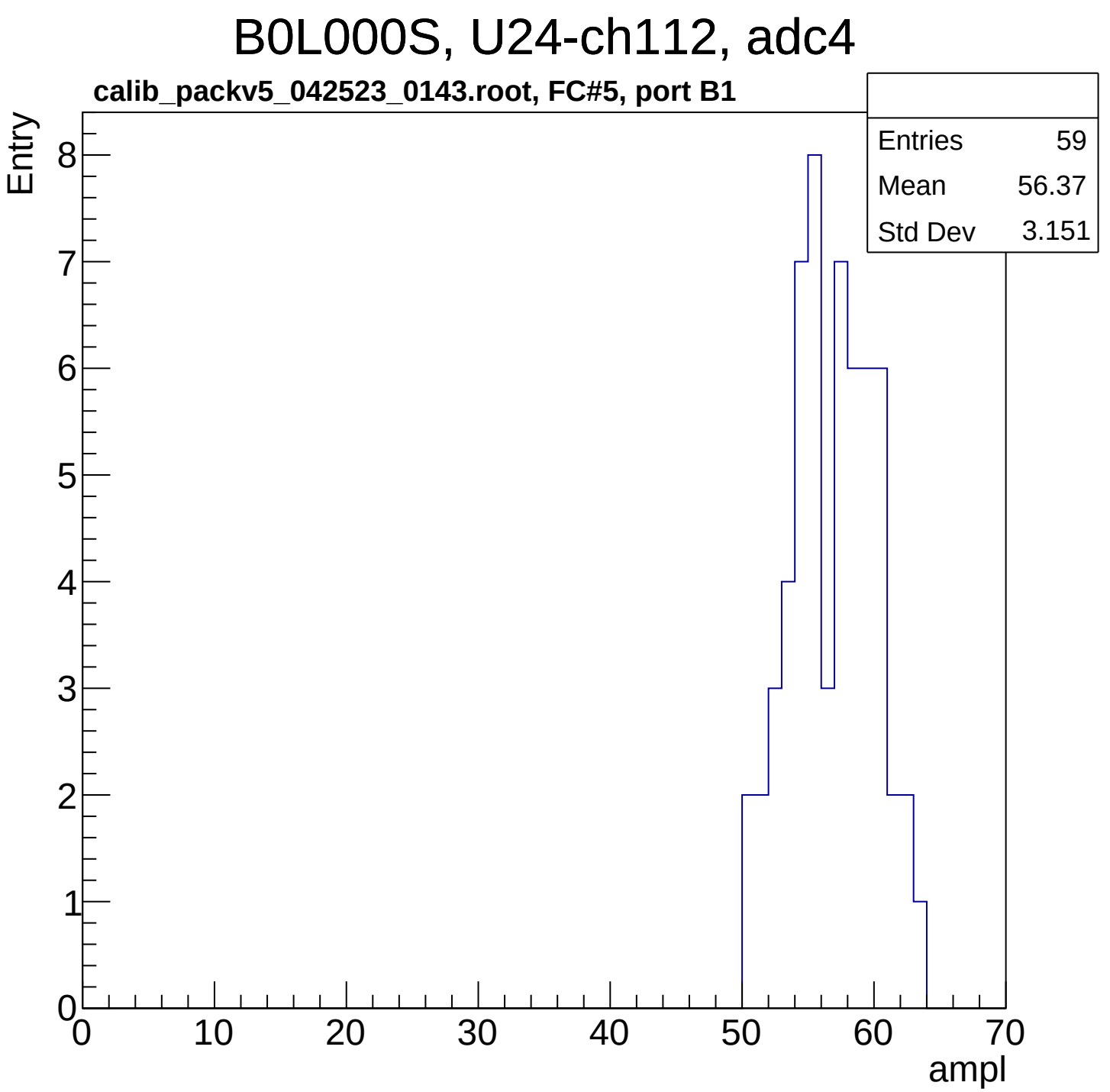
B0L000S, U24-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.37
Std Dev 3.151
Entry
ampl
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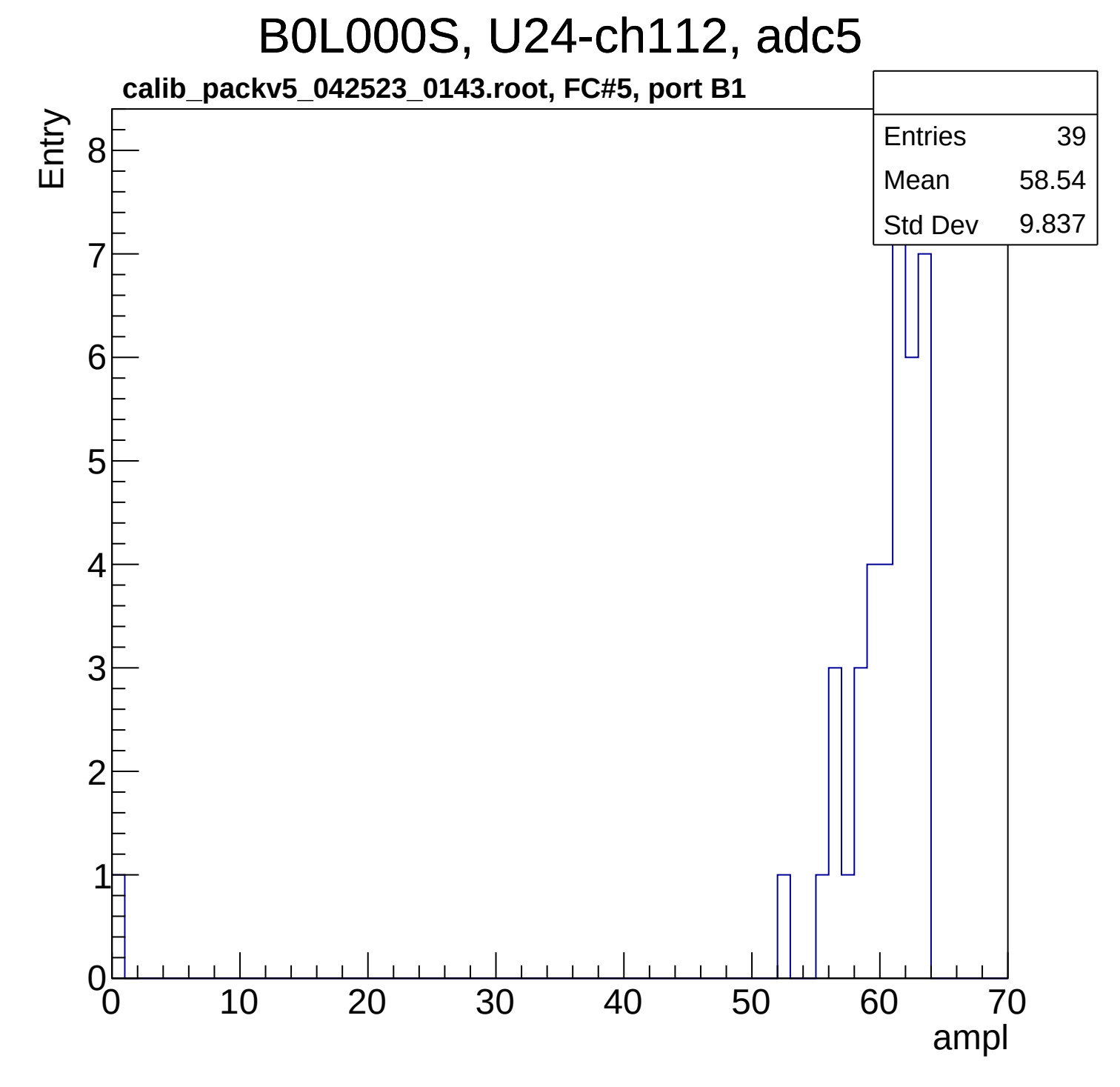
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.54
Std Dev 9.837
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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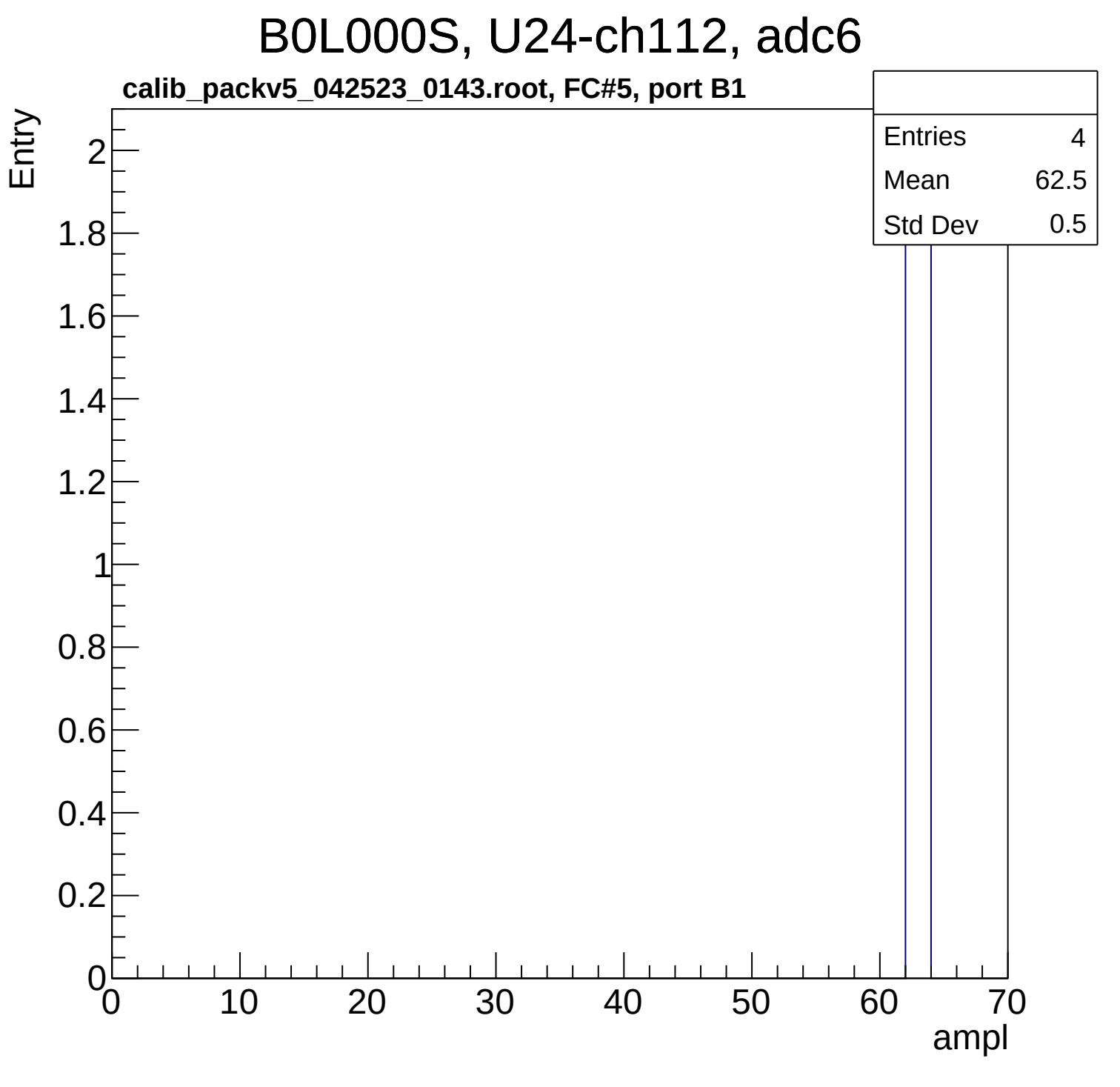
B0L000S, U24-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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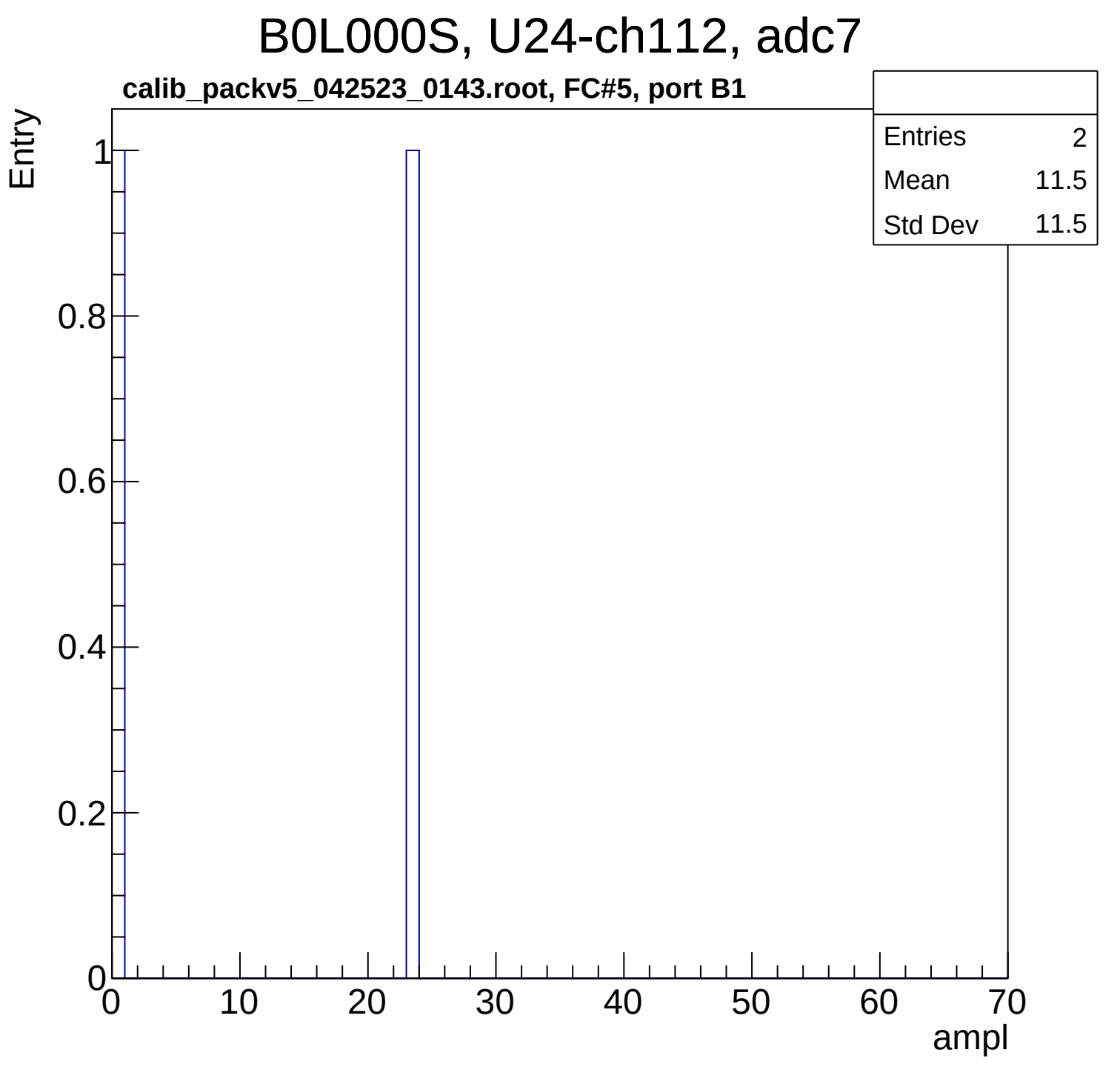
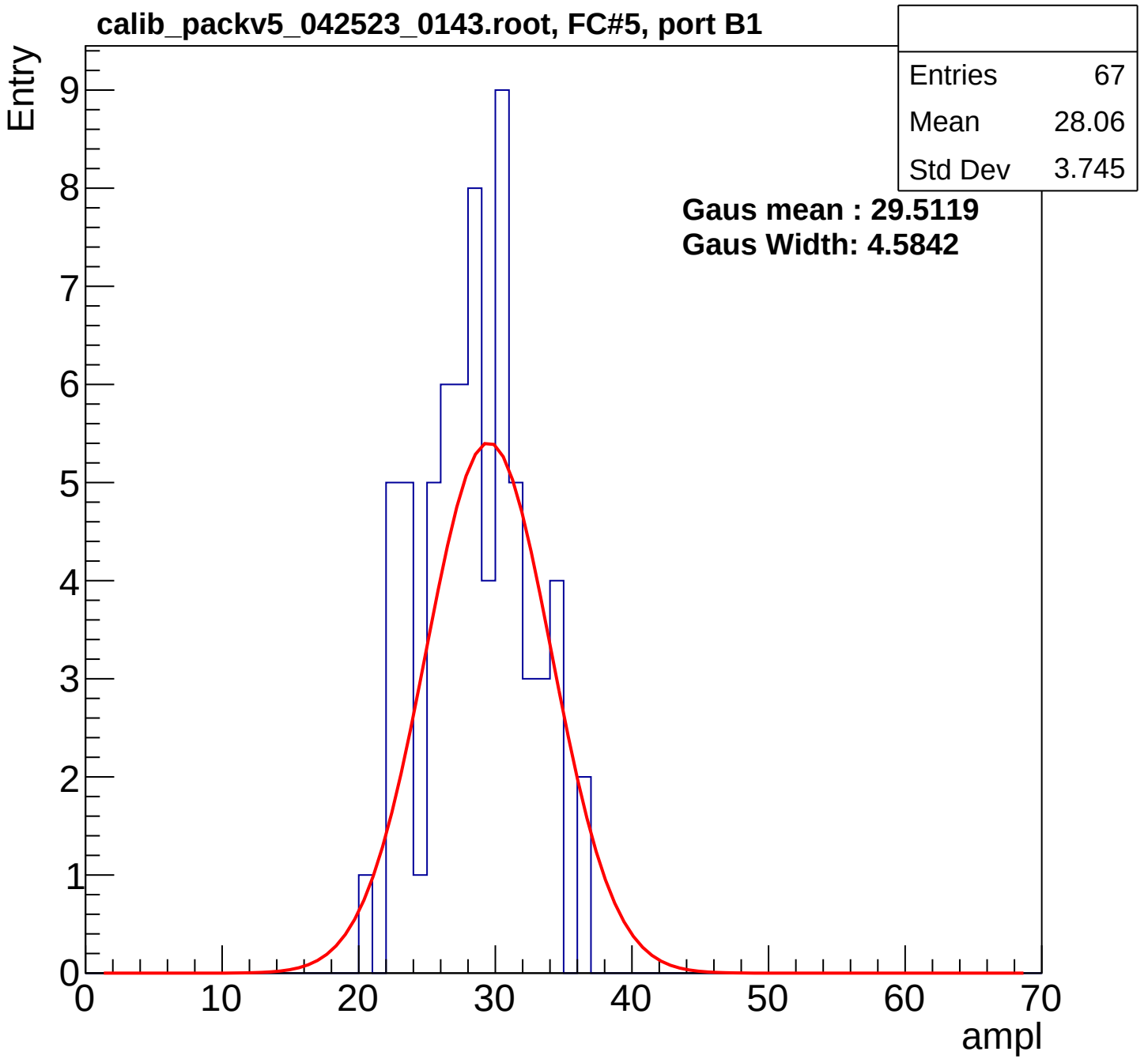
40
50
60
70


B0L000S, U24-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.06
Std Dev 3.745
Gaus mean : 29.5119
Gaus Width: 4.5842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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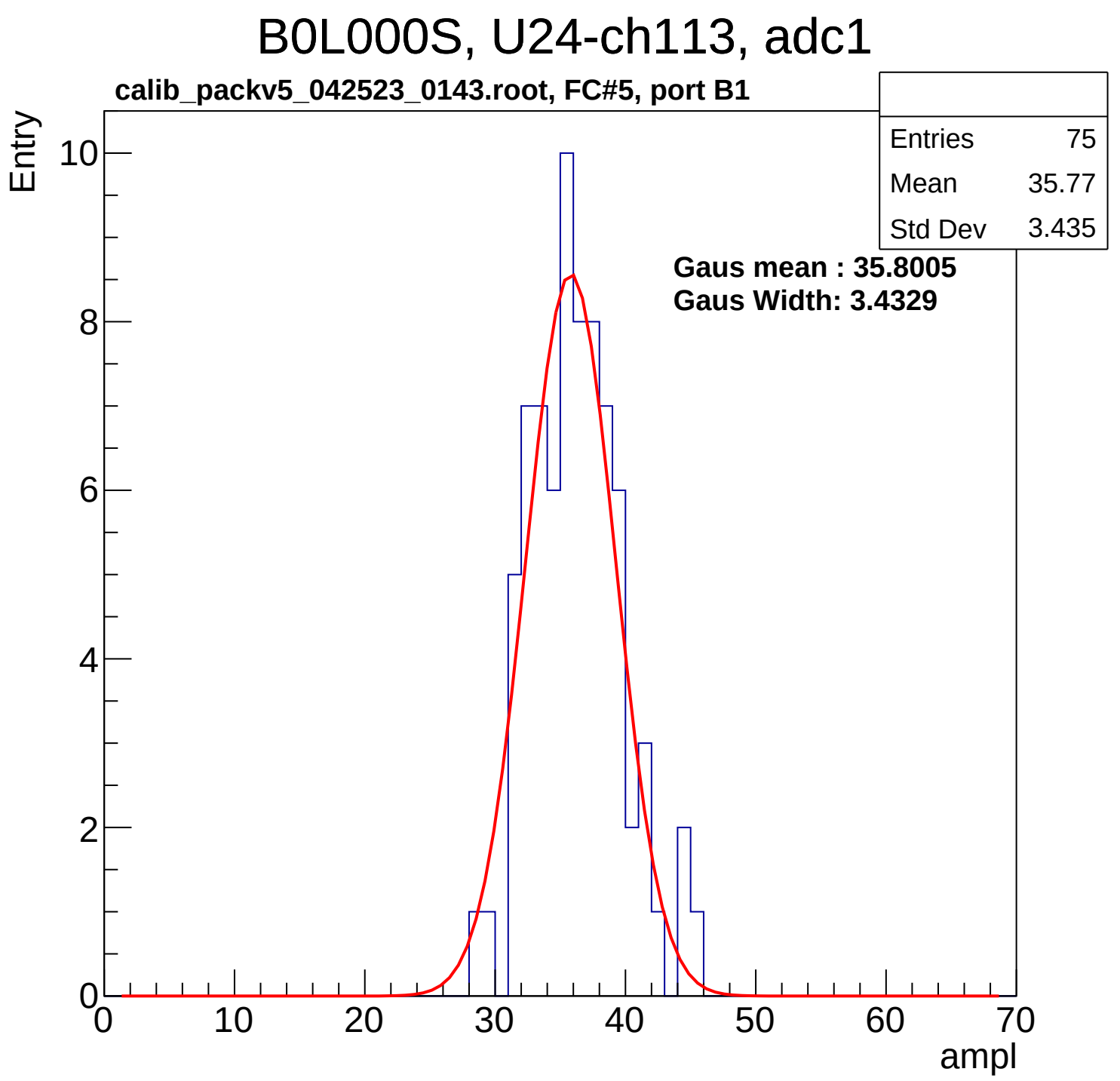
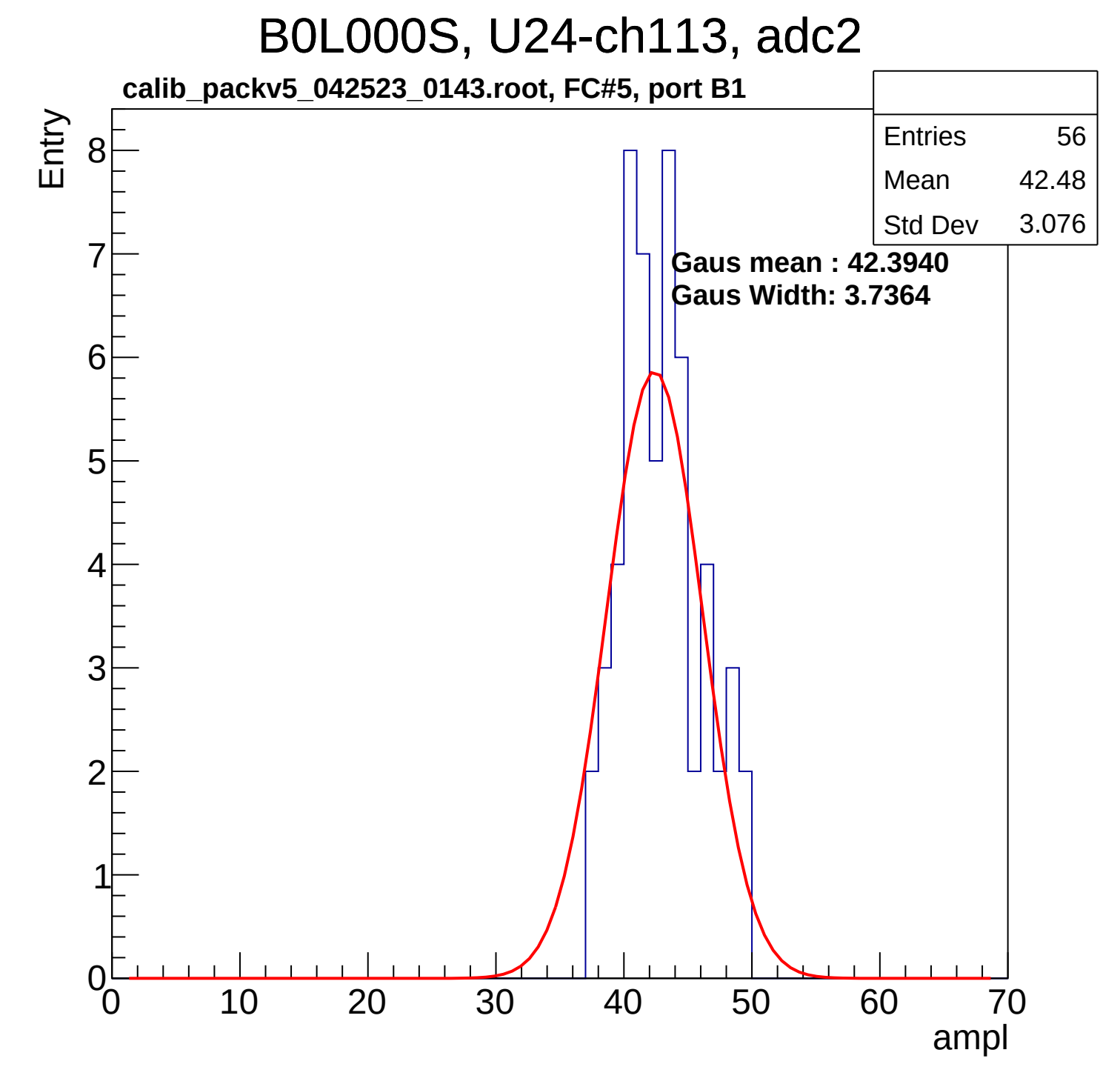
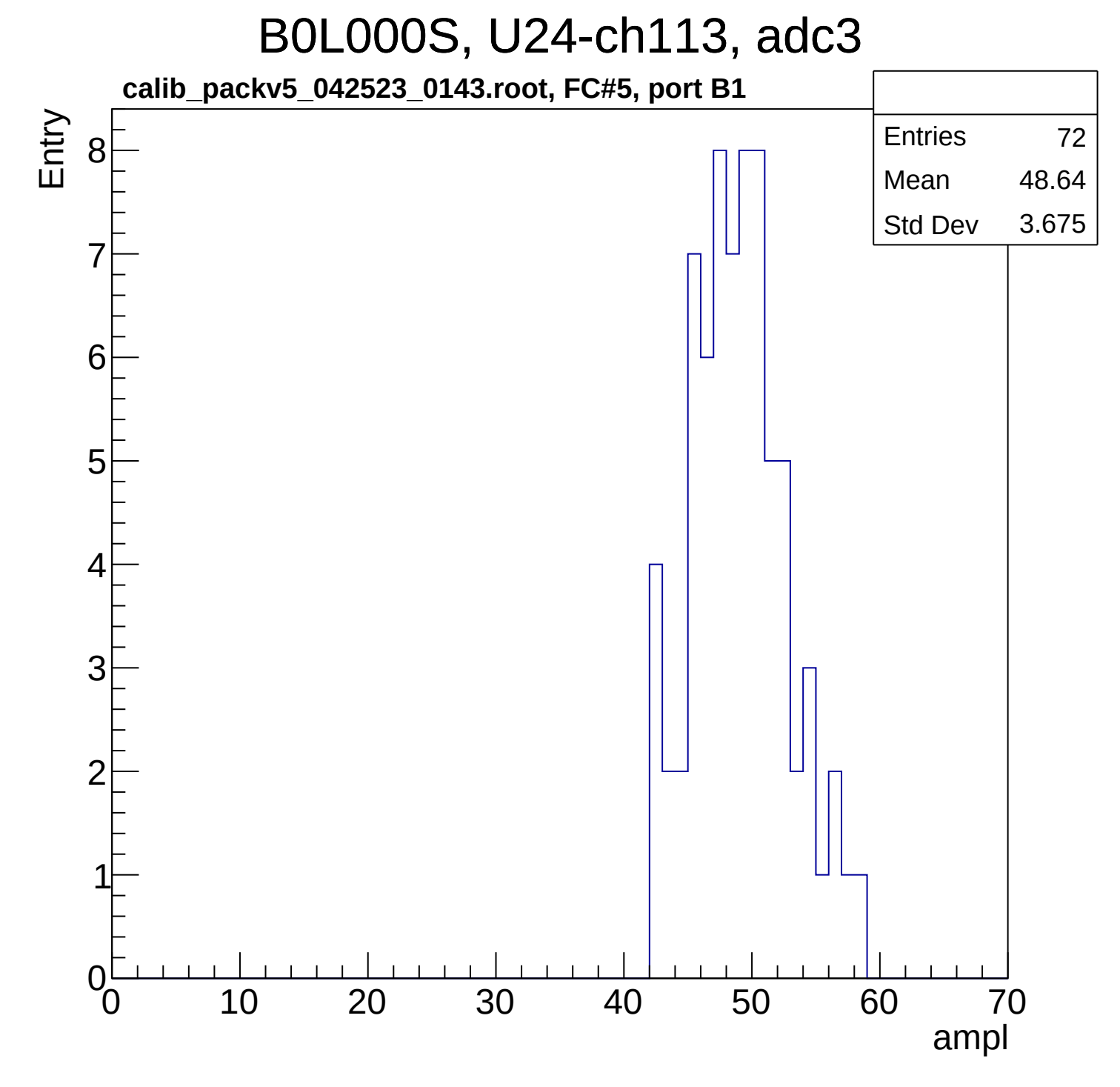
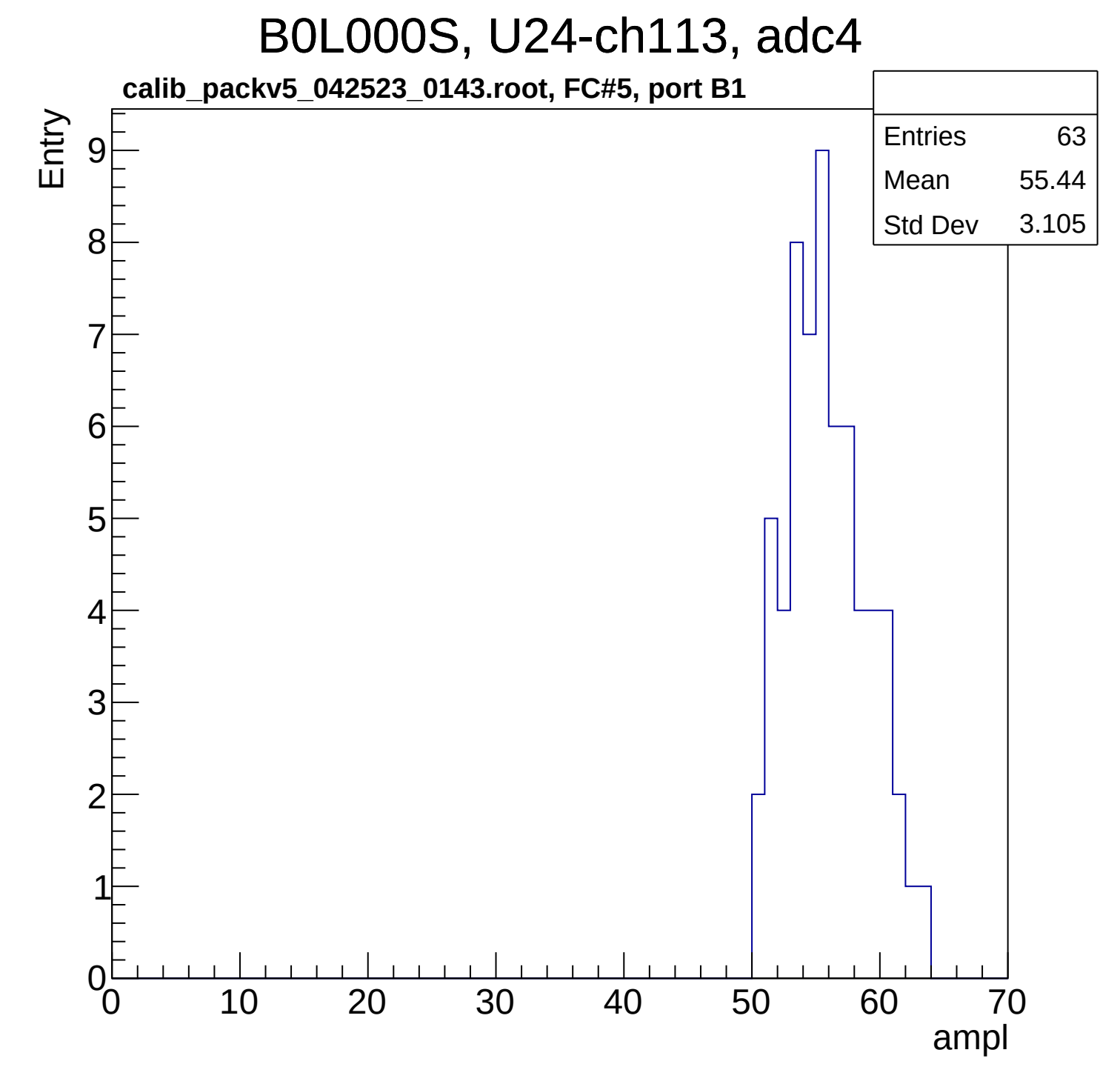
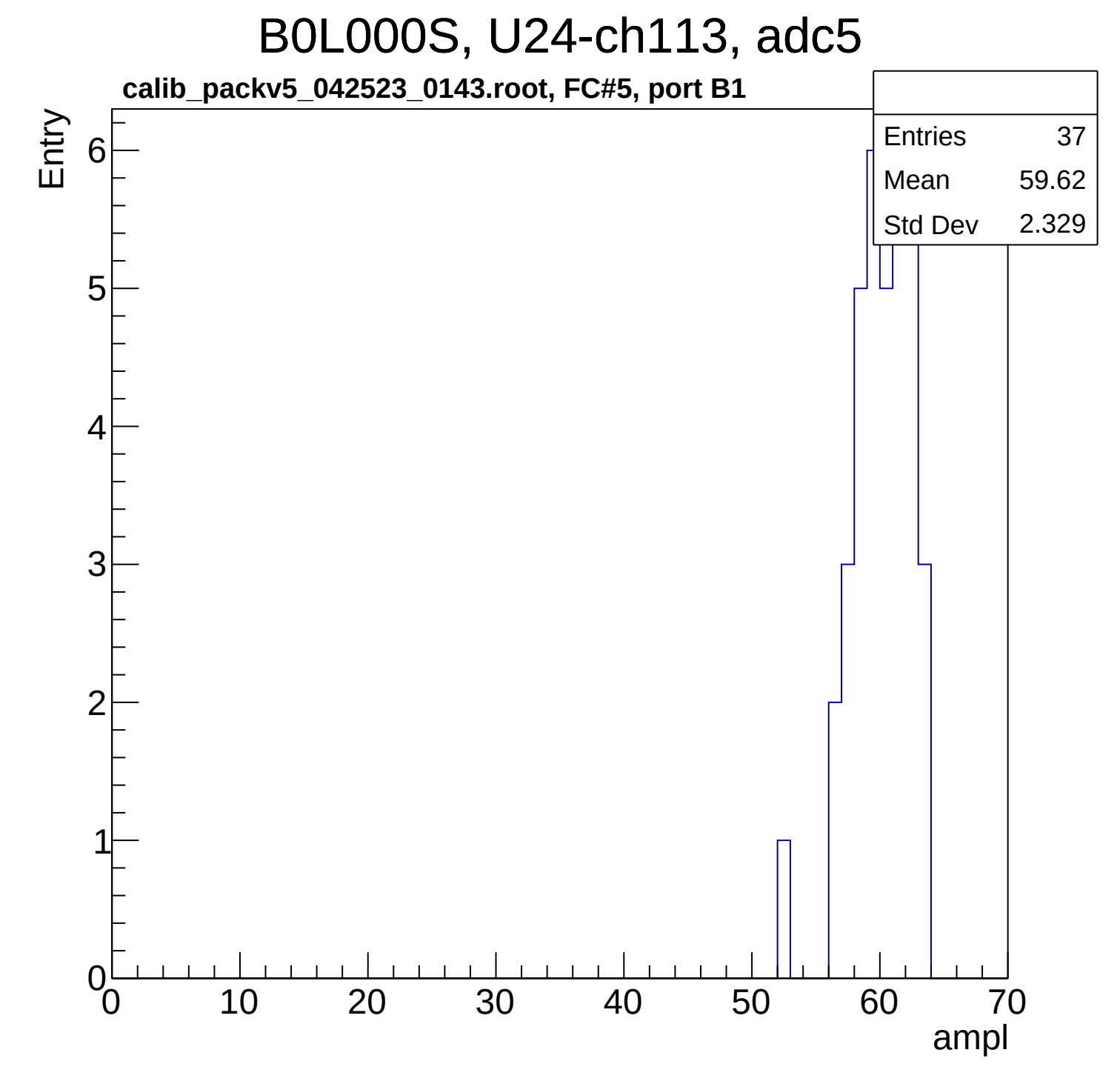
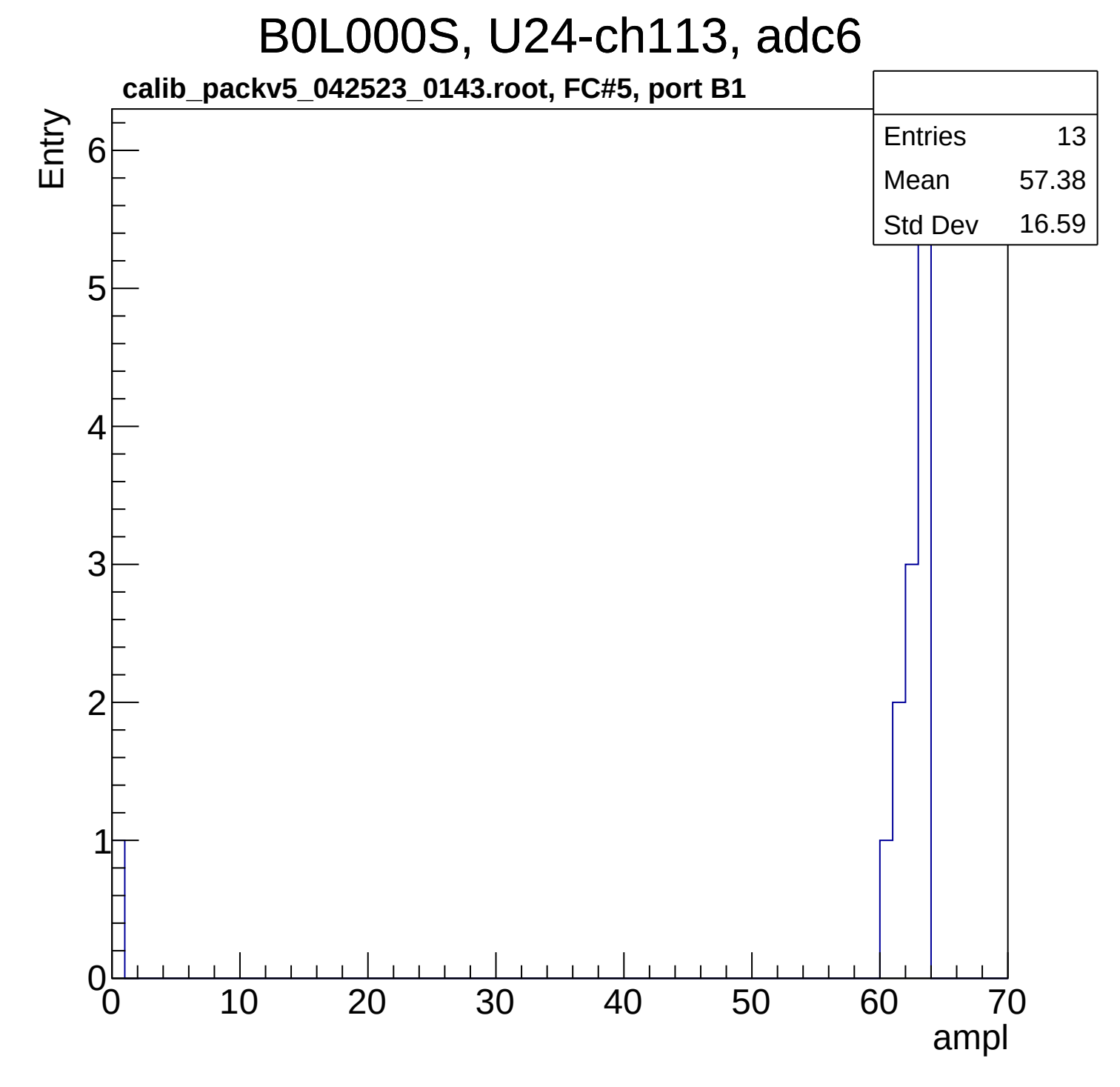

B0L000S, U24-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.77
Std Dev 3.435
Gaus mean : 35.8005
Gaus Width: 3.4329
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

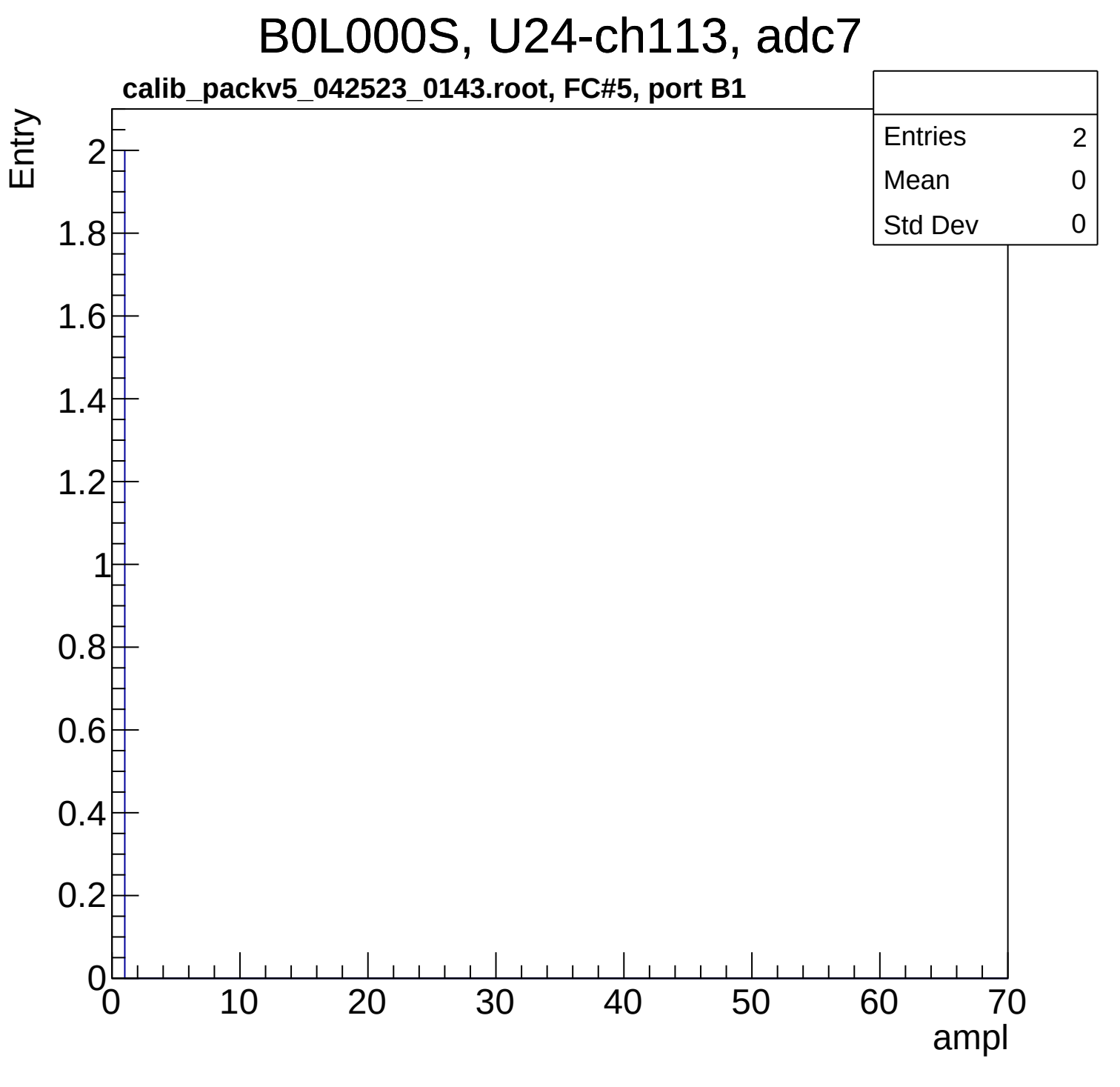

B0L000S, U24-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.48
Std Dev 3.076
Gaus mean : 42.3940
Gaus Width: 3.7364
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

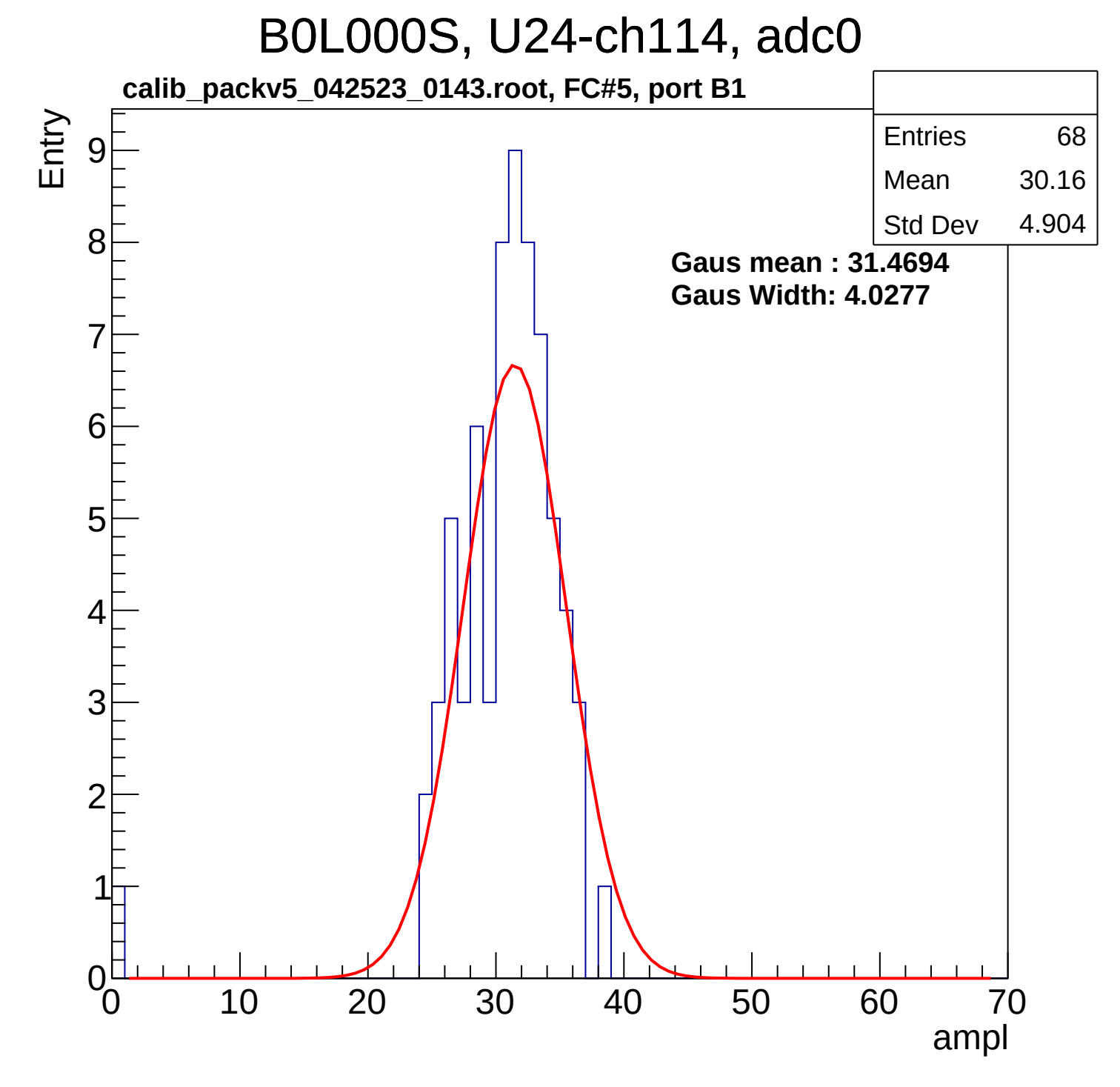

B0L000S, U24-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.64
Std Dev 3.675
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 55.44
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.62
Std Dev 2.329
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U24-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.38
Std Dev 16.59
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.16
Std Dev 4.904
Gaus mean : 31.4694
Gaus Width: 4.0277
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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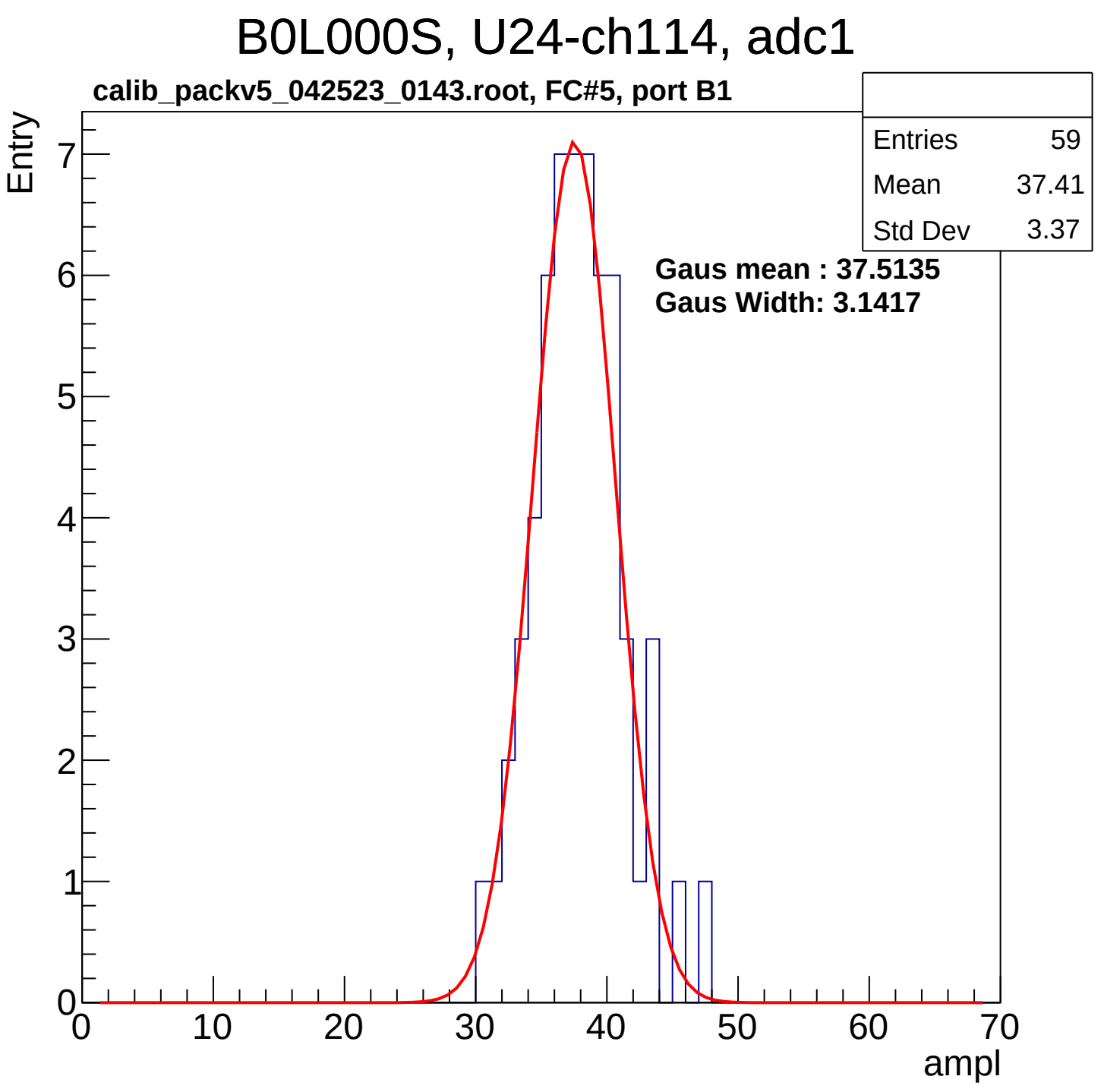
B0L000S, U24-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.41
Std Dev 3.37
Gaus mean : 37.5135
Gaus Width: 3.1417
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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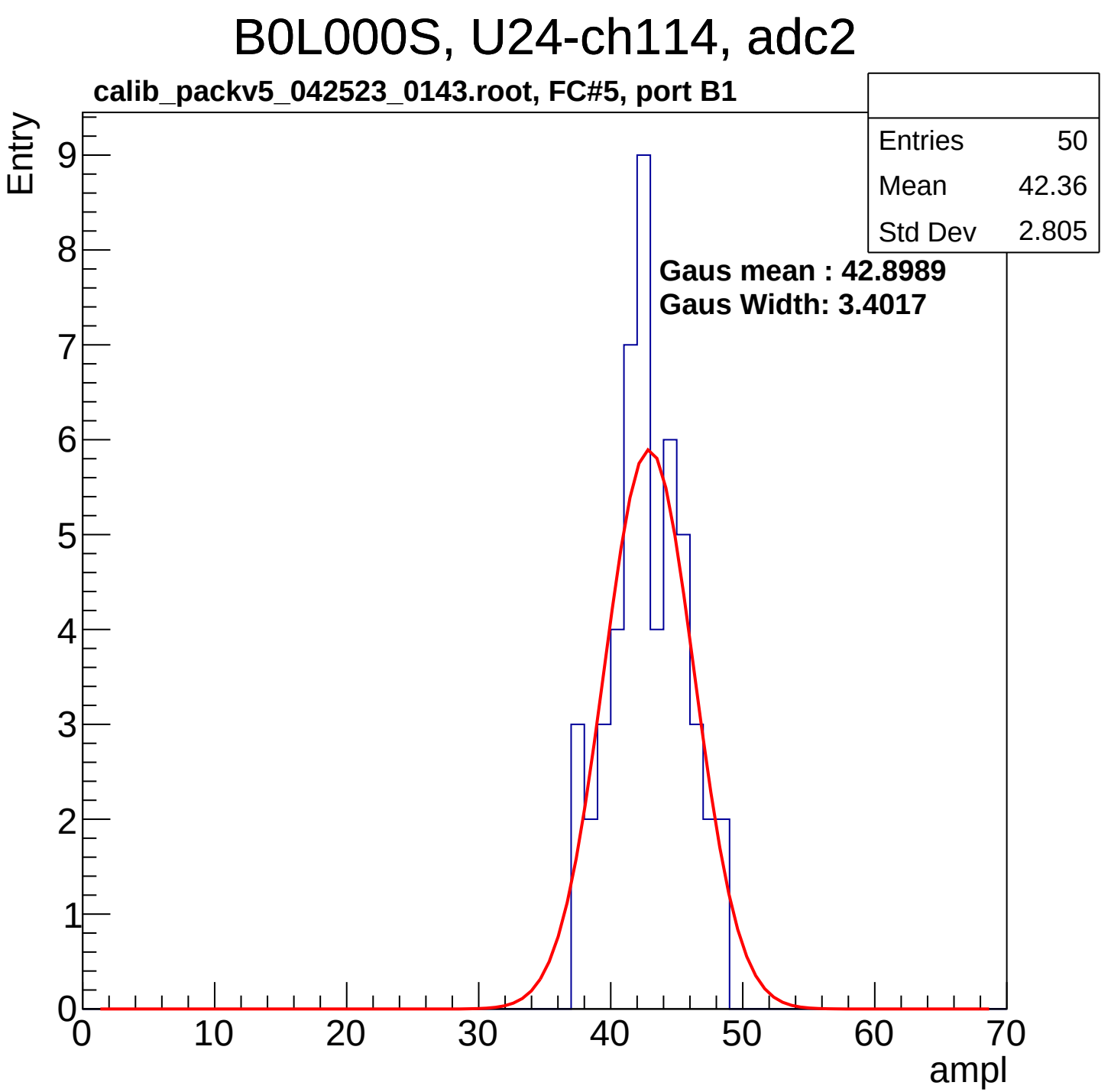
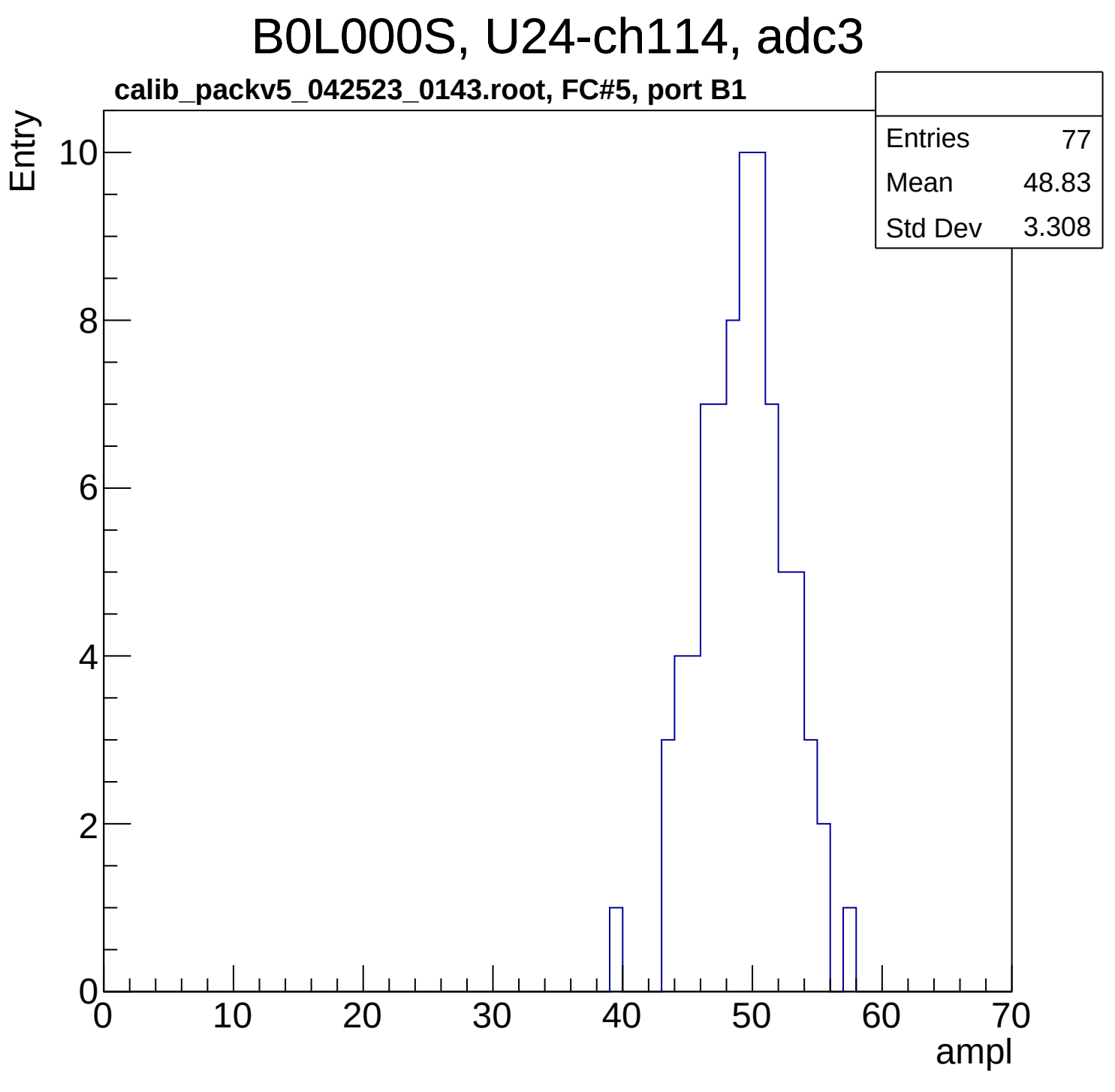
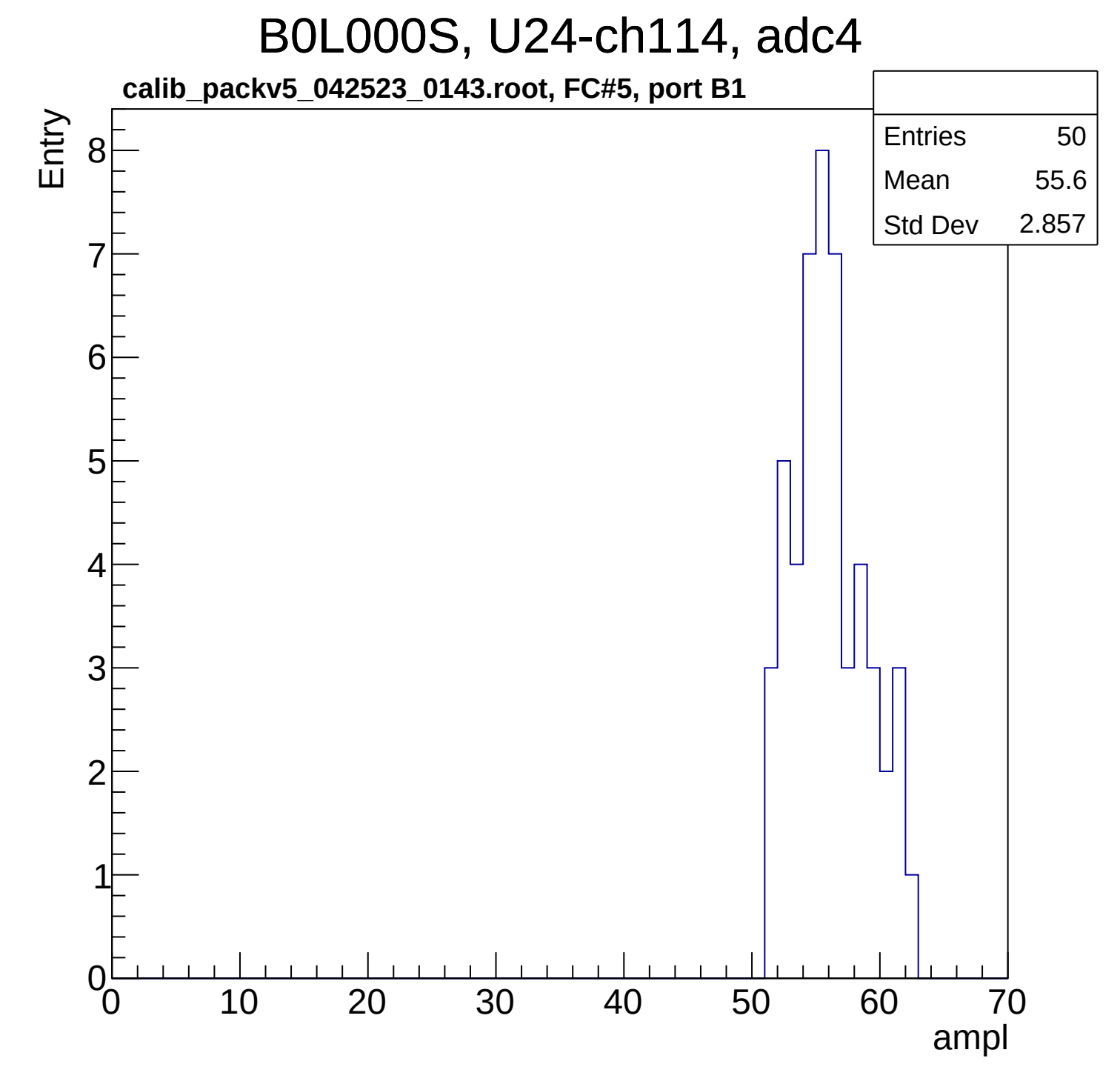
50
60
70


B0L000S, U24-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.36
Std Dev 2.805
Gaus mean : 42.8989
Gaus Width: 3.4017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 48.83
Std Dev 3.308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.6
Std Dev 2.857
Entry
ampl
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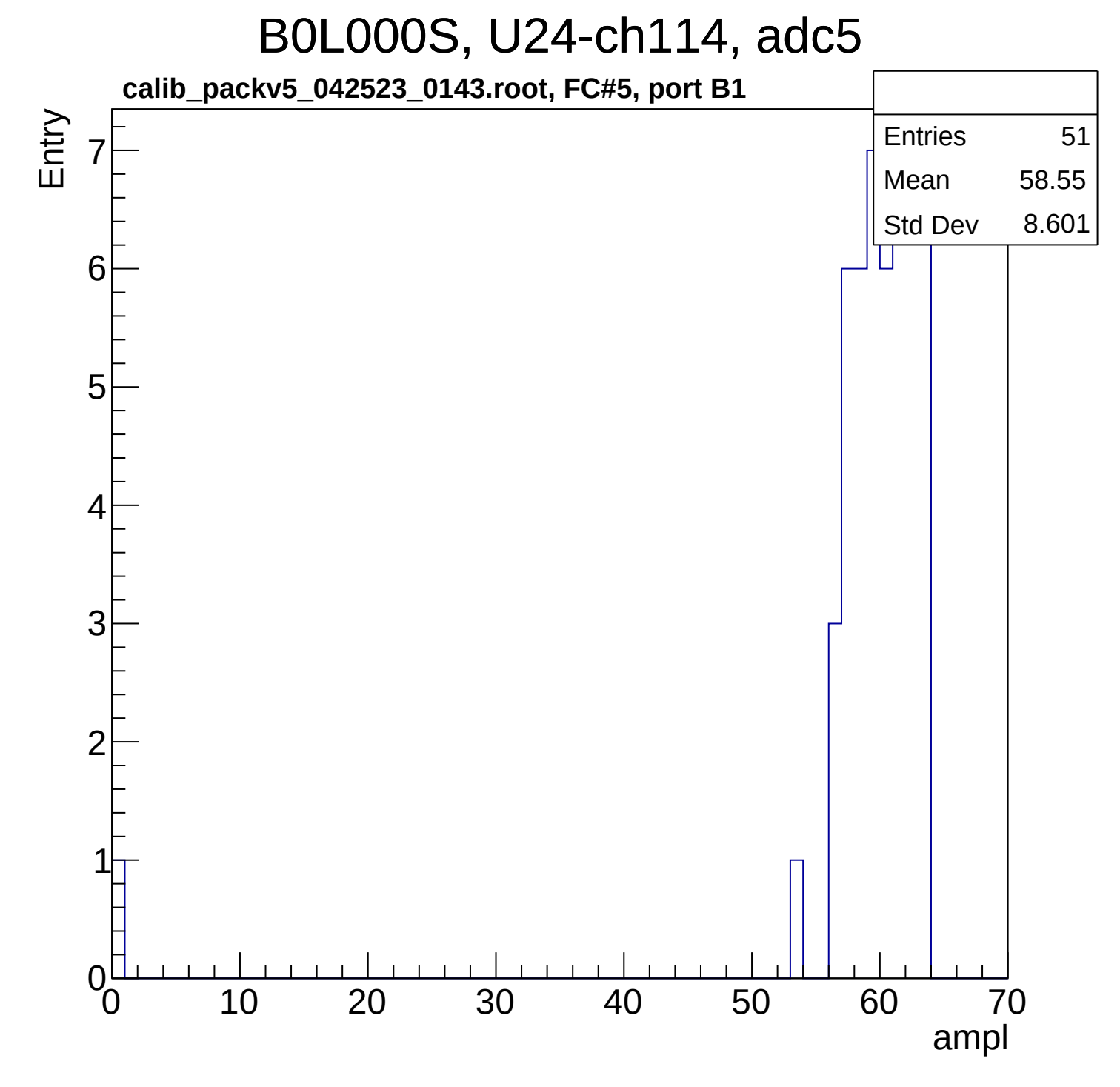
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.55
Std Dev 8.601
Entry
ampl
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1
2
3
4
5
6
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0
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20
30
40
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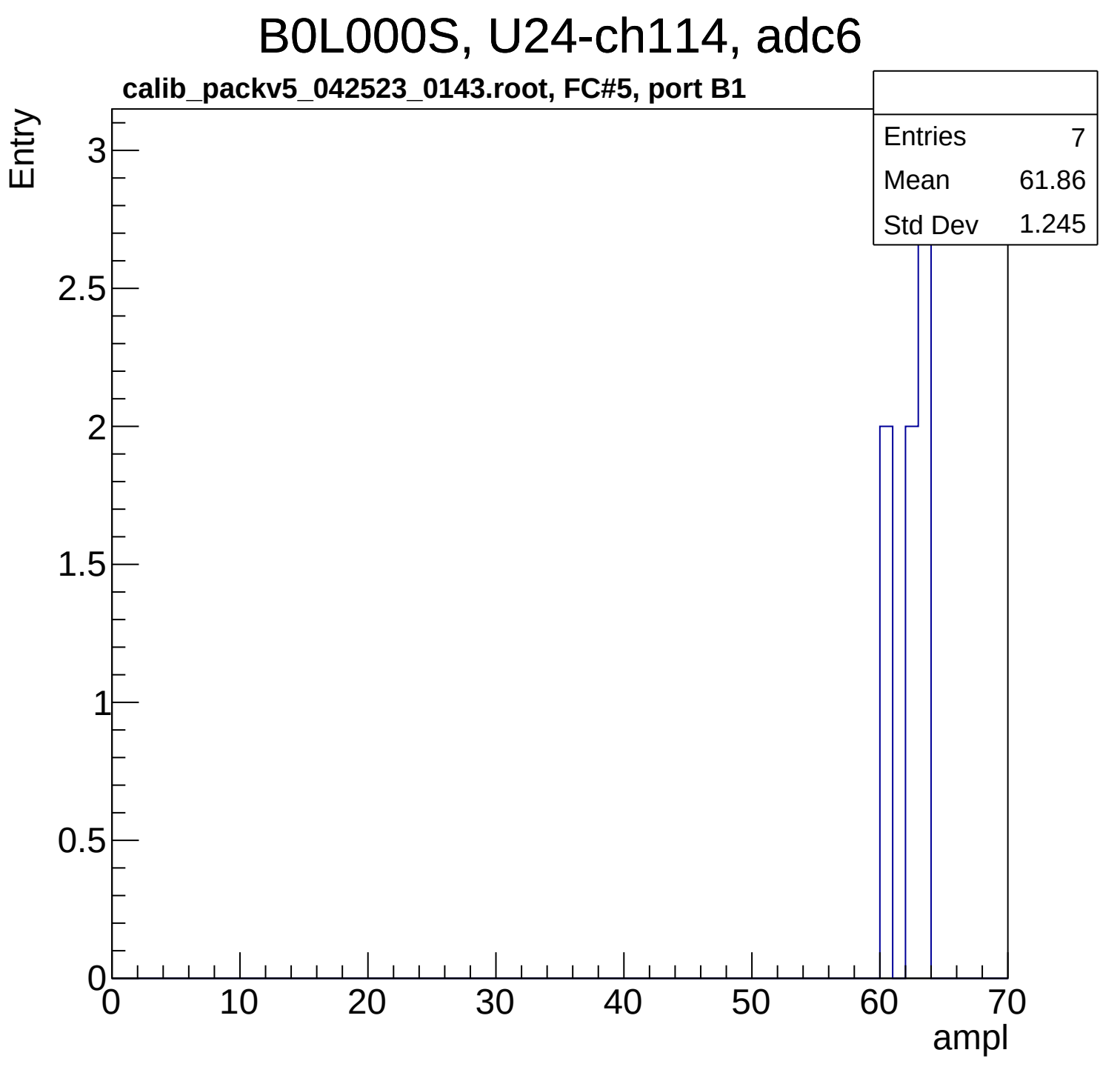
60
70


B0L000S, U24-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.86
Std Dev 1.245
Entry
ampl
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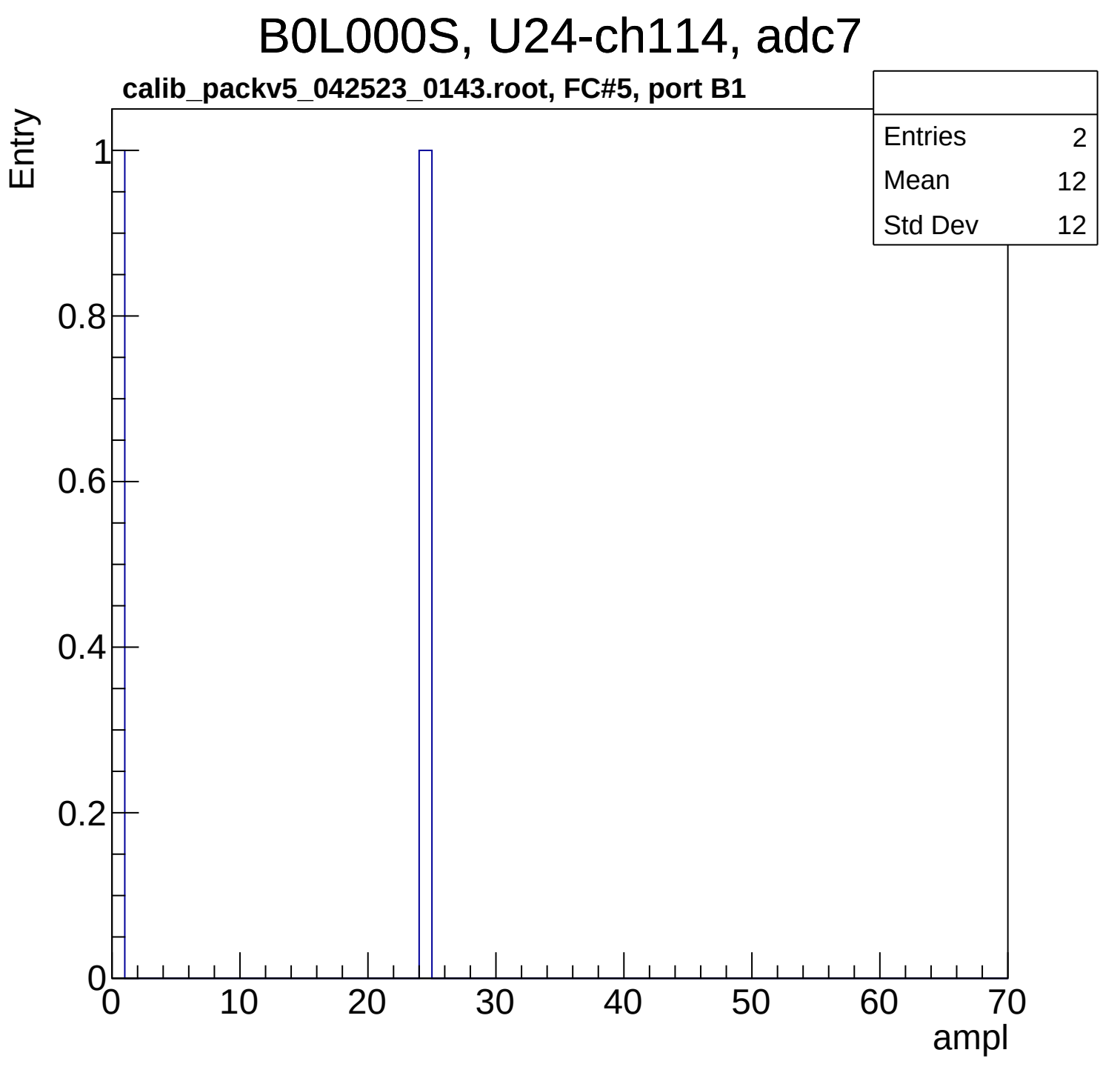
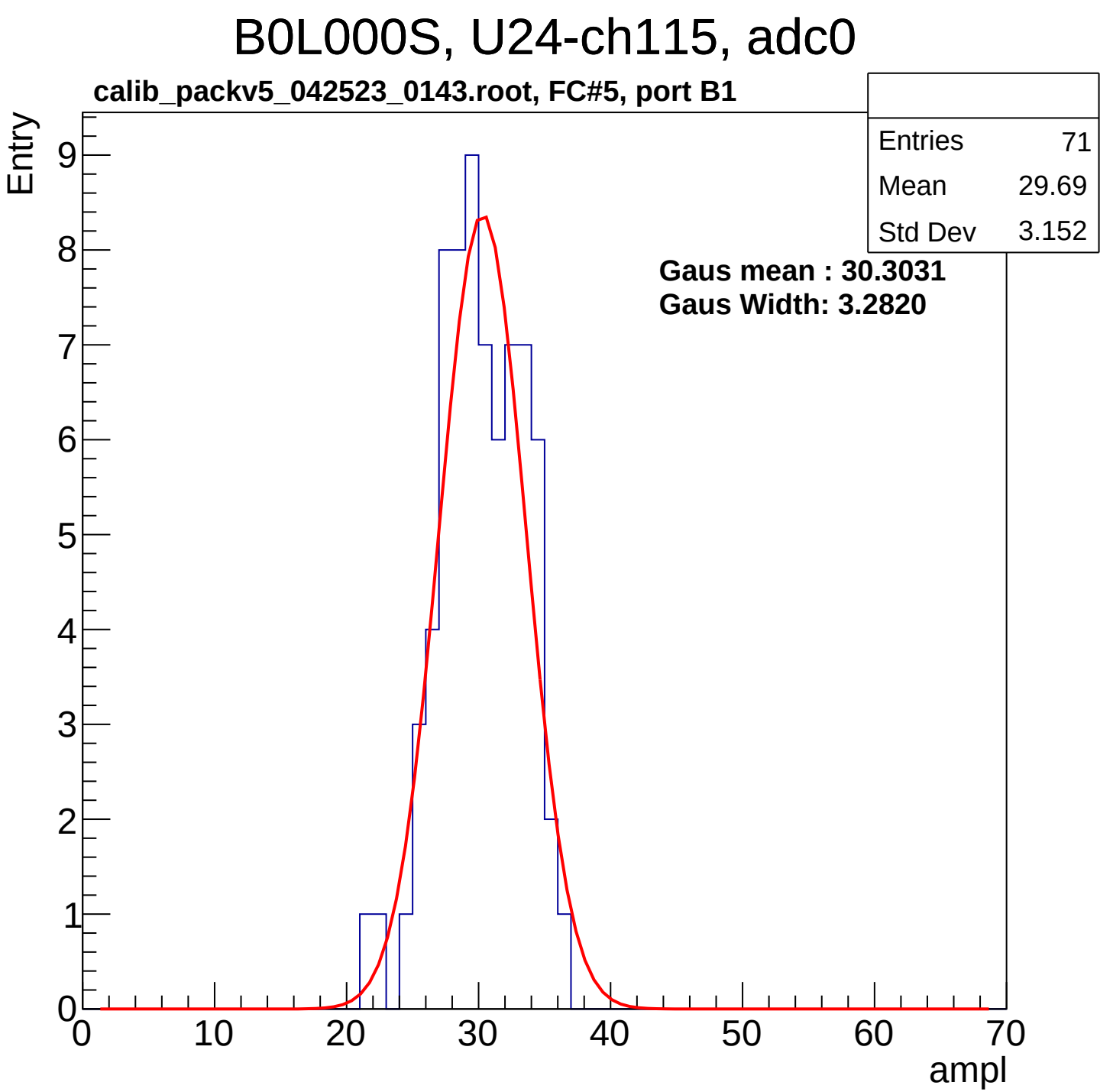
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.69
Std Dev 3.152
Gaus mean : 30.3031
Gaus Width: 3.2820
Entry
ampl
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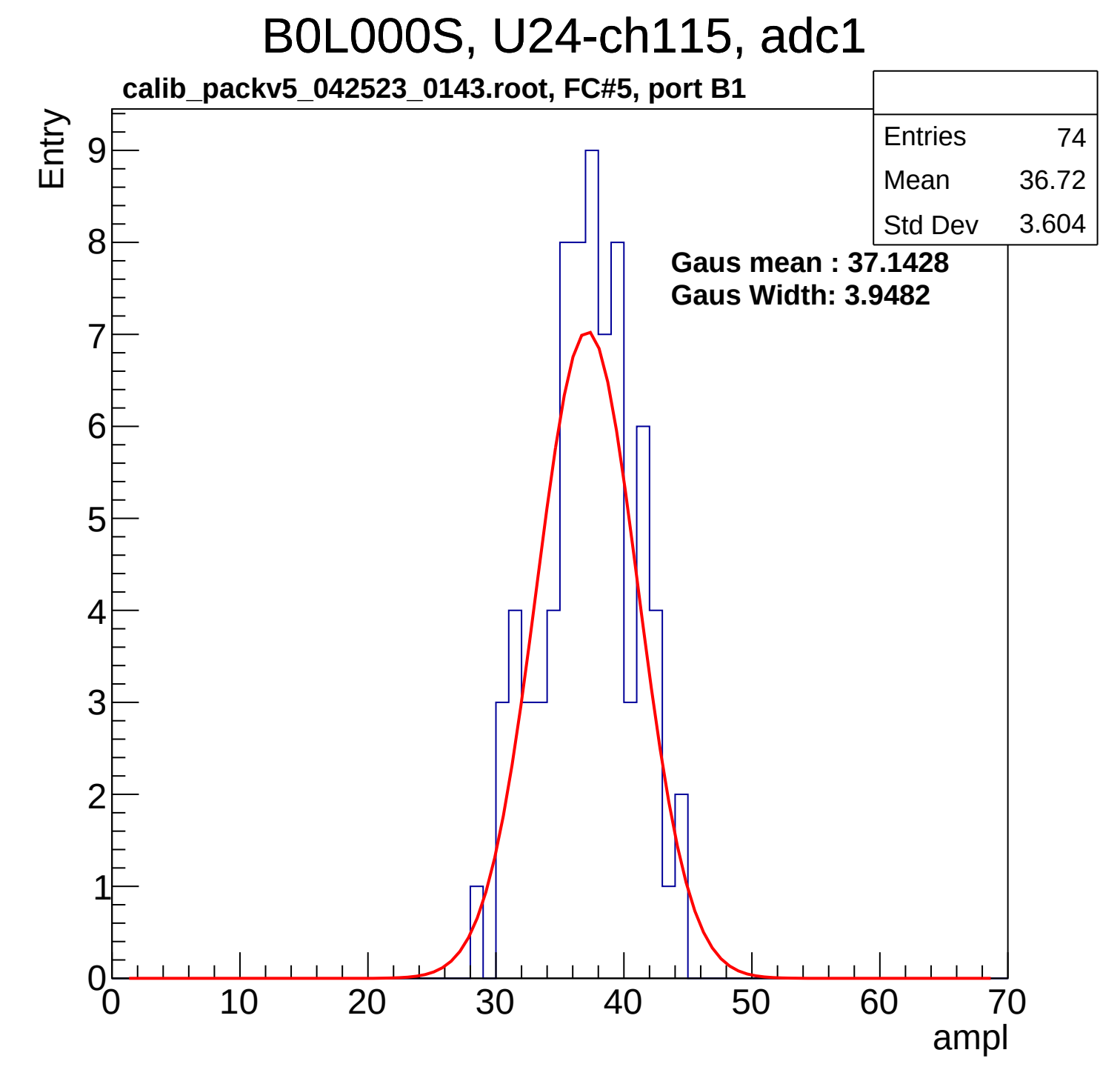
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.72
Std Dev 3.604
Gaus mean : 37.1428
Gaus Width: 3.9482
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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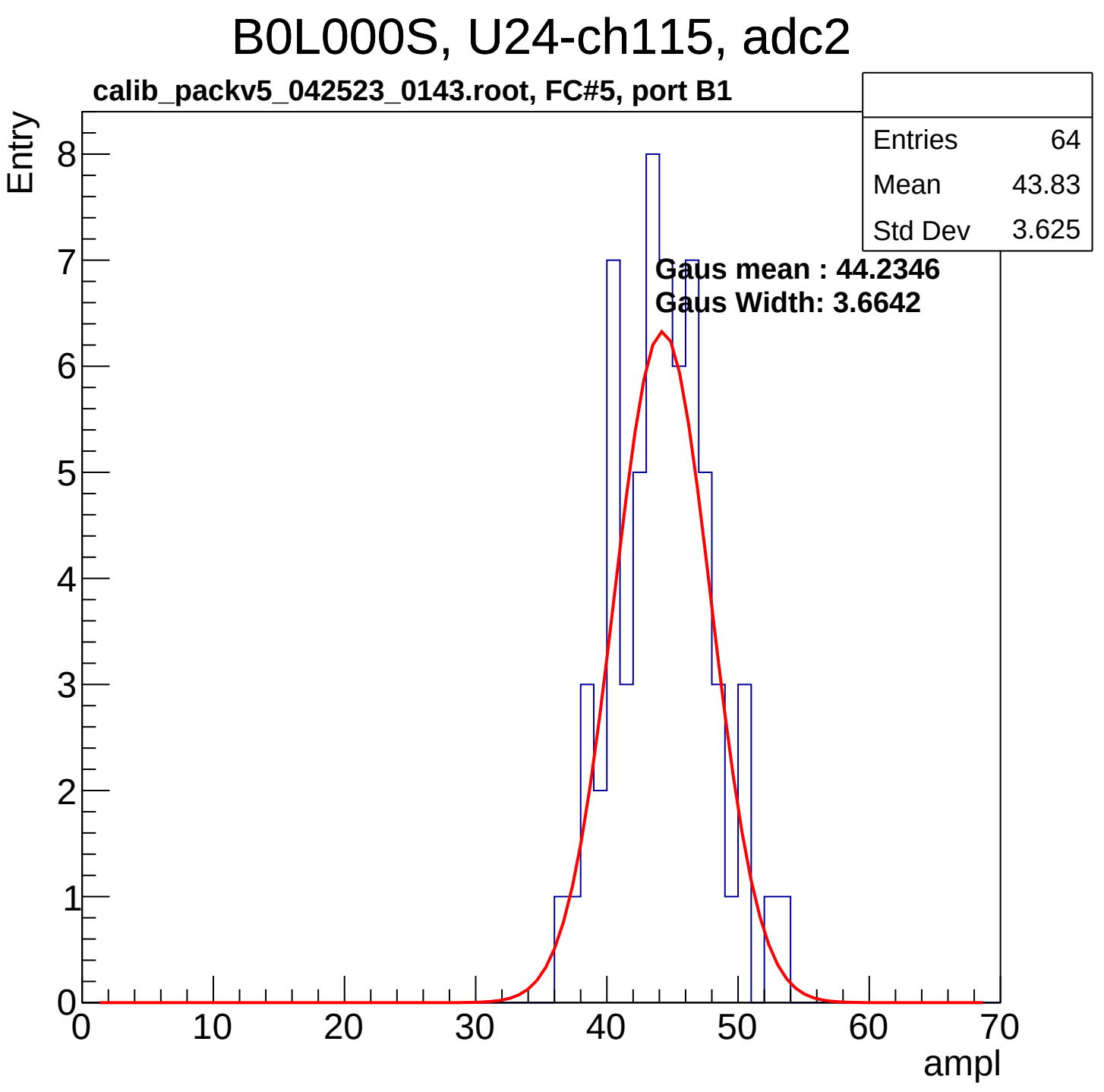
B0L000S, U24-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.83
Std Dev 3.625
Gaus mean : 44.2346
Gaus Width: 3.6642
Entry
ampl
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1
2
3
4
5
6
7
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0
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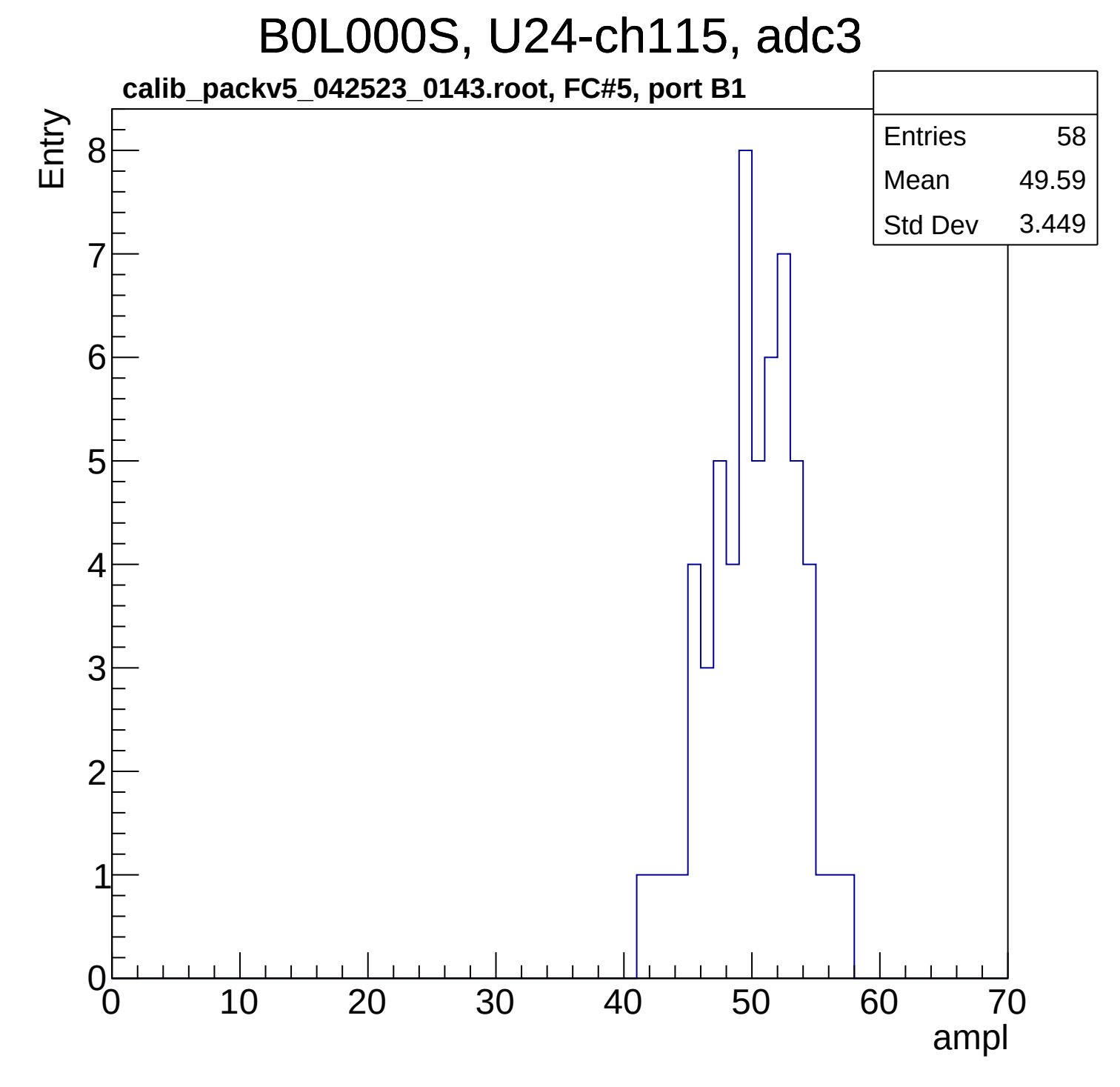
B0L000S, U24-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.59
Std Dev 3.449
Entry
ampl
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1
2
3
4
5
6
7
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40
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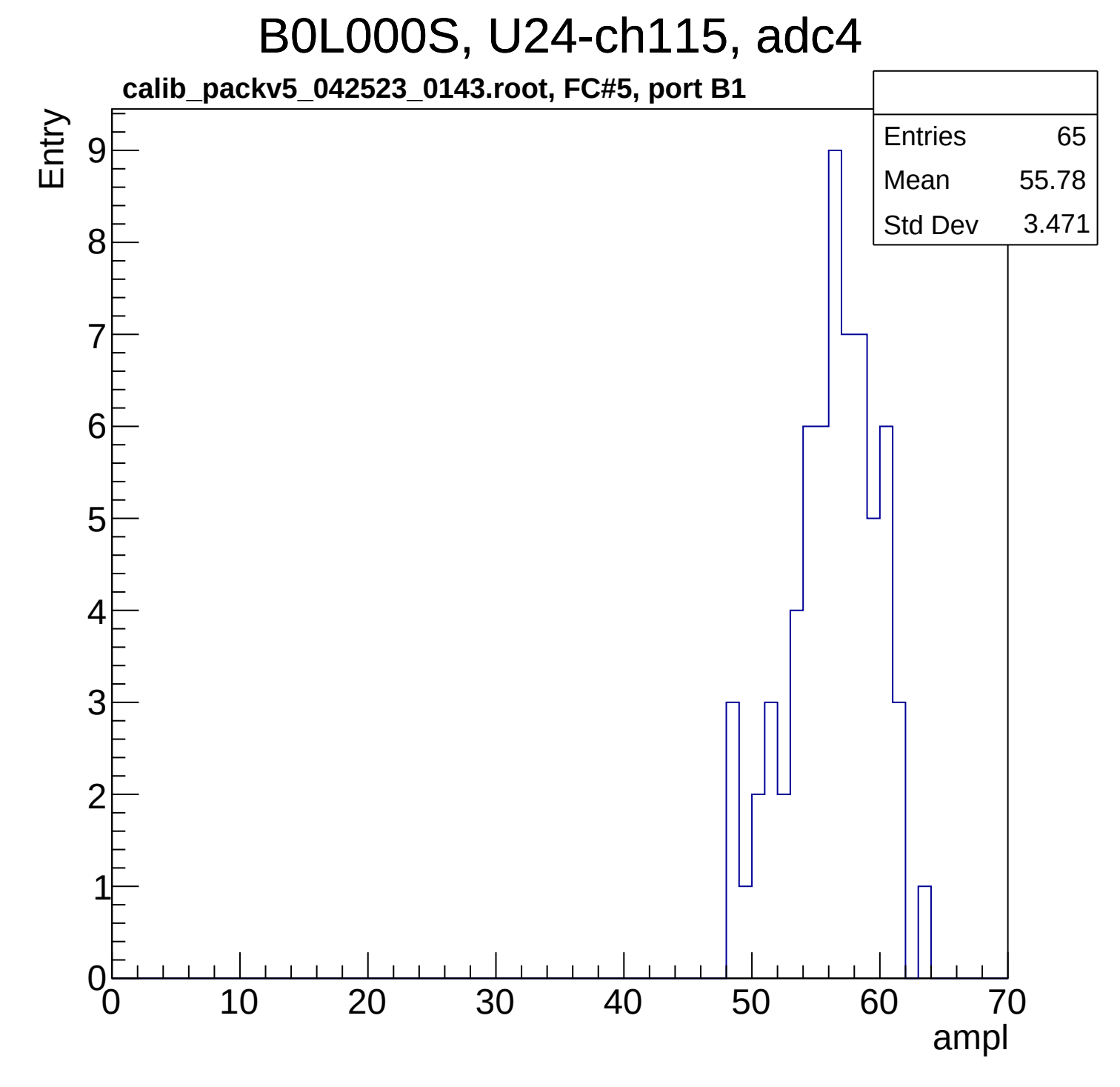
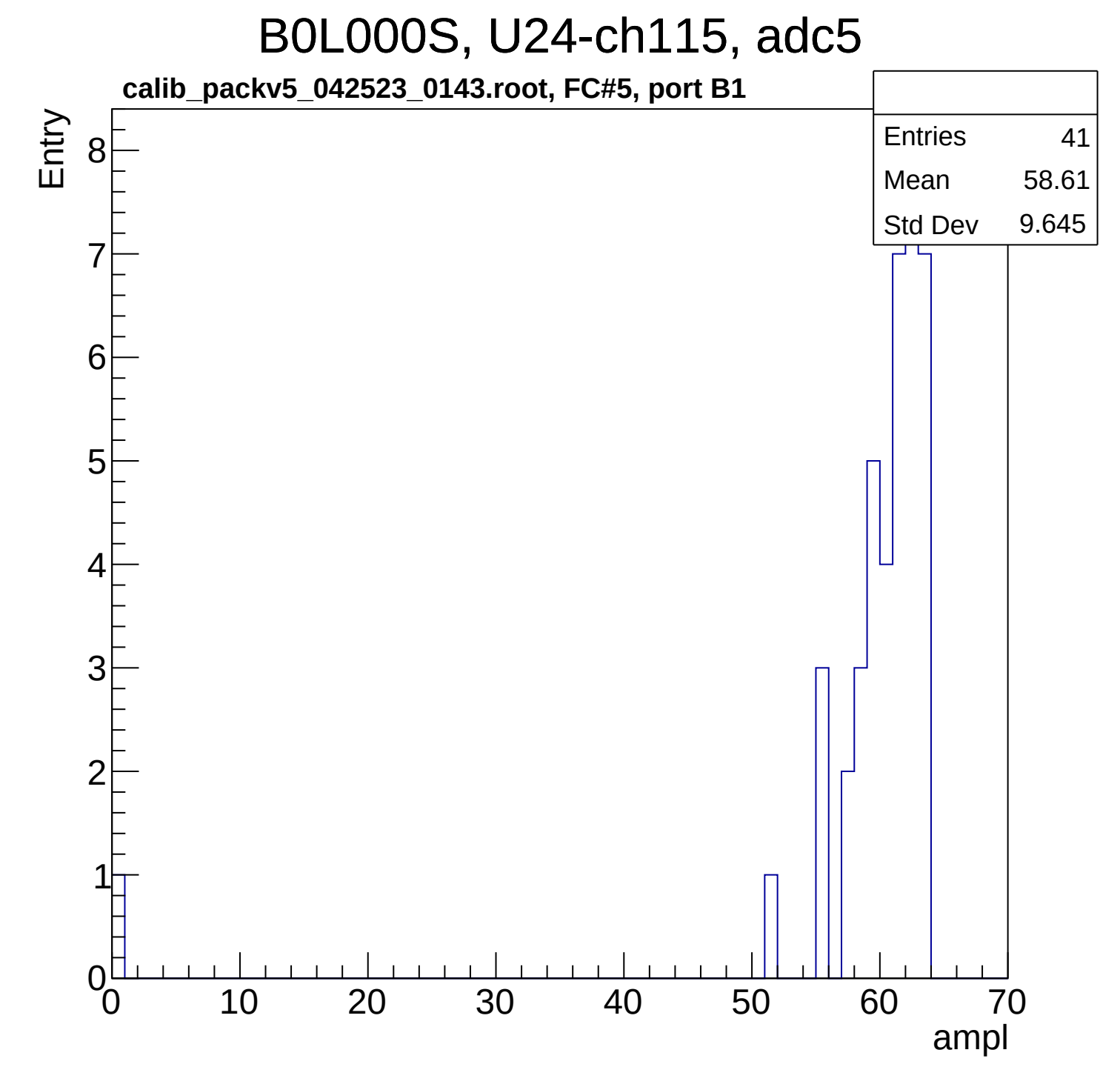

B0L000S, U24-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.78
Std Dev 3.471
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.61
Std Dev 9.645
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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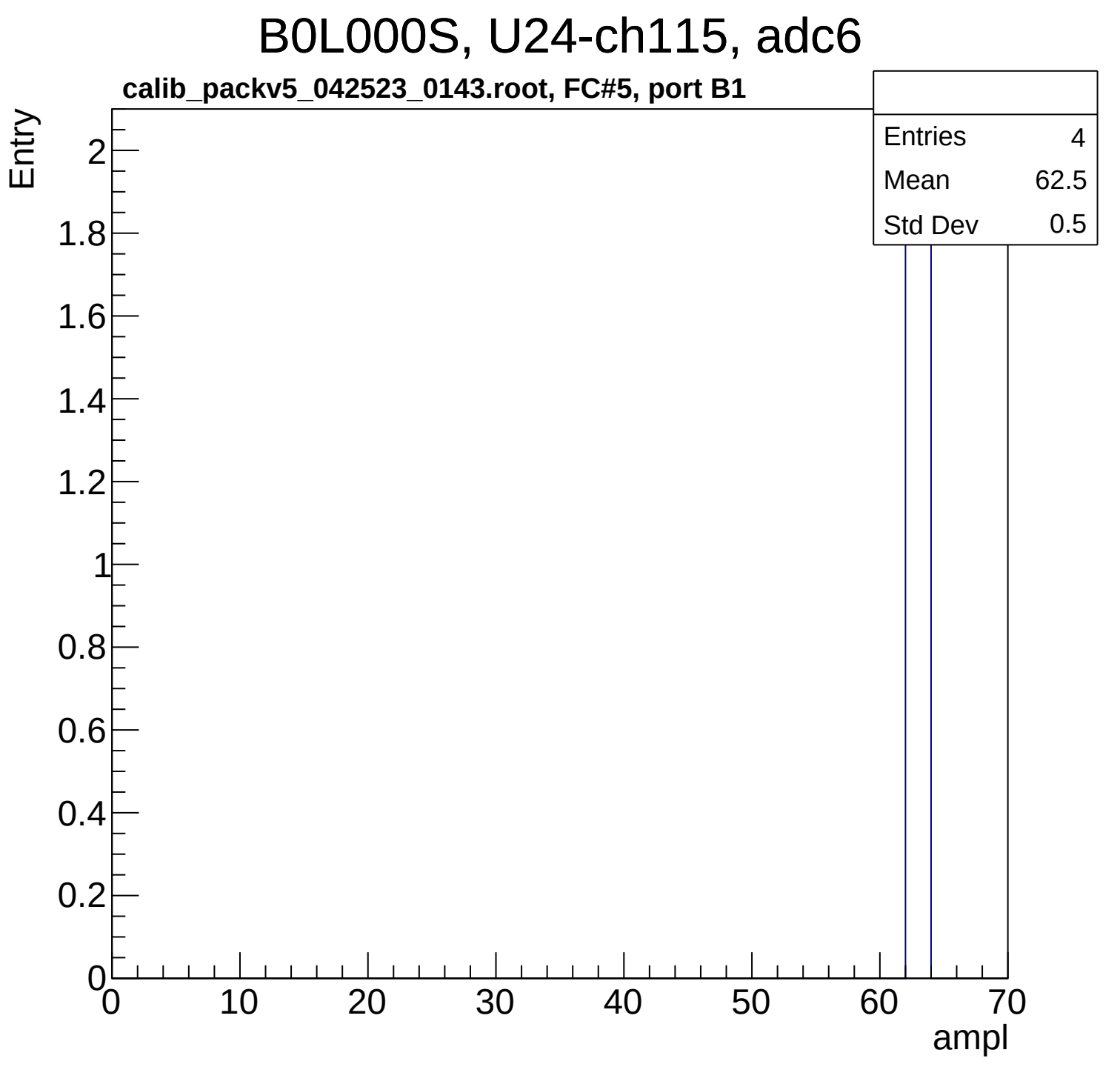
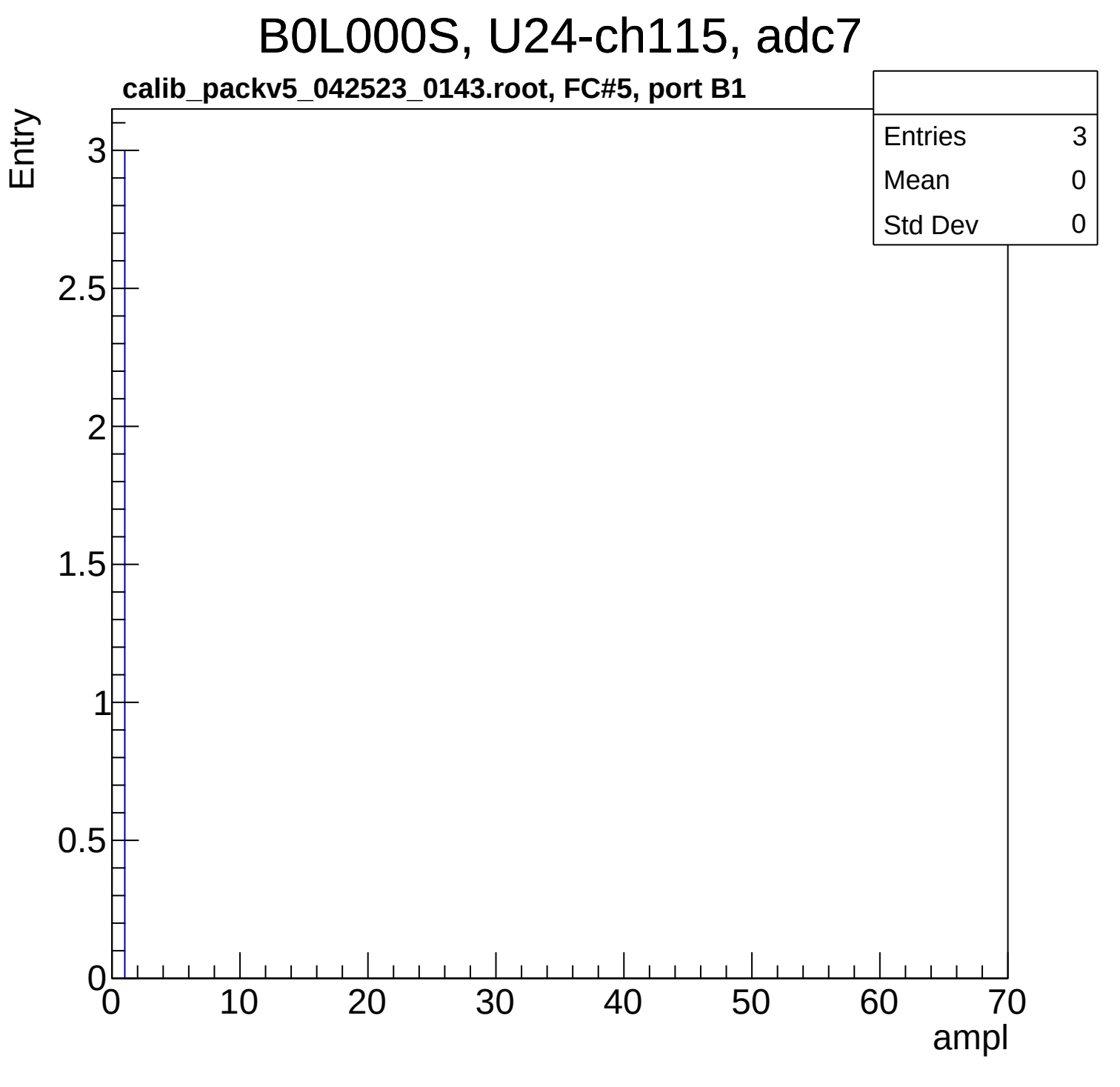
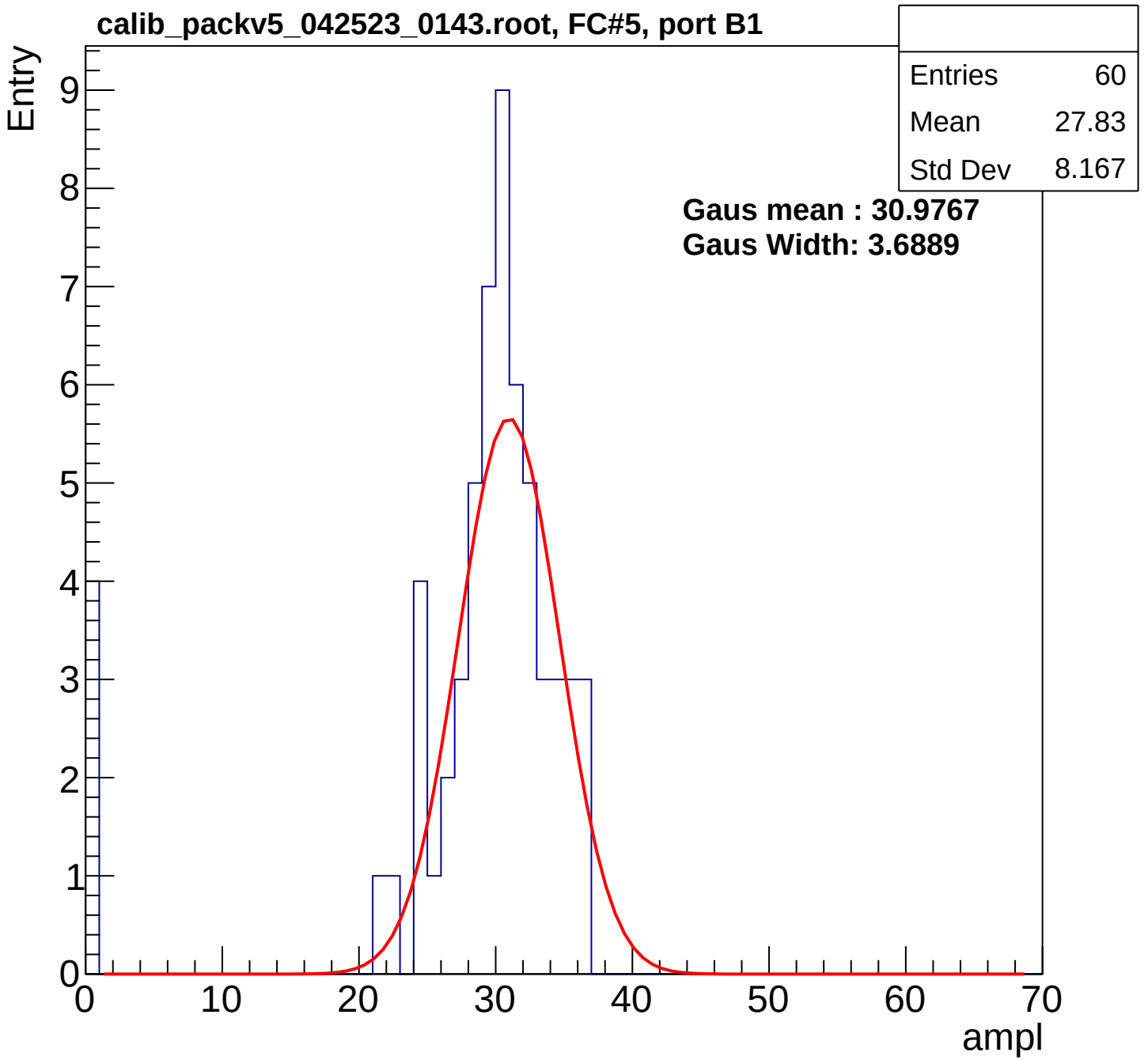
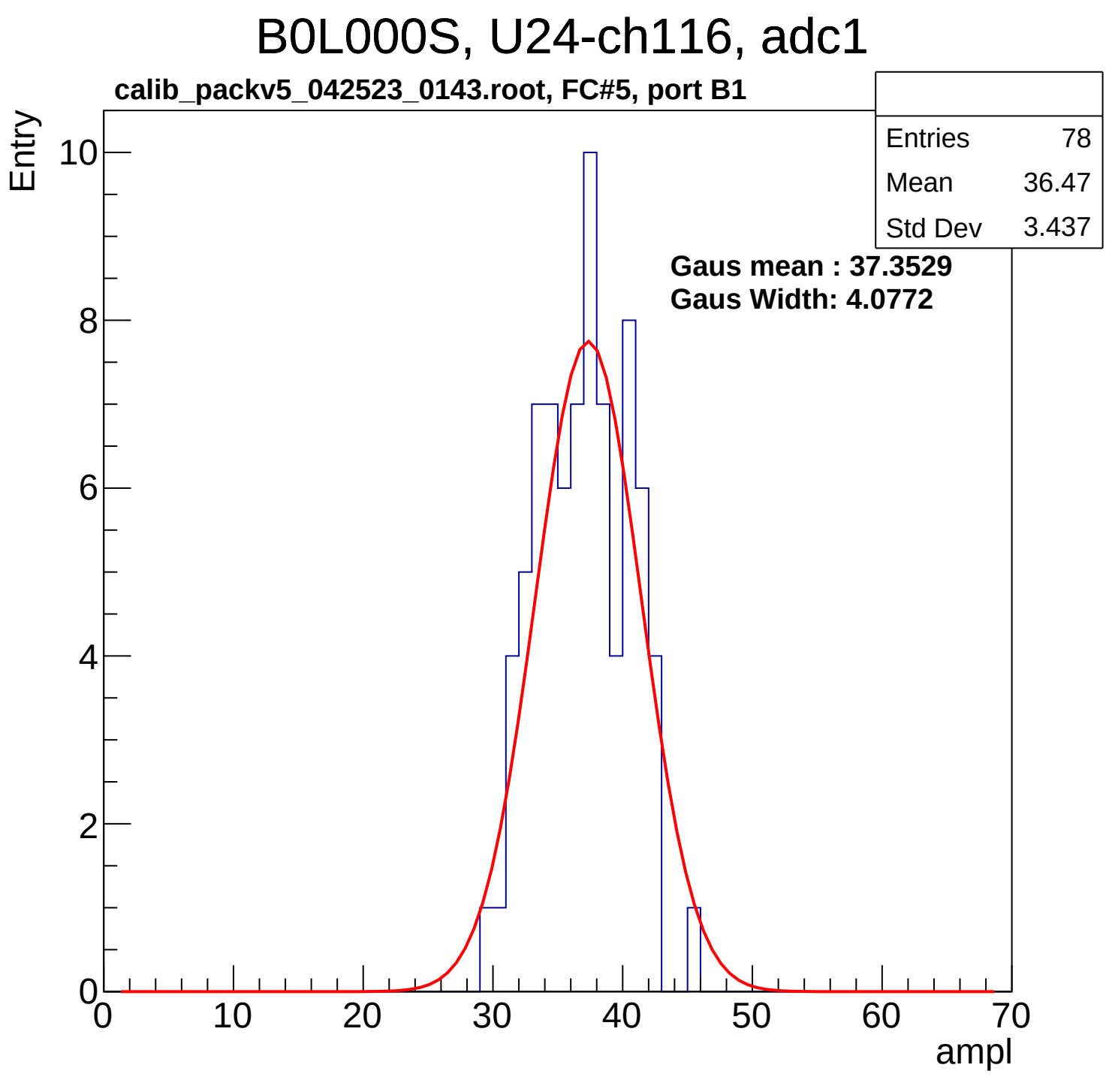
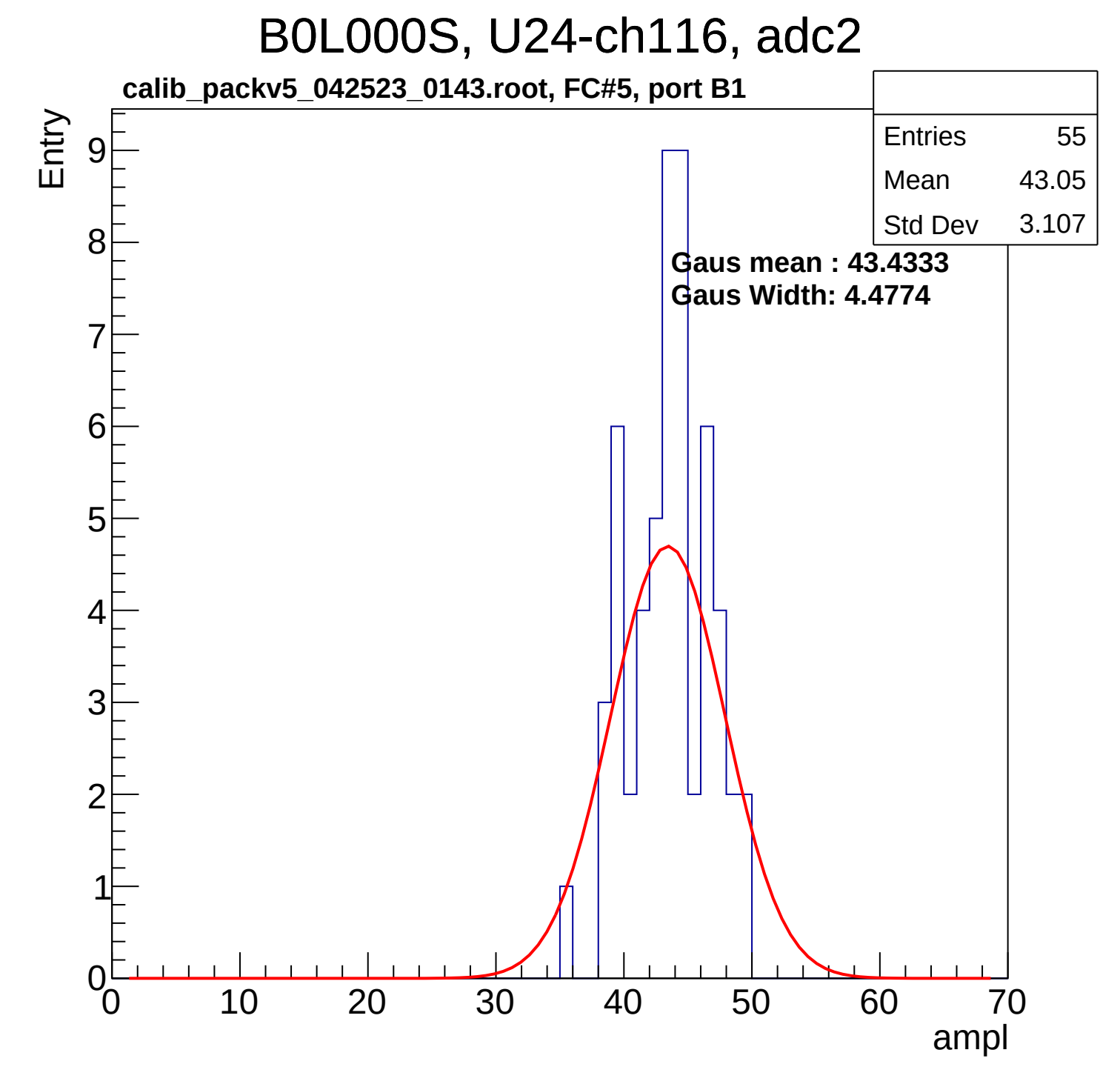
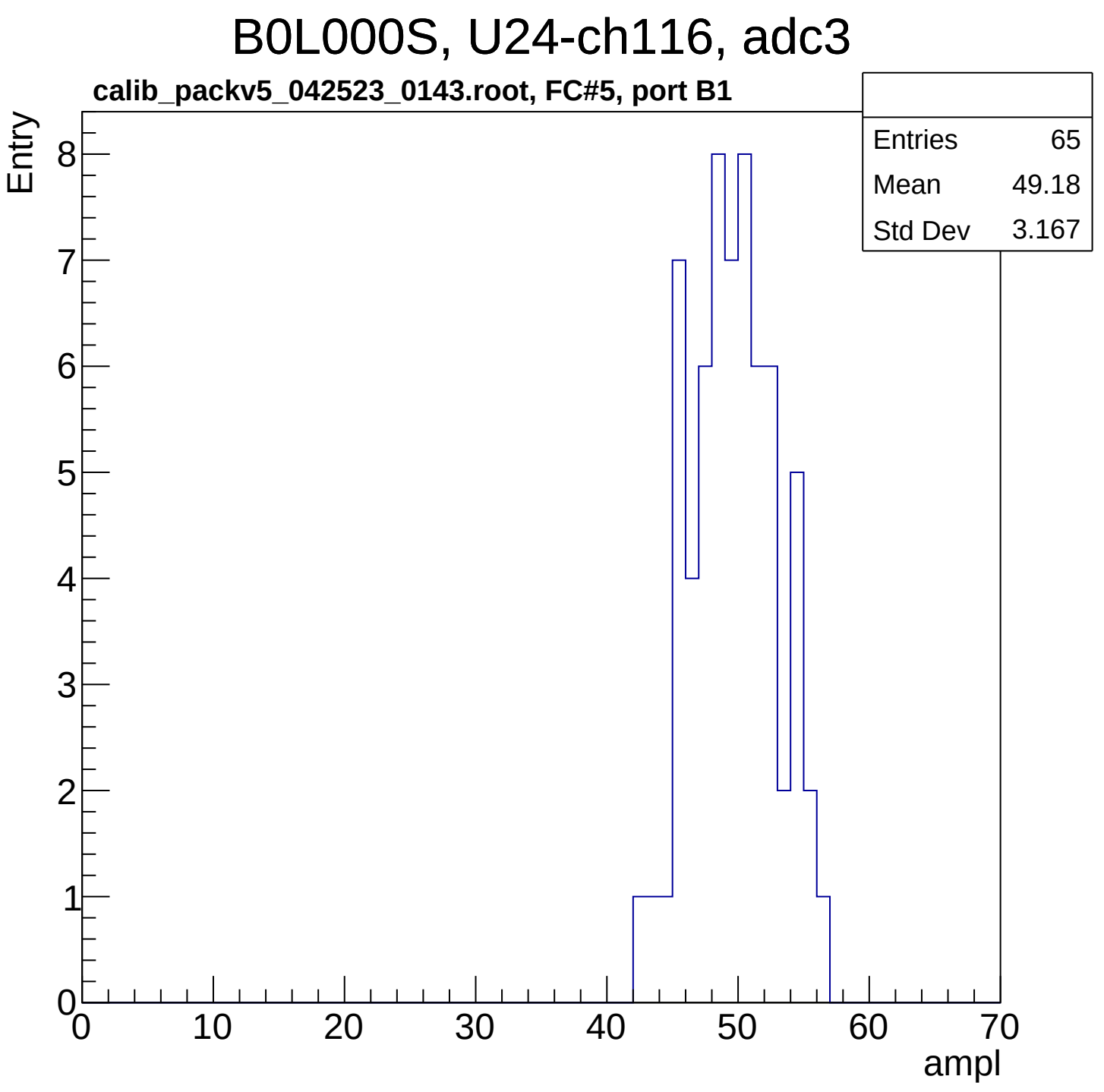

B0L000S, U24-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

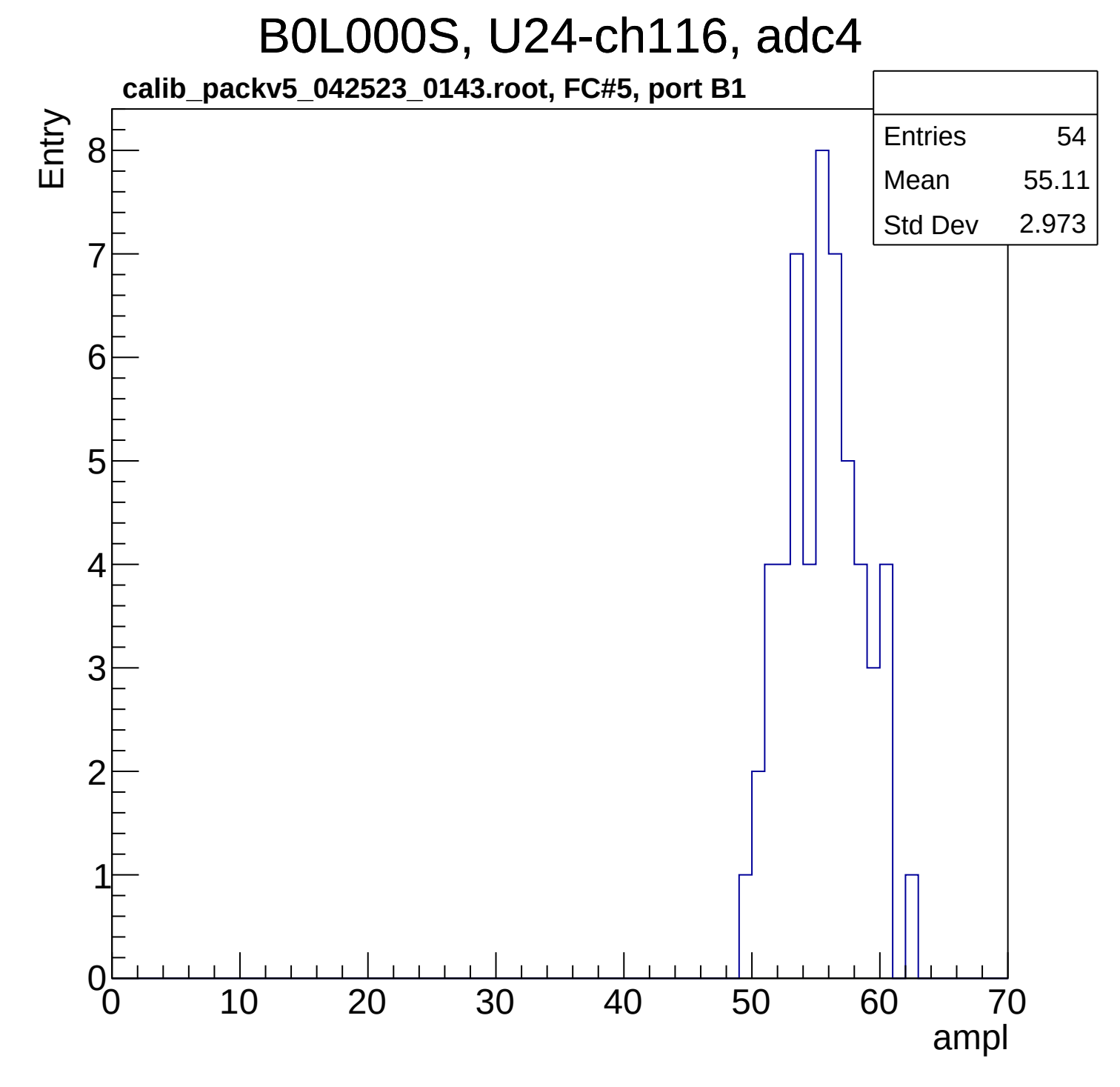

B0L000S, U24-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

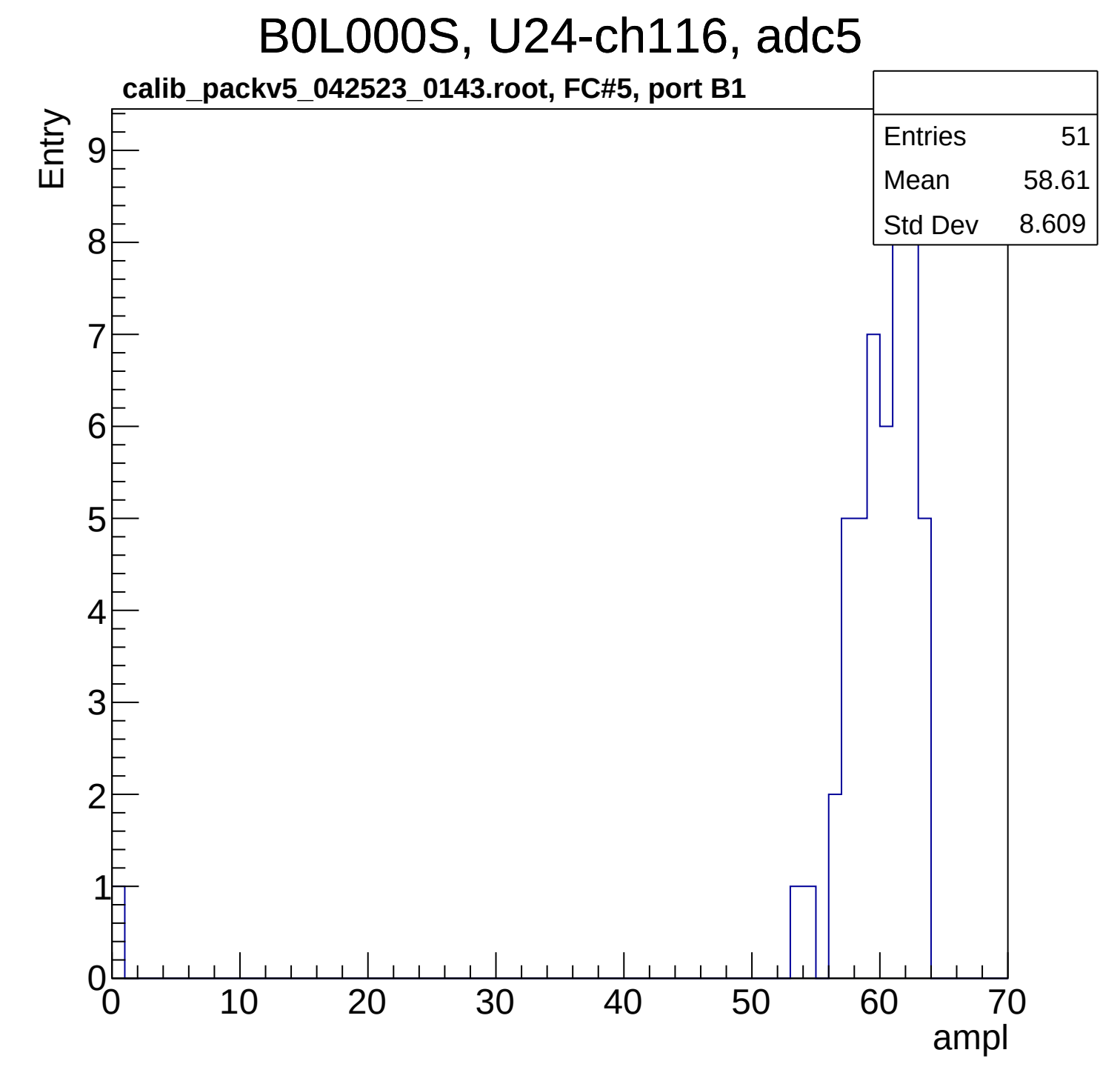

B0L000S, U24-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 27.83
Std Dev 8.167
Gaus mean : 30.9767
Gaus Width: 3.6889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

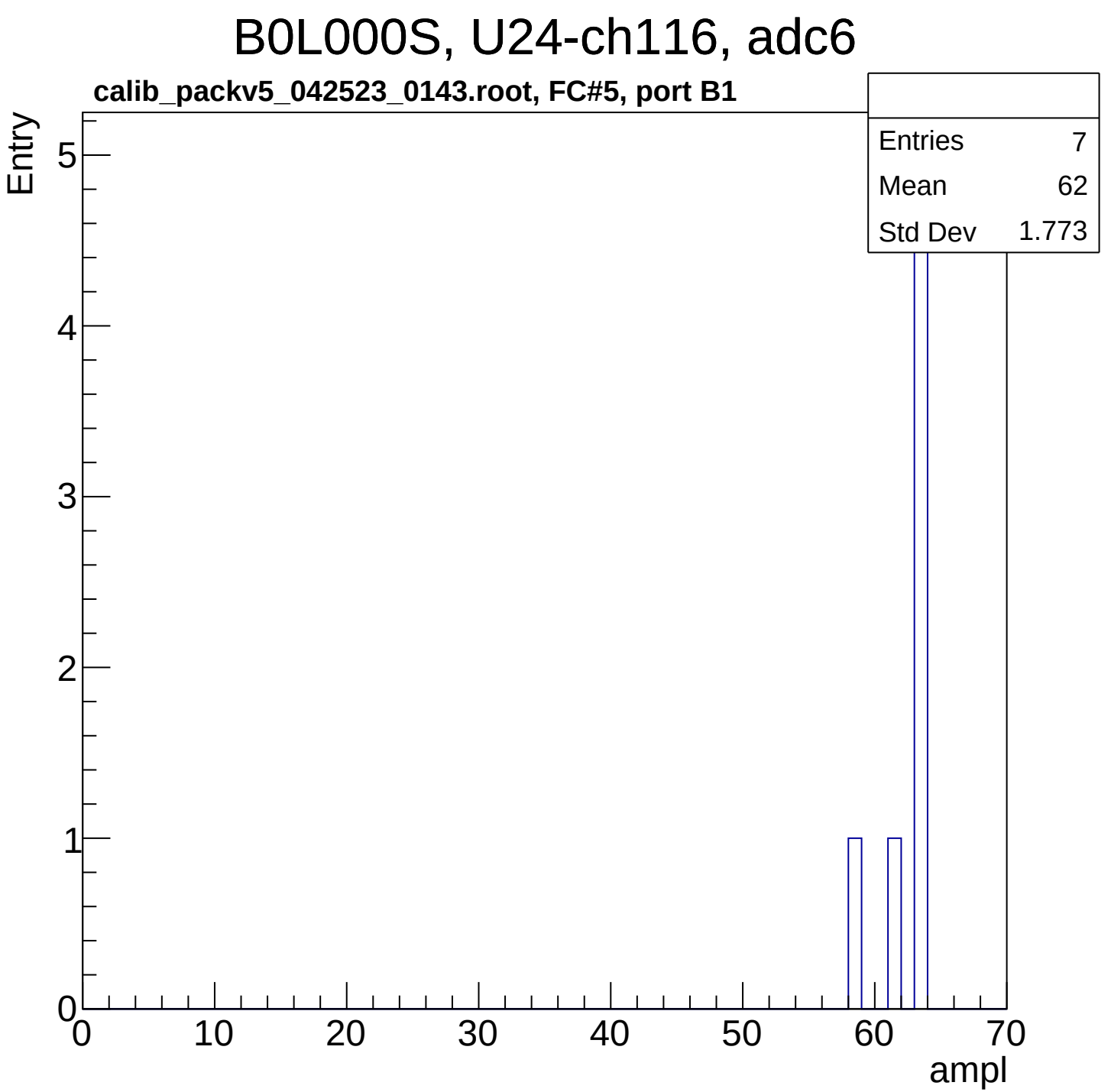

B0L000S, U24-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.47
Std Dev 3.437
Gaus mean : 37.3529
Gaus Width: 4.0772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

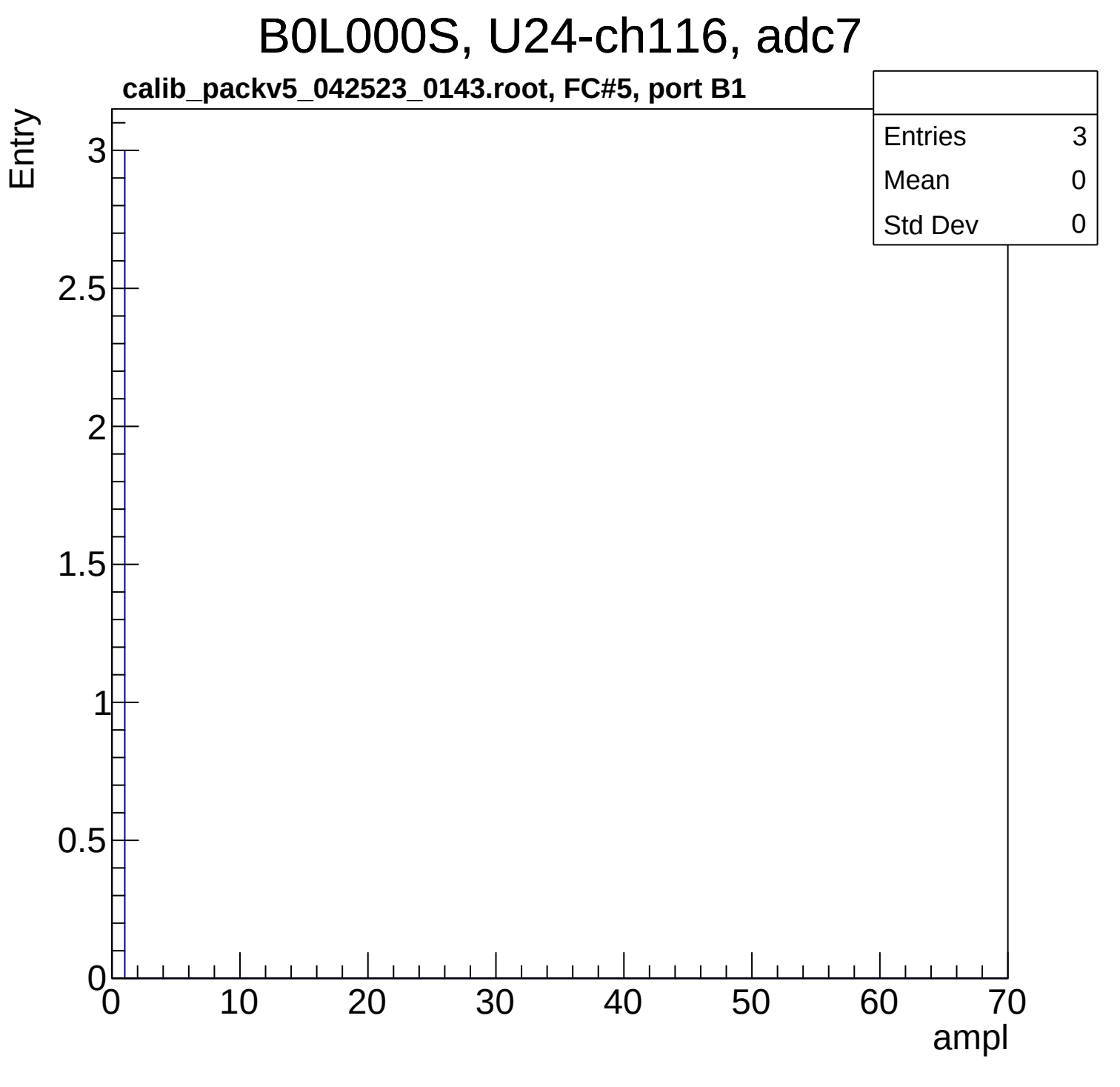

B0L000S, U24-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.05
Std Dev 3.107
Gaus mean : 43.4333
Gaus Width: 4.4774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

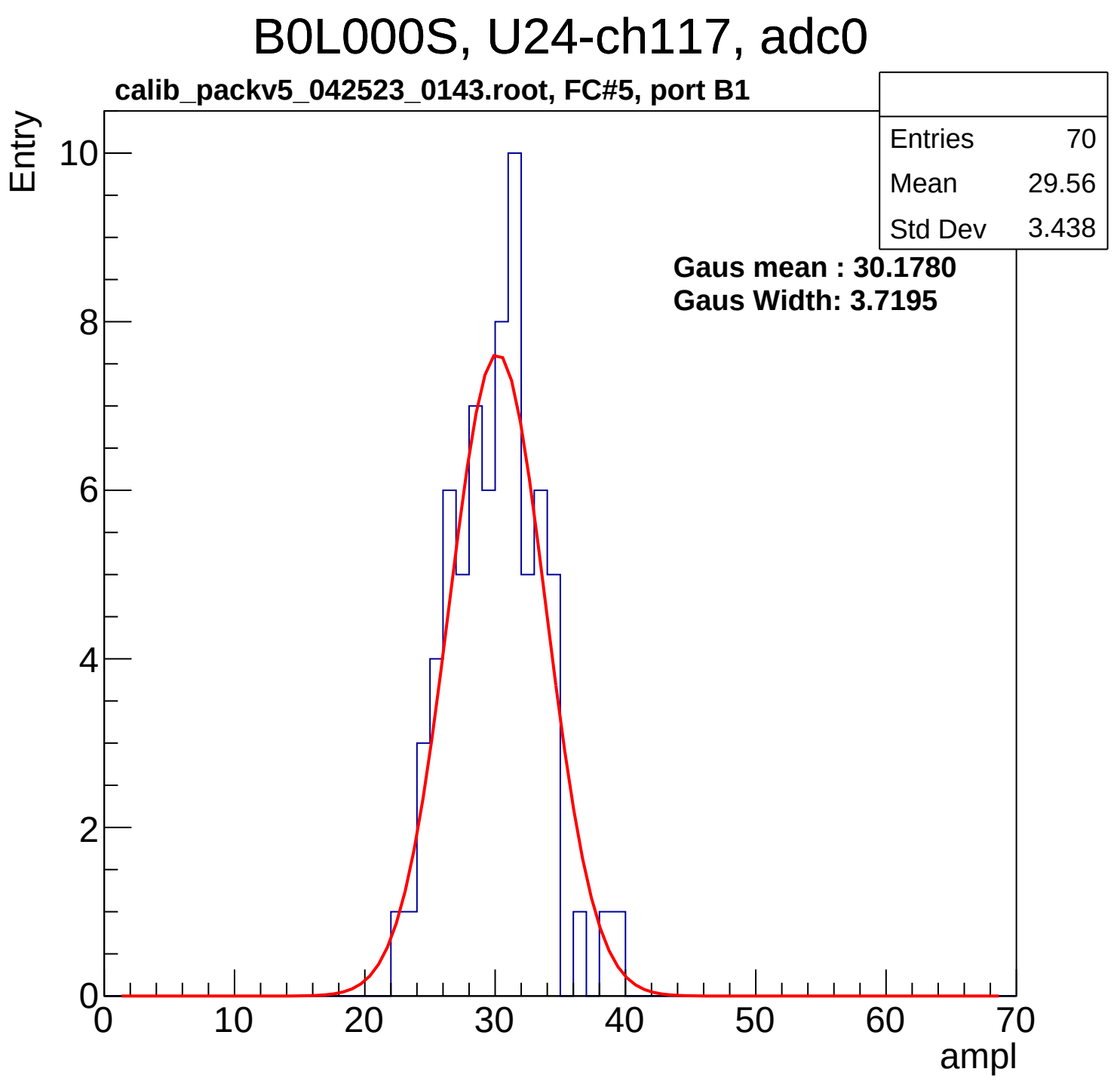

B0L000S, U24-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 49.18
Std Dev 3.167
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

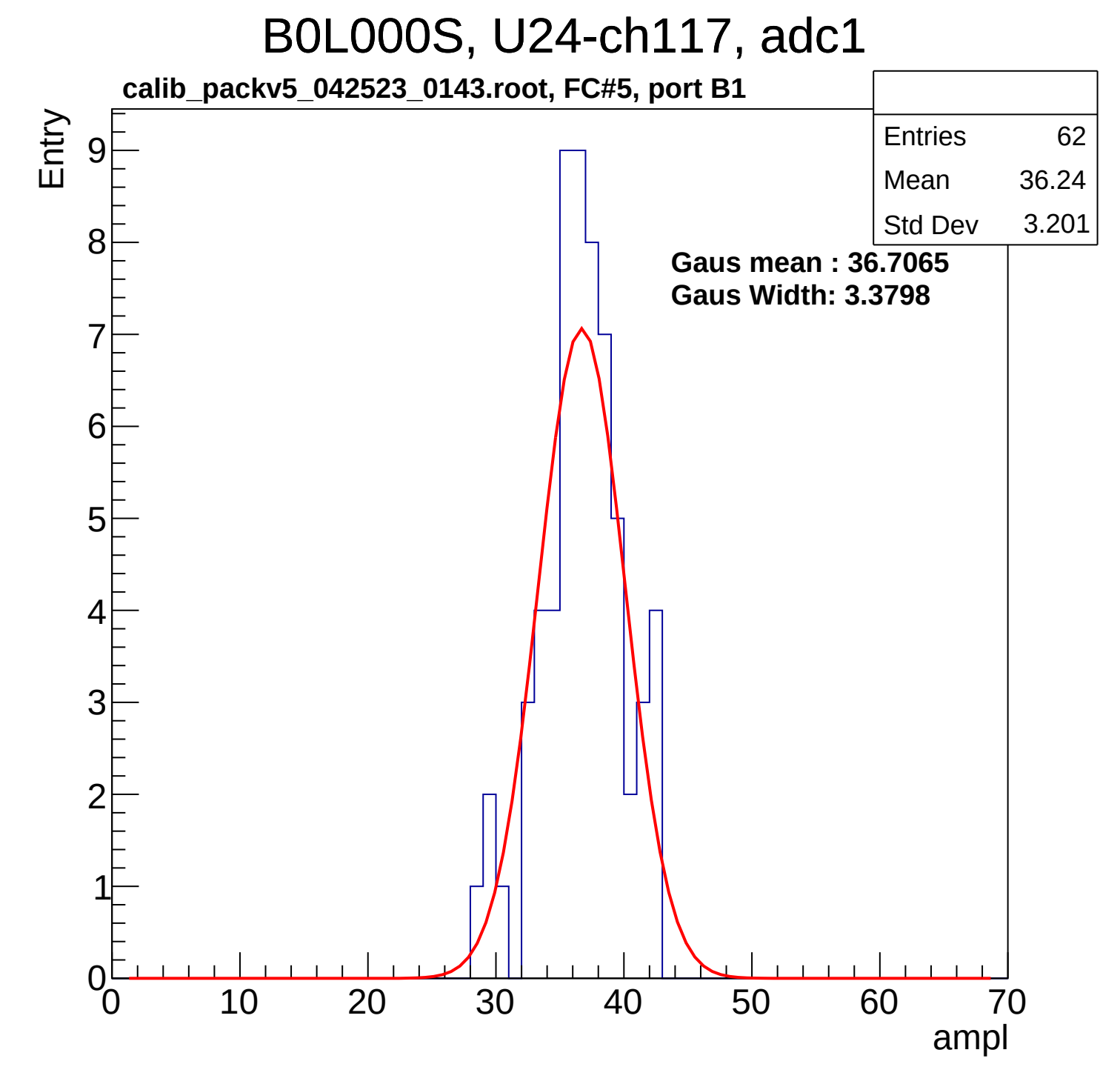

B0L000S, U24-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.11
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

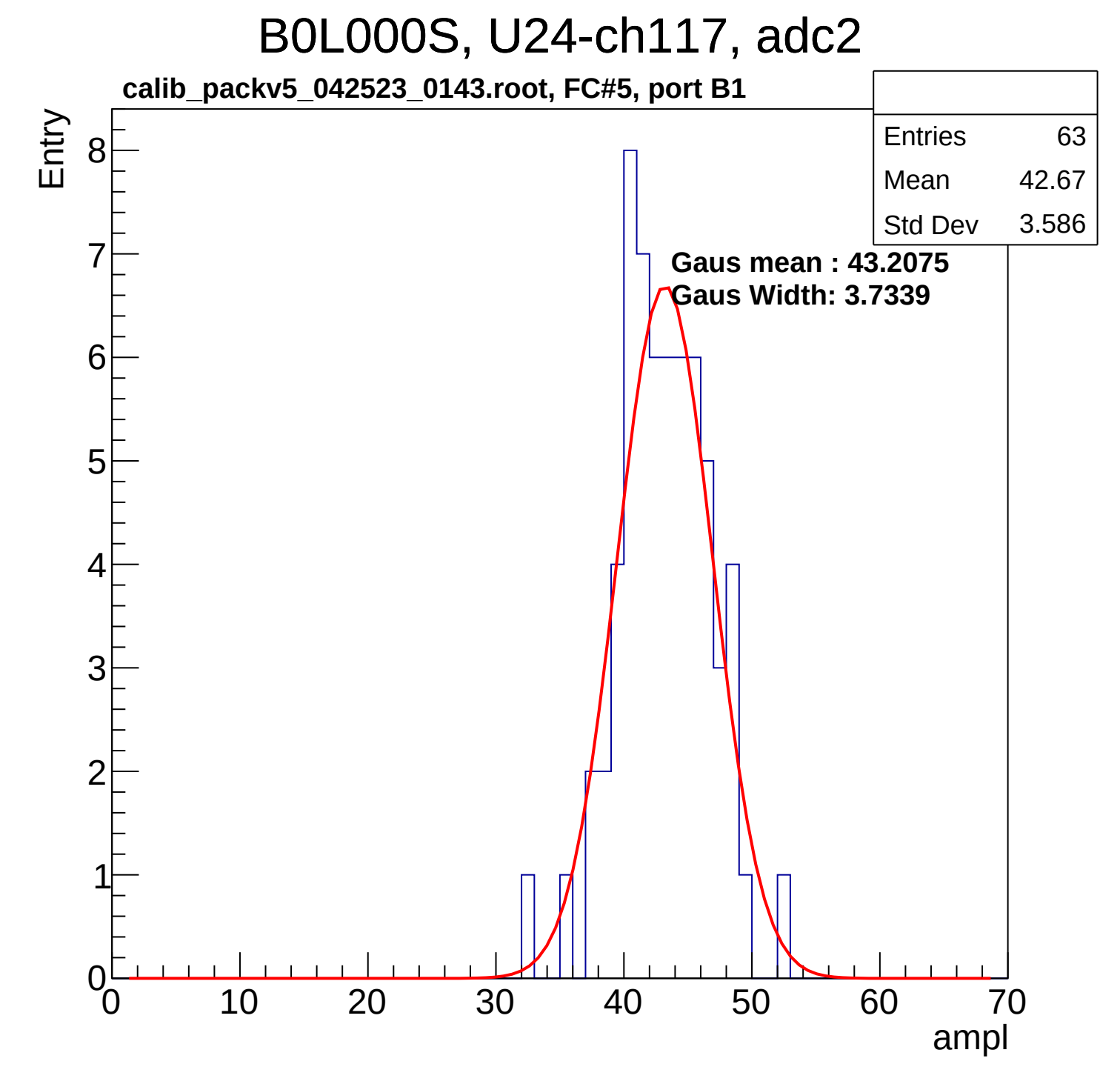

B0L000S, U24-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.61
Std Dev 8.609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

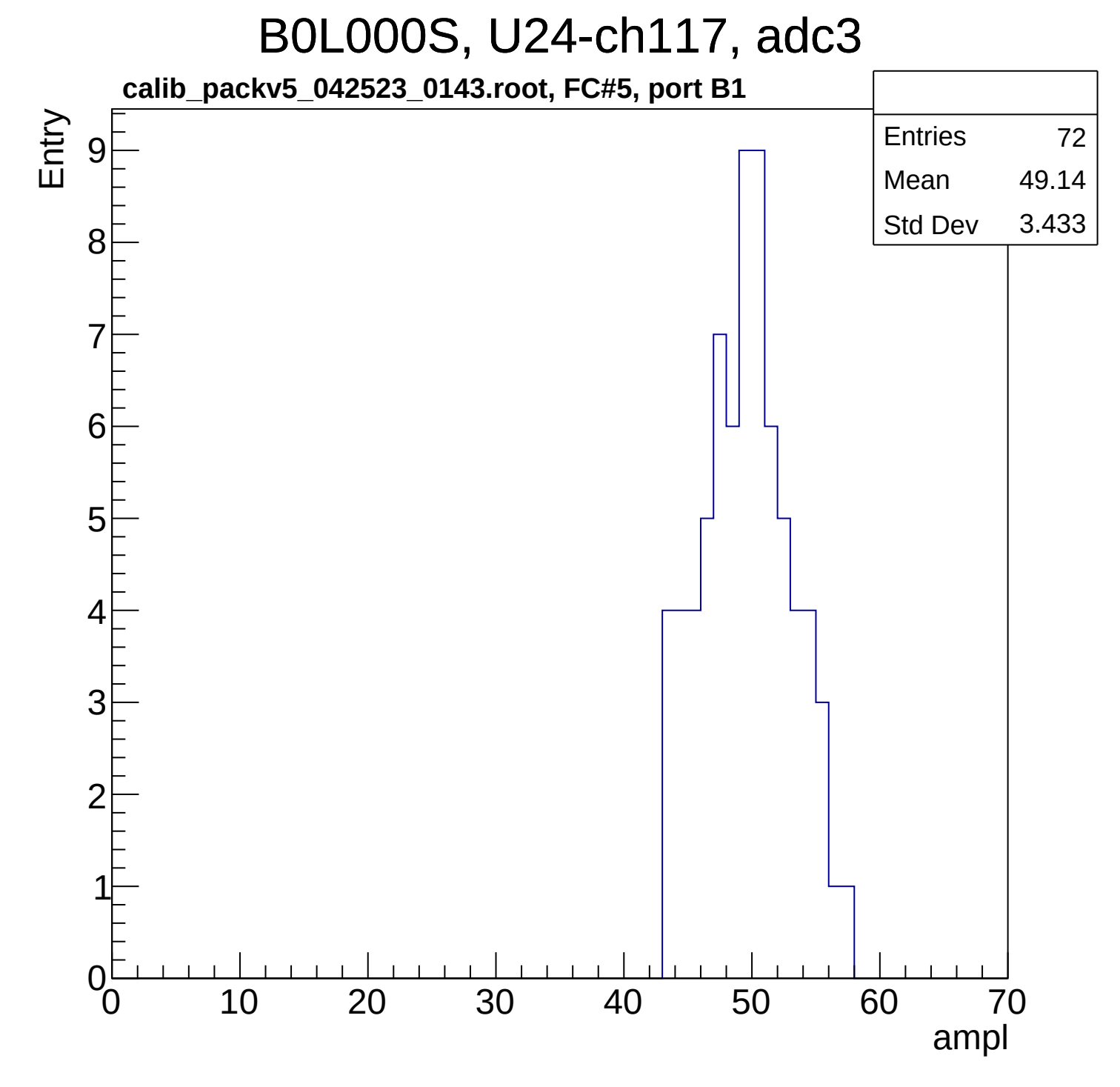

B0L000S, U24-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 1.773
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

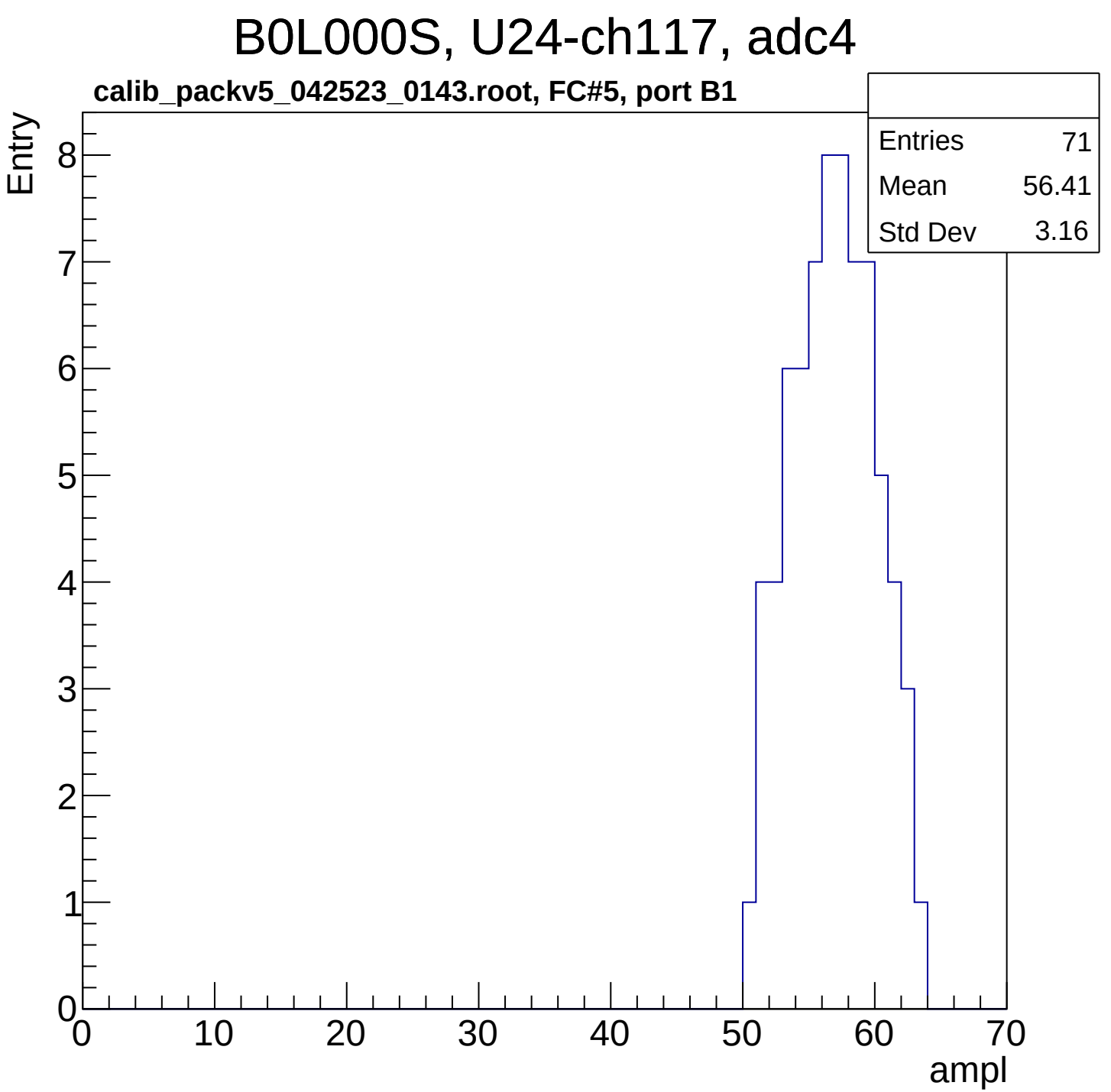

B0L000S, U24-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

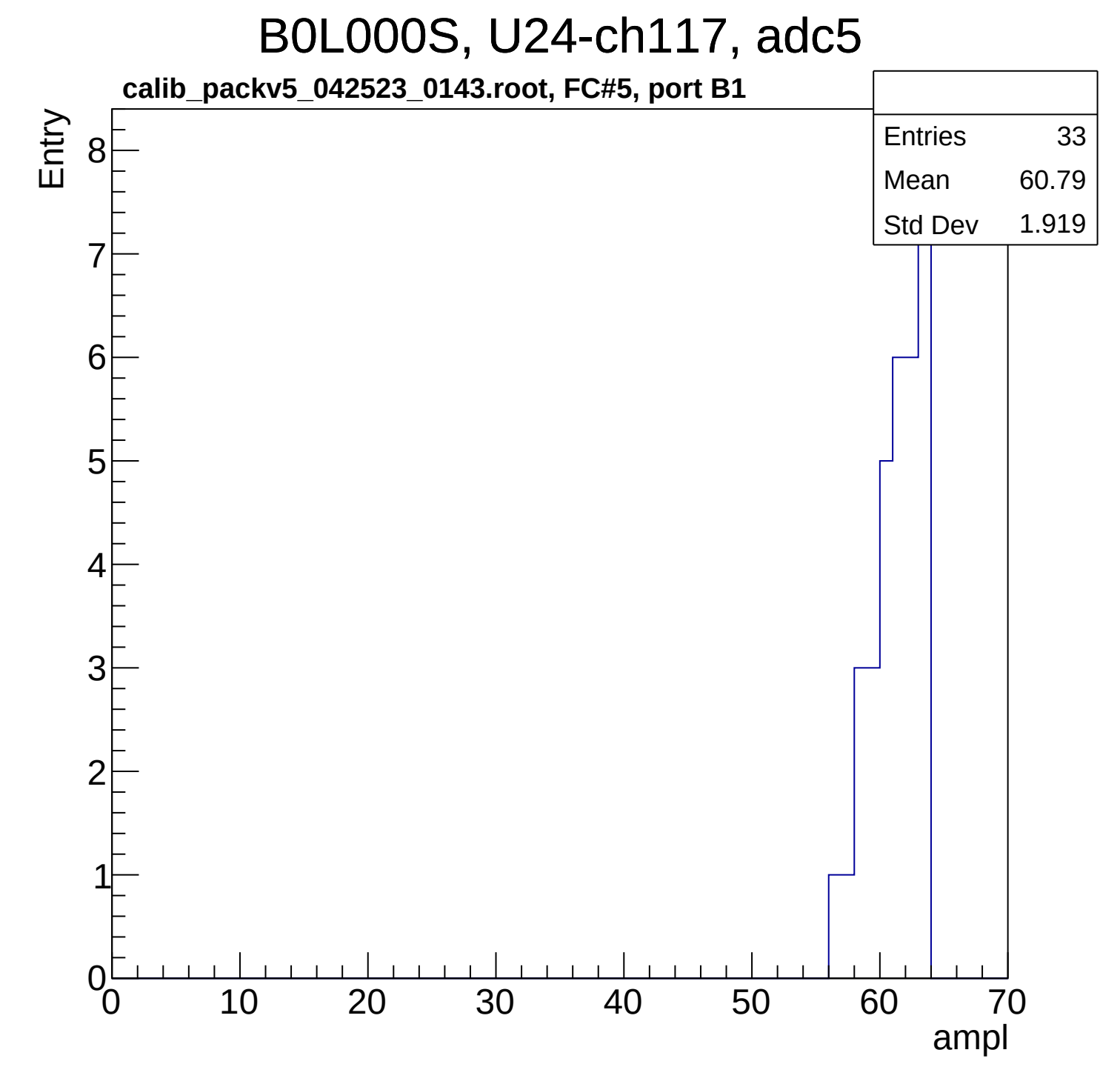

B0L000S, U24-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.56
Std Dev 3.438
Gaus mean : 30.1780
Gaus Width: 3.7195
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

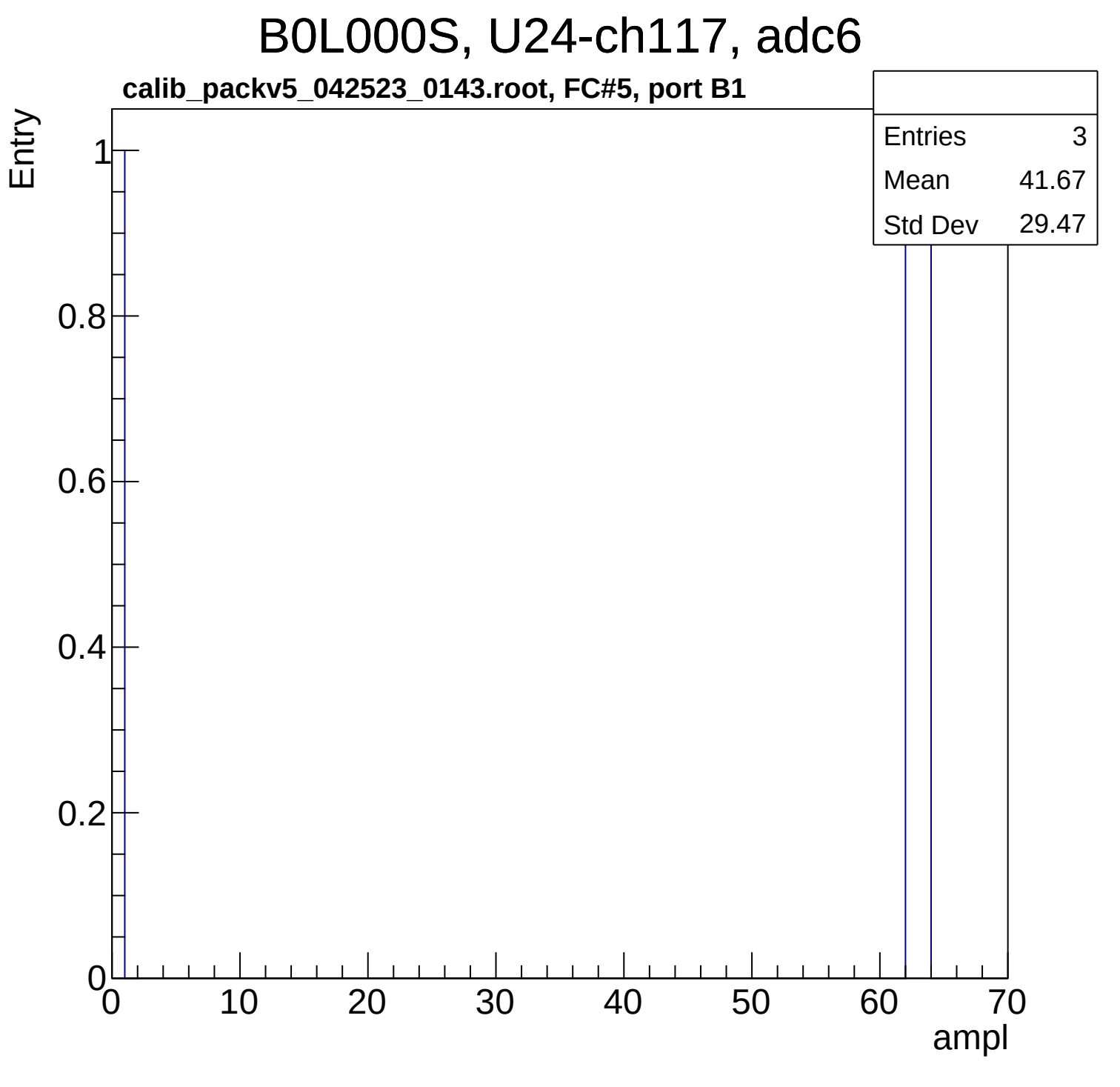

B0L000S, U24-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.24
Std Dev 3.201
Gaus mean : 36.7065
Gaus Width: 3.3798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

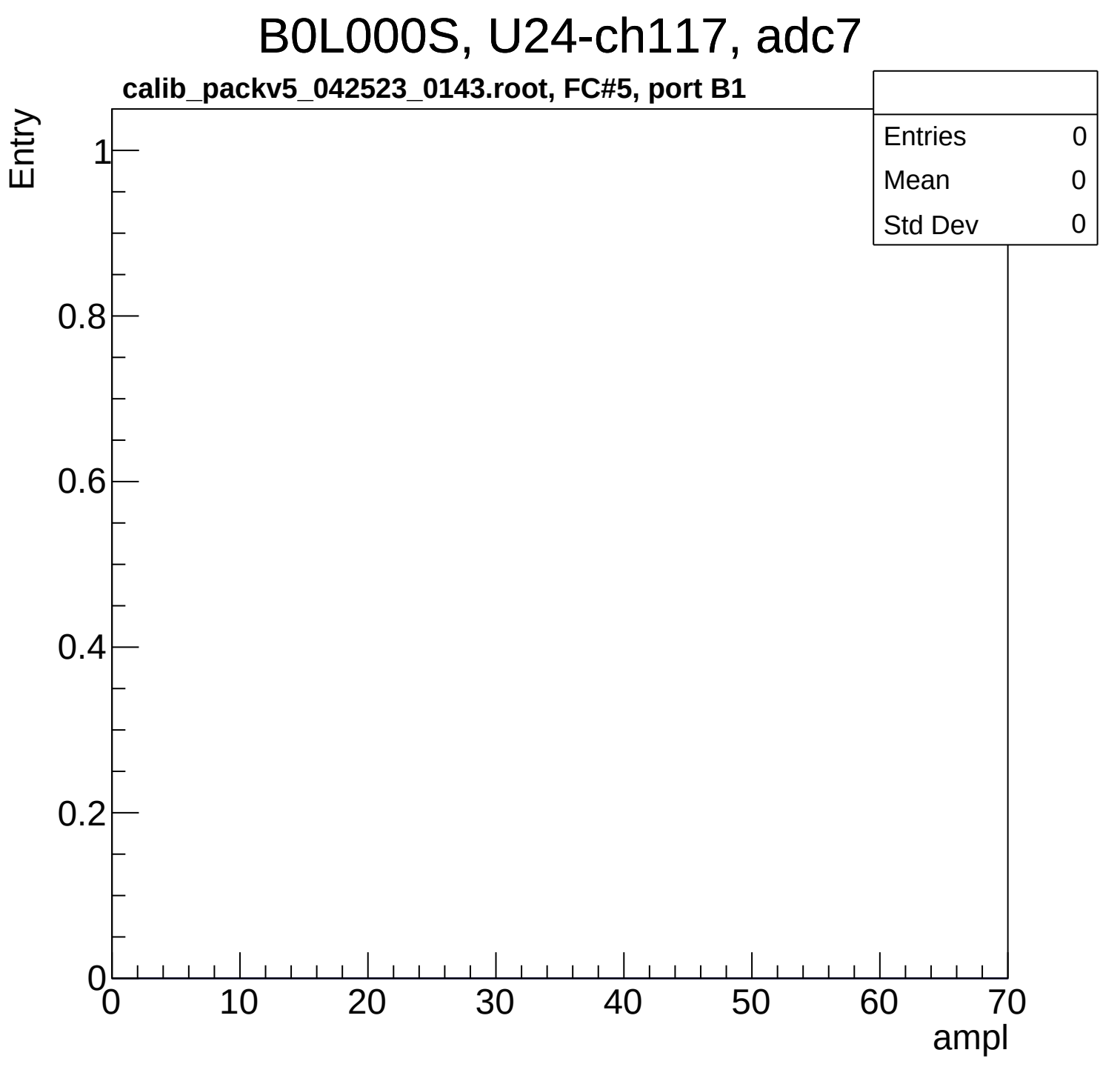

B0L000S, U24-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.67
Std Dev 3.586
Gaus mean : 43.2075
Gaus Width: 3.7339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

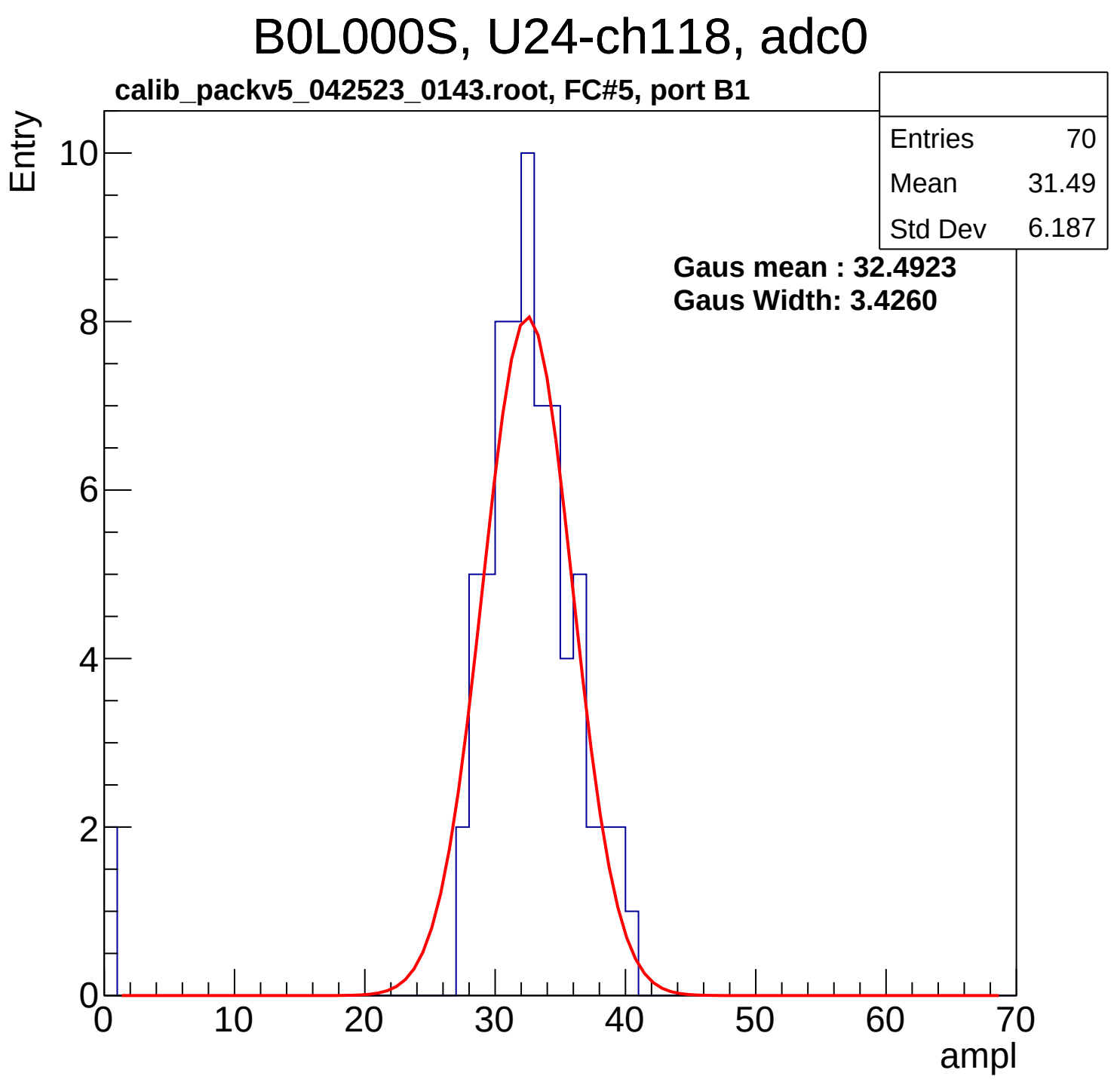

B0L000S, U24-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.14
Std Dev 3.433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

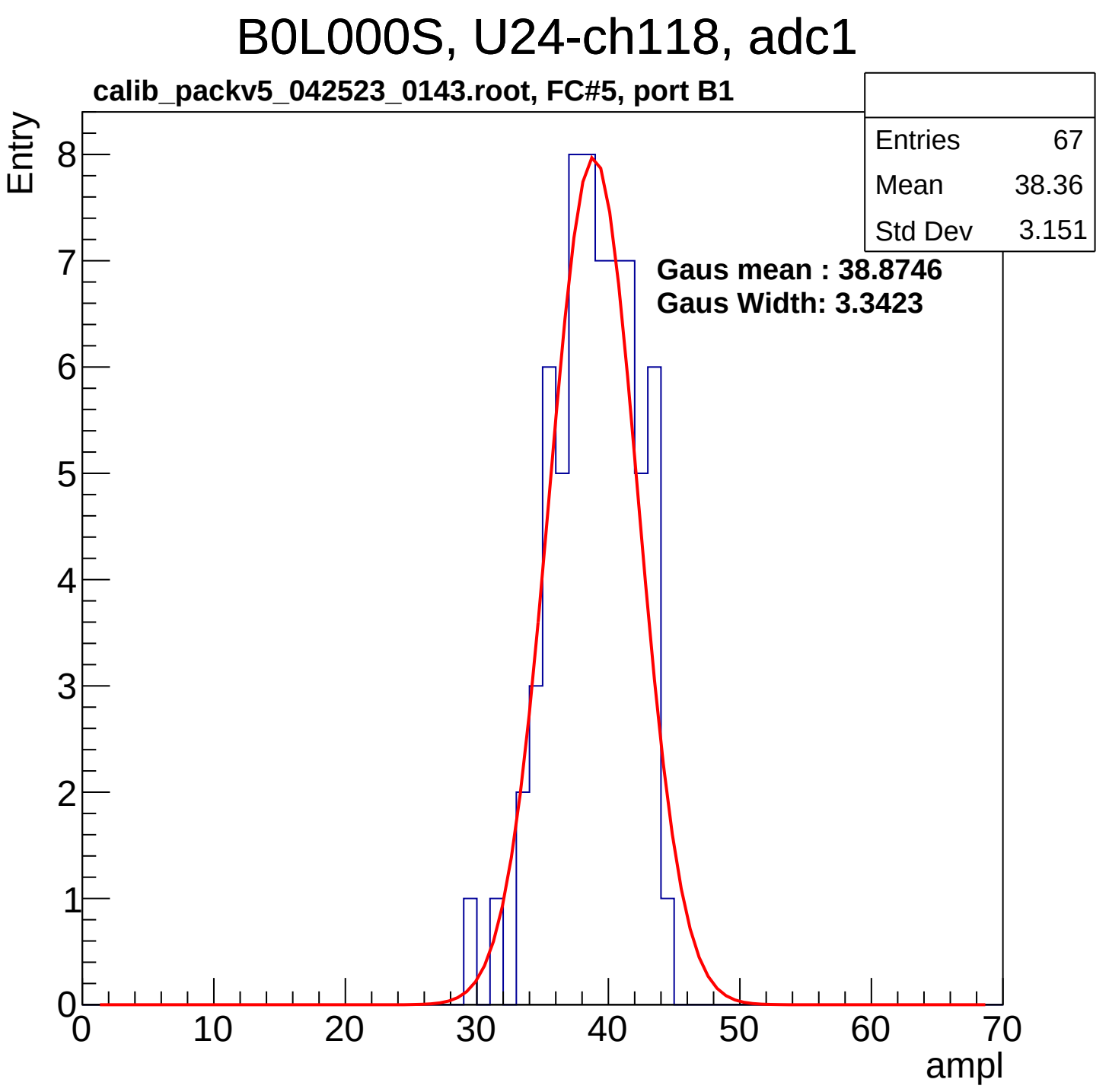

B0L000S, U24-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 56.41
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

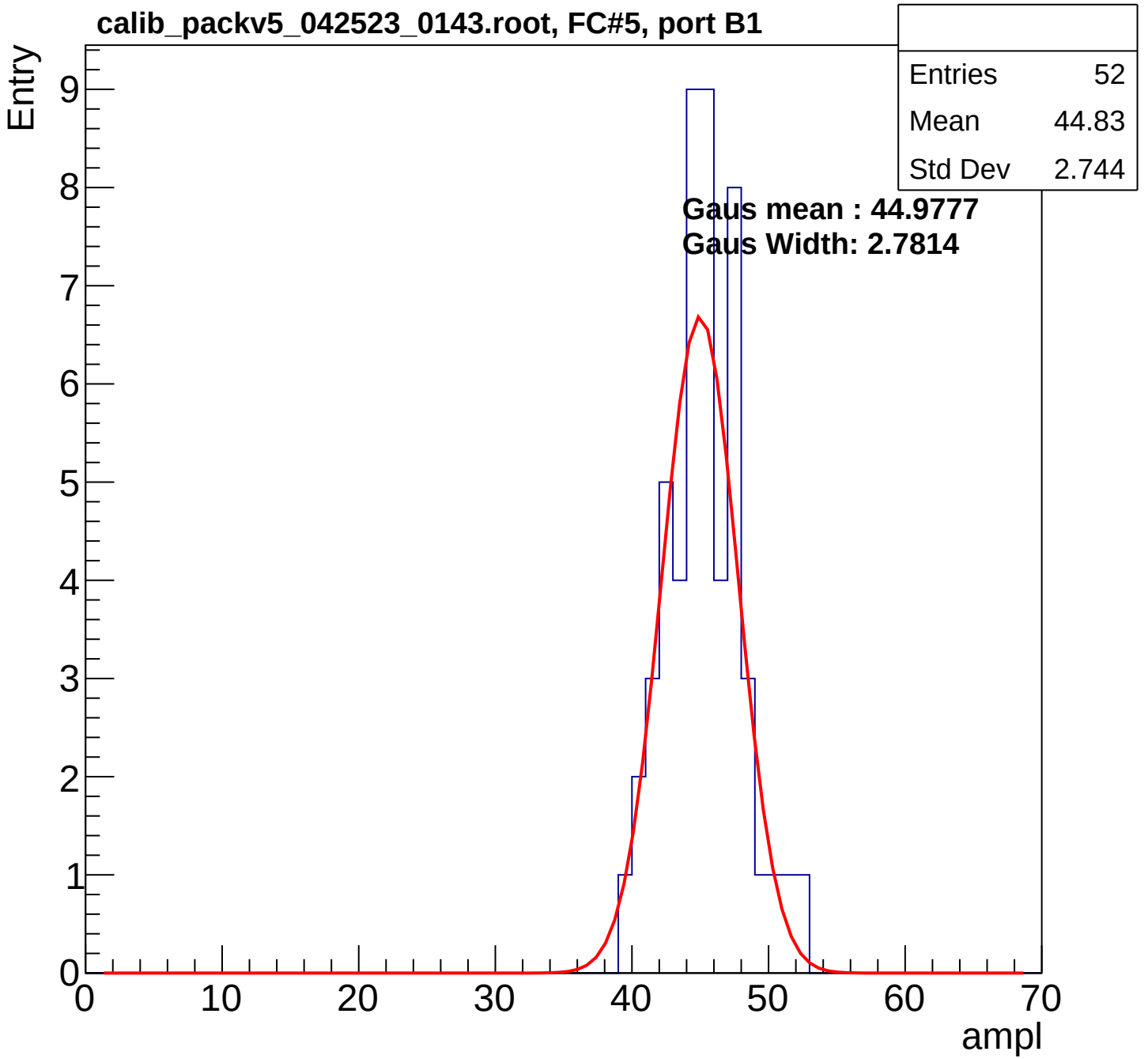

B0L000S, U24-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.79
Std Dev 1.919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

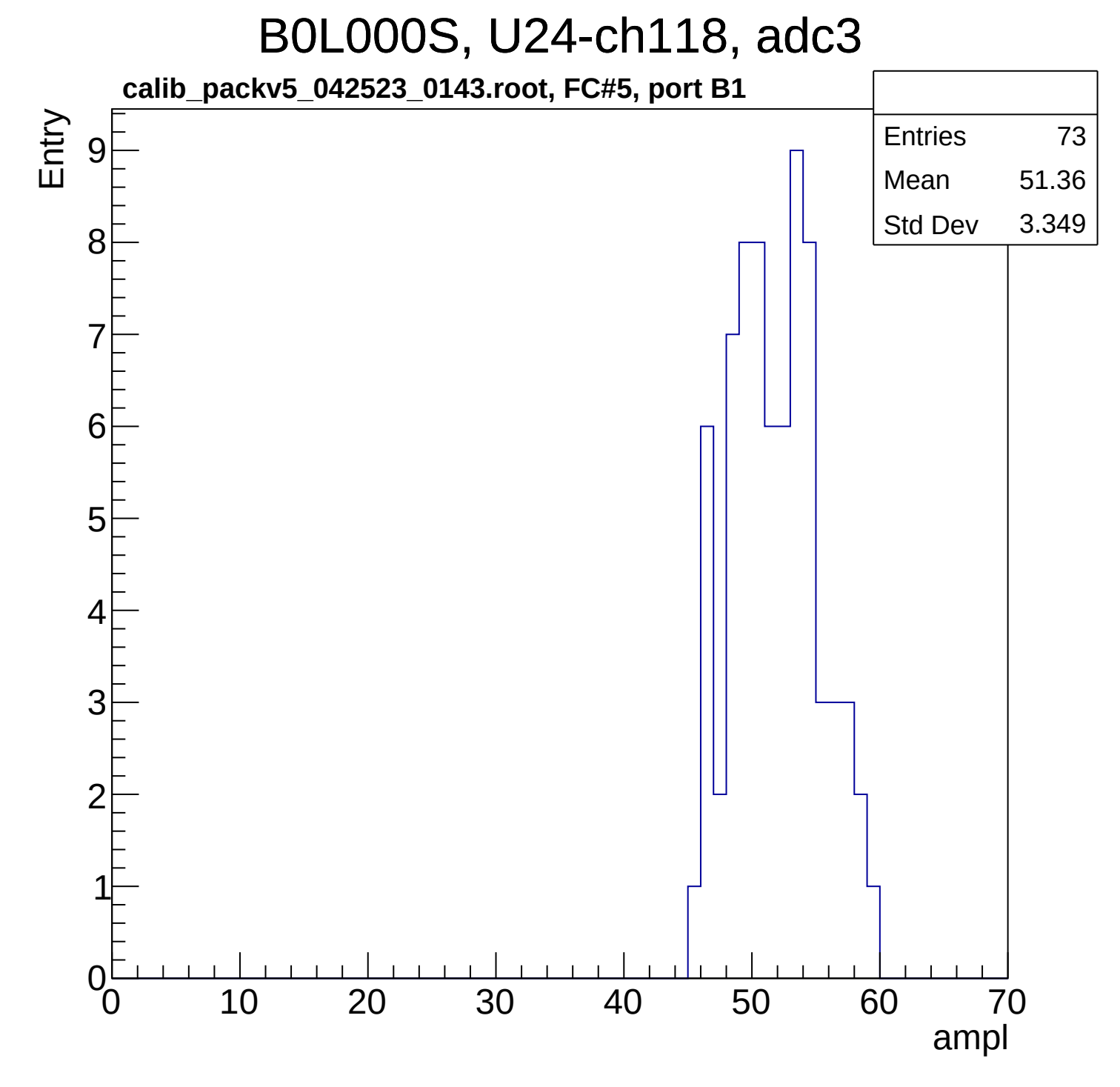

B0L000S, U24-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 31.49
Std Dev 6.187
Gaus mean : 32.4923
Gaus Width: 3.4260
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 38.36
Std Dev 3.151
Gaus mean : 38.8746
Gaus Width: 3.3423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 44.83
Std Dev 2.744
Gaus mean : 44.9777
Gaus Width: 2.7814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 51.36
Std Dev 3.349
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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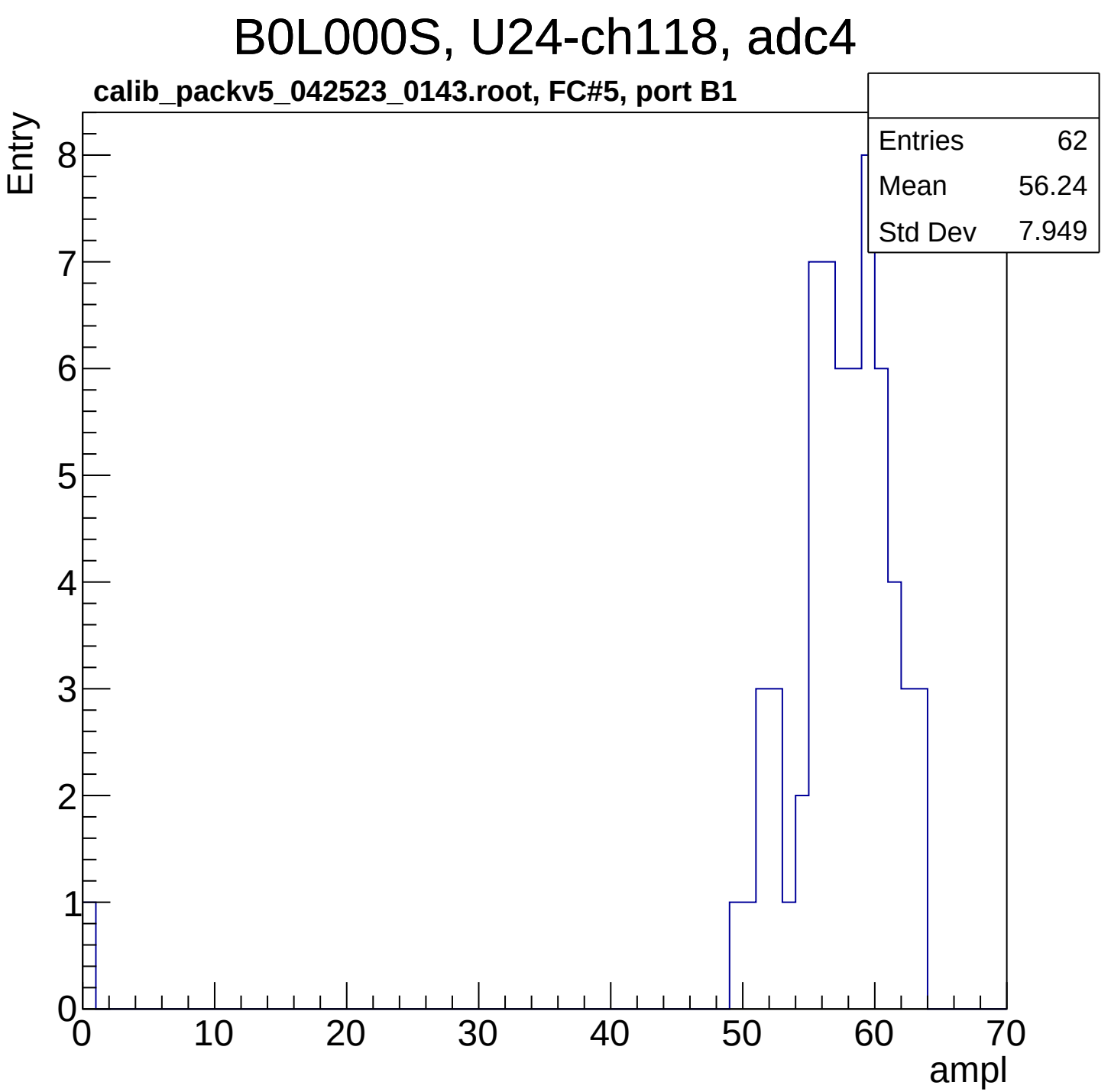
B0L000S, U24-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.24
Std Dev 7.949
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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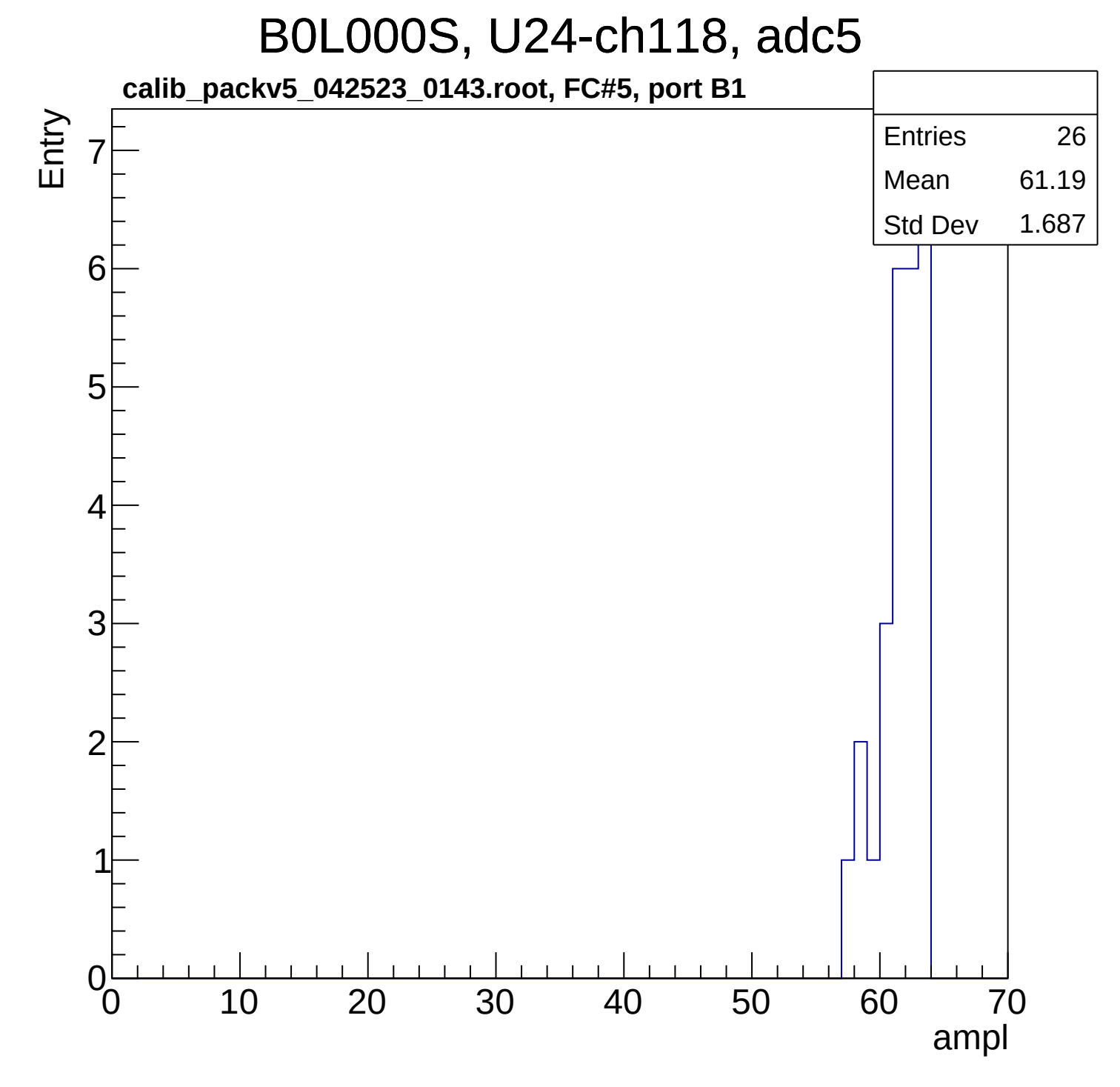
B0L000S, U24-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 61.19
Std Dev 1.687
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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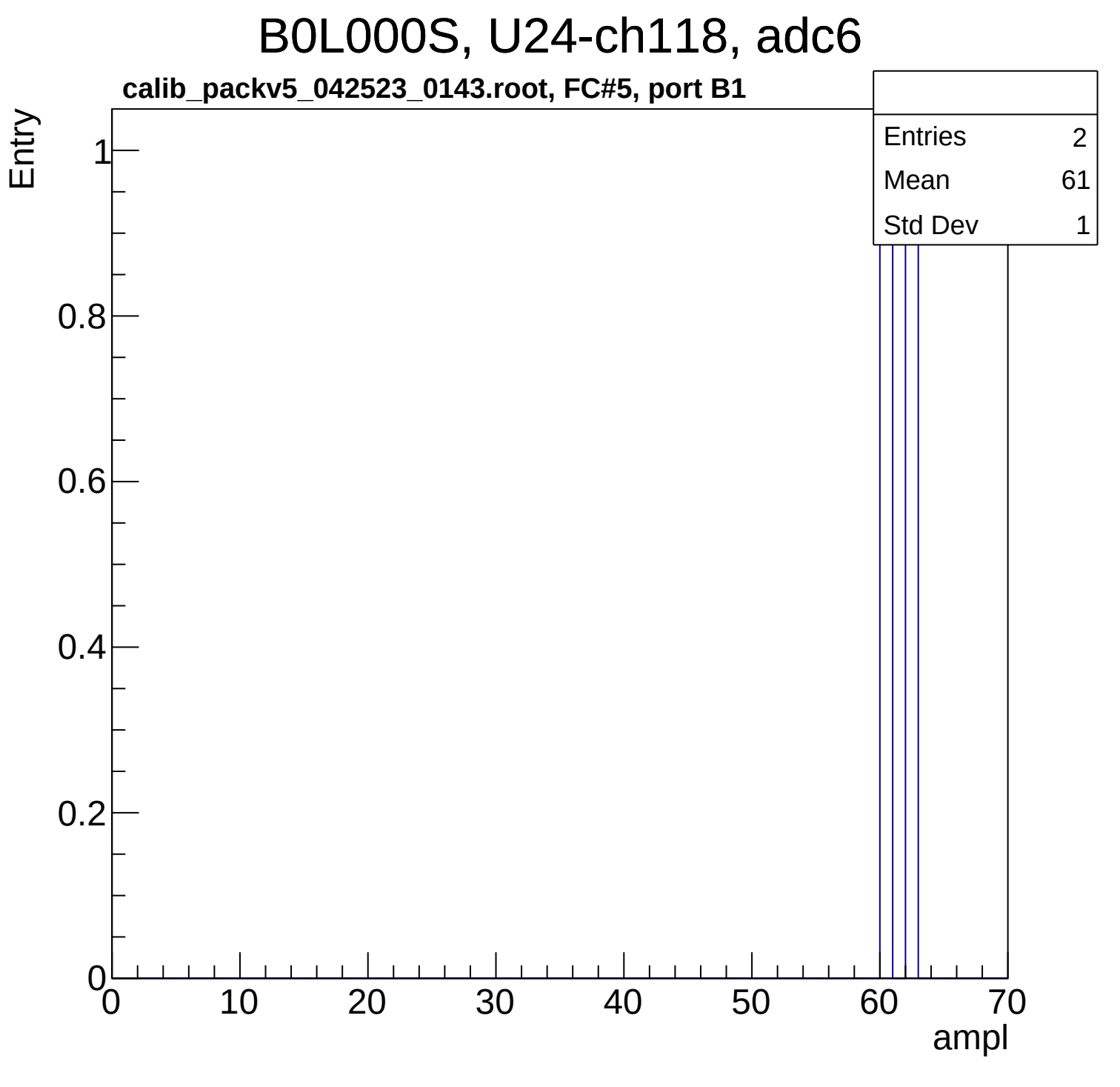
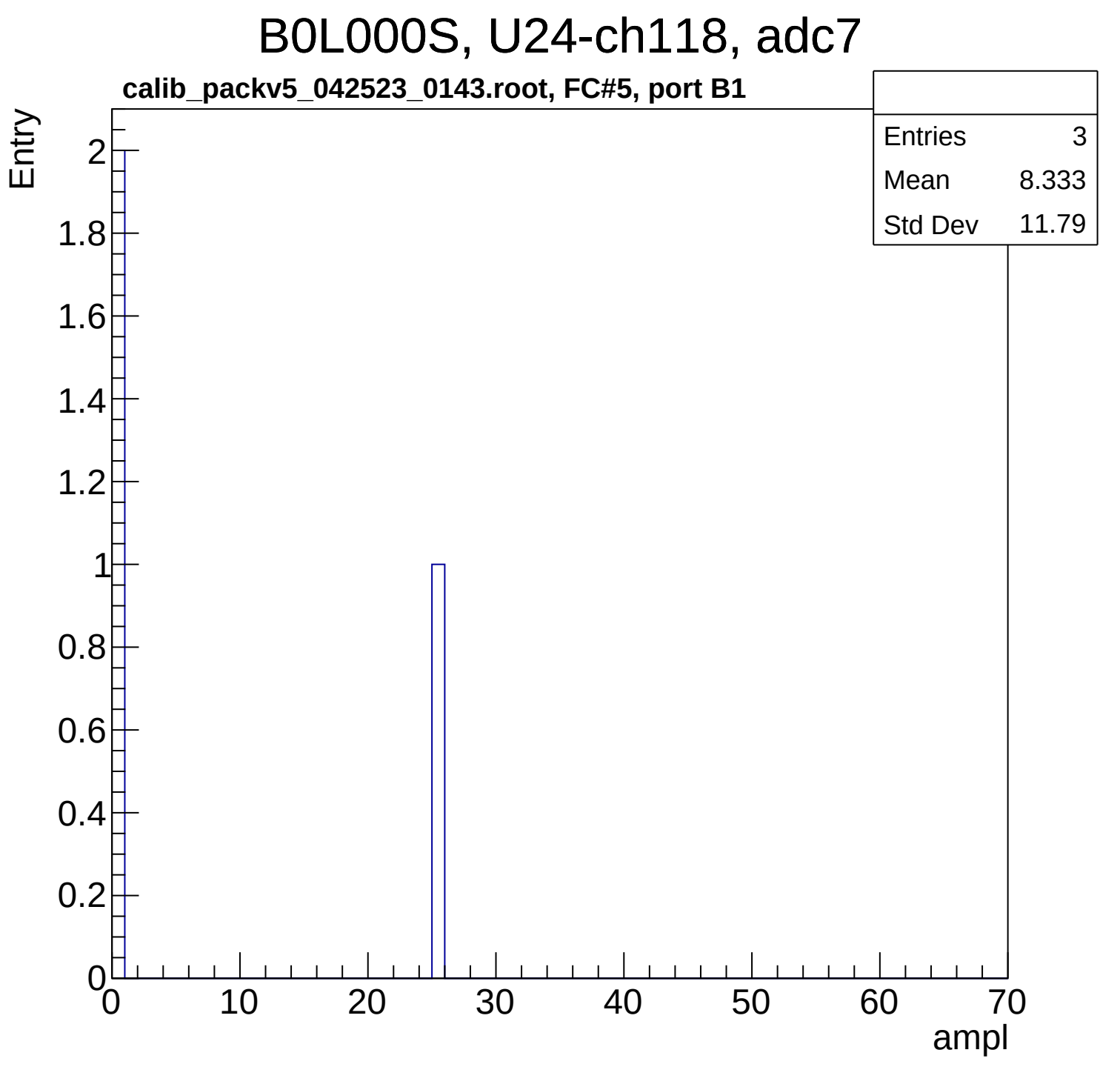
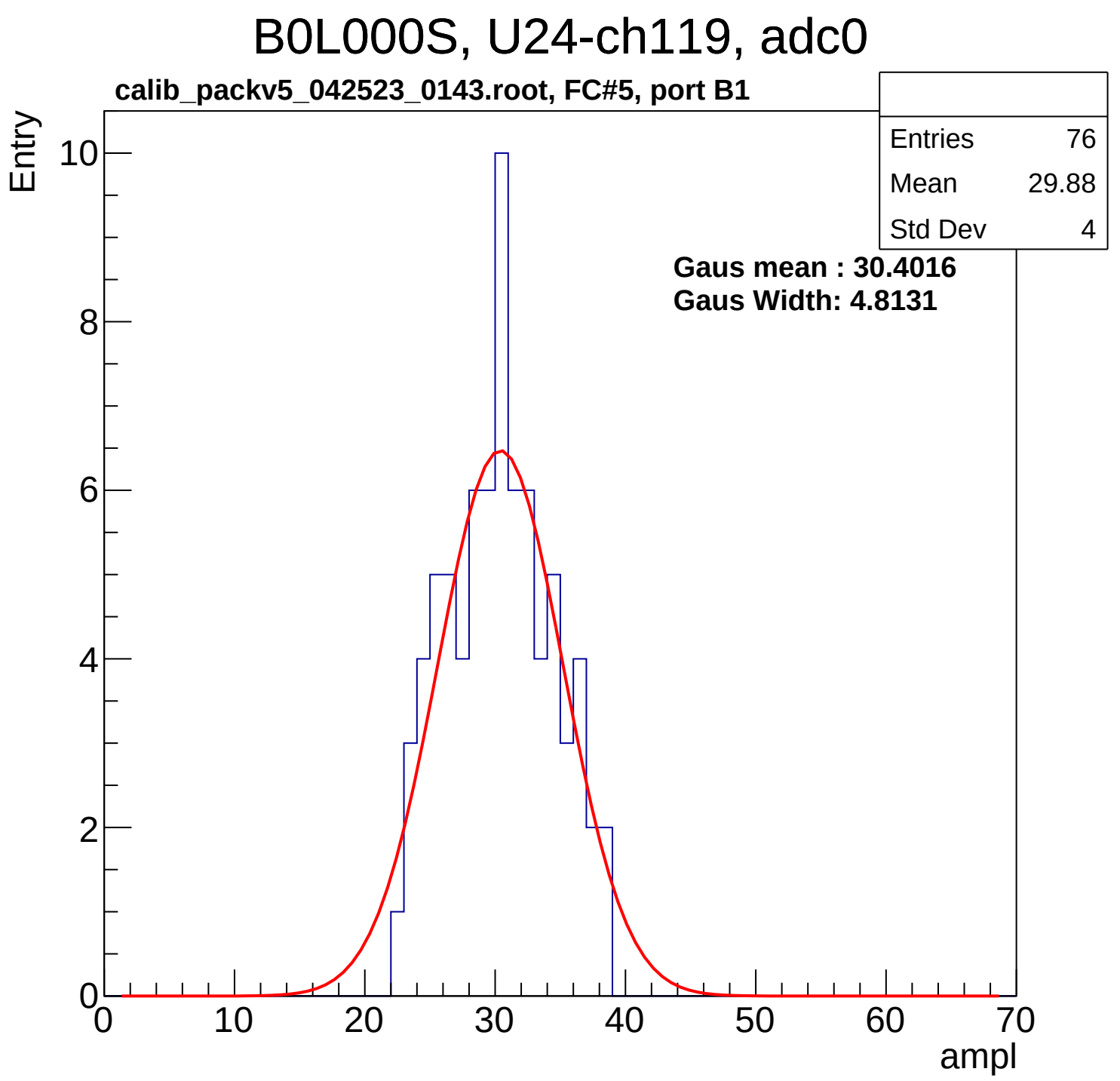

B0L000S, U24-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.88
Std Dev 4
Gaus mean : 30.4016
Gaus Width: 4.8131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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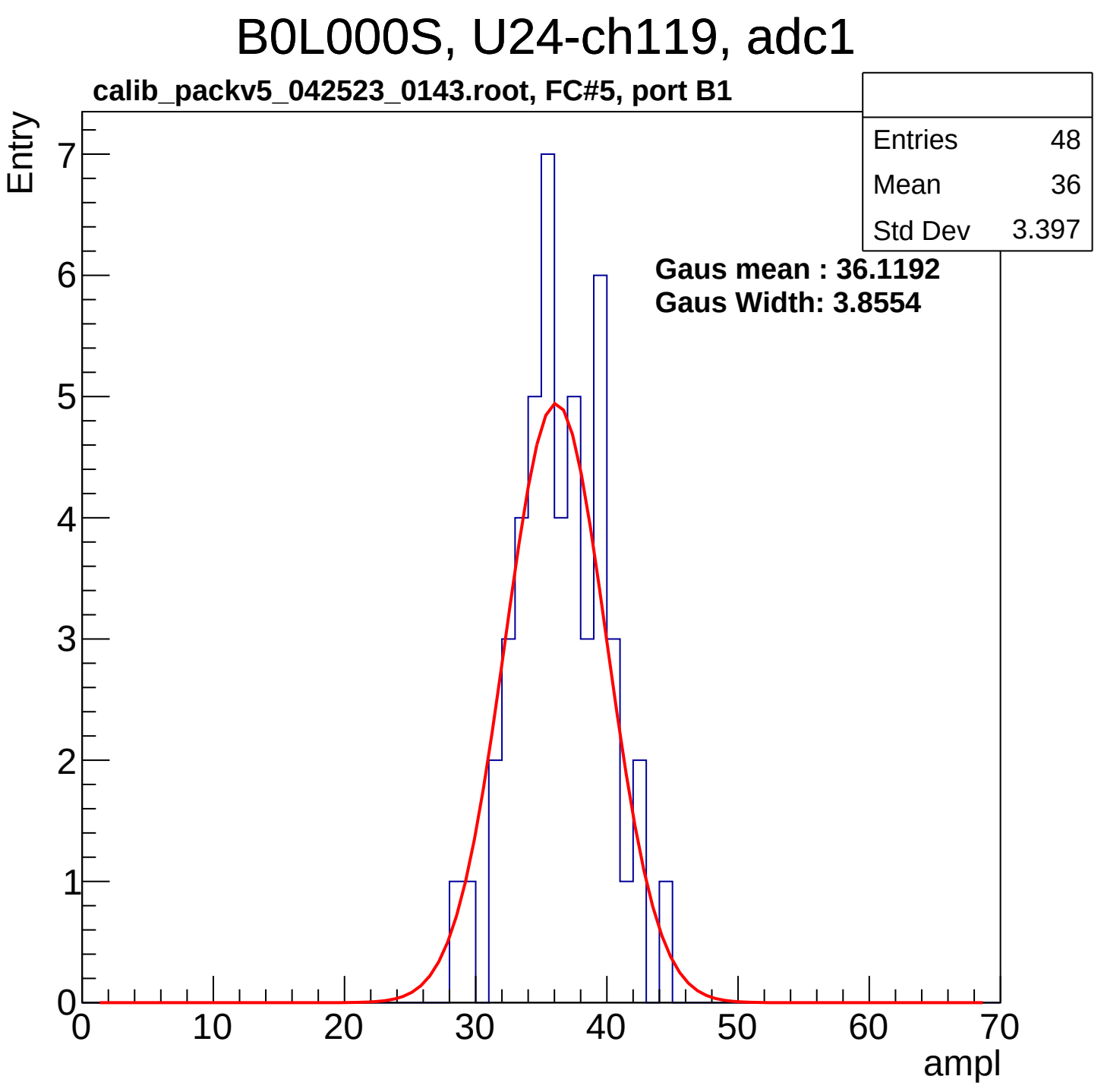
B0L000S, U24-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 36
Std Dev 3.397
Gaus mean : 36.1192
Gaus Width: 3.8554
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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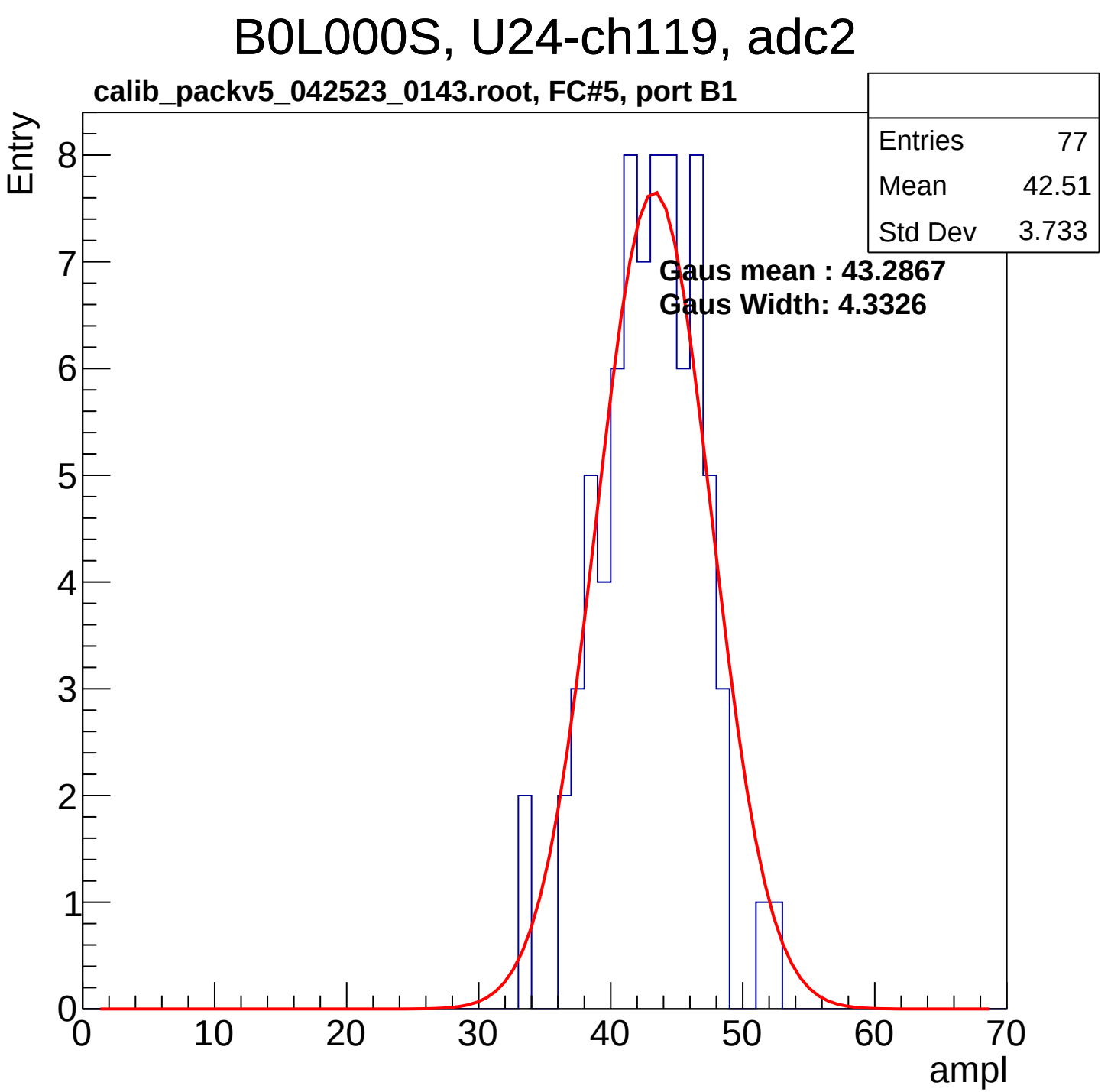
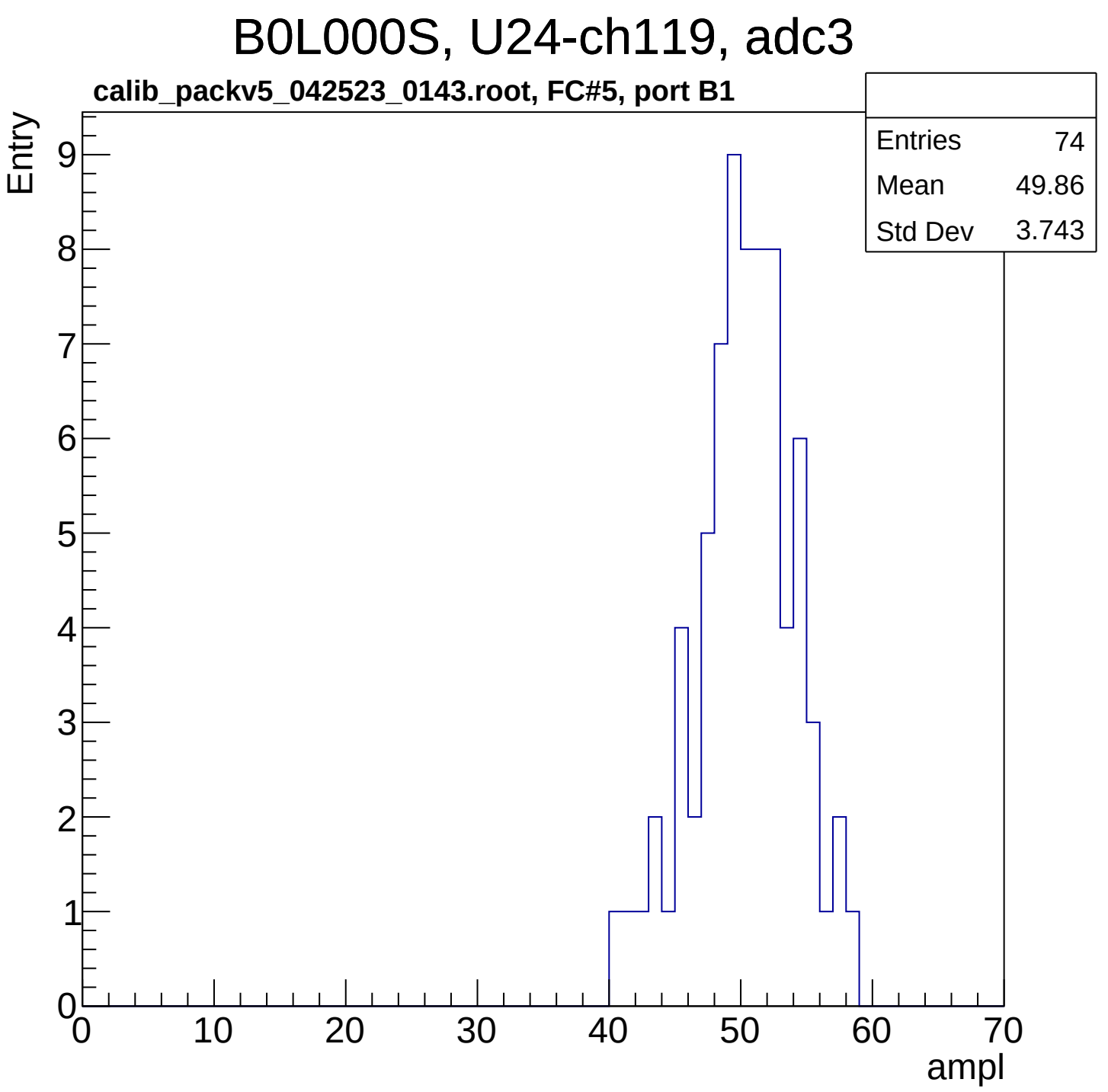

B0L000S, U24-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 42.51
Std Dev 3.733
Gaus mean : 43.2867
Gaus Width: 4.3326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.86
Std Dev 3.743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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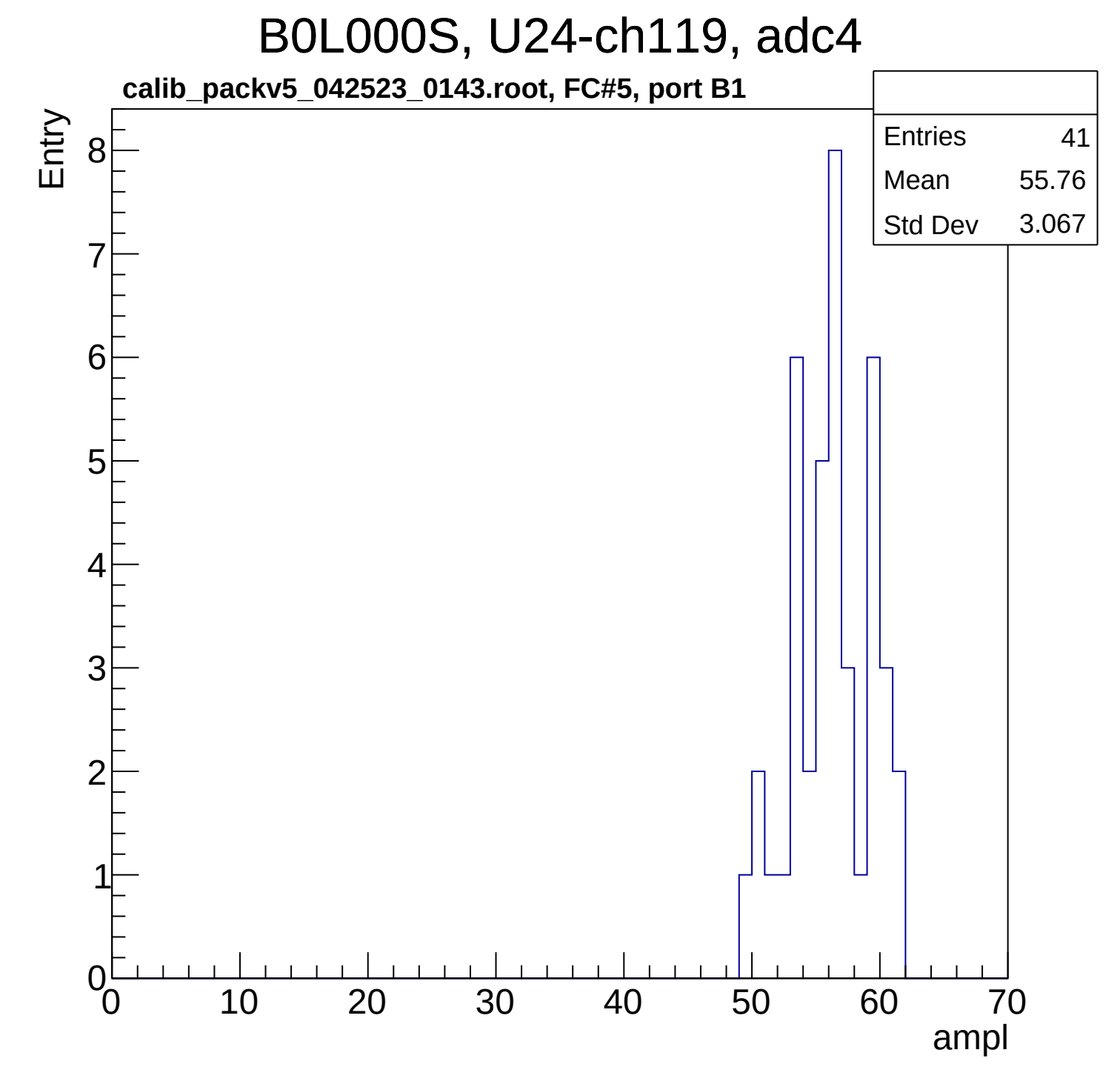
B0L000S, U24-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 55.76
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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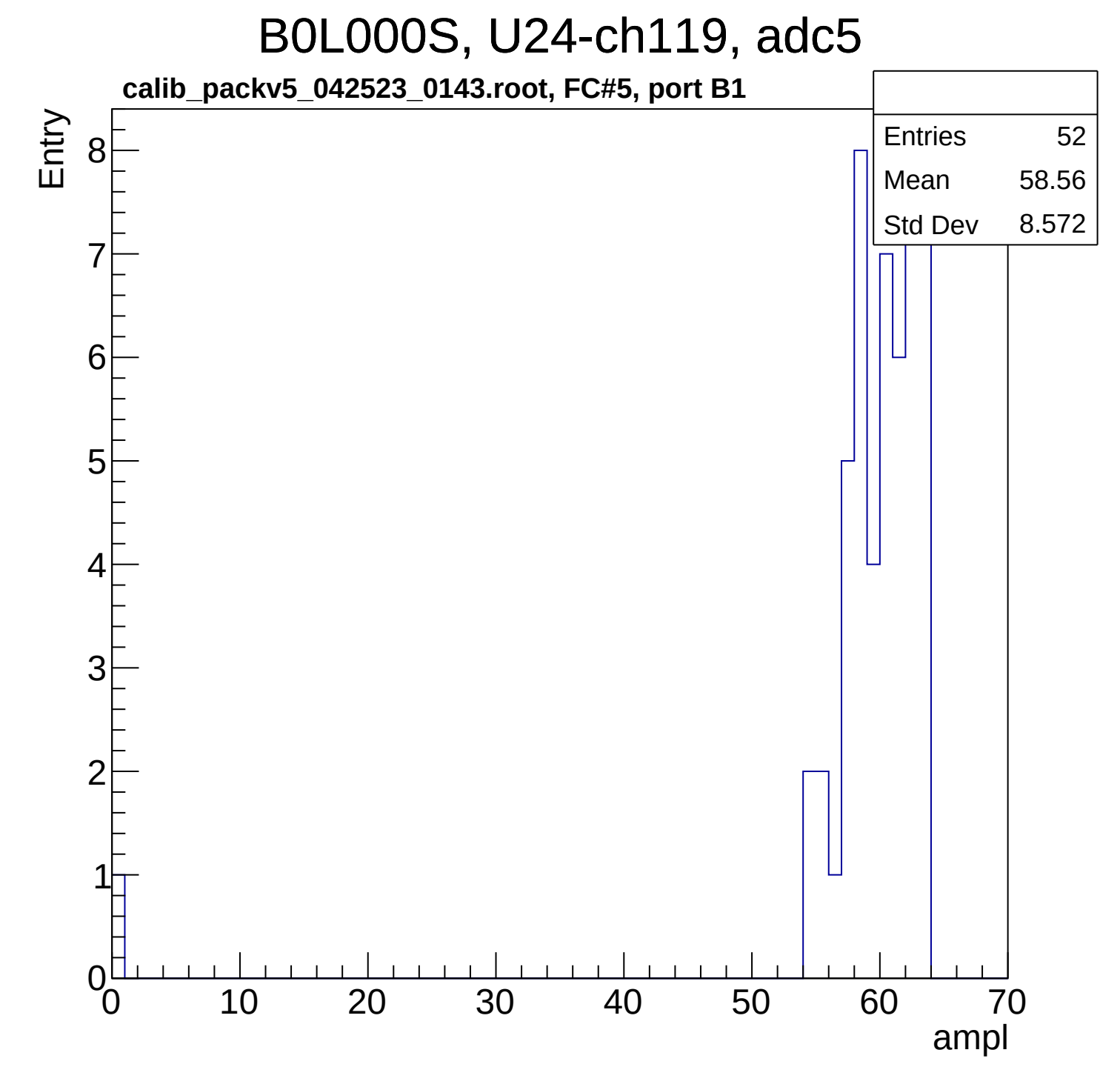
B0L000S, U24-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.56
Std Dev 8.572
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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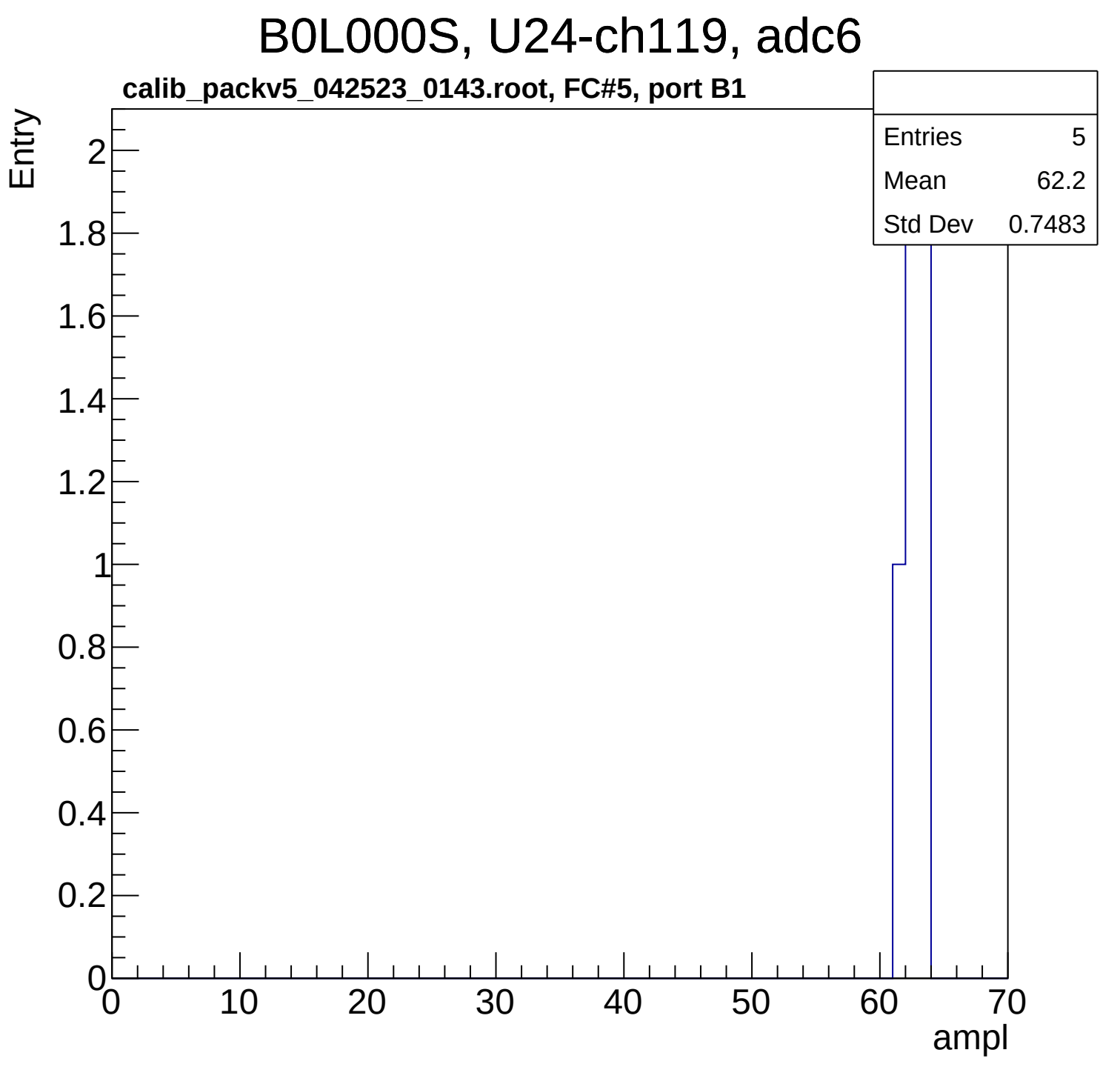
B0L000S, U24-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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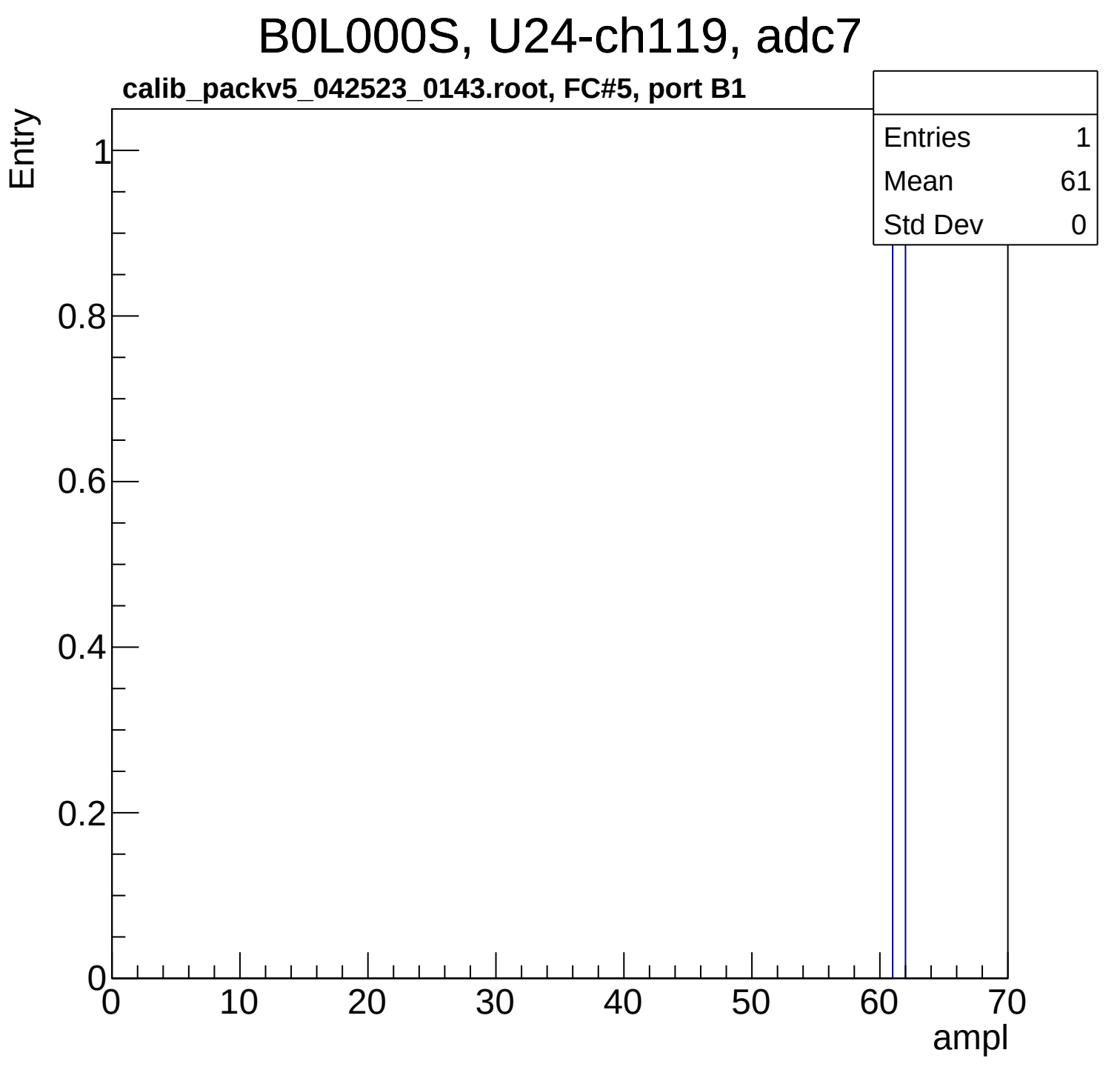
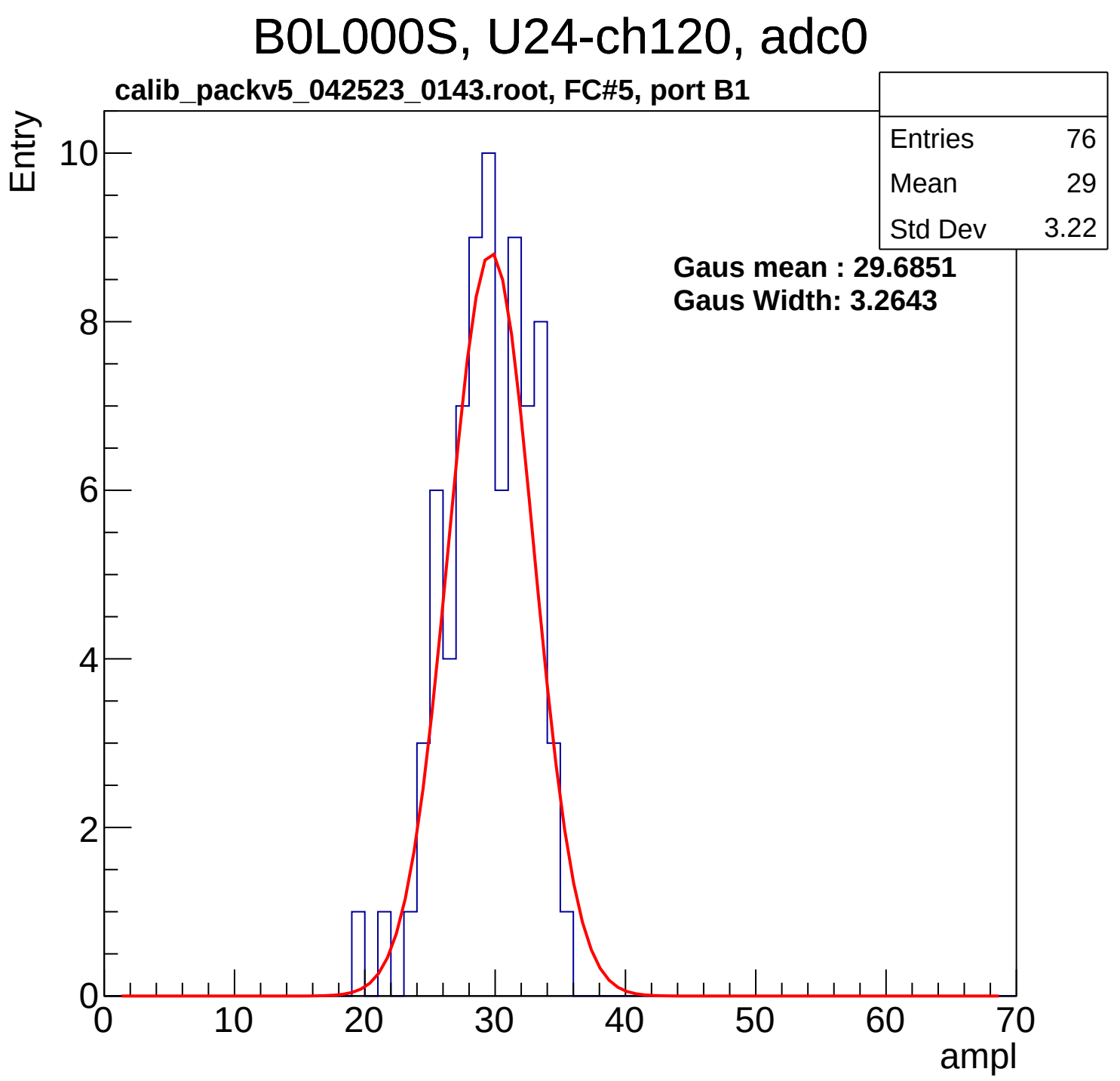
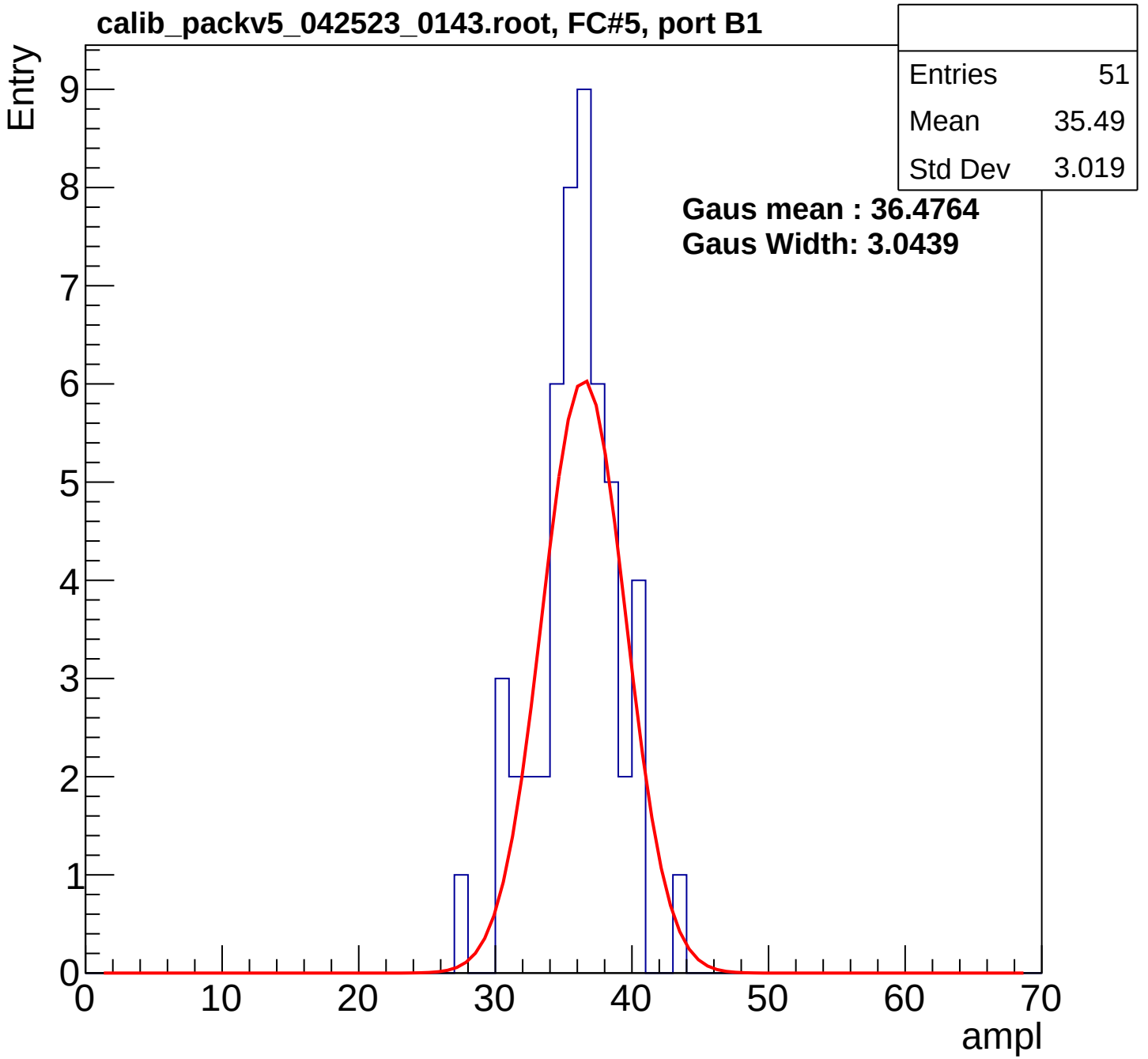
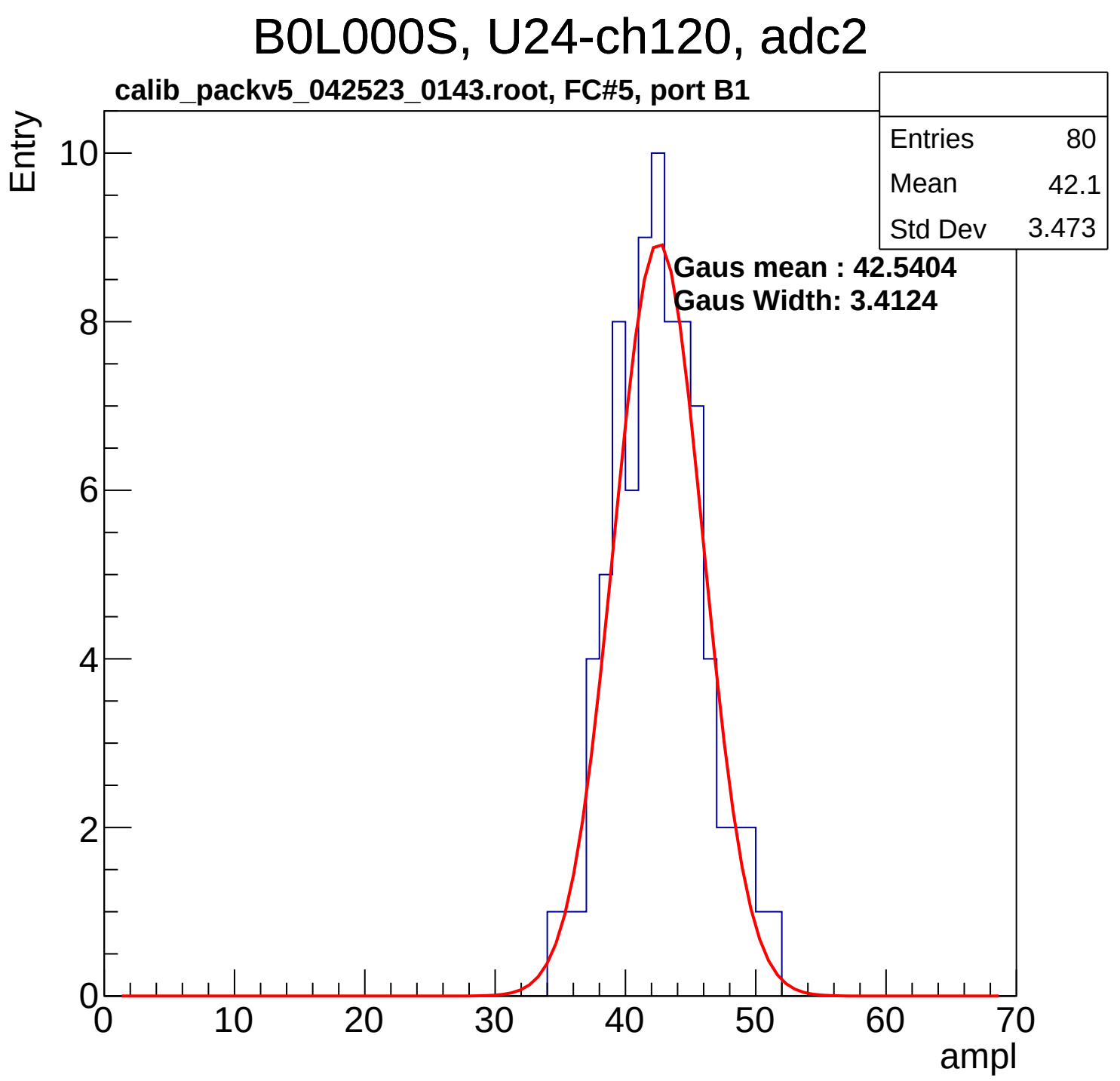

B0L000S, U24-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29
Std Dev 3.22
Gaus mean : 29.6851
Gaus Width: 3.2643
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 35.49
Std Dev 3.019
Gaus mean : 36.4764
Gaus Width: 3.0439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 42.1
Std Dev 3.473
Gaus mean : 42.5404
Gaus Width: 3.4124
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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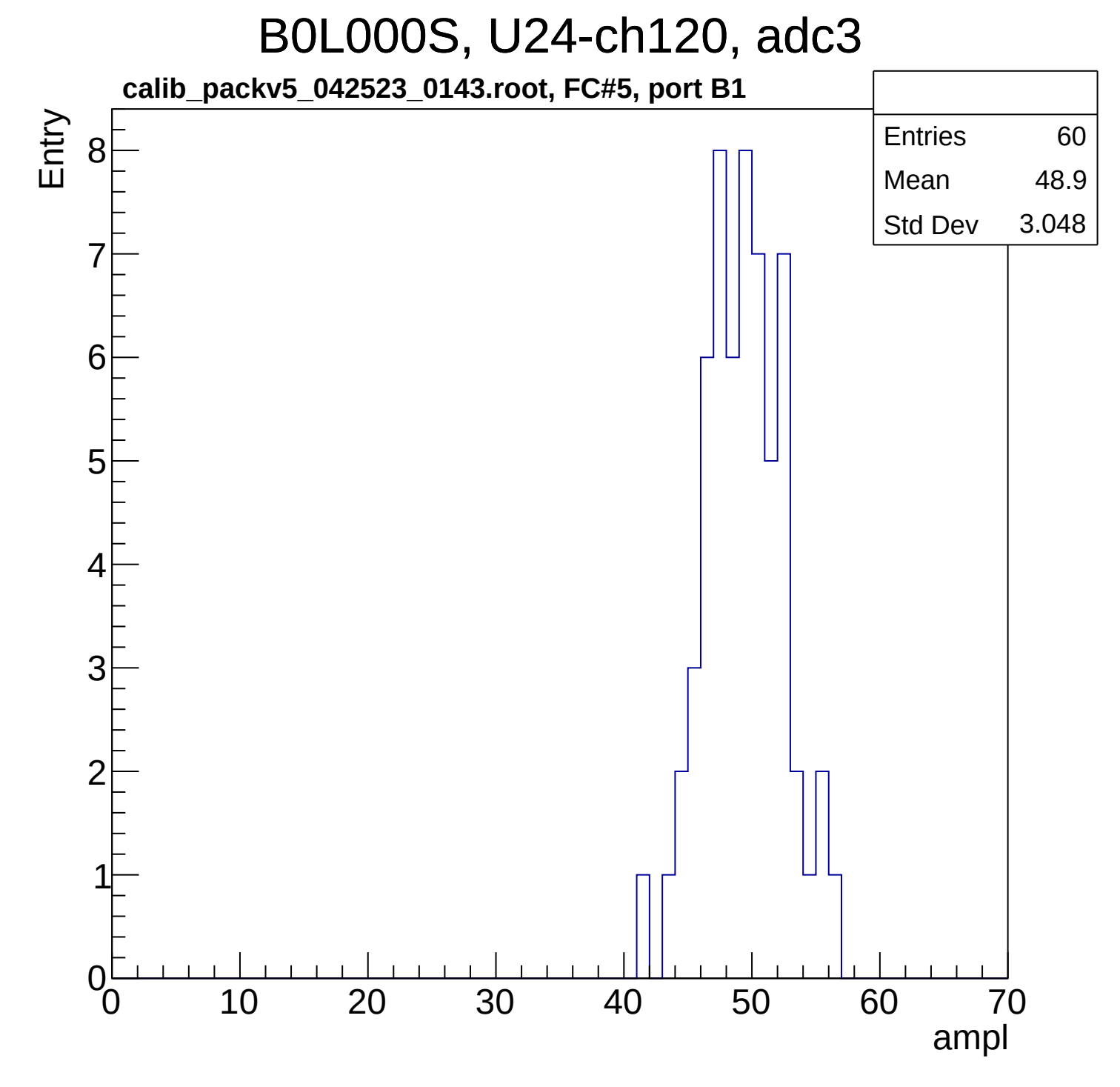
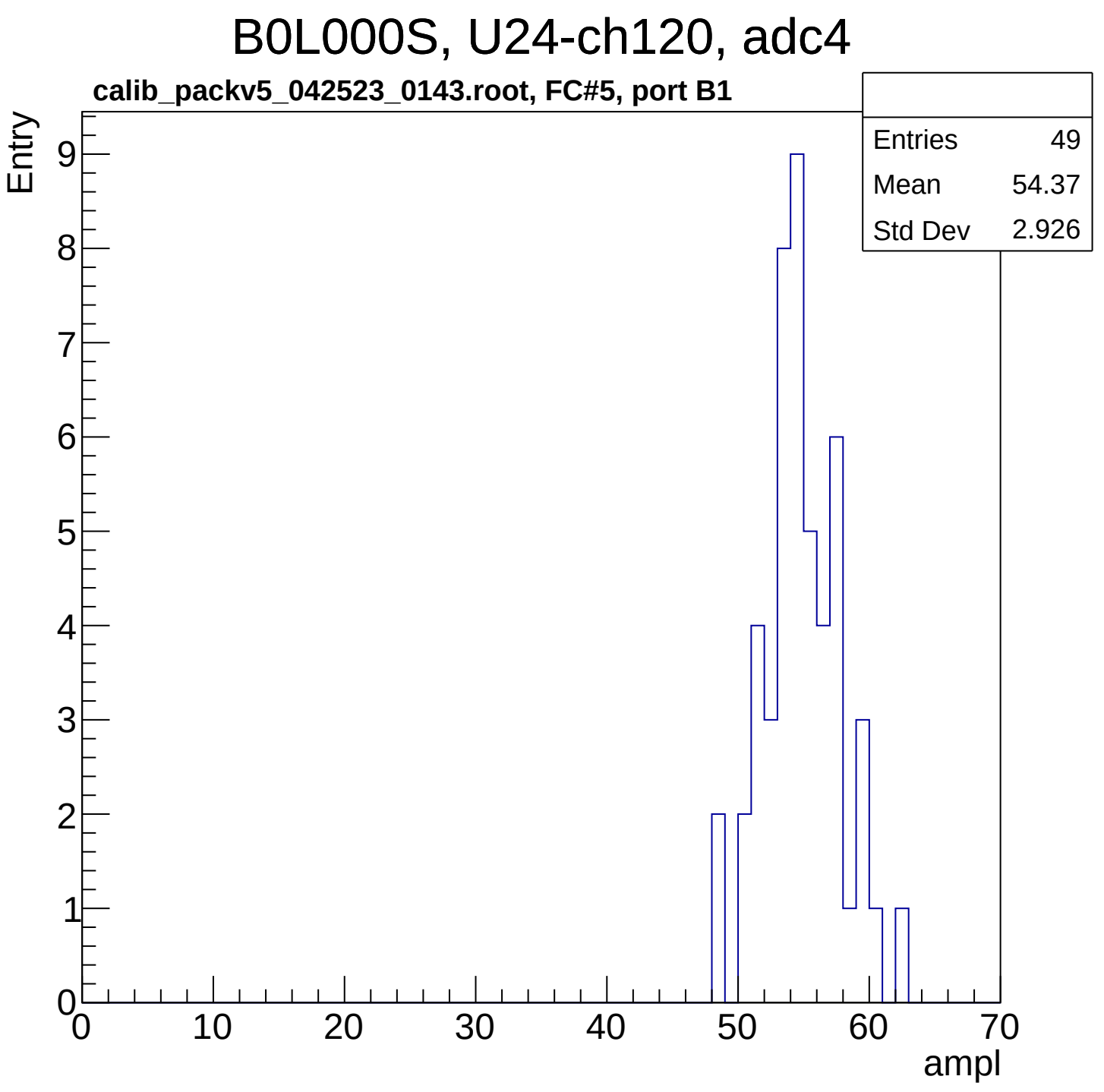
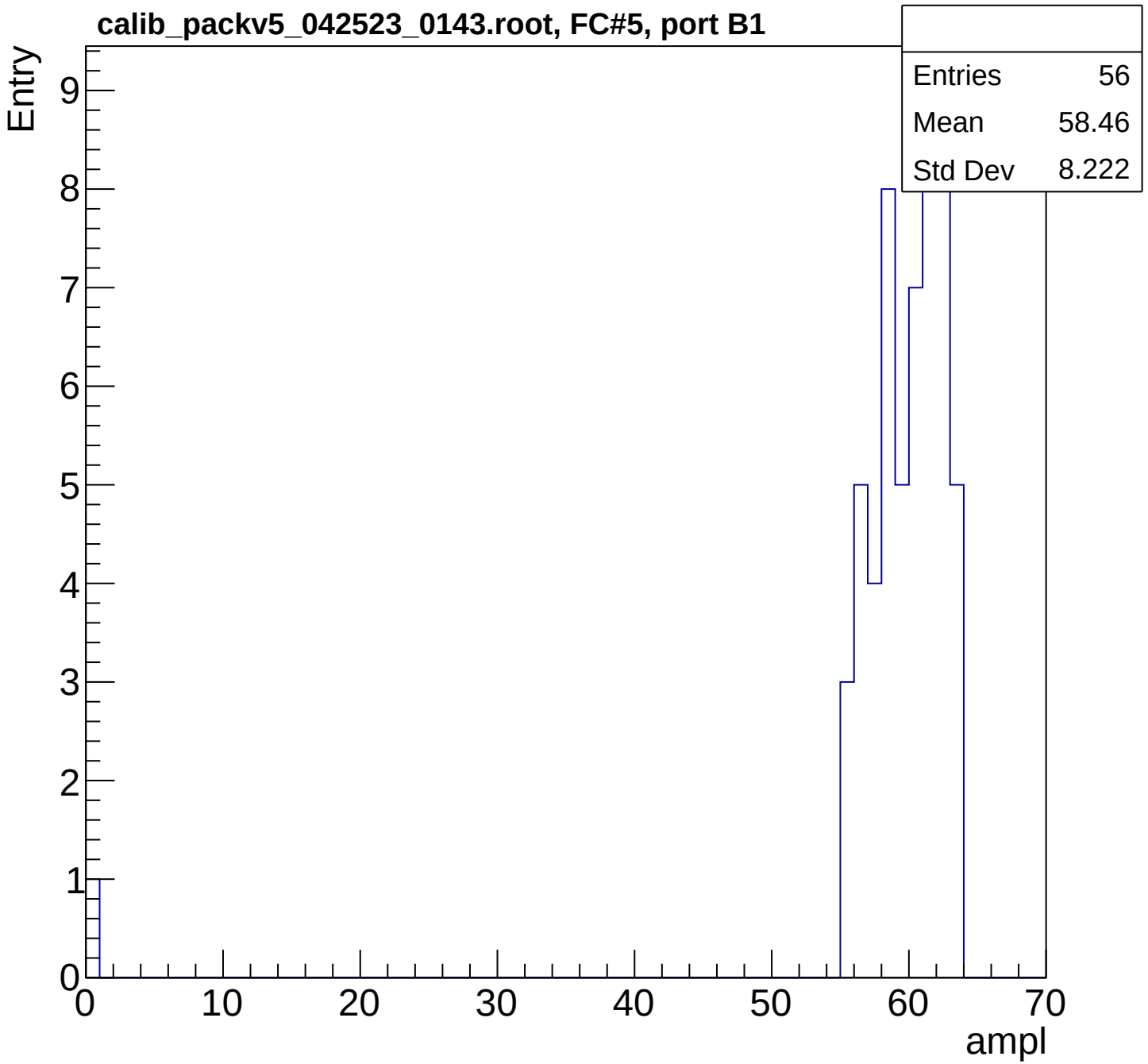

B0L000S, U24-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.9
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 54.37
Std Dev 2.926
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.46
Std Dev 8.222
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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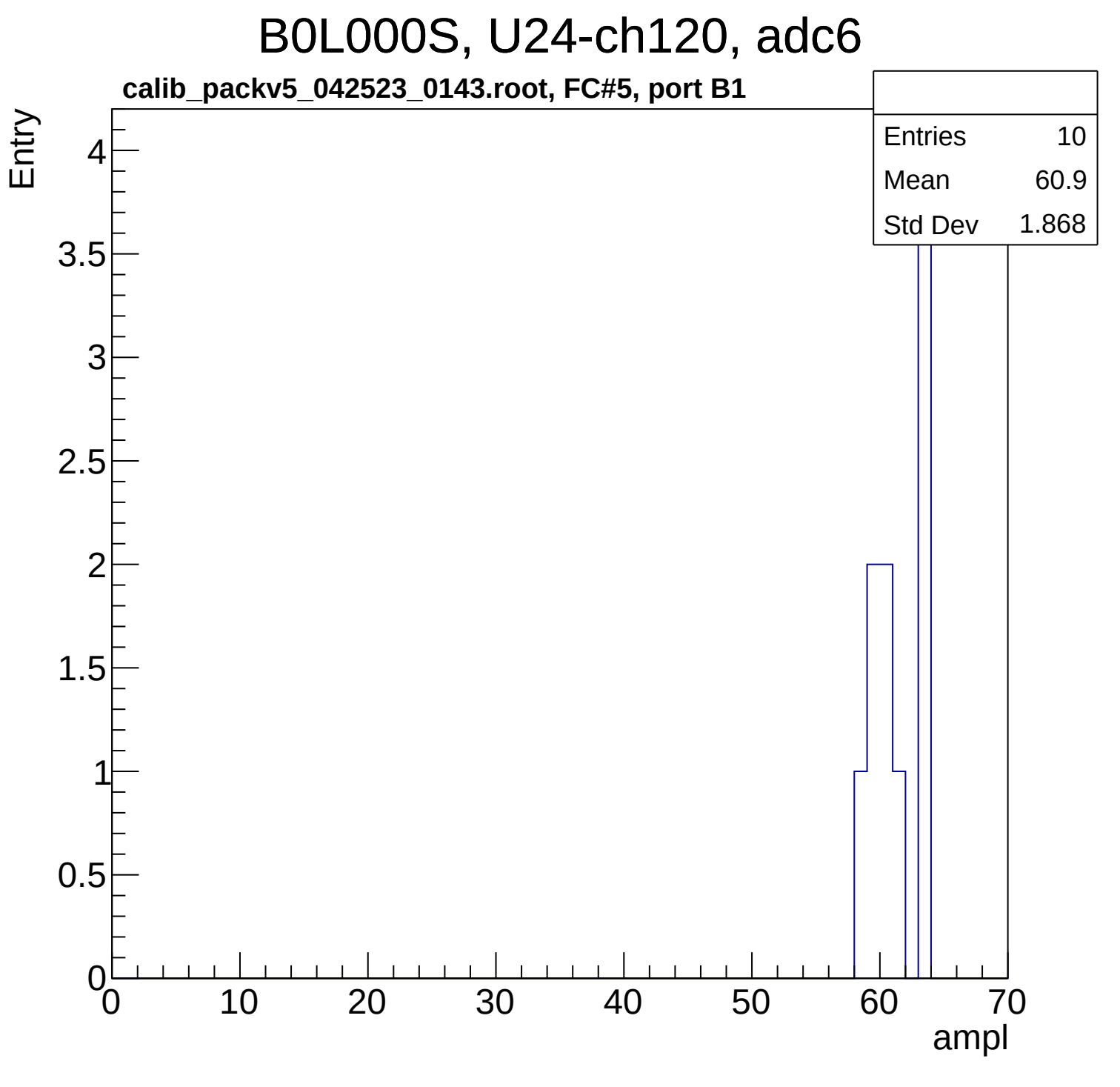
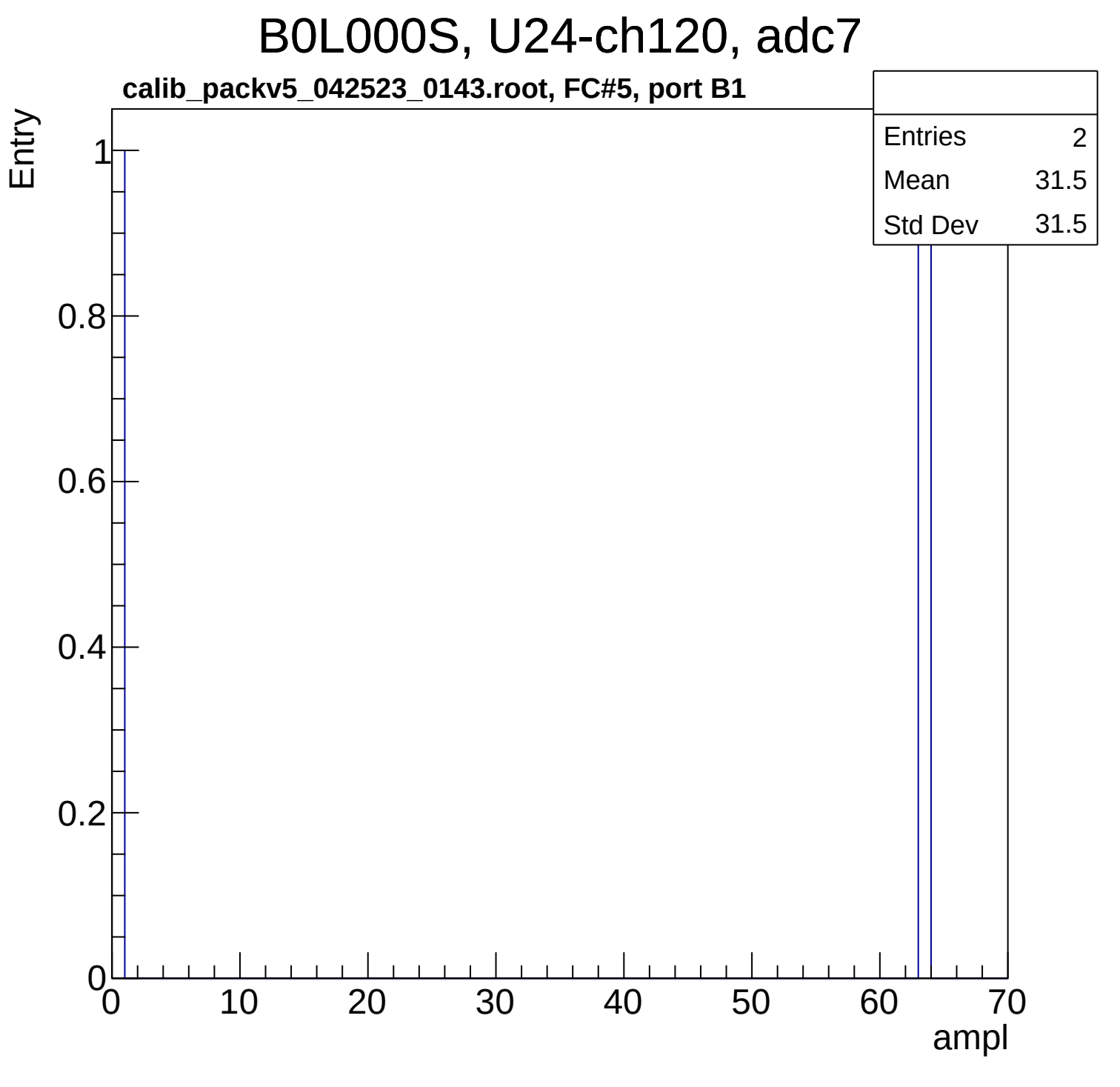
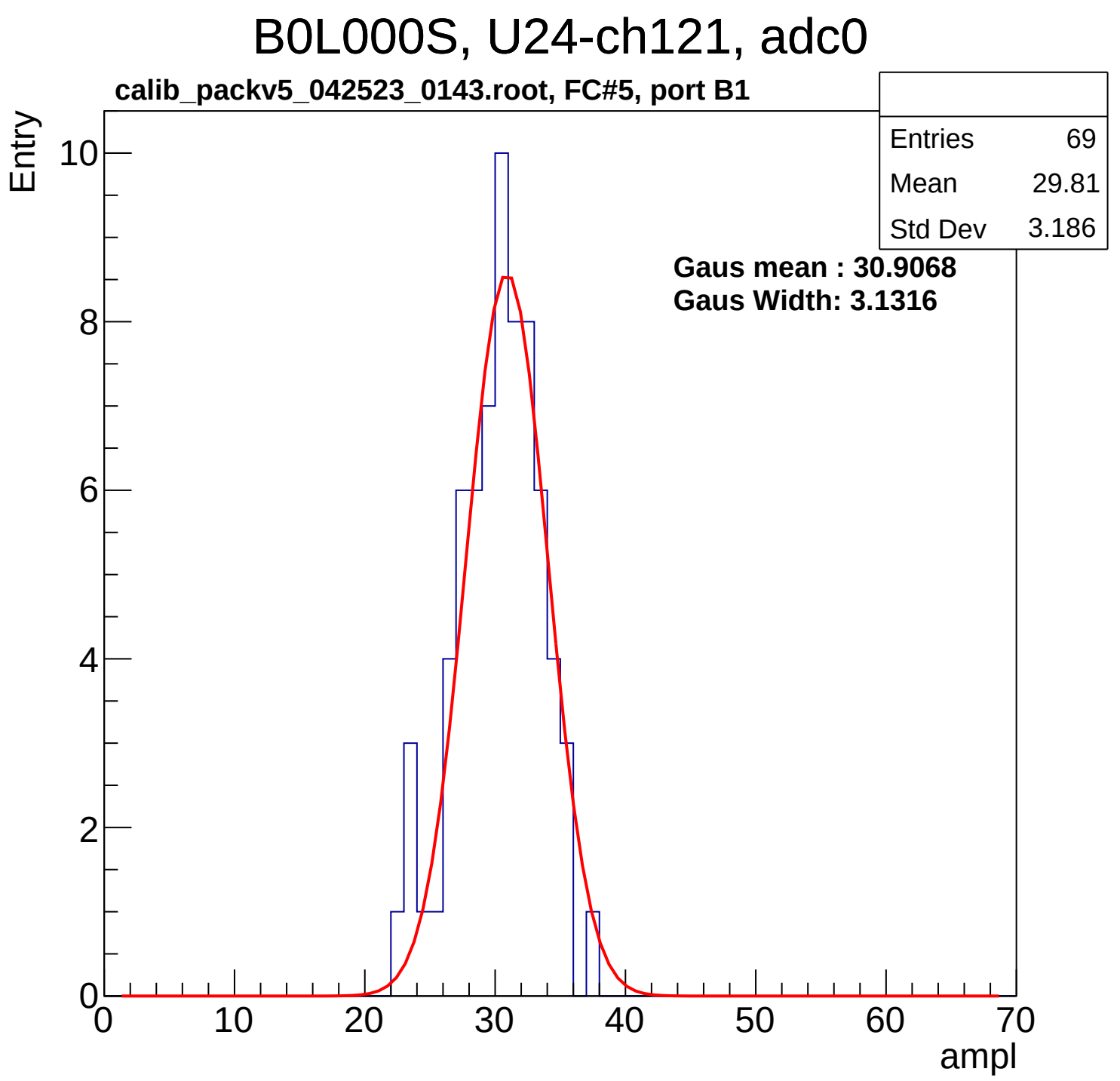
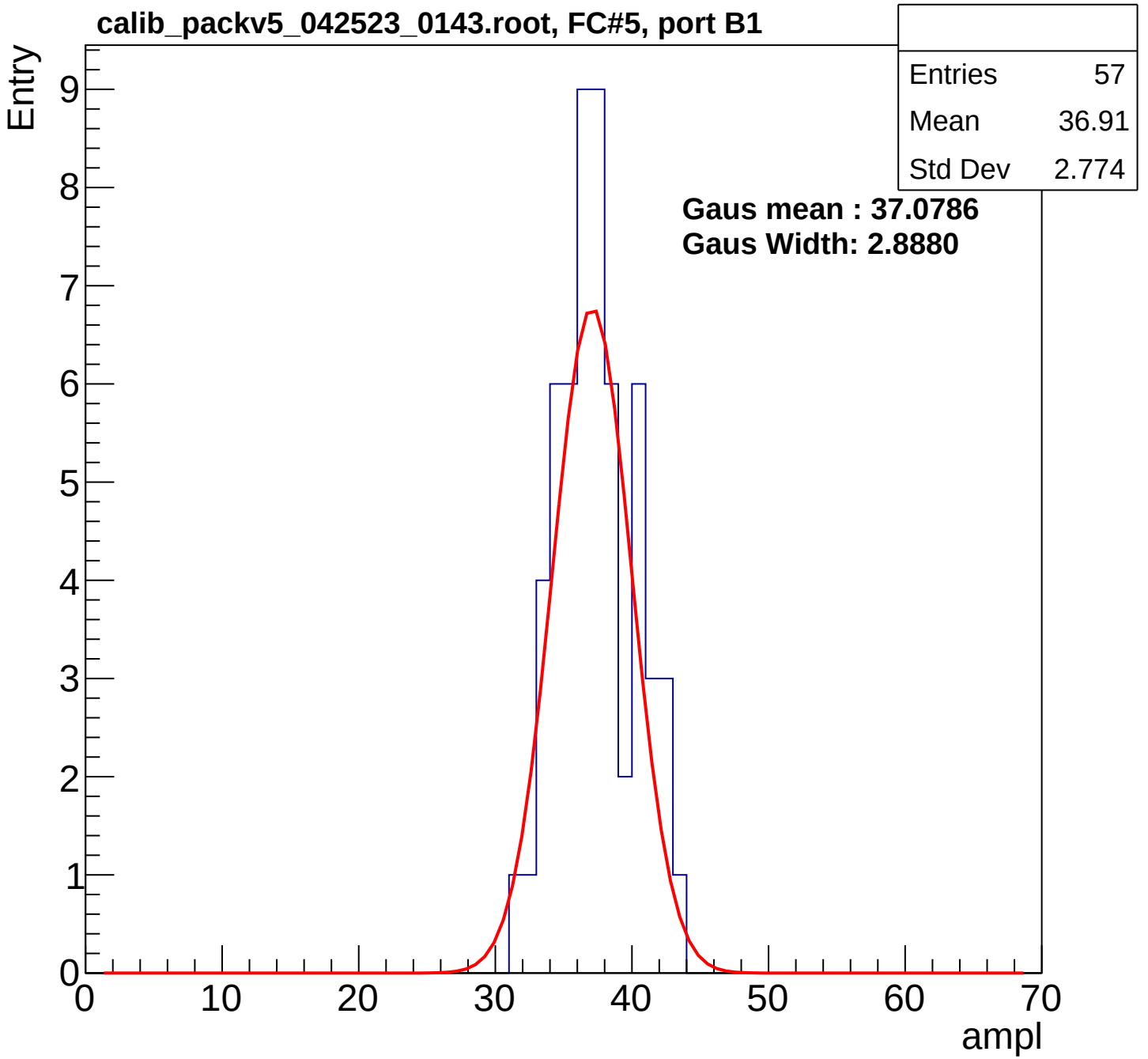
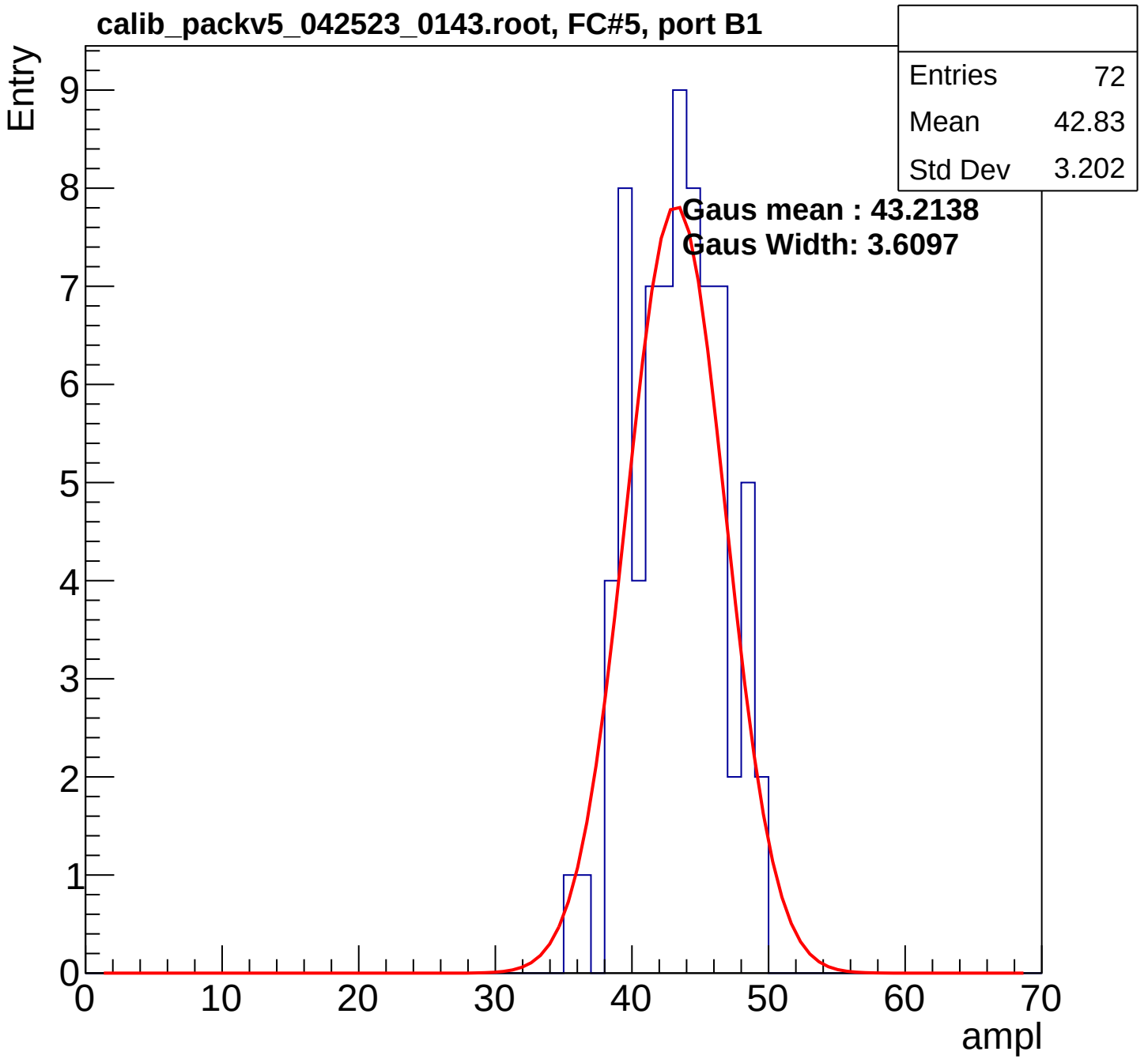
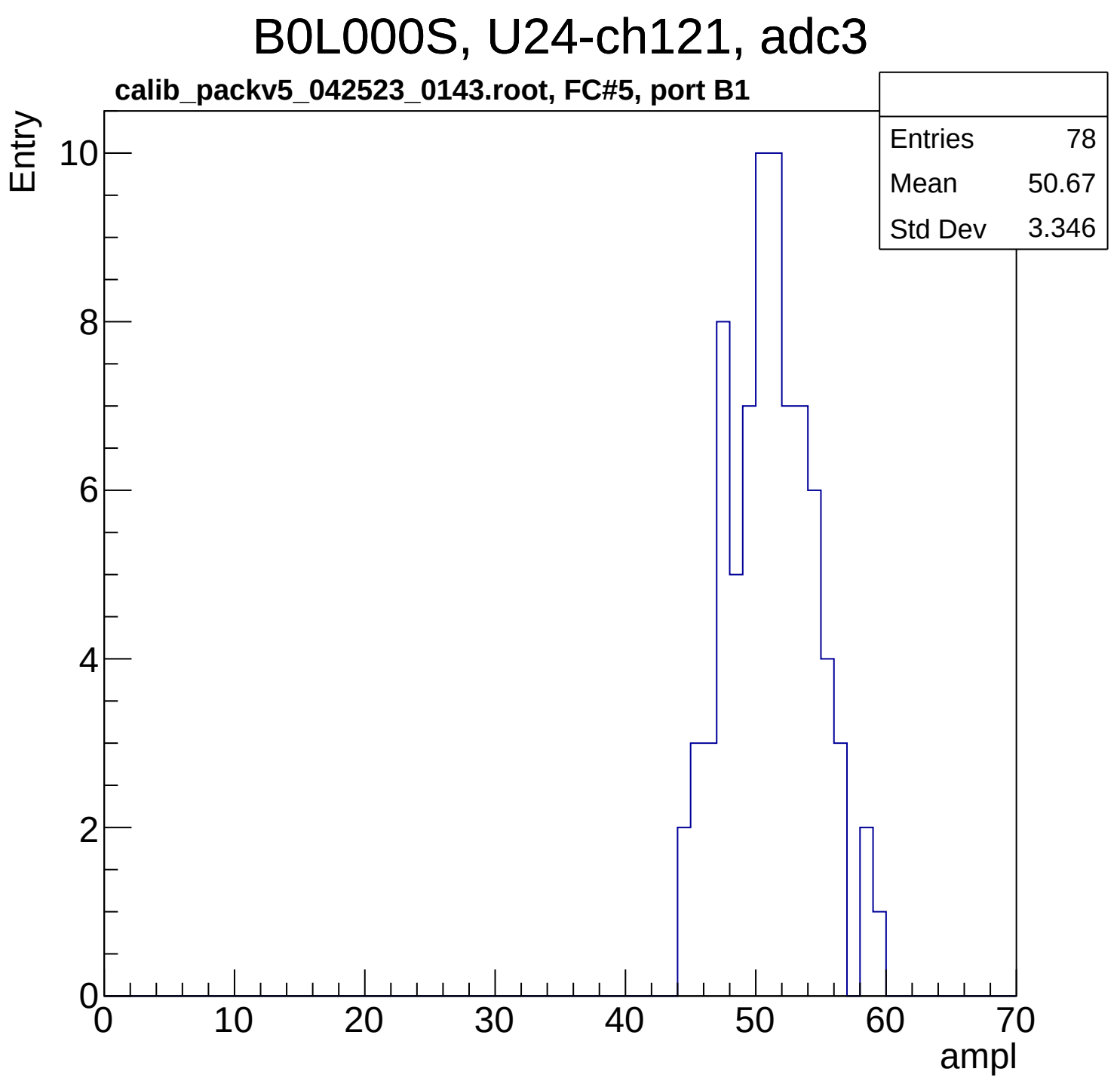

B0L000S, U24-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 60.9
Std Dev 1.868
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

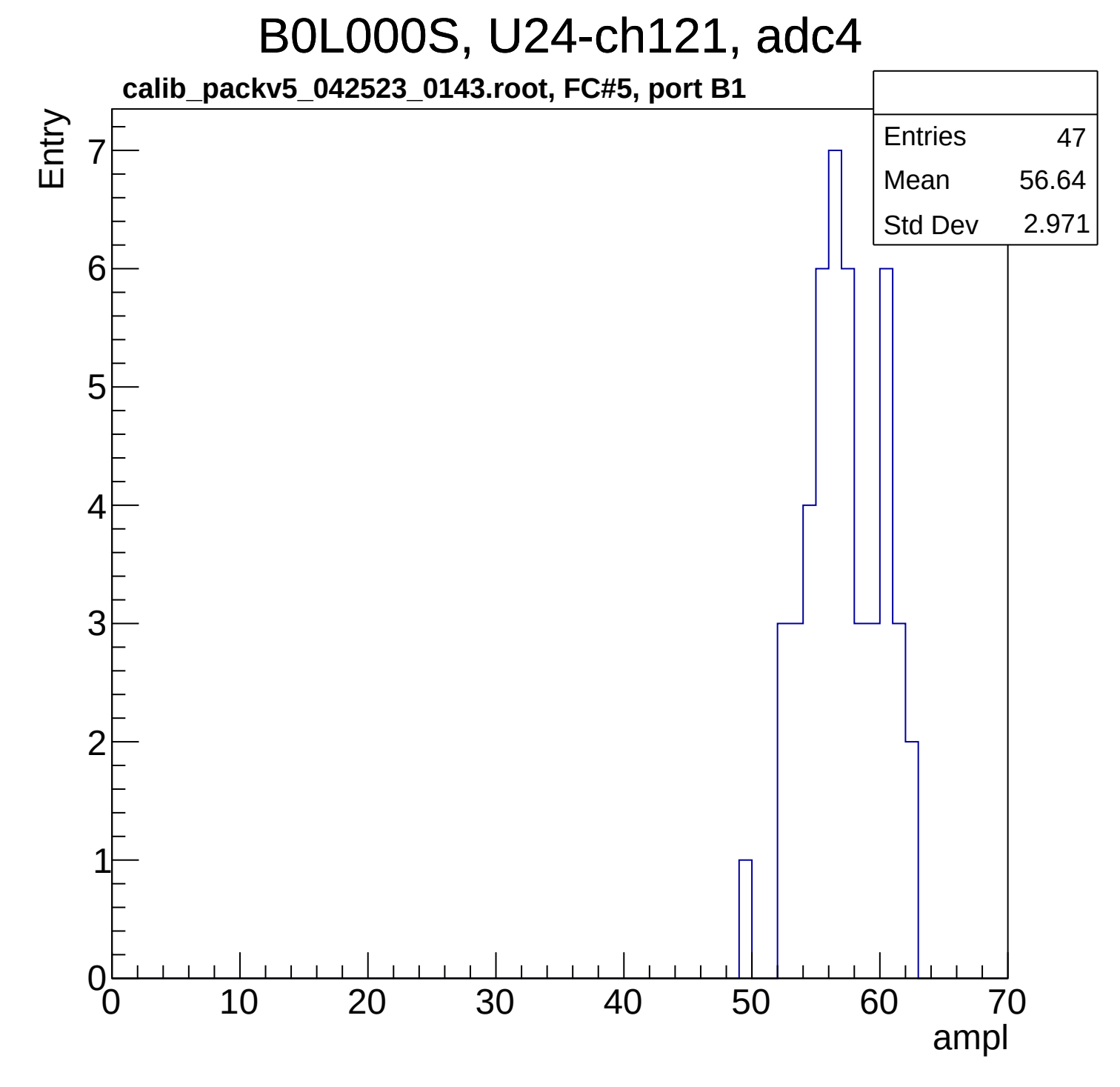

B0L000S, U24-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

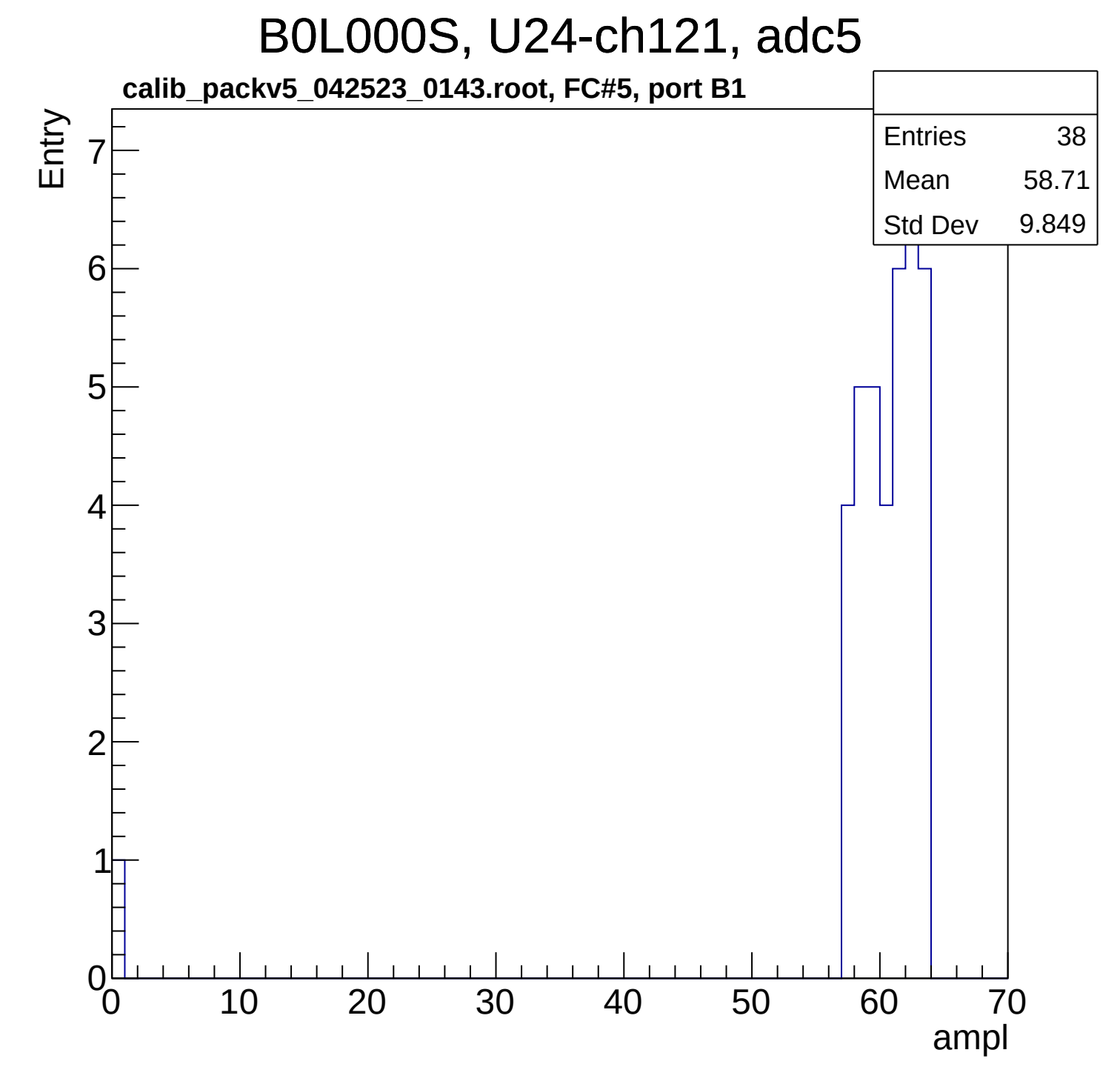

B0L000S, U24-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.81
Std Dev 3.186
Gaus mean : 30.9068
Gaus Width: 3.1316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.91
Std Dev 2.774
Gaus mean : 37.0786
Gaus Width: 2.8880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.83
Std Dev 3.202
Gaus mean : 43.2138
Gaus Width: 3.6097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 50.67
Std Dev 3.346
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 56.64
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.71
Std Dev 9.849
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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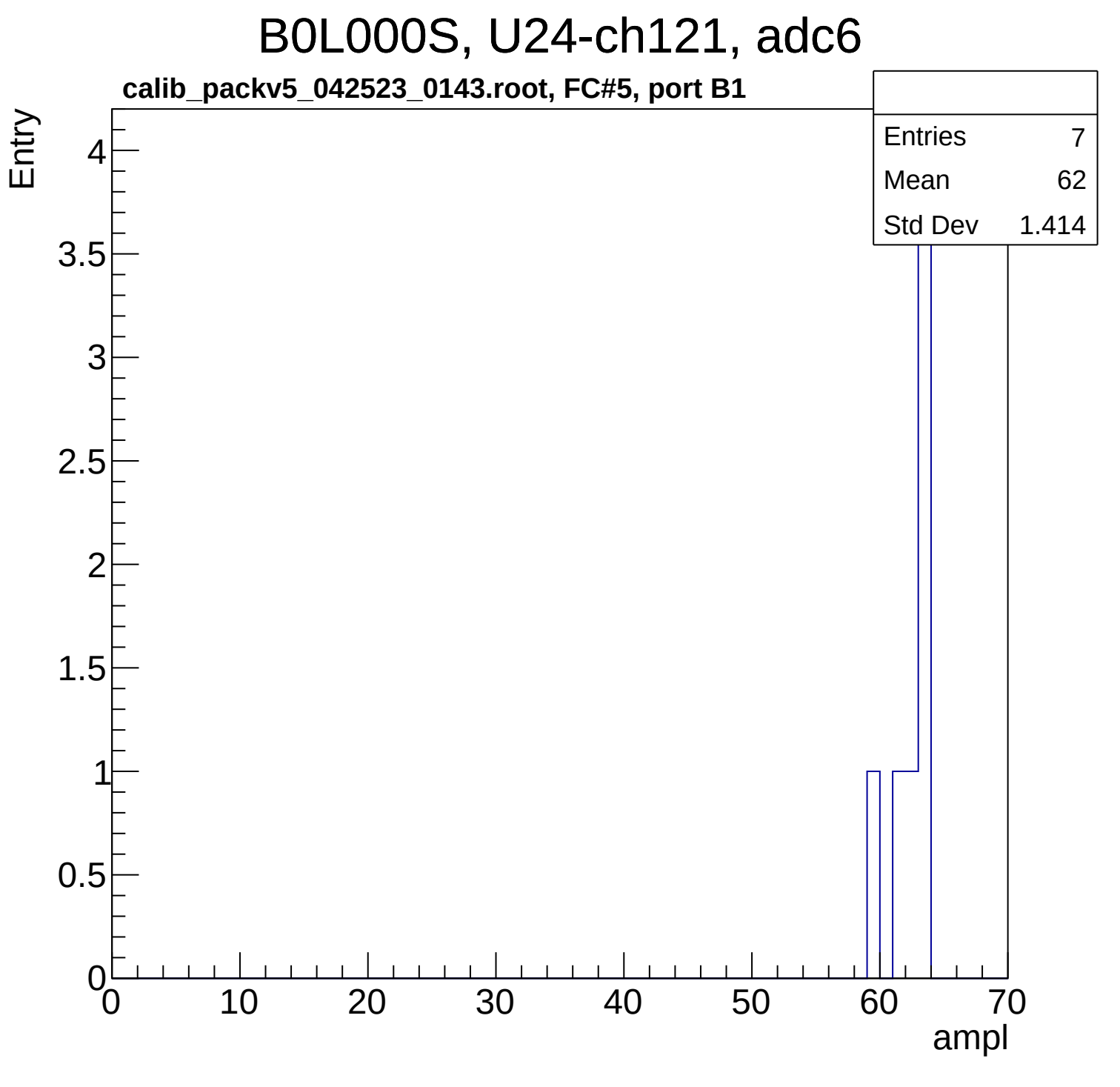
B0L000S, U24-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
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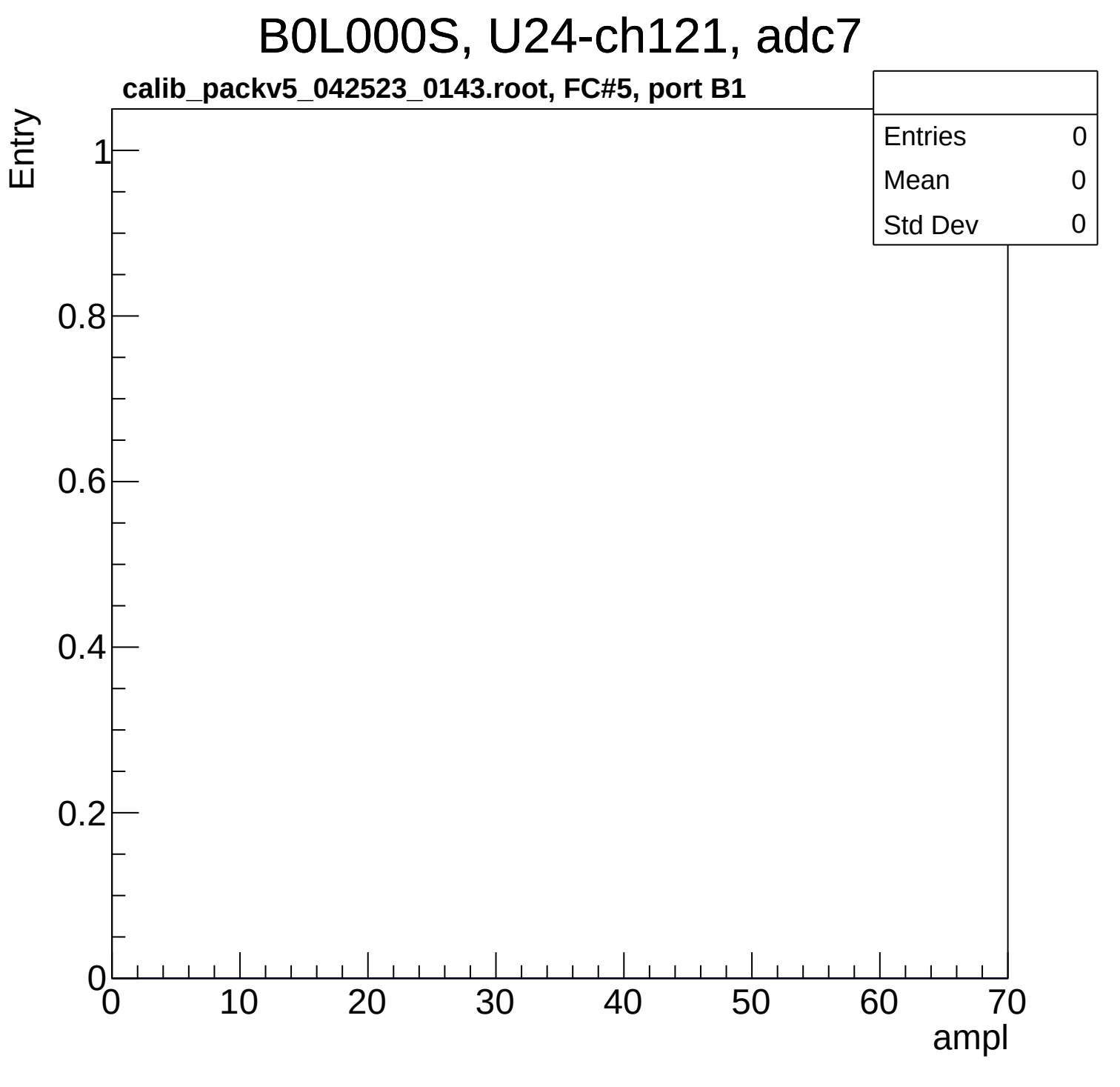
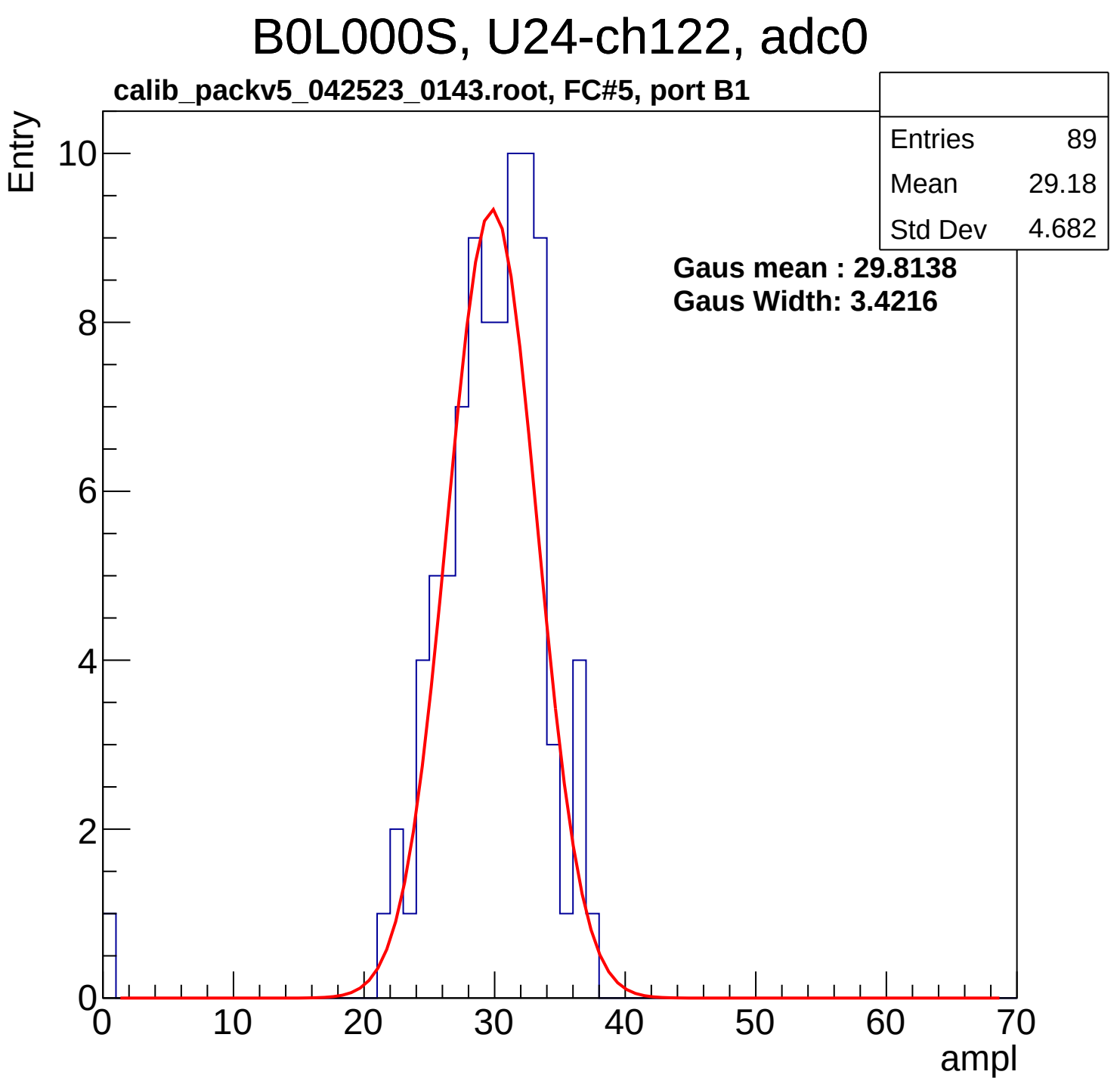
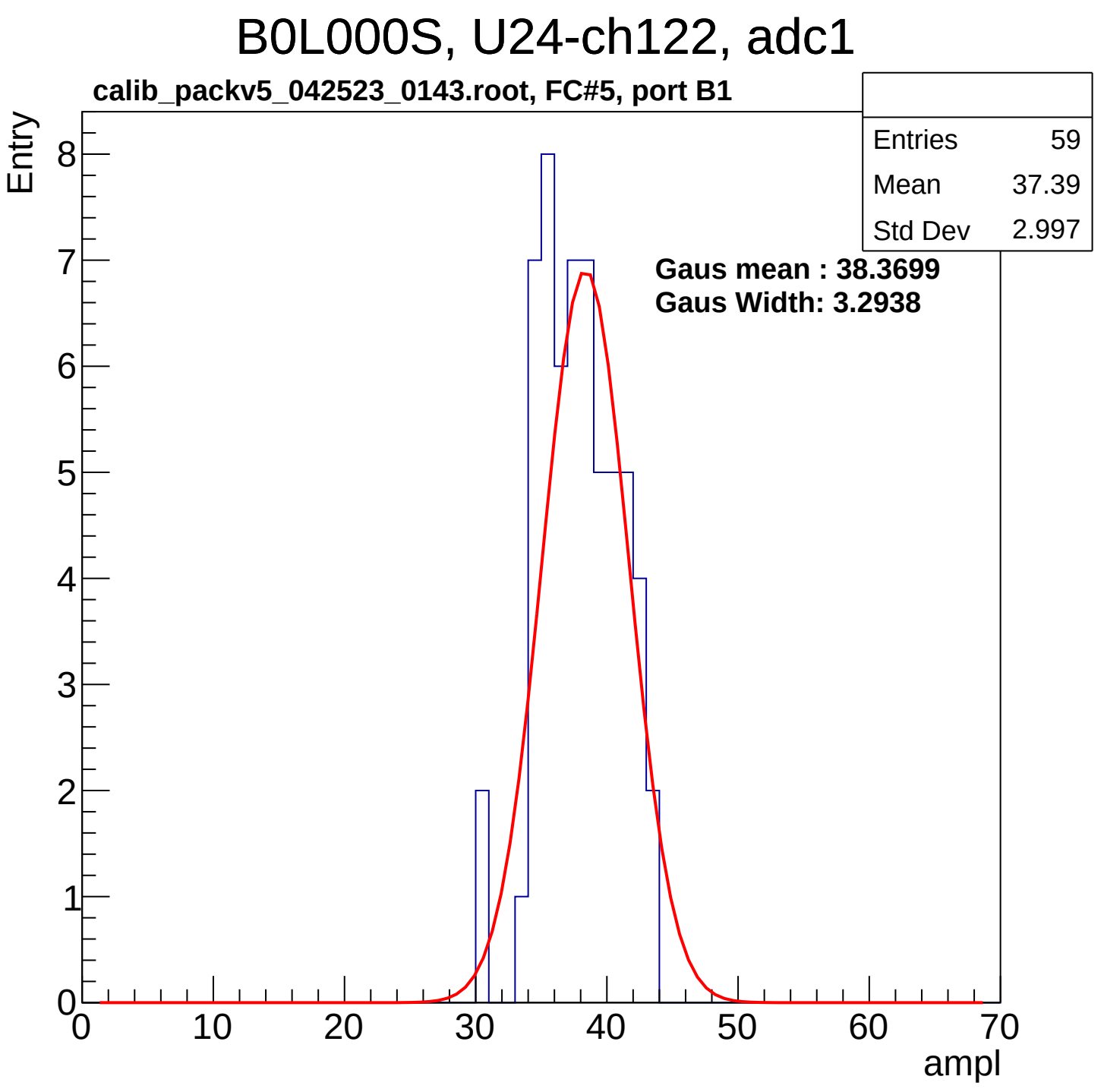
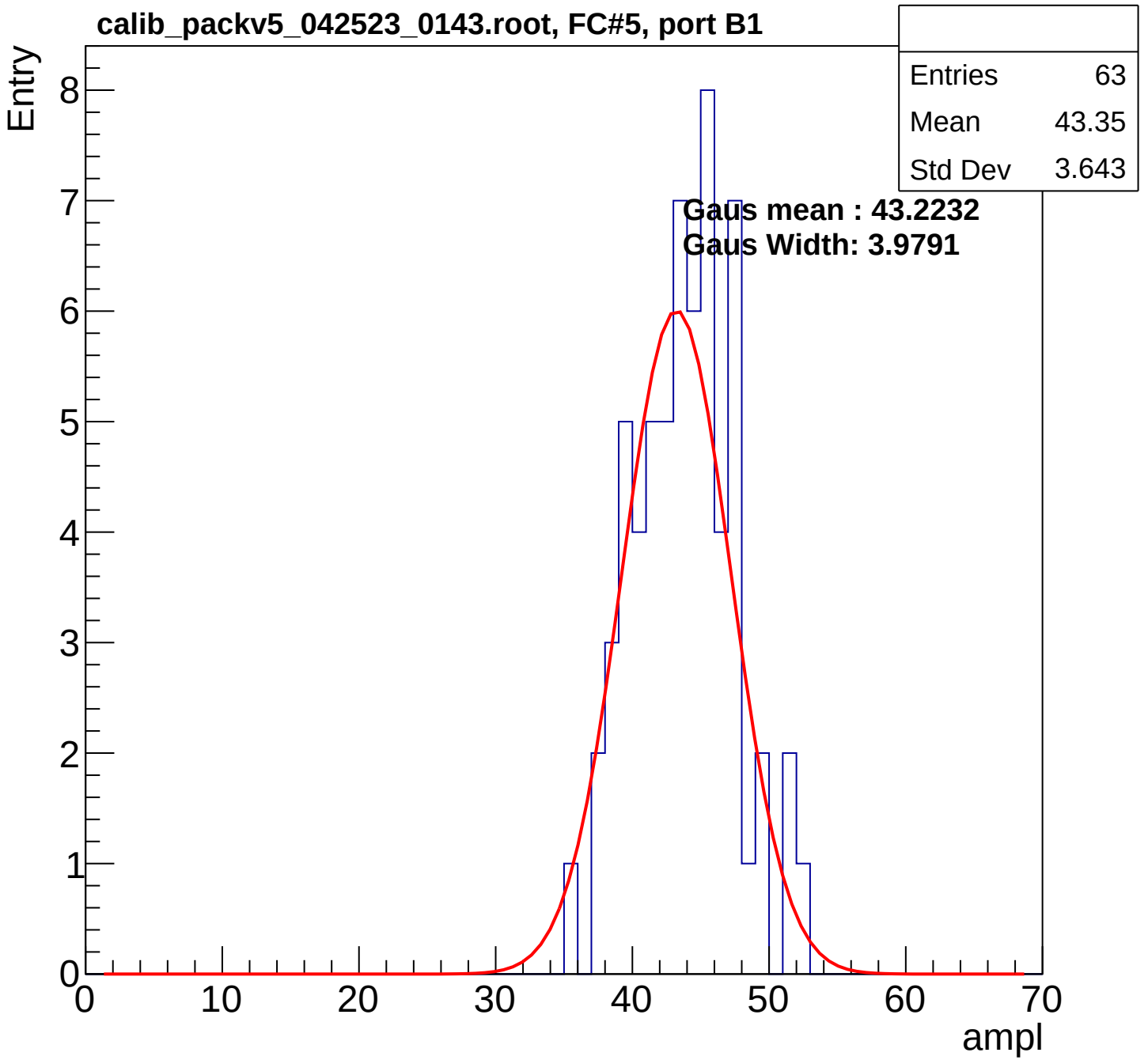
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U24-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 89
Mean 29.18
Std Dev 4.682
Gaus mean : 29.8138
Gaus Width: 3.4216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.39
Std Dev 2.997
Gaus mean : 38.3699
Gaus Width: 3.2938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.35
Std Dev 3.643
Gaus mean : 43.2232
Gaus Width: 3.9791
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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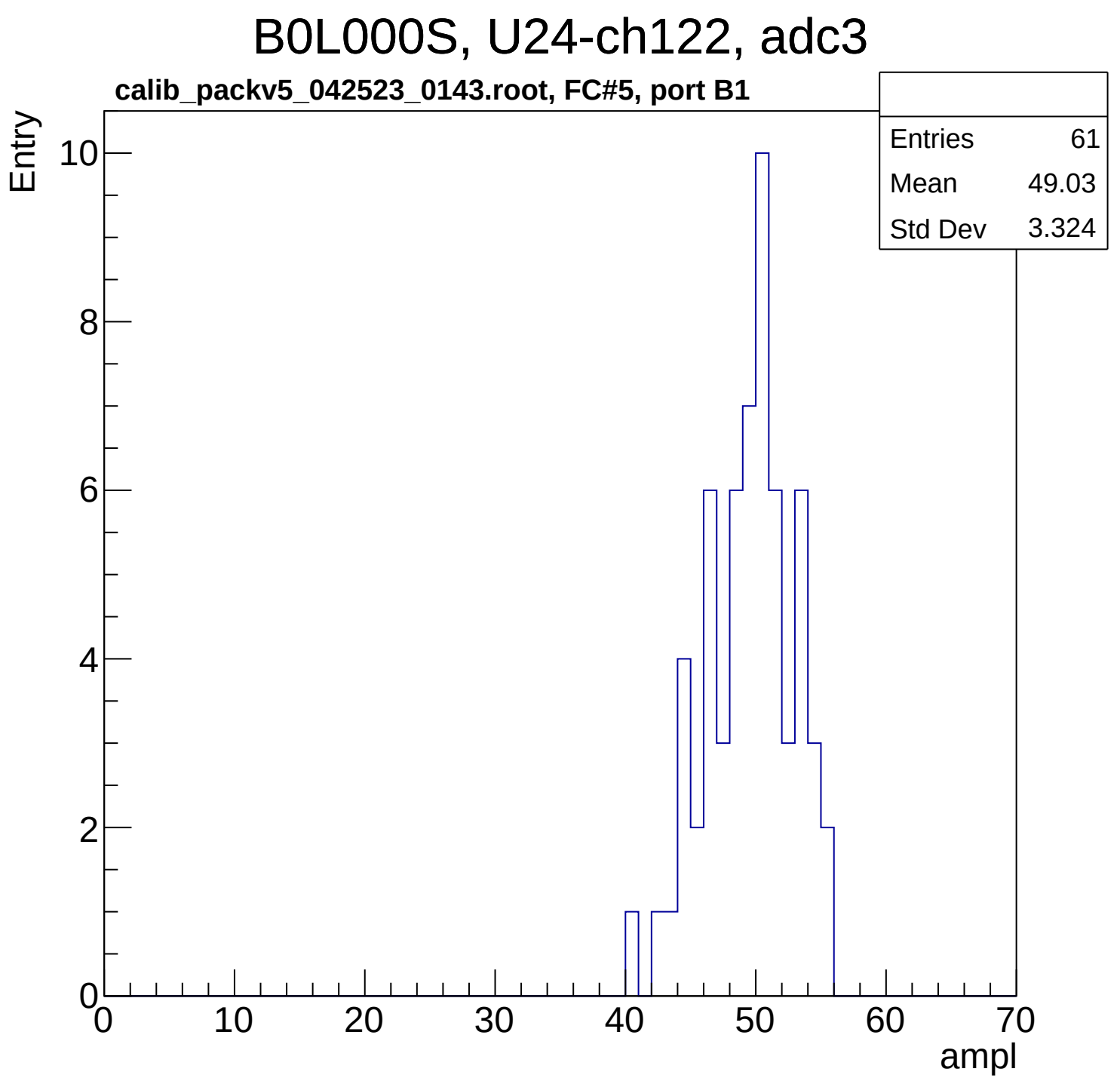
B0L000S, U24-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.03
Std Dev 3.324
Entry
ampl
0
2
4
6
8
10
0
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60
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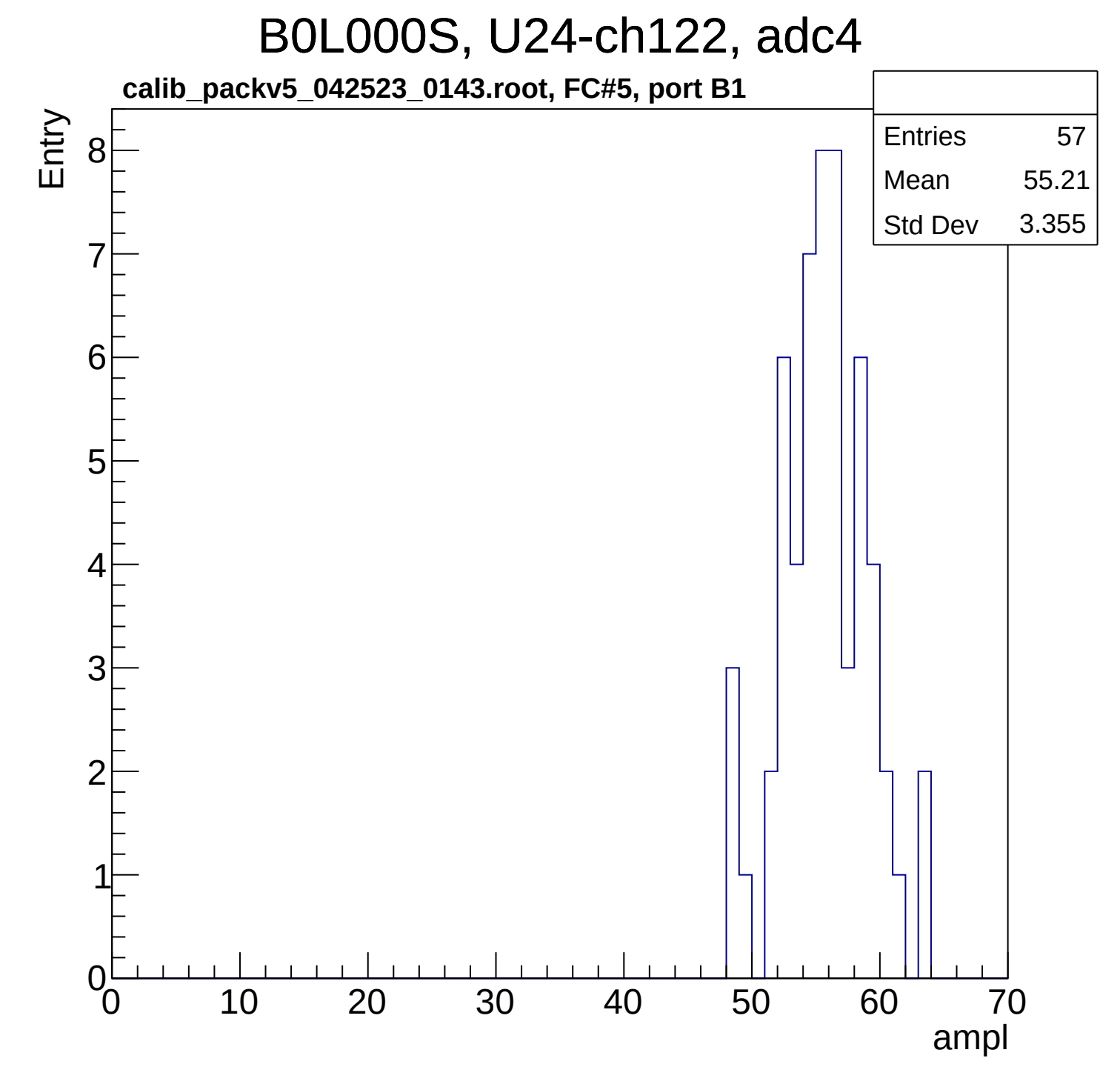
B0L000S, U24-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.21
Std Dev 3.355
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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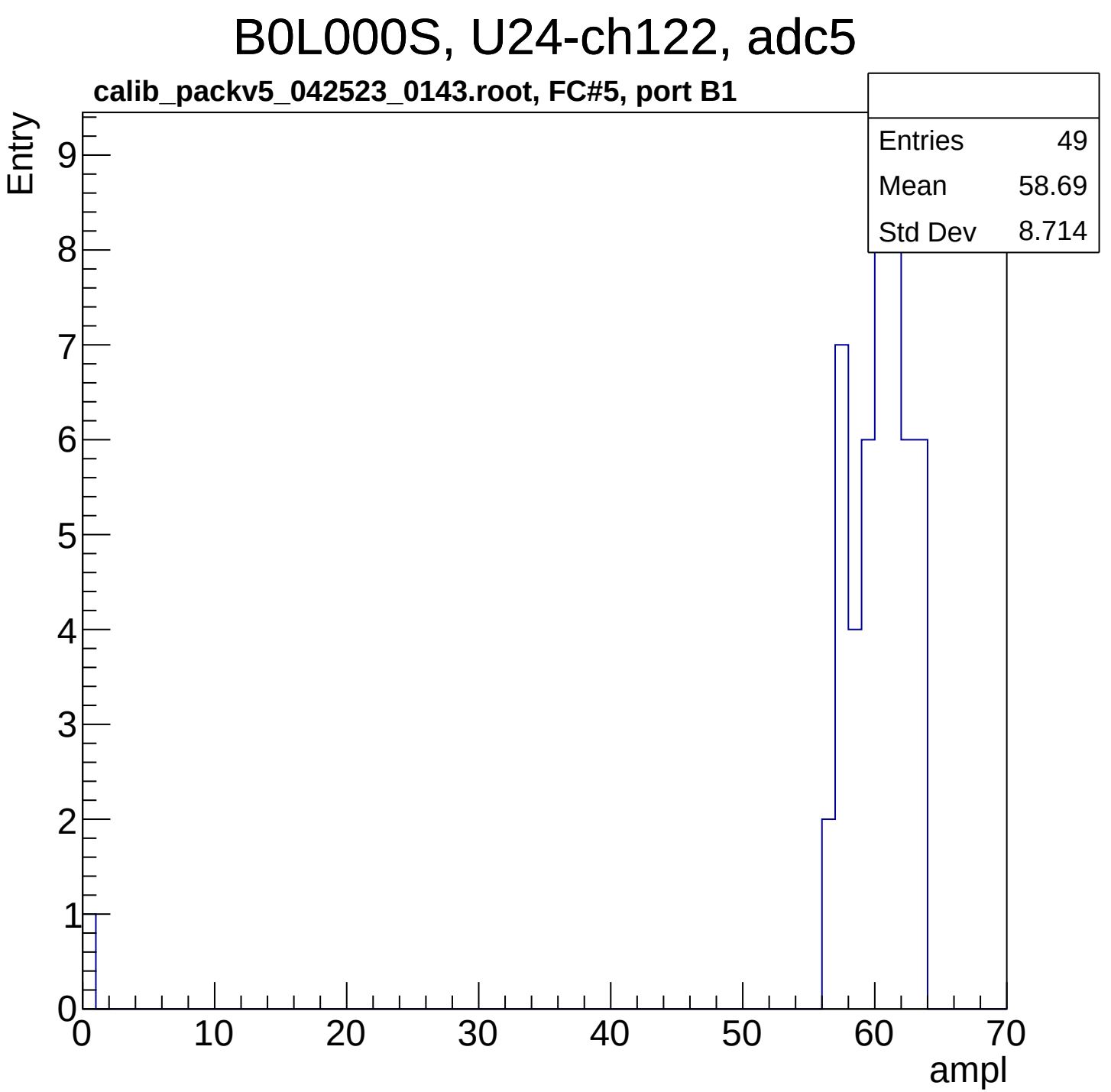
B0L000S, U24-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.69
Std Dev 8.714
Entry
ampl
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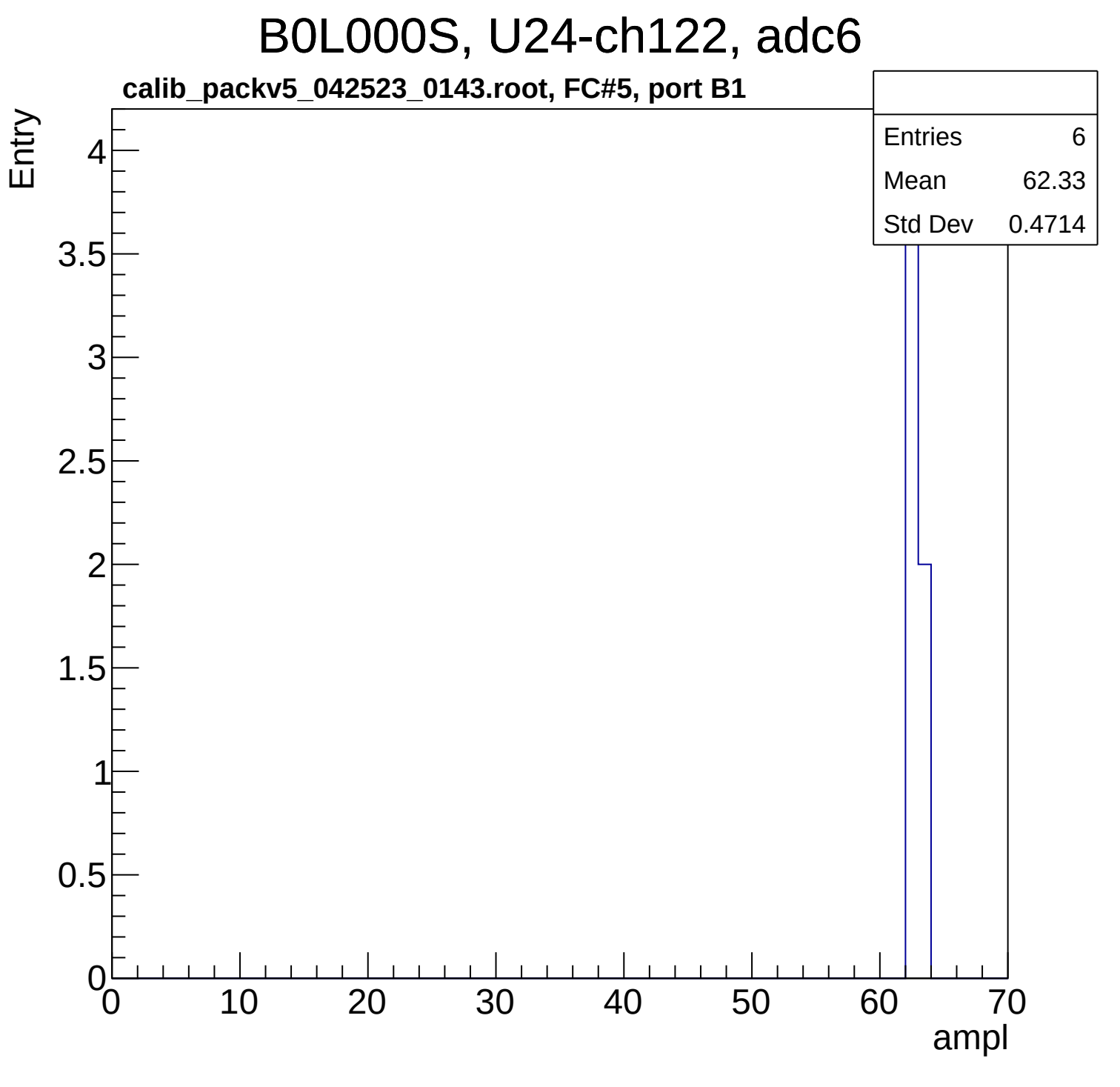
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
50
60
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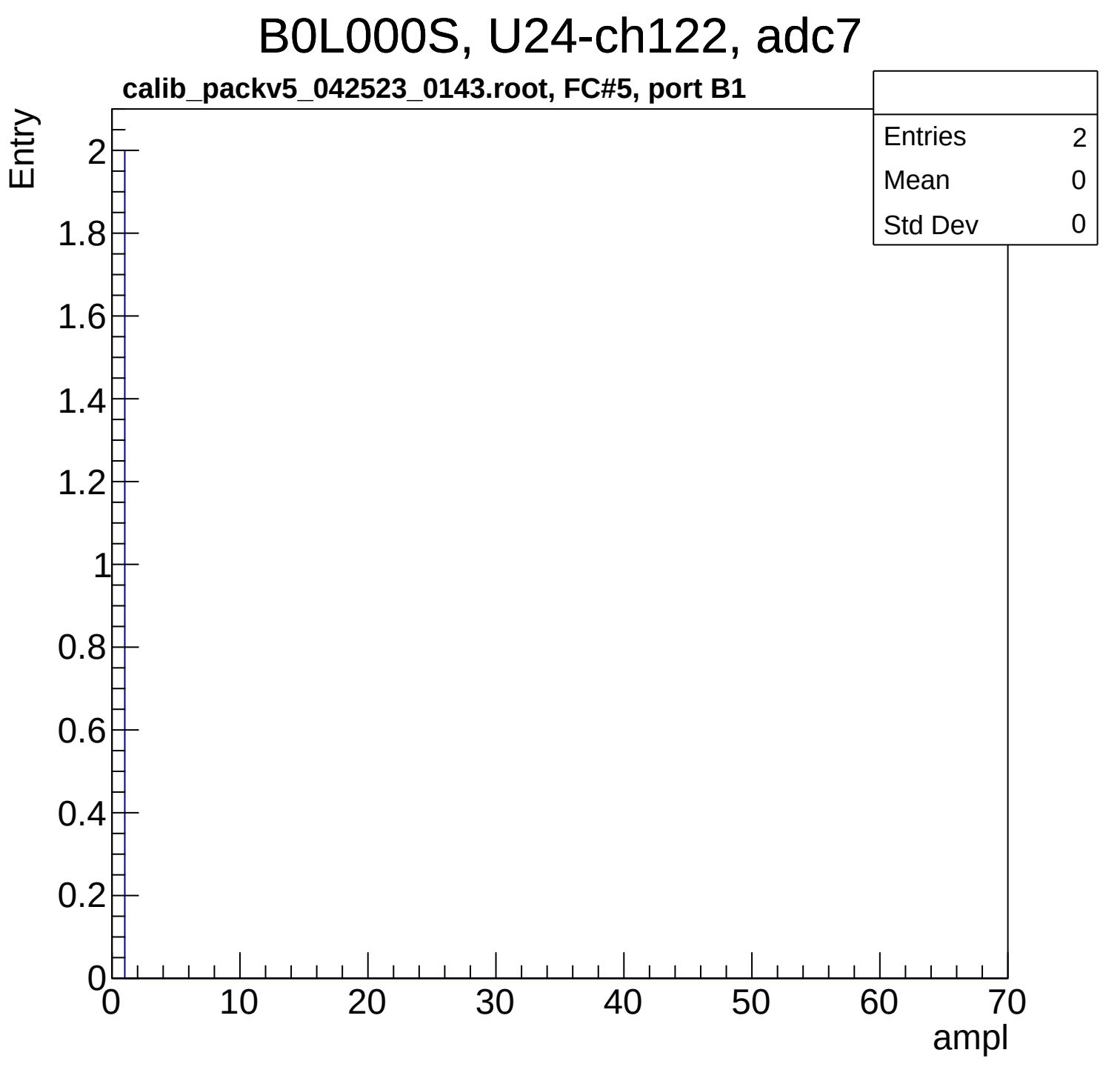
B0L000S, U24-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
20
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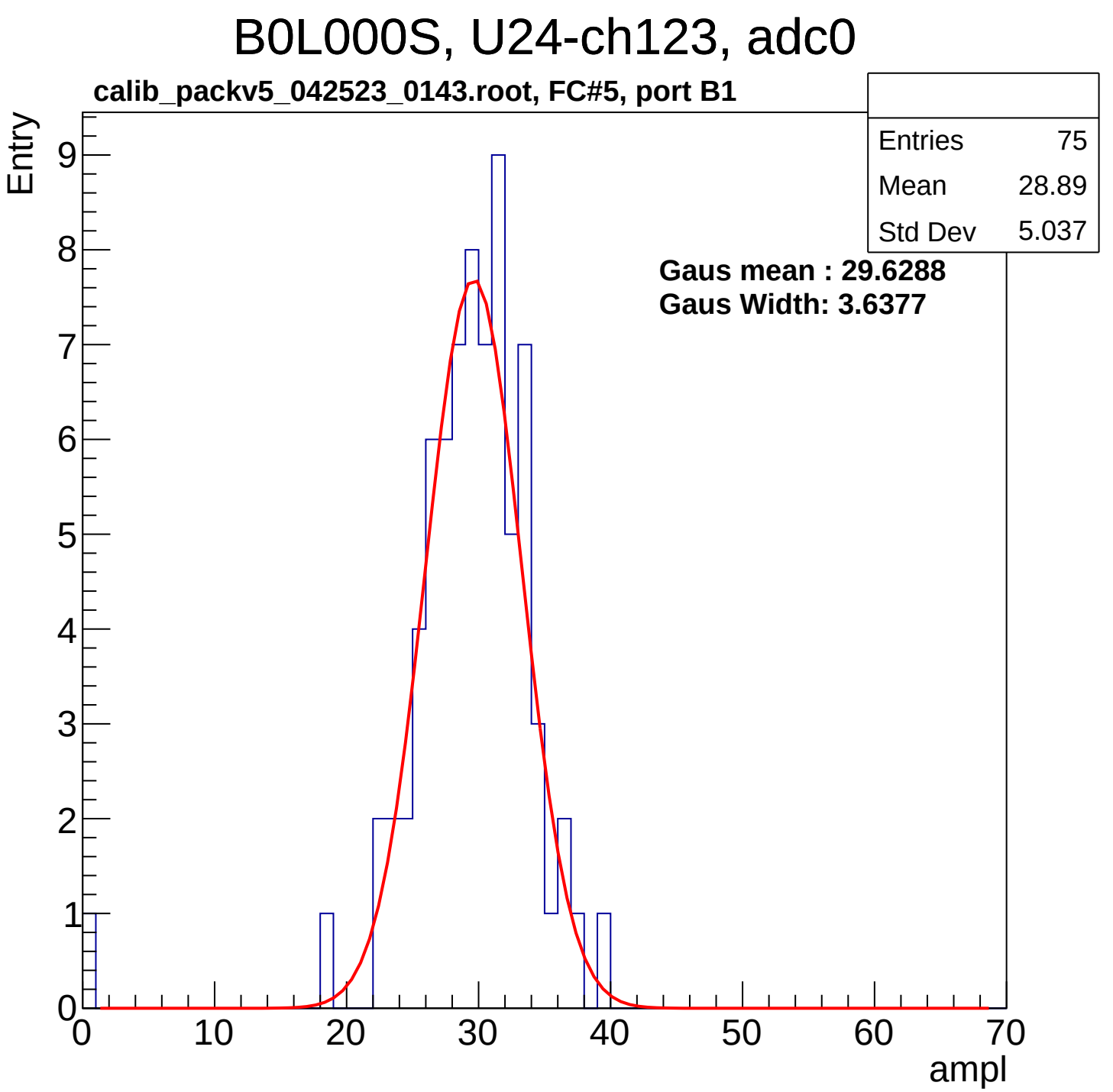
50
60
70
ampl


B0L000S, U24-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.89
Std Dev 5.037
Gaus mean : 29.6288
Gaus Width: 3.6377
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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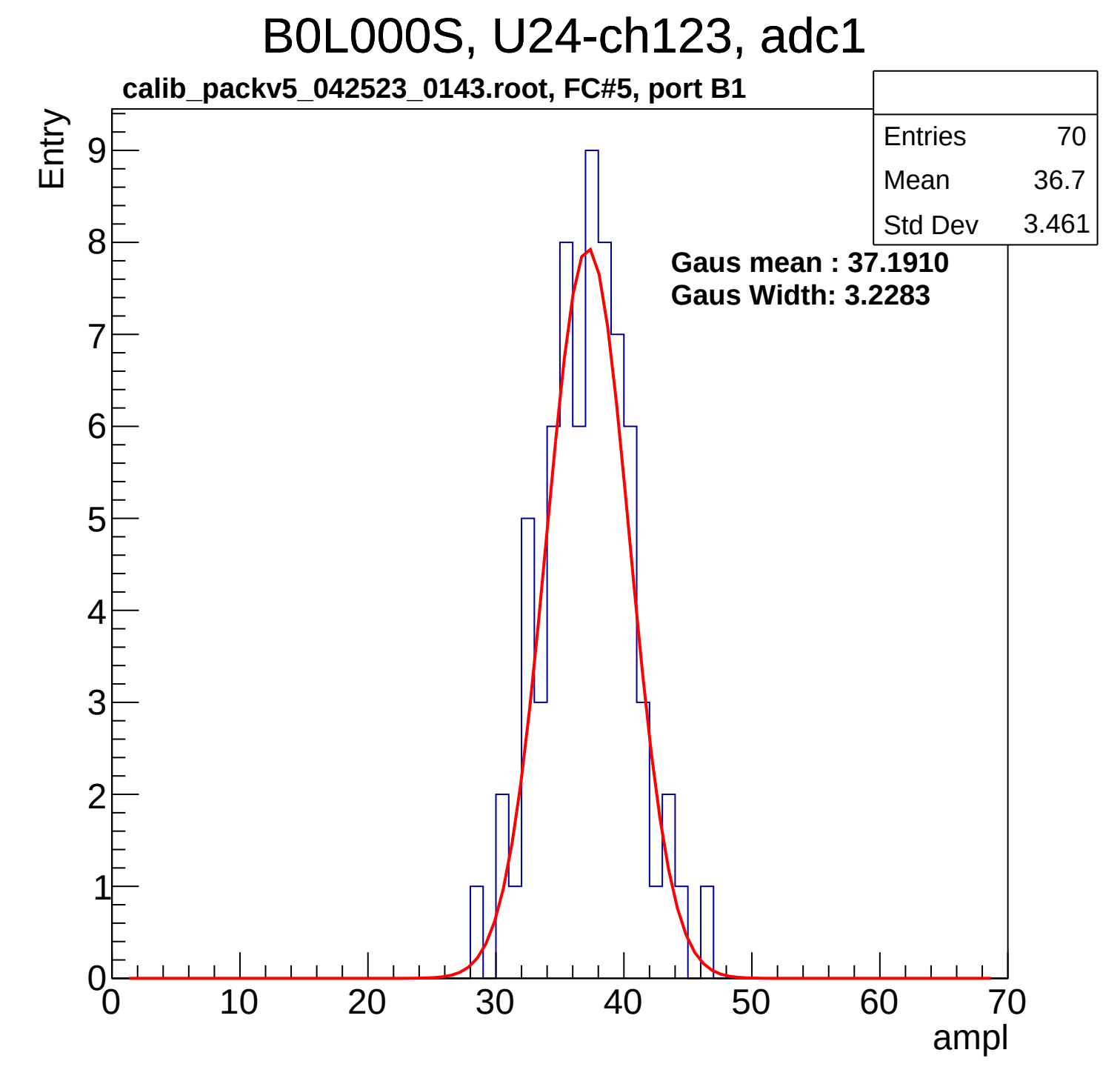
B0L000S, U24-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.7
Std Dev 3.461
Gaus mean : 37.1910
Gaus Width: 3.2283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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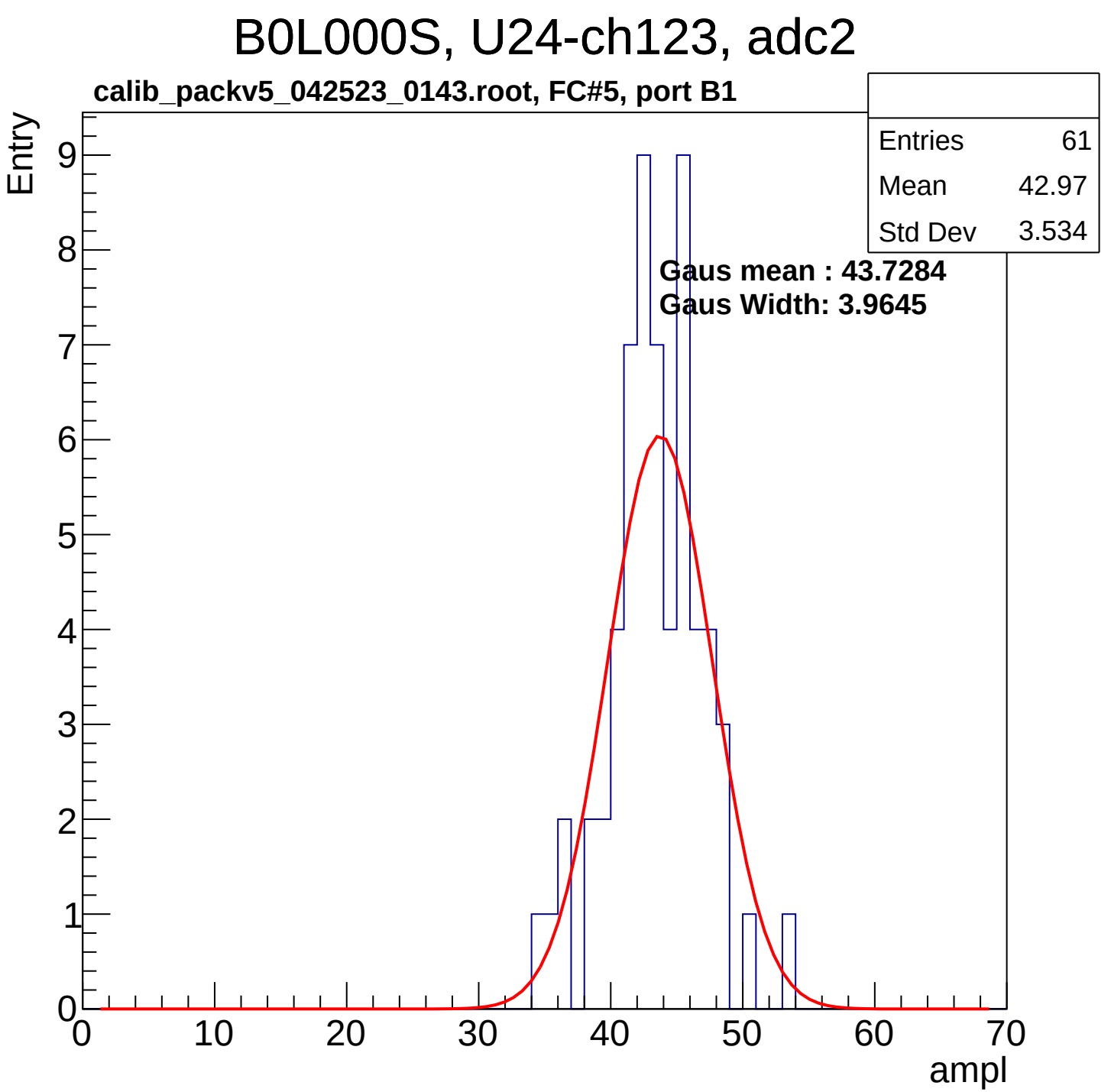
B0L000S, U24-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.97
Std Dev 3.534
Gaus mean : 43.7284
Gaus Width: 3.9645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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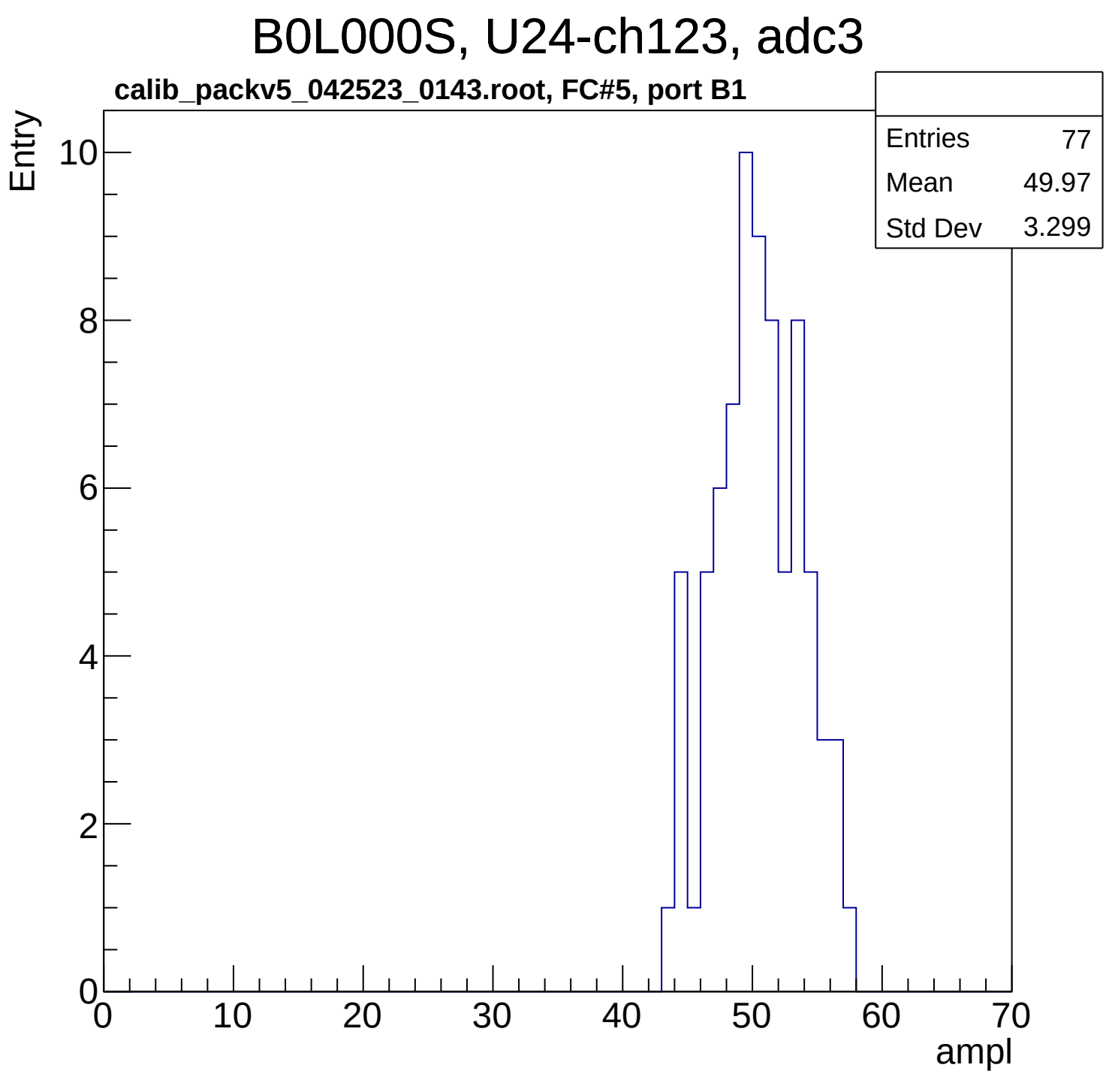
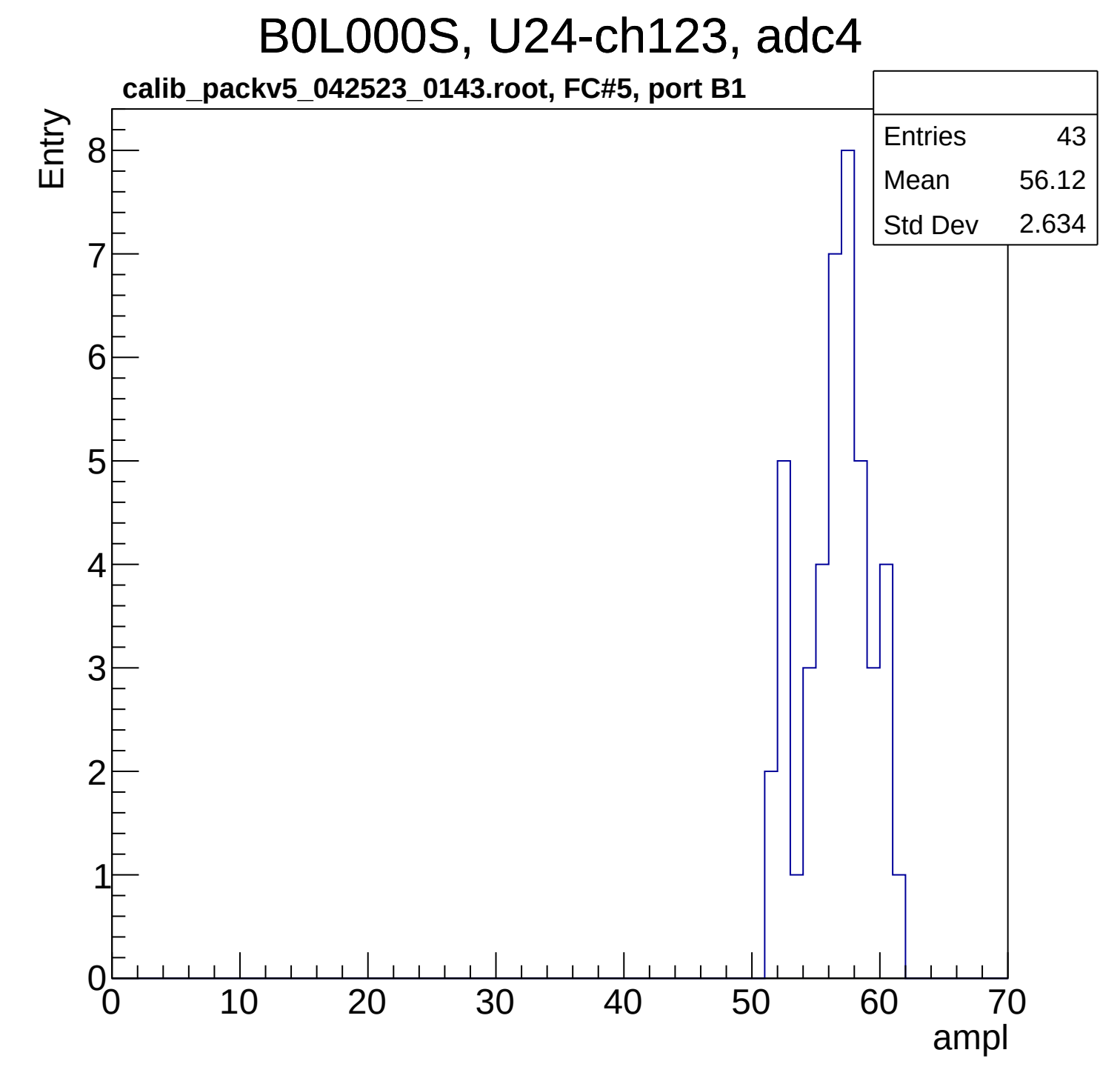
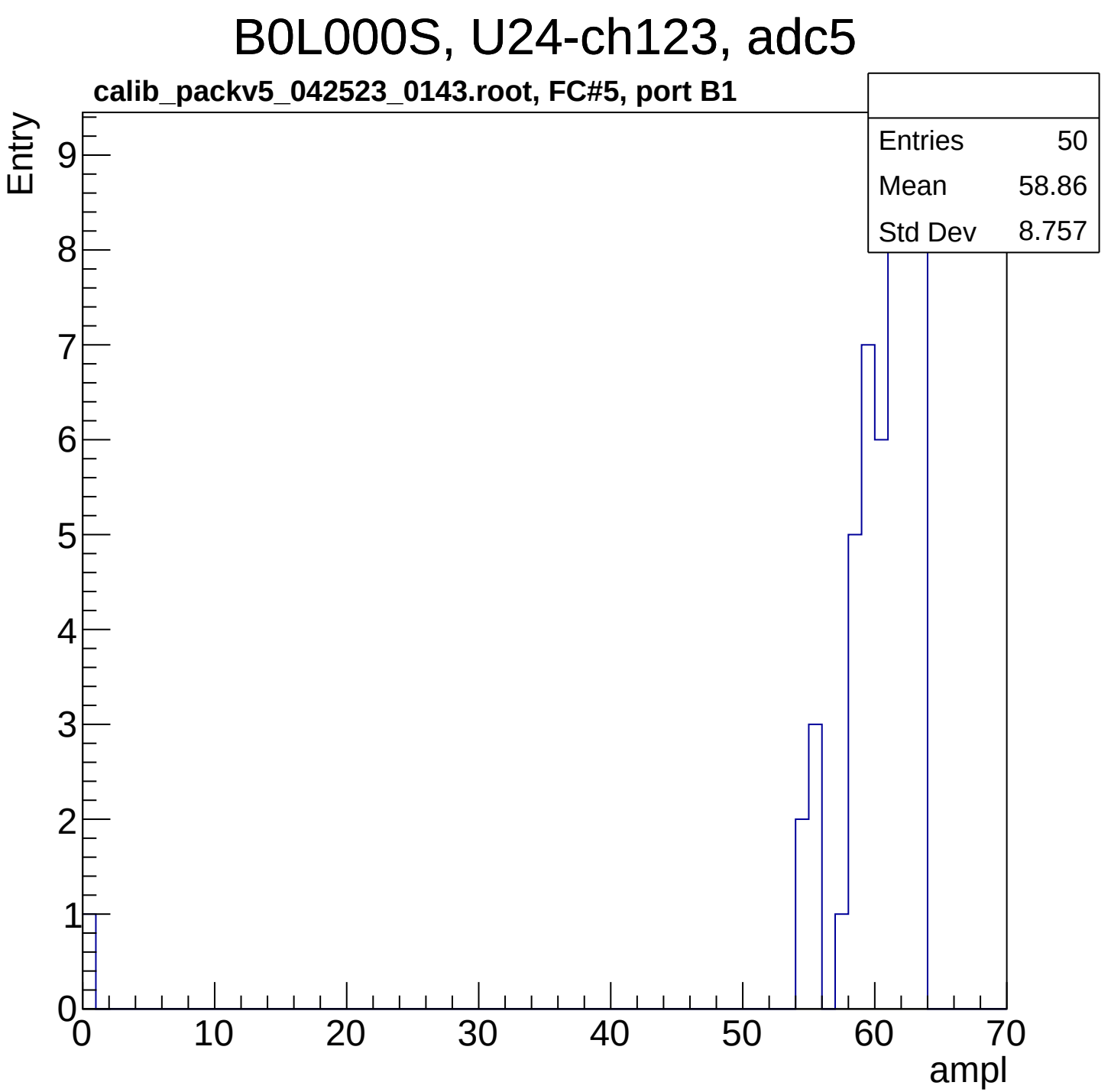

B0L000S, U24-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 49.97
Std Dev 3.299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 56.12
Std Dev 2.634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.86
Std Dev 8.757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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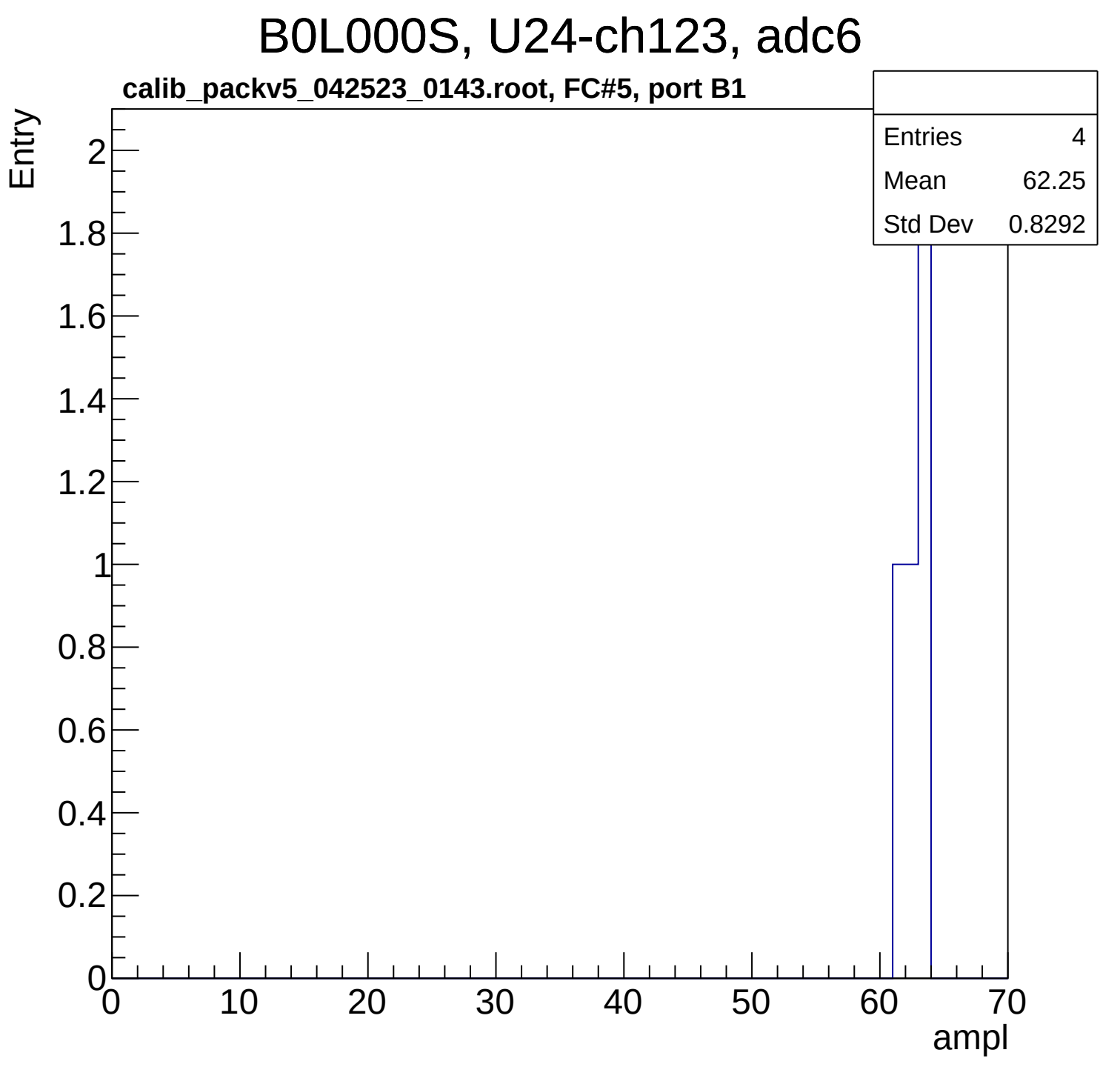
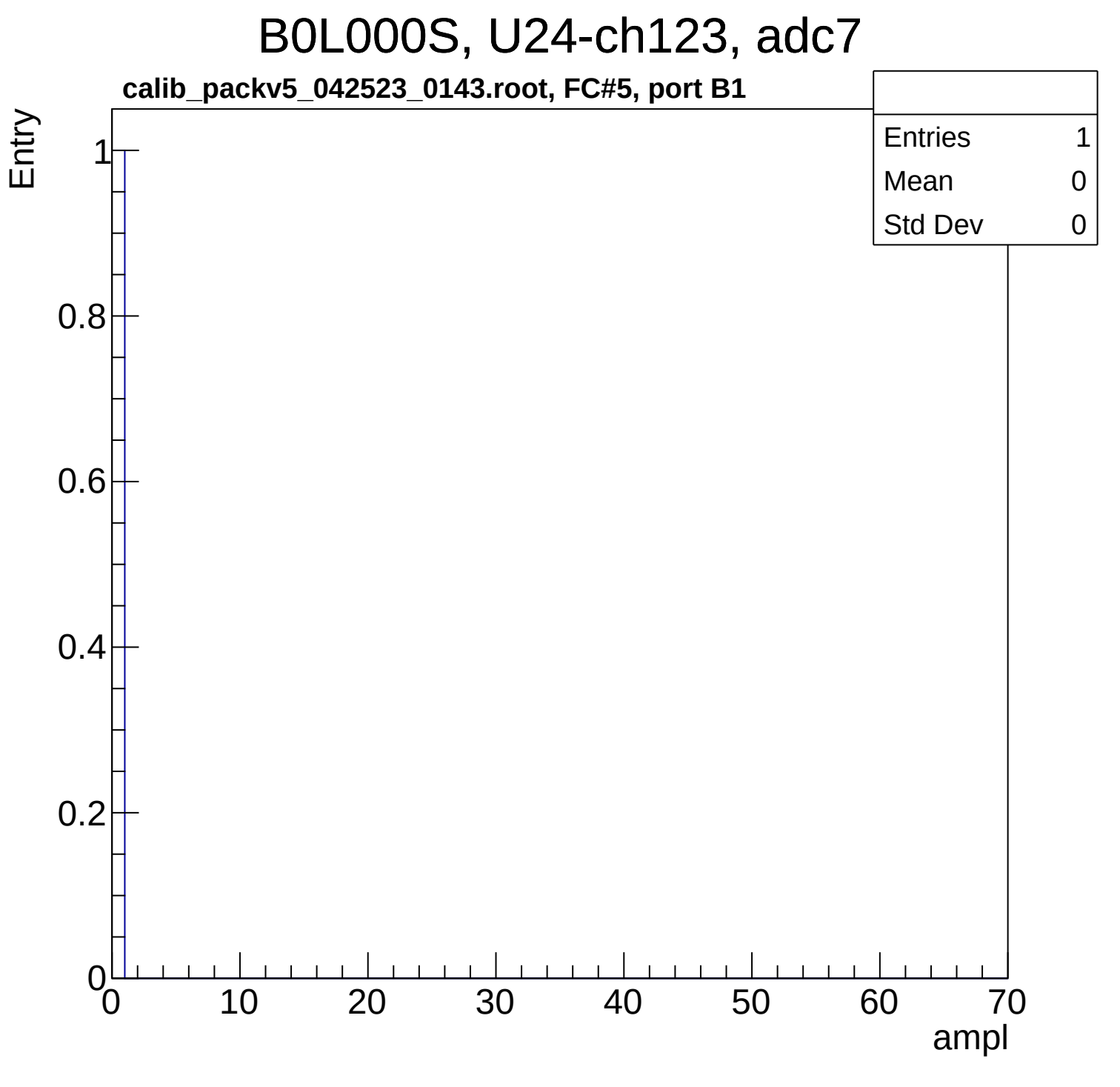
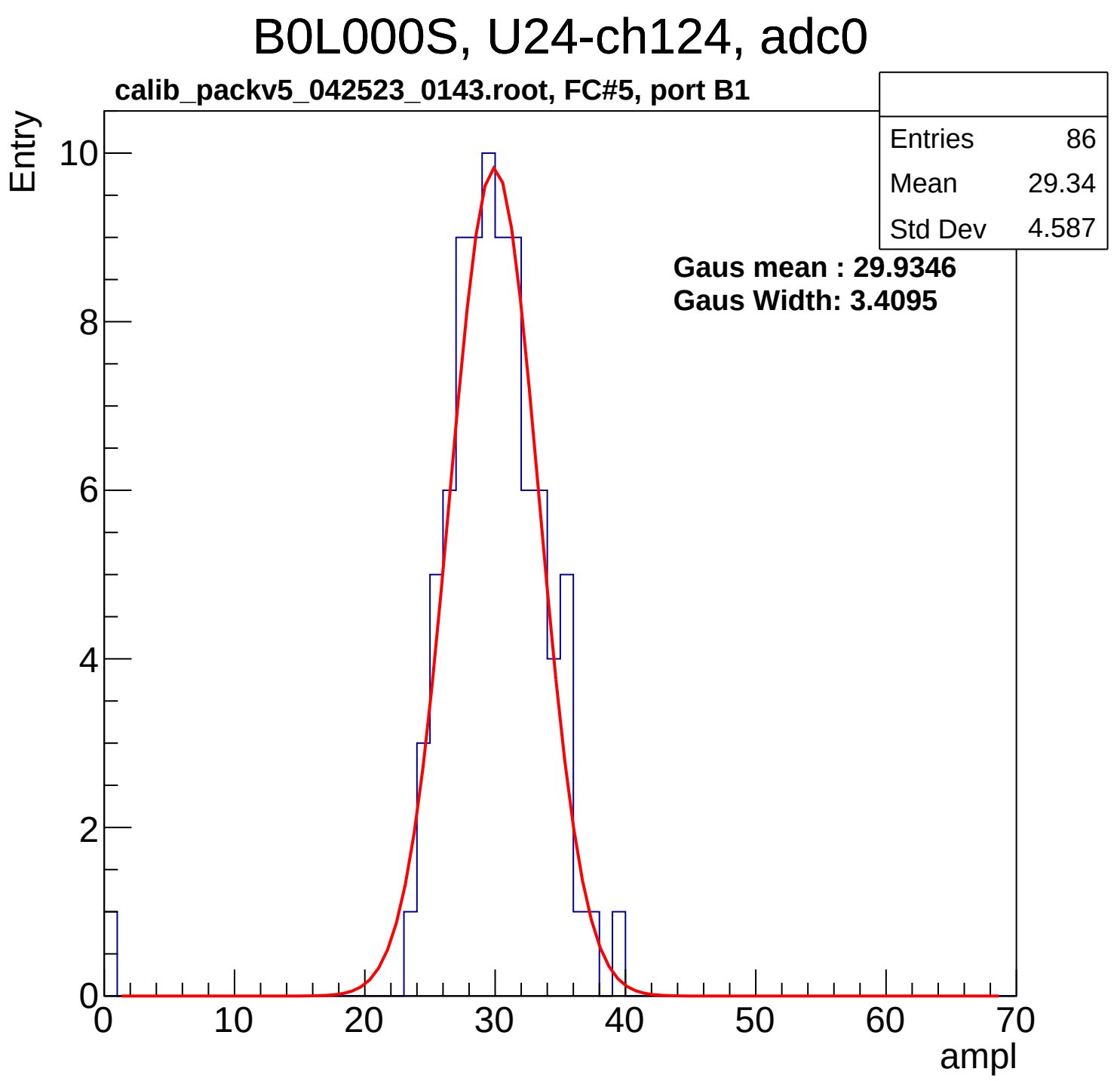
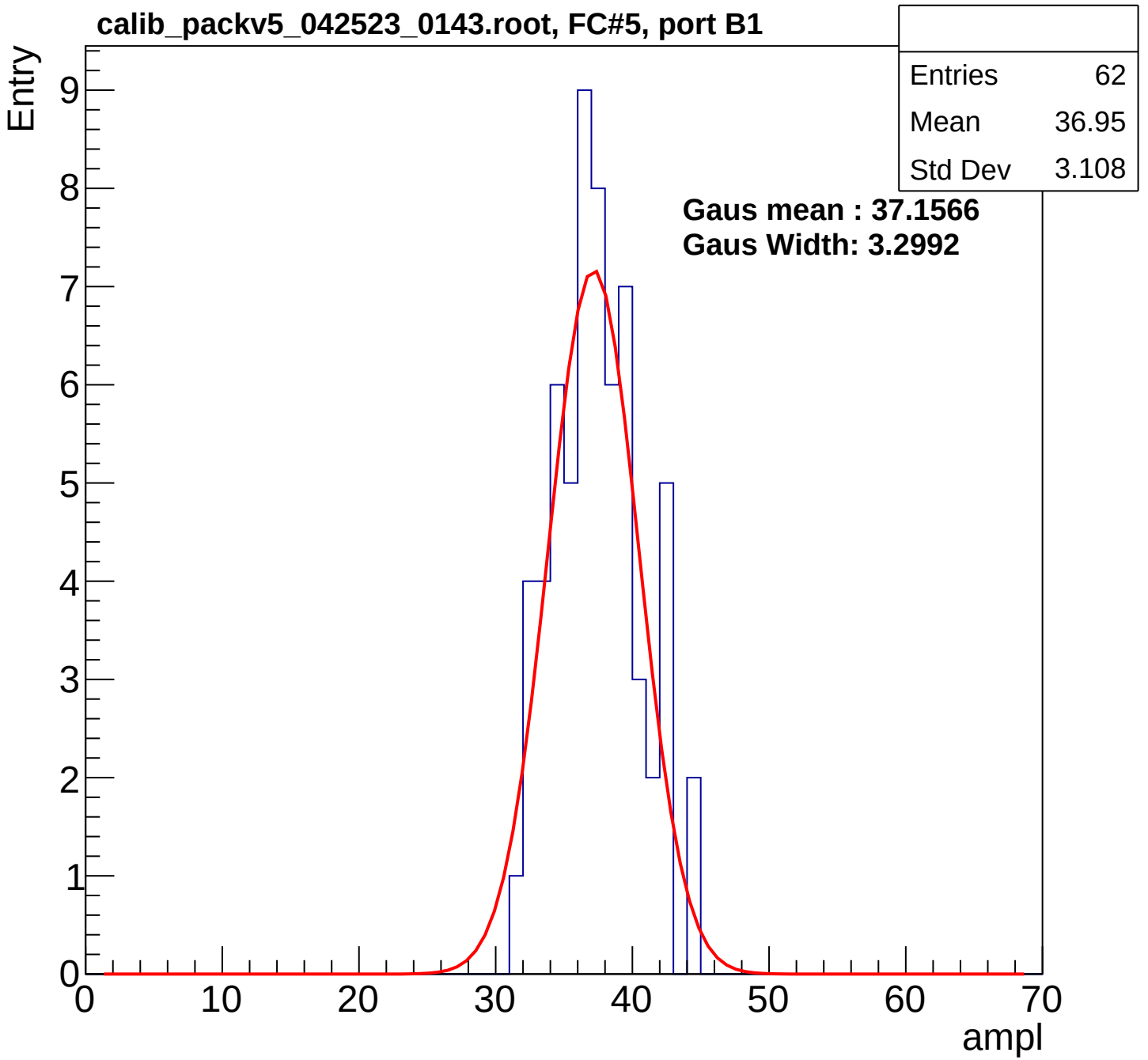
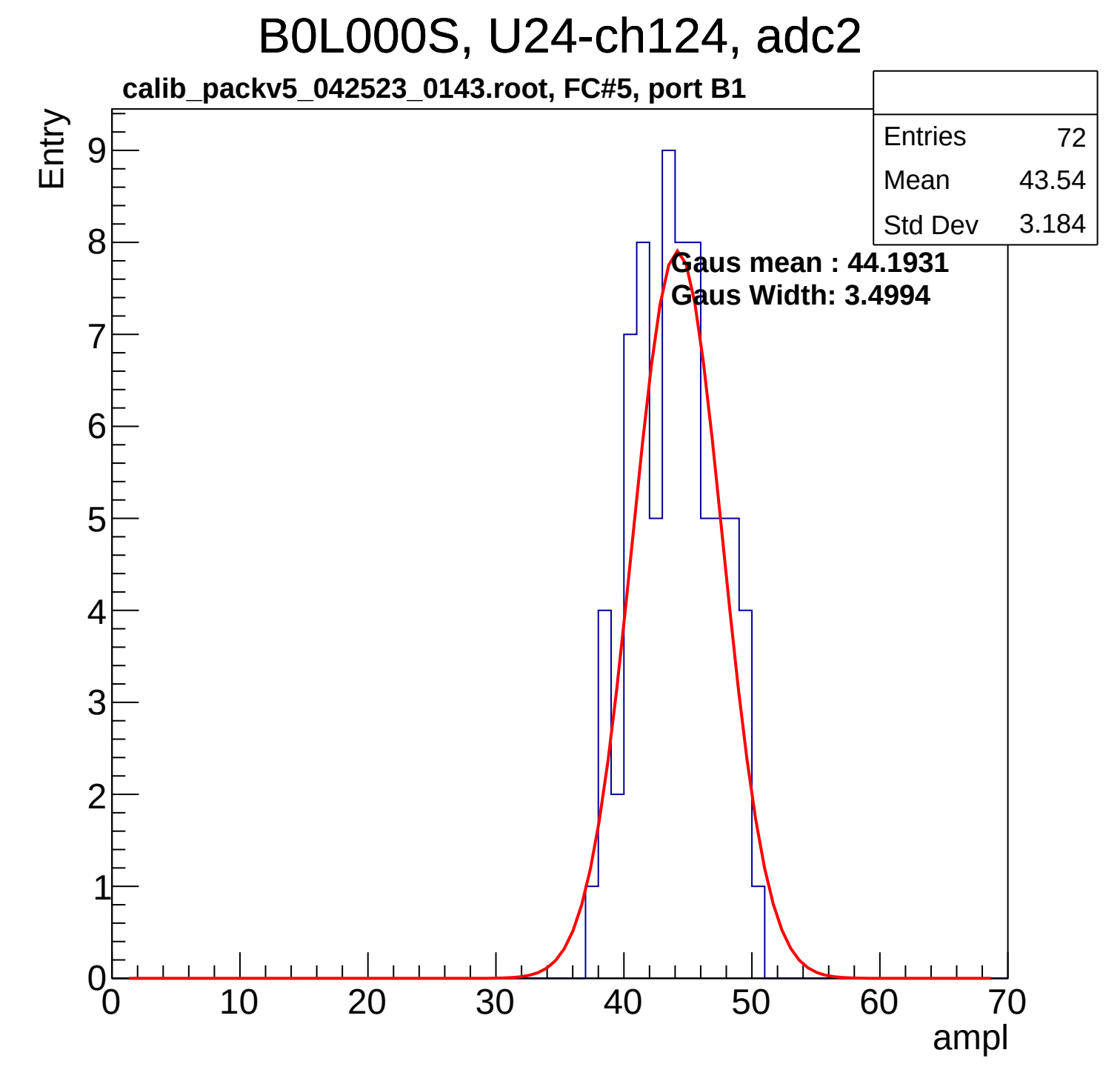
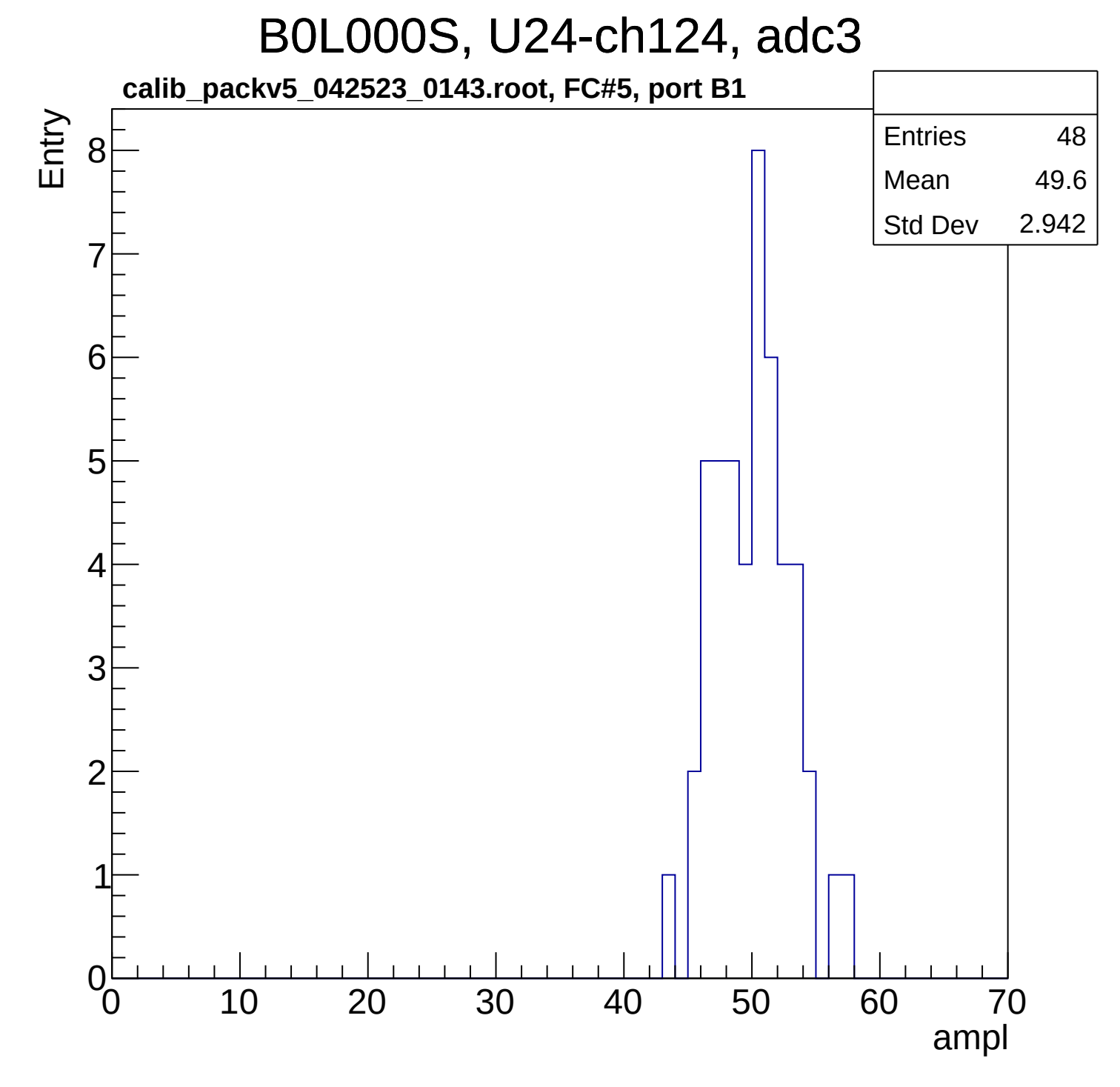

B0L000S, U24-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

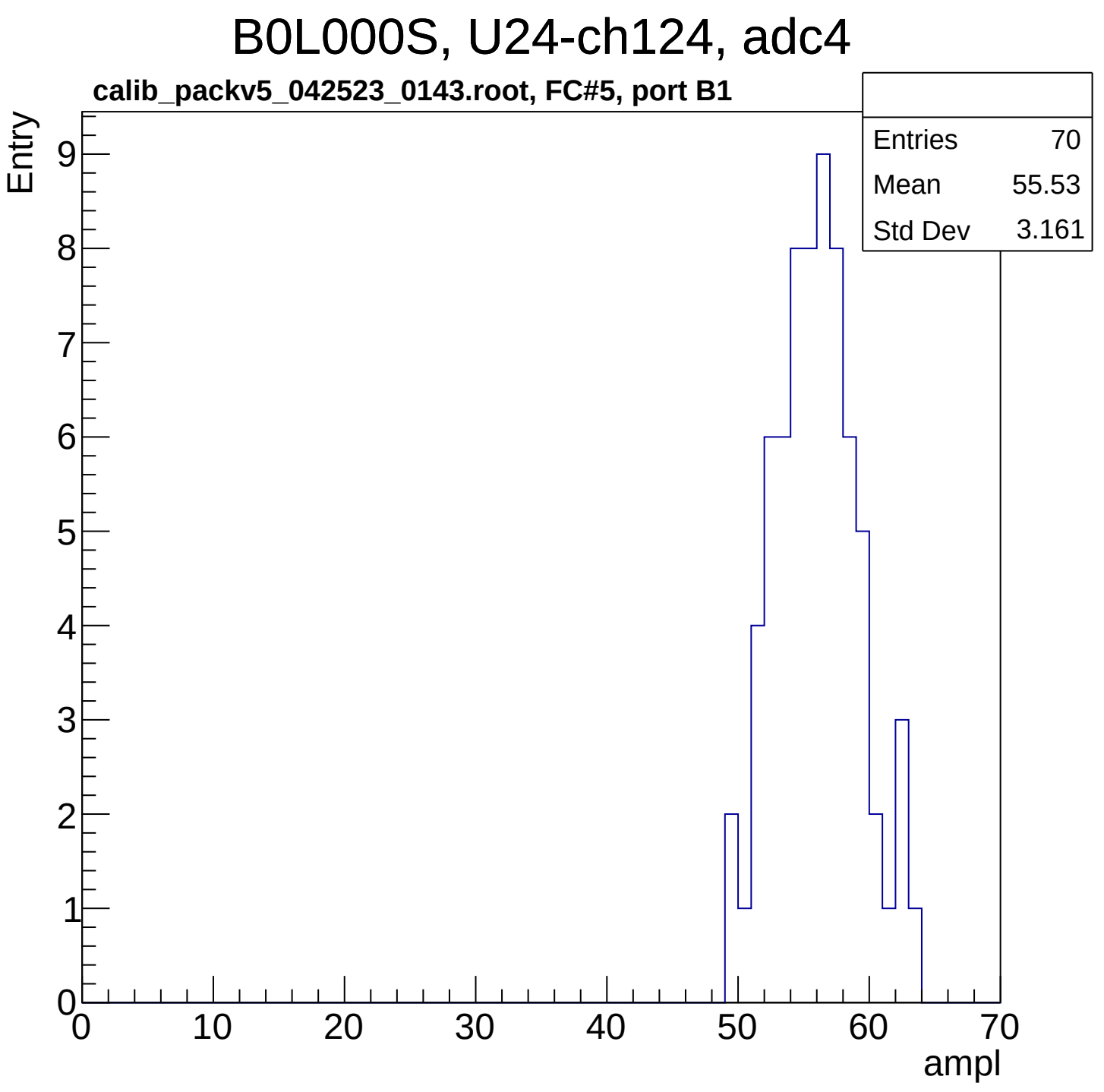

B0L000S, U24-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

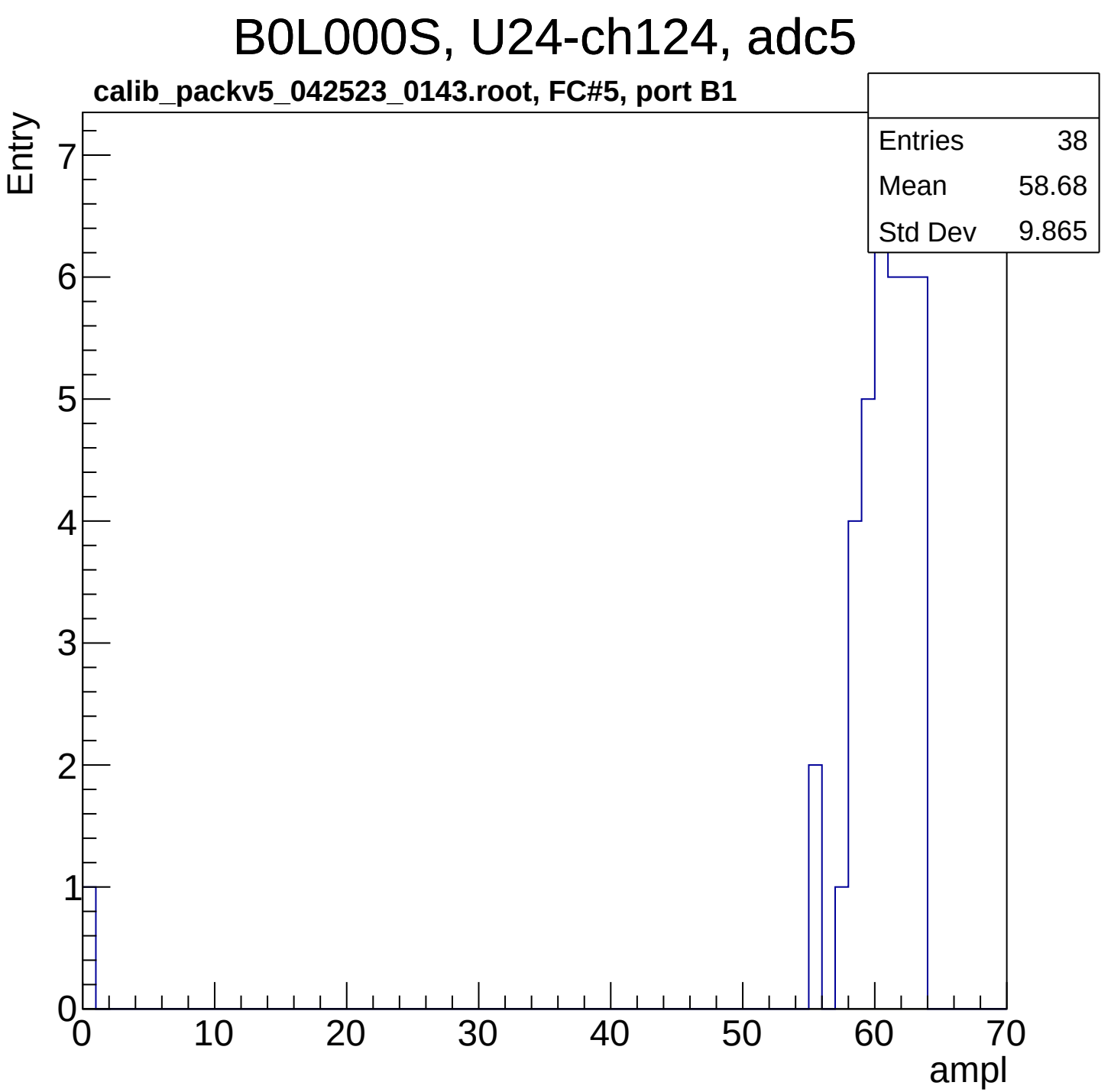

B0L000S, U24-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 29.34
Std Dev 4.587
Gaus mean : 29.9346
Gaus Width: 3.4095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

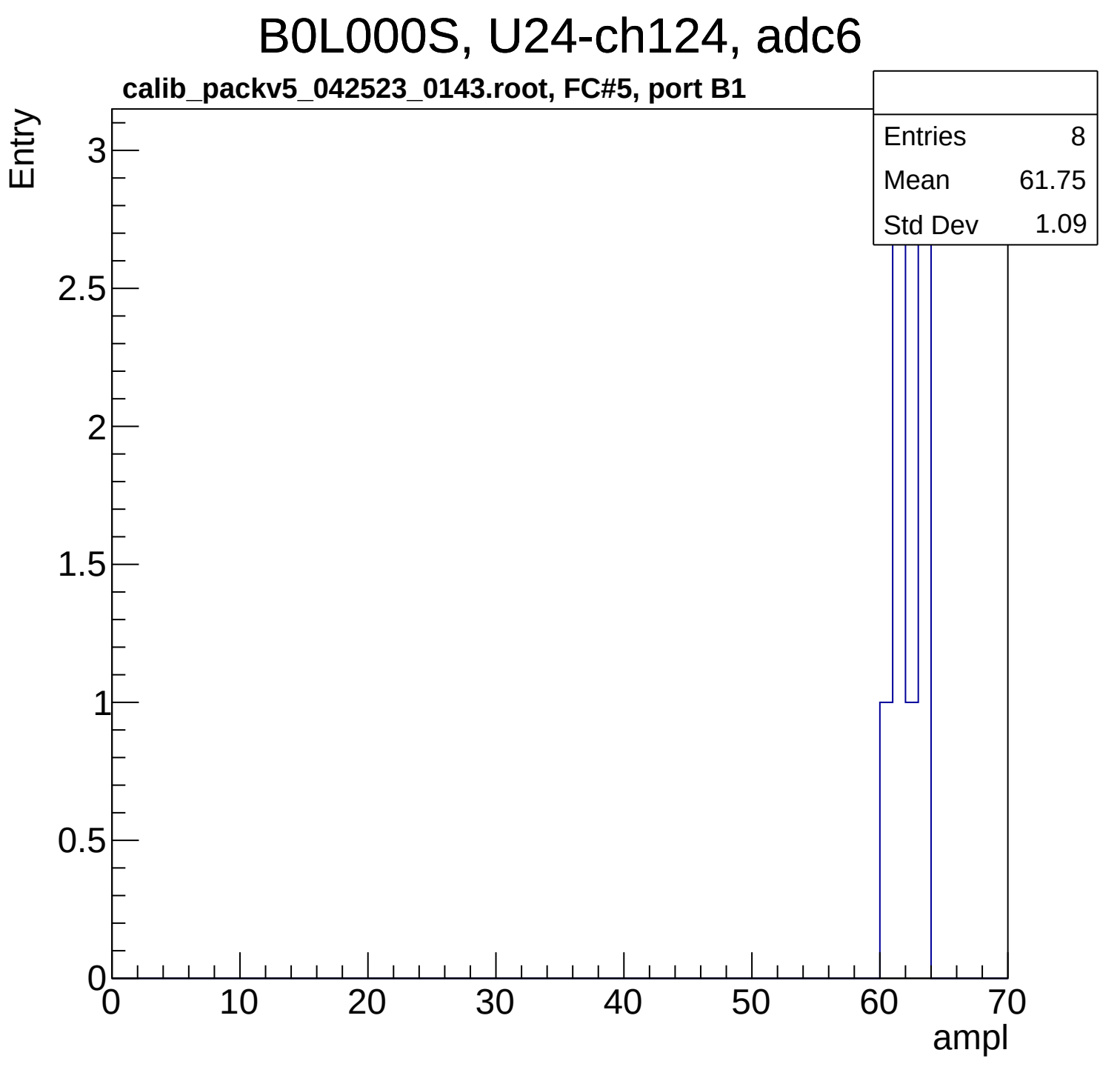

B0L000S, U24-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.95
Std Dev 3.108
Gaus mean : 37.1566
Gaus Width: 3.2992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

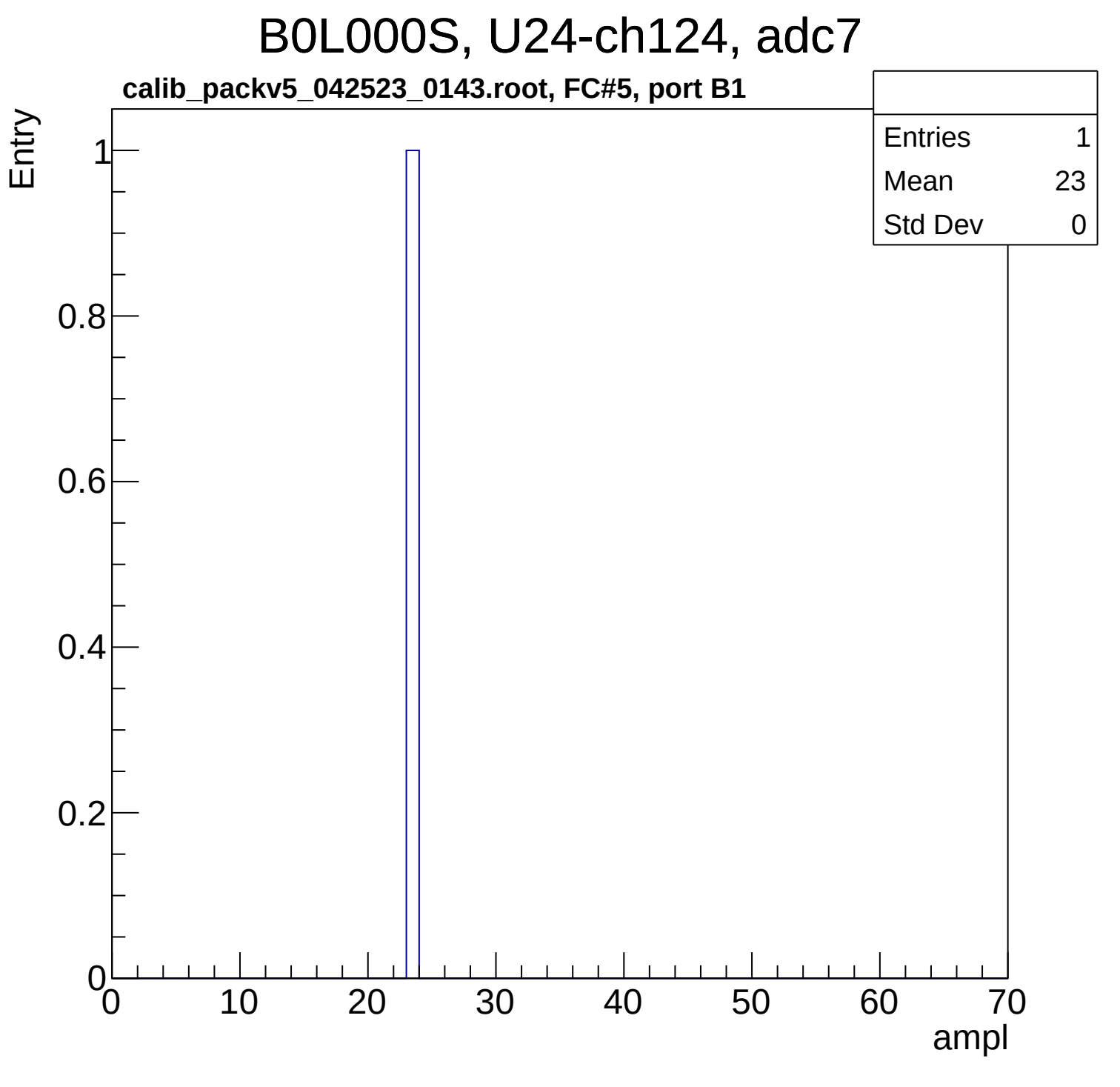

B0L000S, U24-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.54
Std Dev 3.184
Gaus mean : 44.1931
Gaus Width: 3.4994
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 49.6
Std Dev 2.942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 55.53
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.68
Std Dev 9.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U24-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U24-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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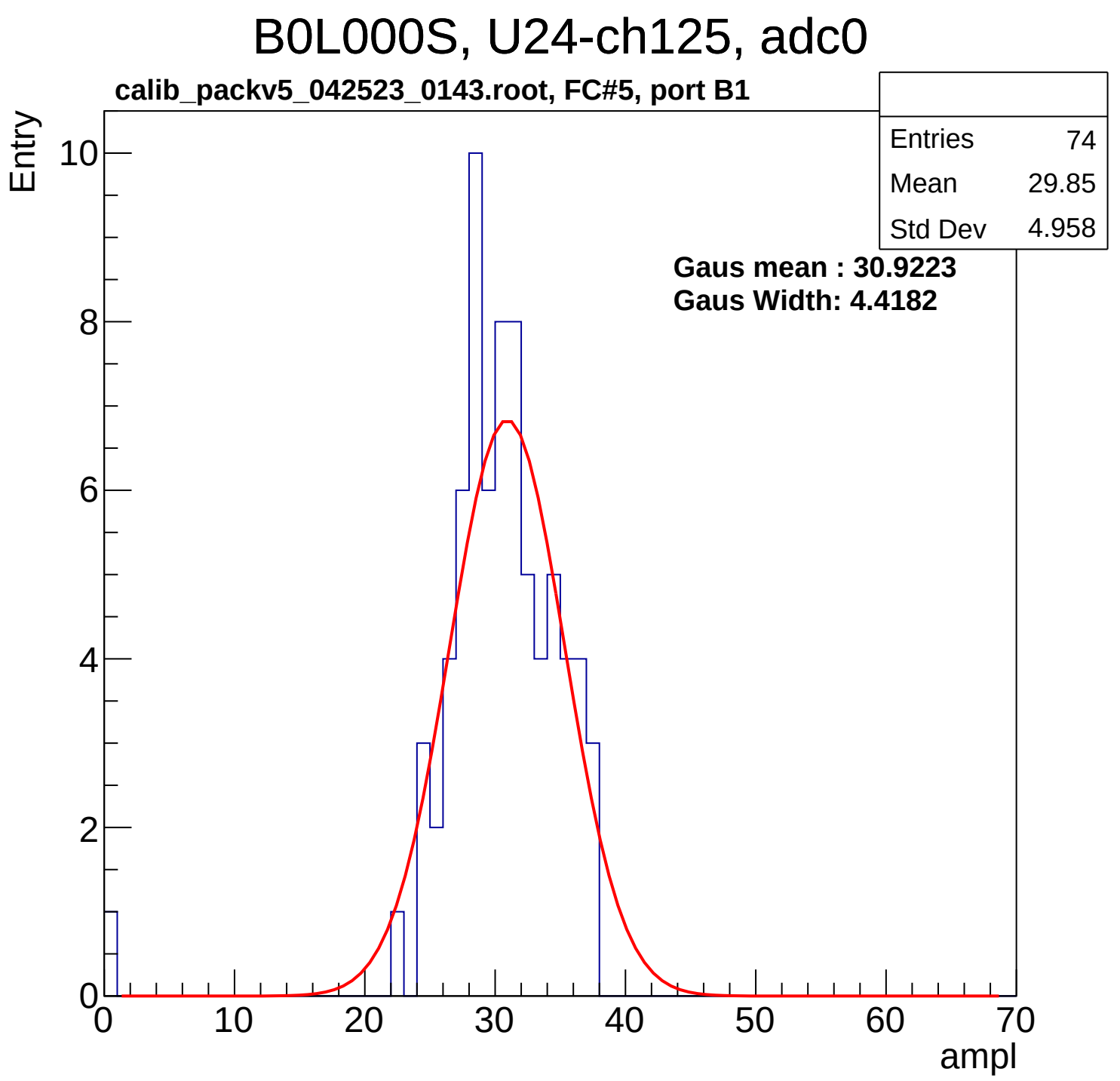
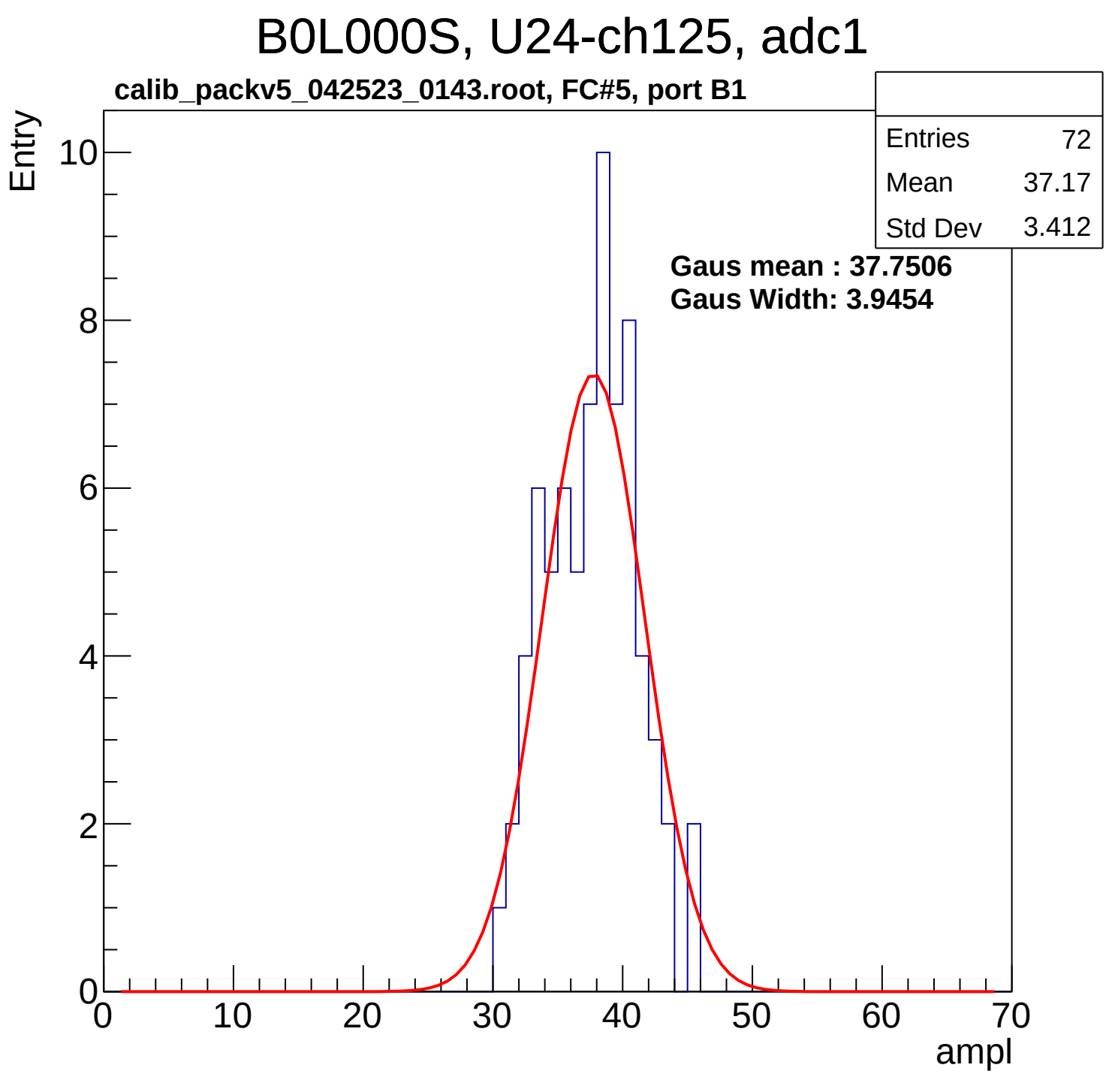
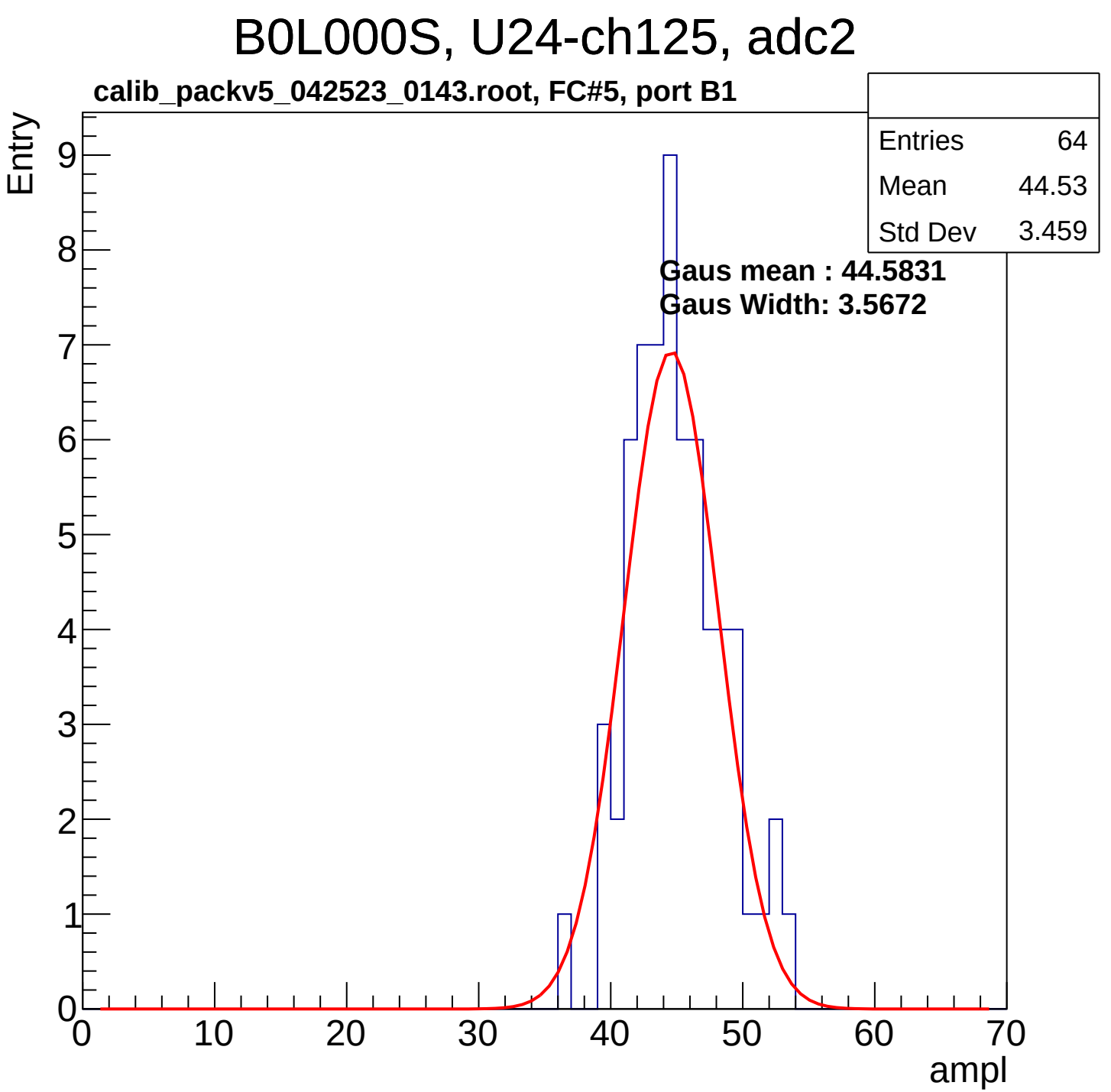
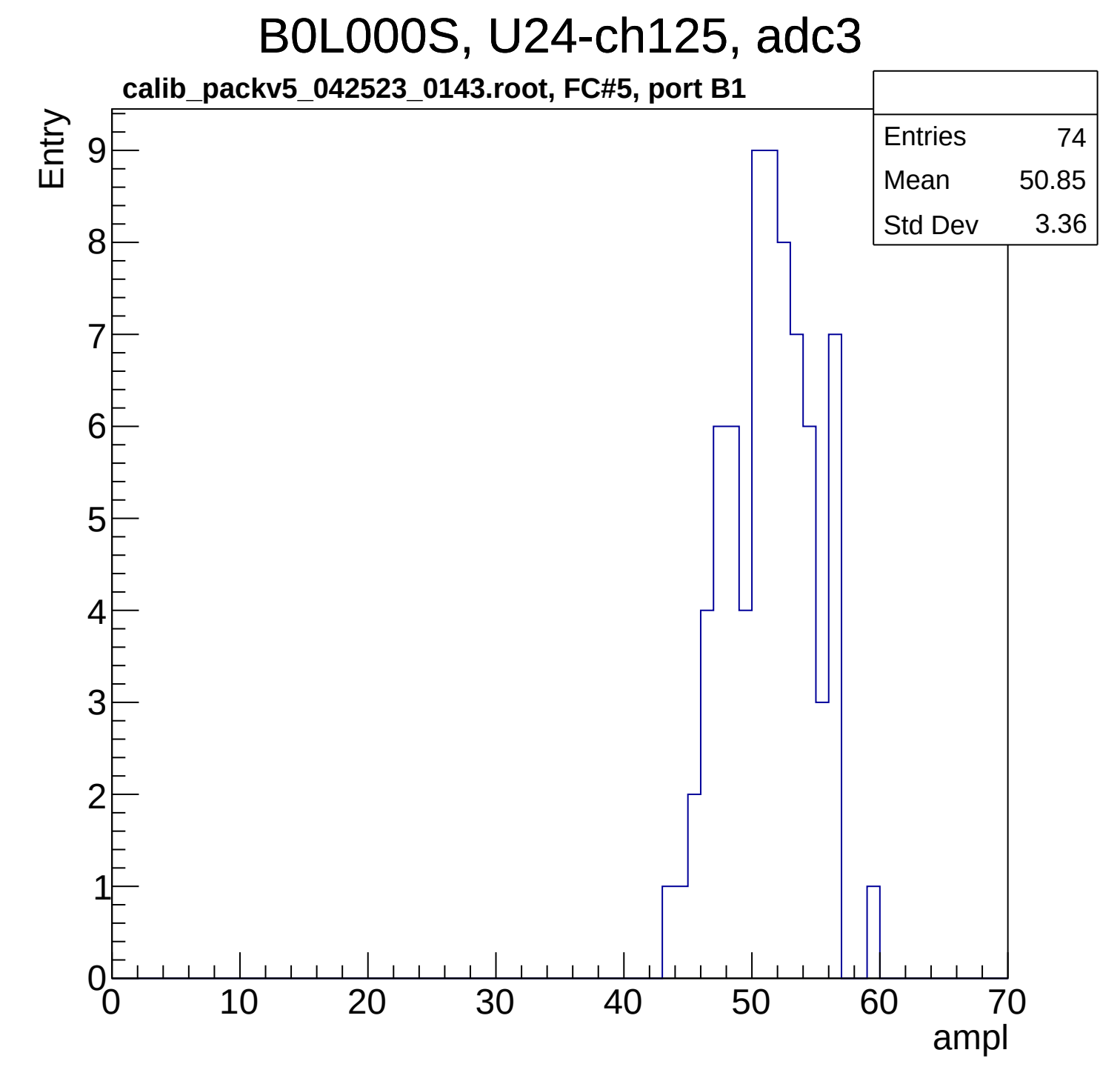
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 29.85
Std Dev 4.958
Gaus mean : 30.9223
Gaus Width: 4.4182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.17
Std Dev 3.412
Gaus mean : 37.7506
Gaus Width: 3.9454
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U24-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 44.53
Std Dev 3.459
Gaus mean : 44.5831
Gaus Width: 3.5672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 50.85
Std Dev 3.36
Entry
ampl
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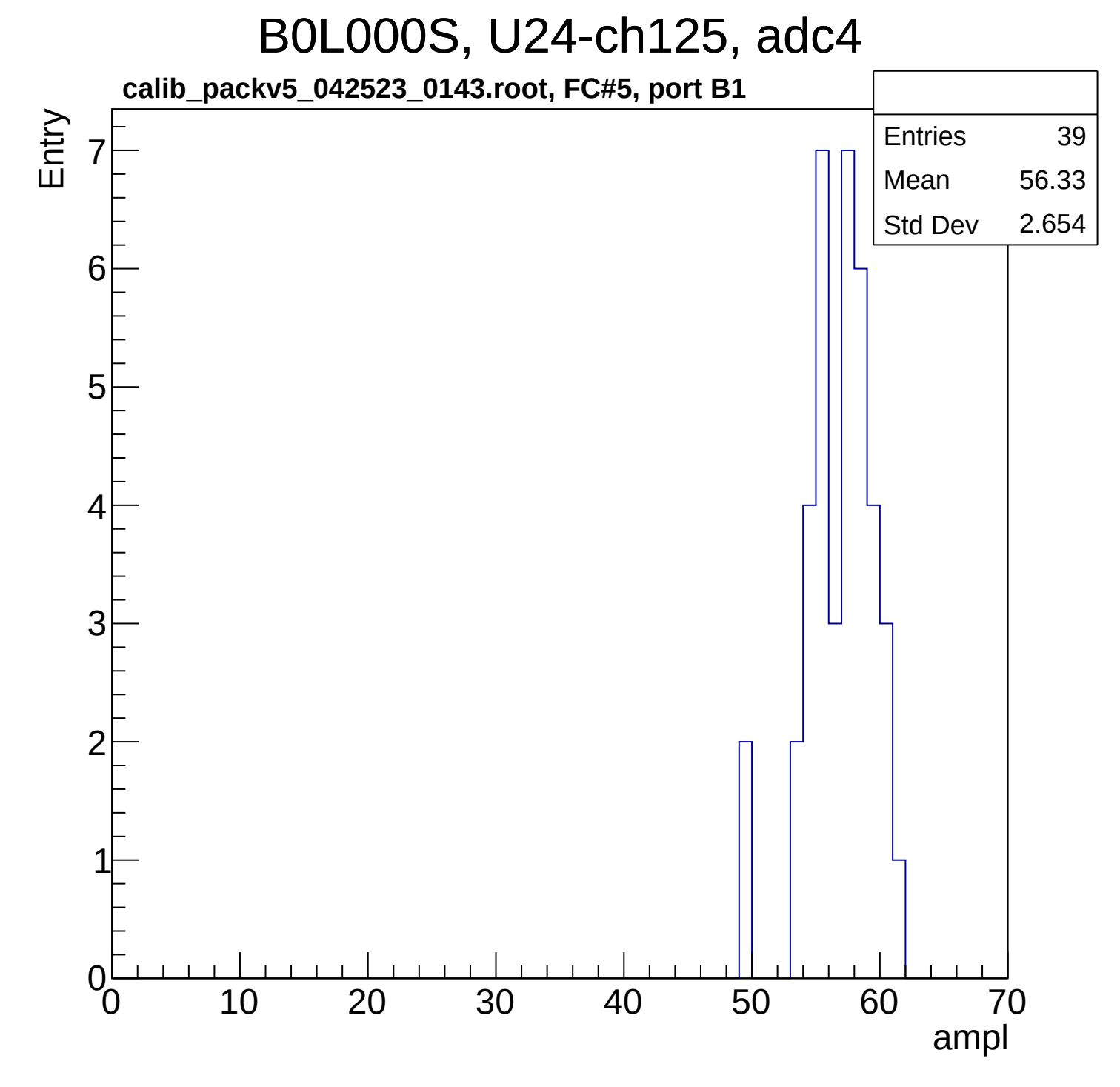
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 56.33
Std Dev 2.654
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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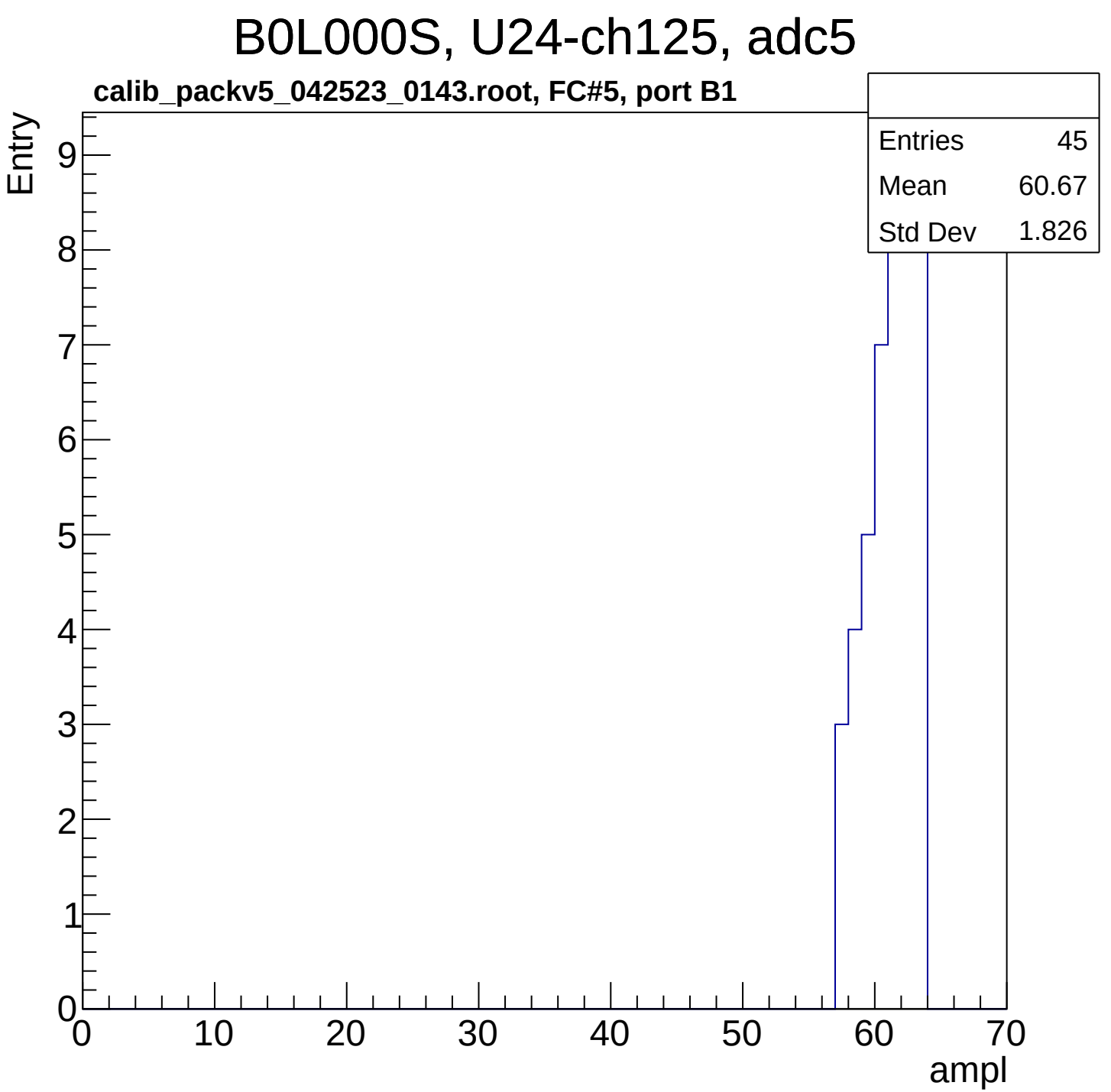
B0L000S, U24-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.67
Std Dev 1.826
Entry
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4
5
6
7
8
9
0
10
20
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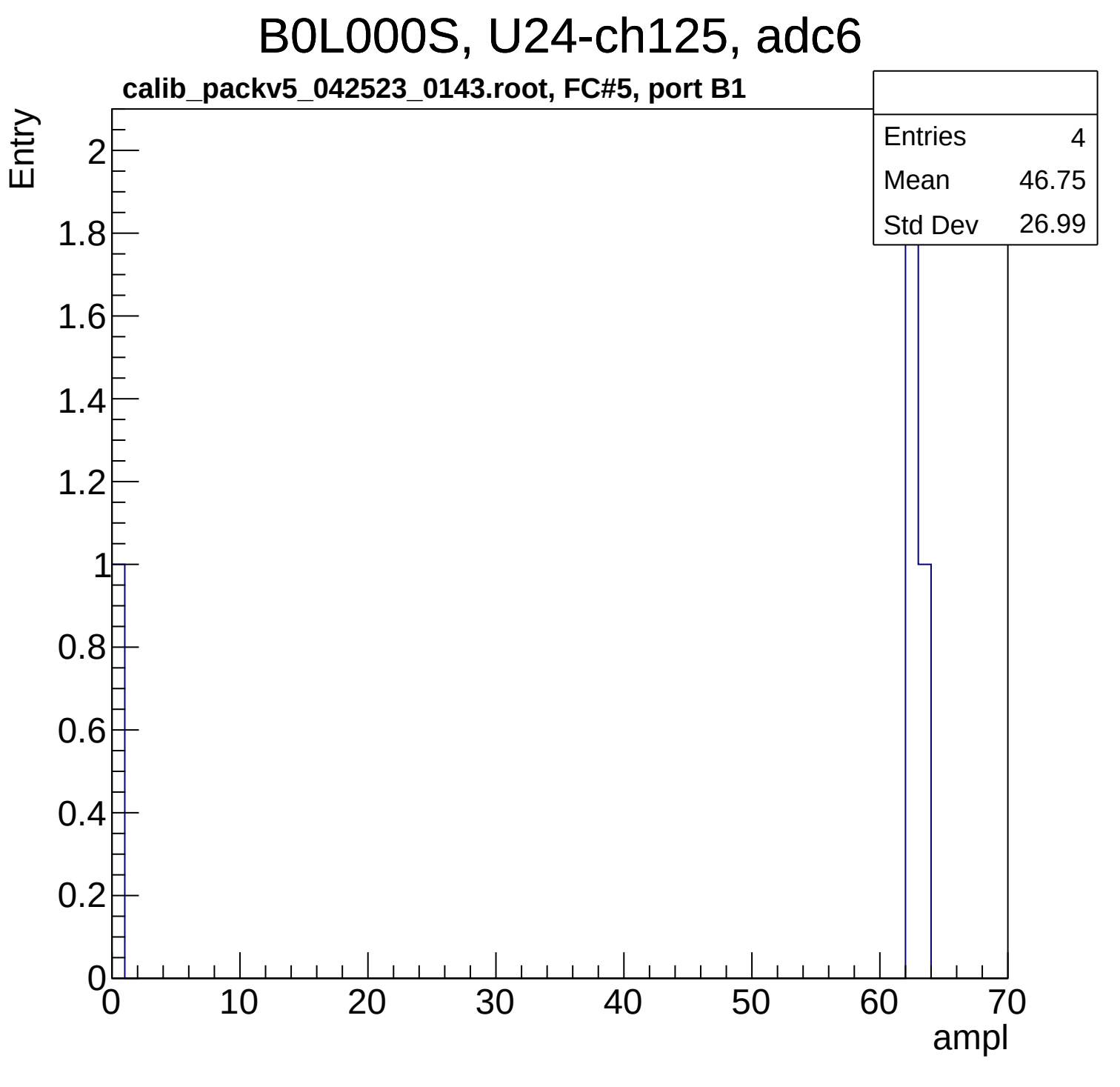
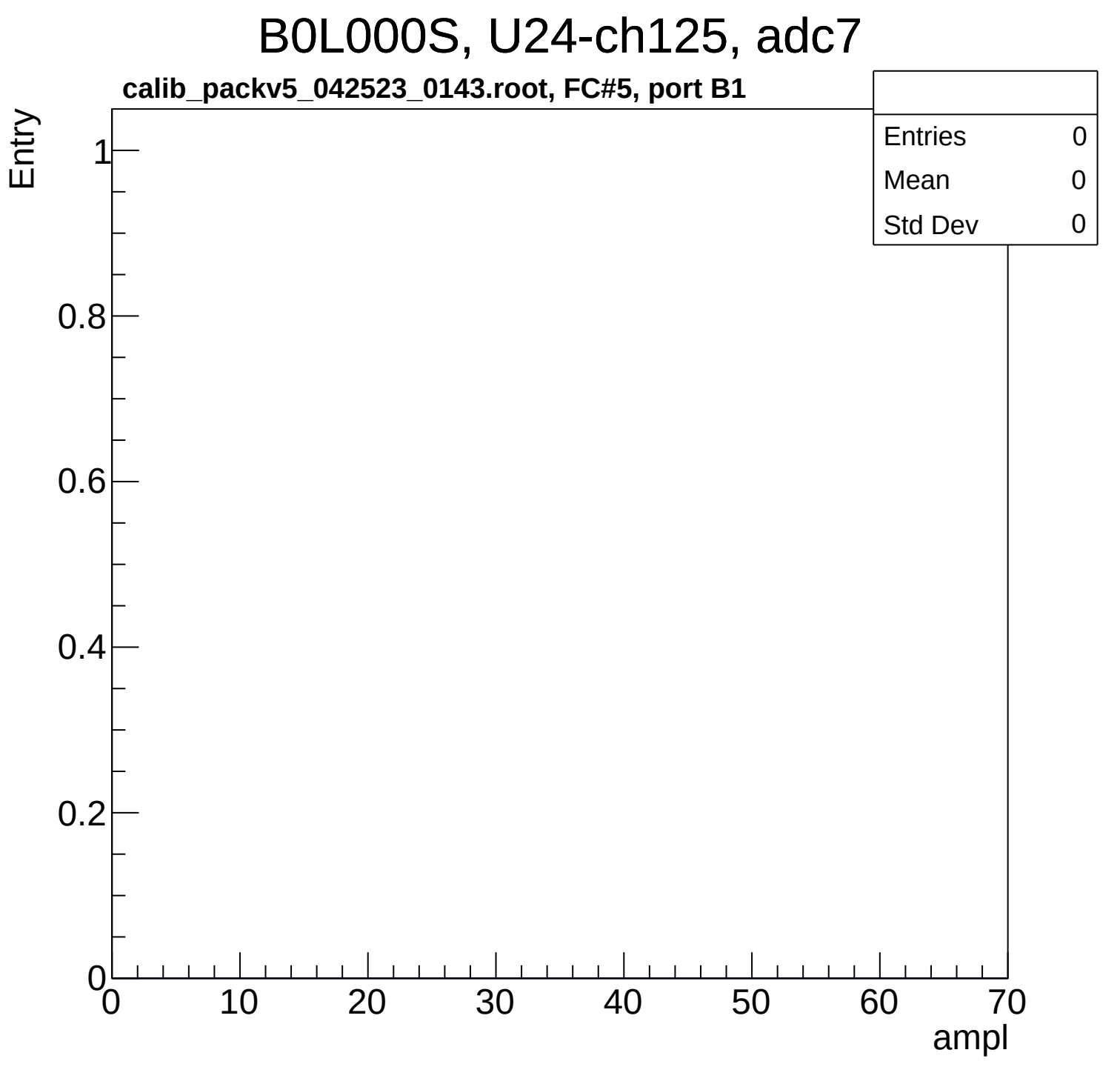
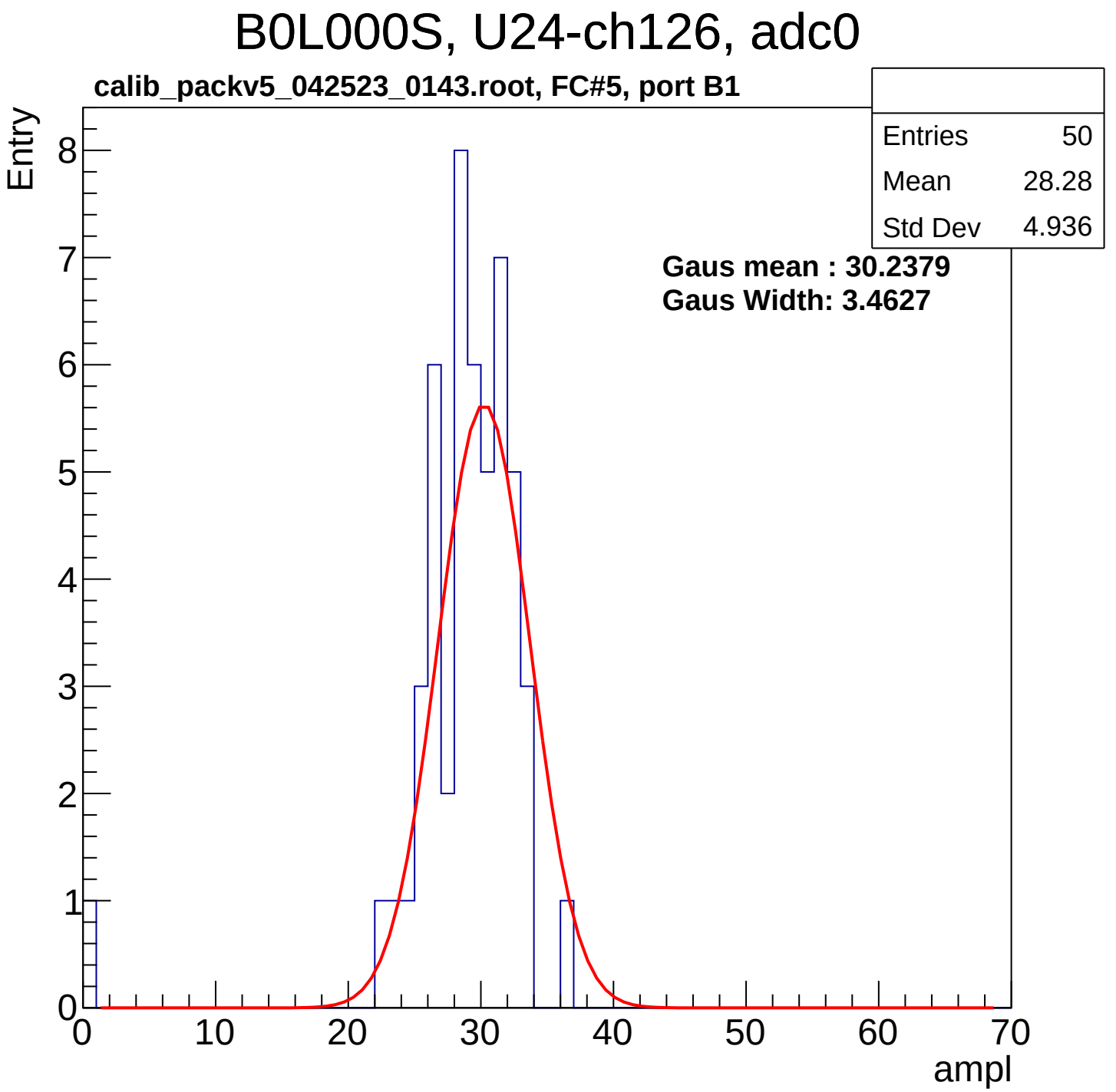
40
50
60
70
ampl


B0L000S, U24-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 28.28
Std Dev 4.936
Gaus mean : 30.2379
Gaus Width: 3.4627
Entry
ampl
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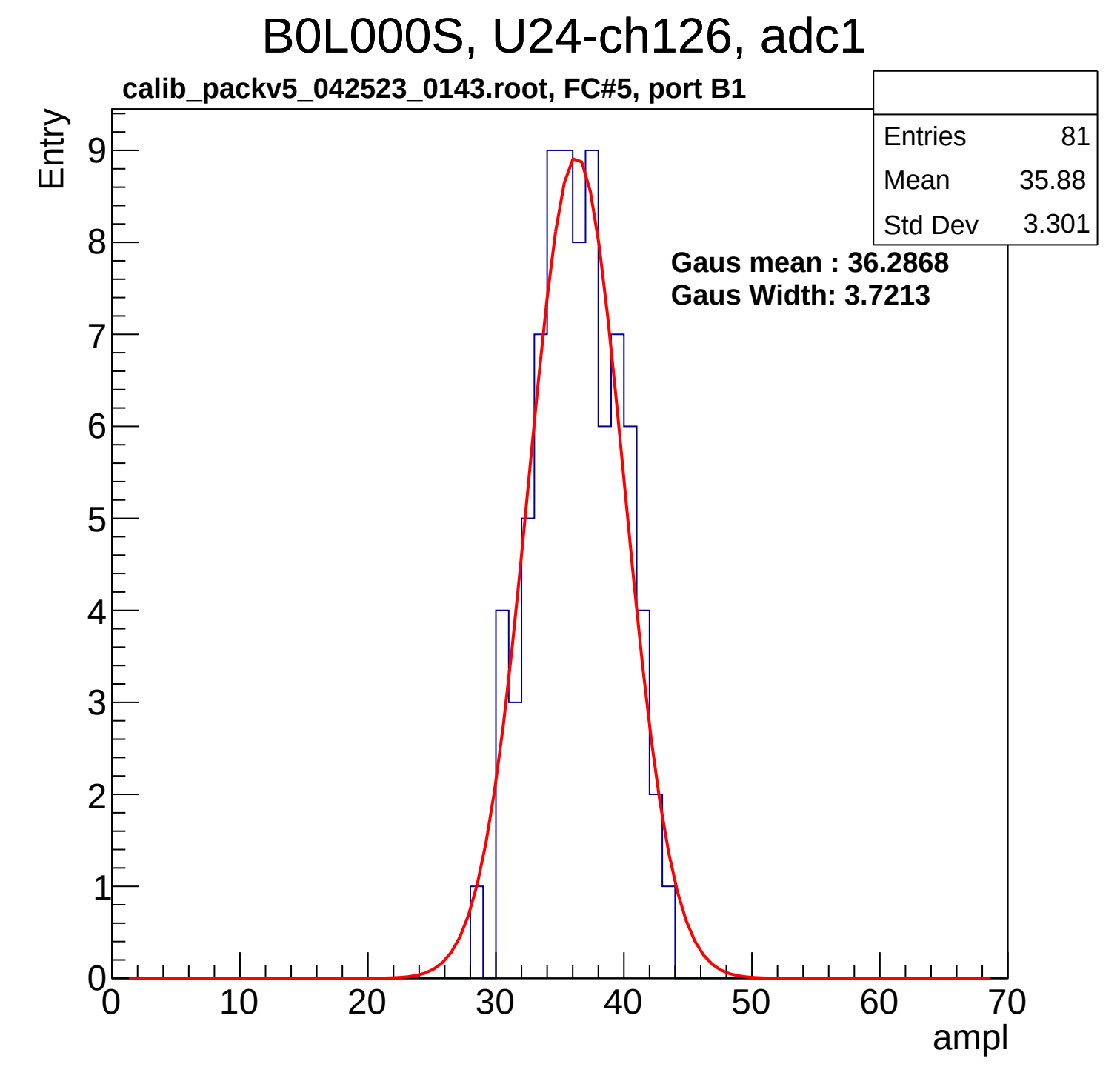
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 35.88
Std Dev 3.301
Gaus mean : 36.2868
Gaus Width: 3.7213
Entry
ampl
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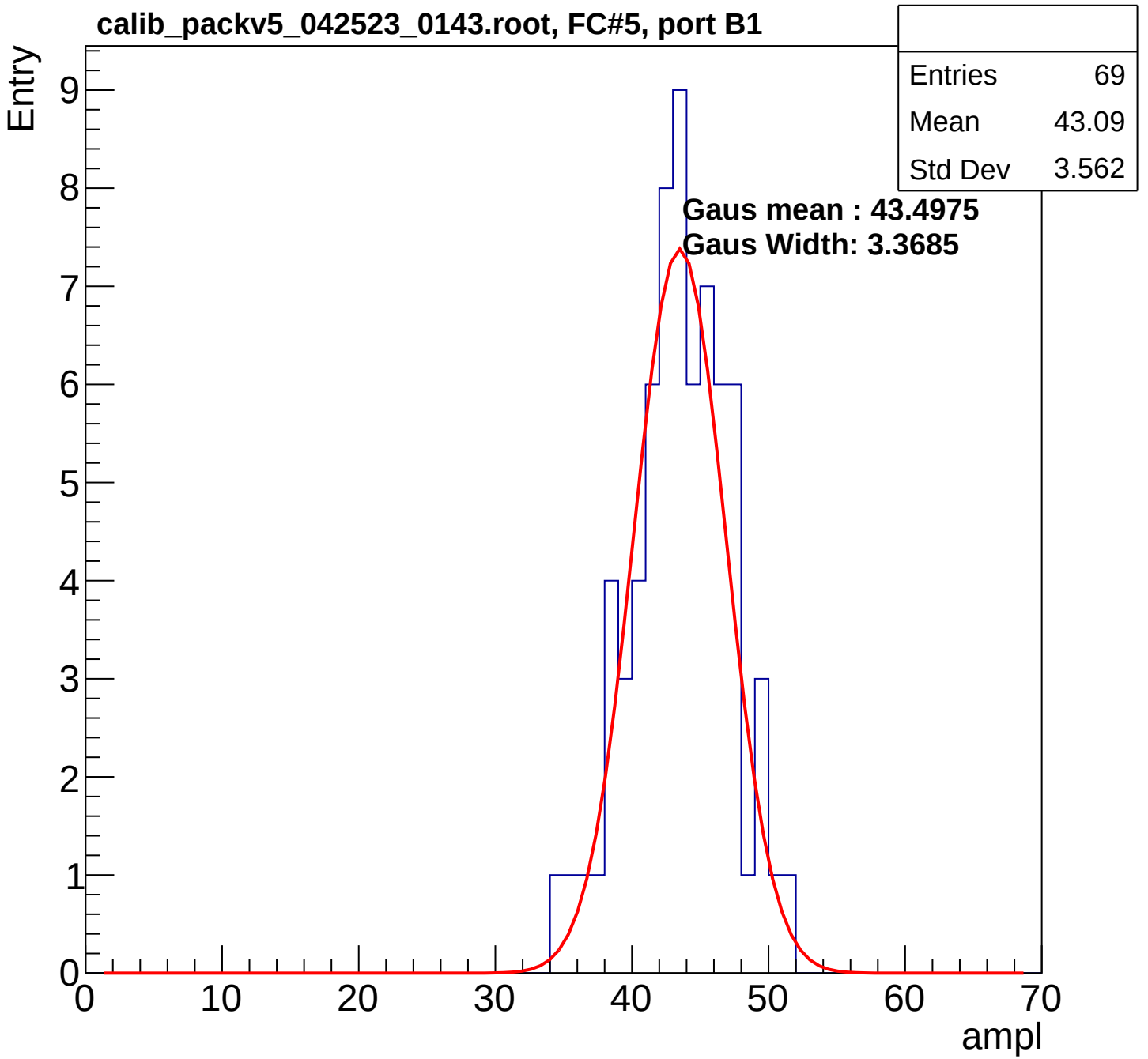
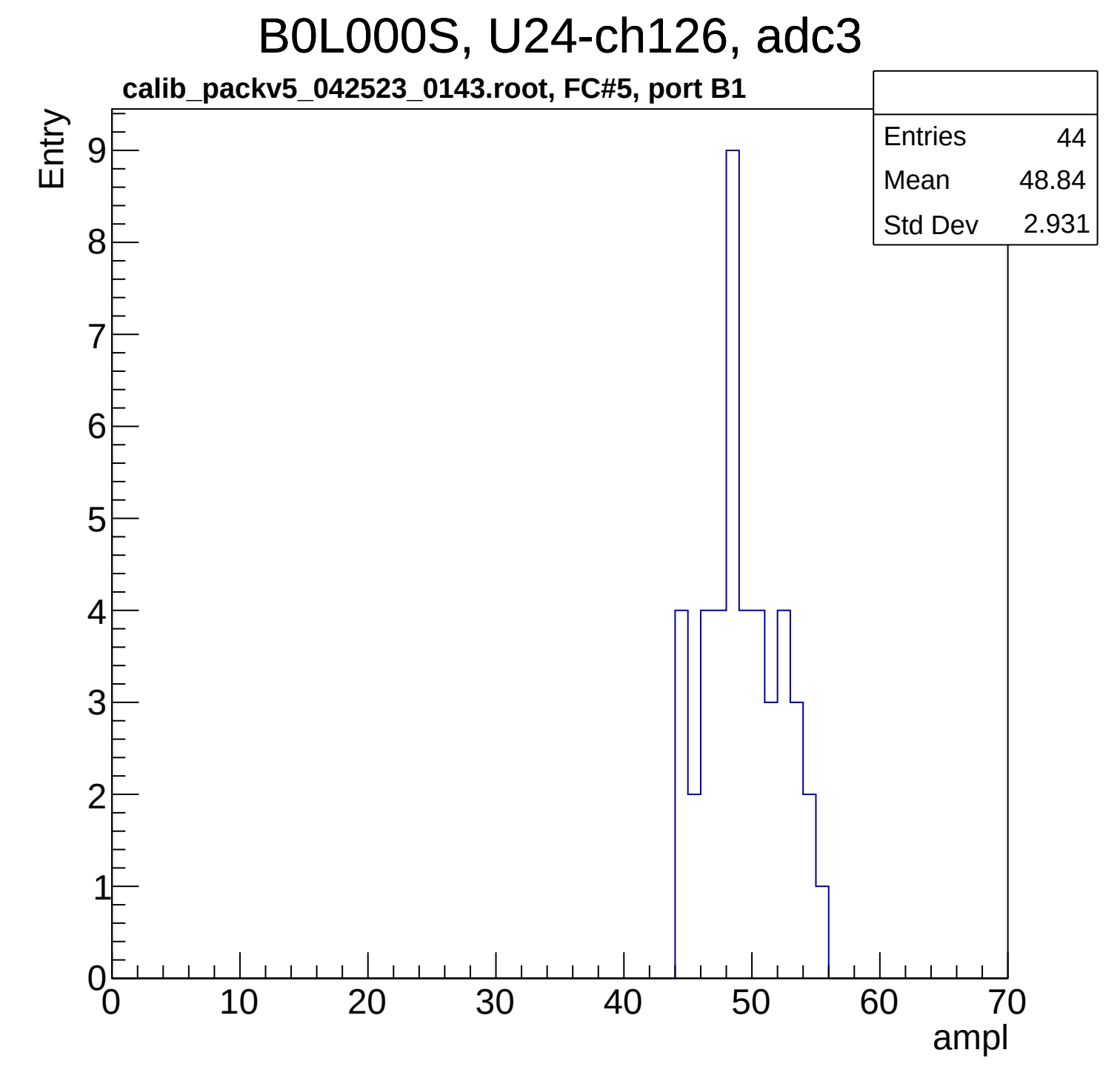
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.09
Std Dev 3.562
Gaus mean : 43.4975
Gaus Width: 3.3685
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U24-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 48.84
Std Dev 2.931
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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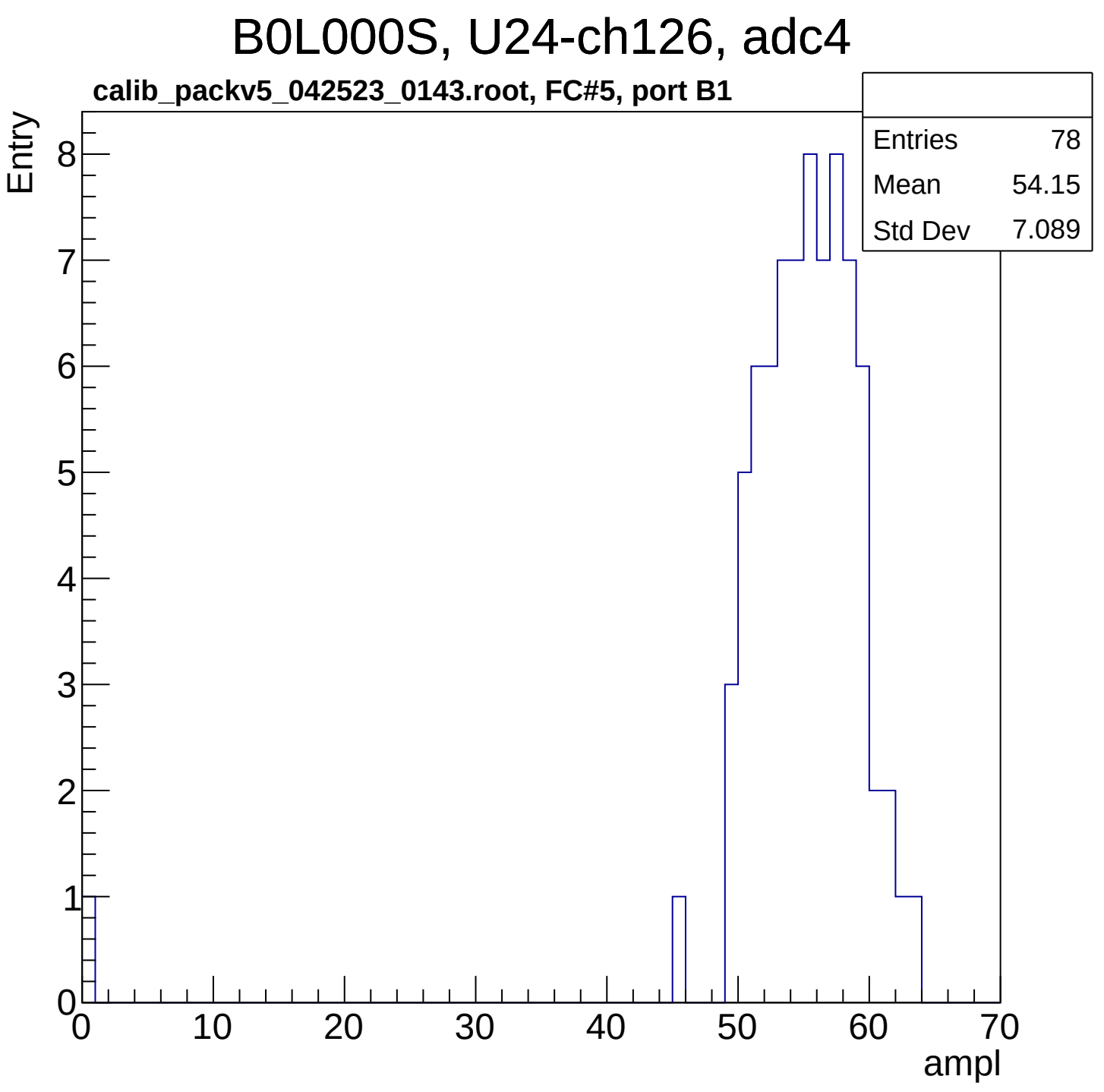
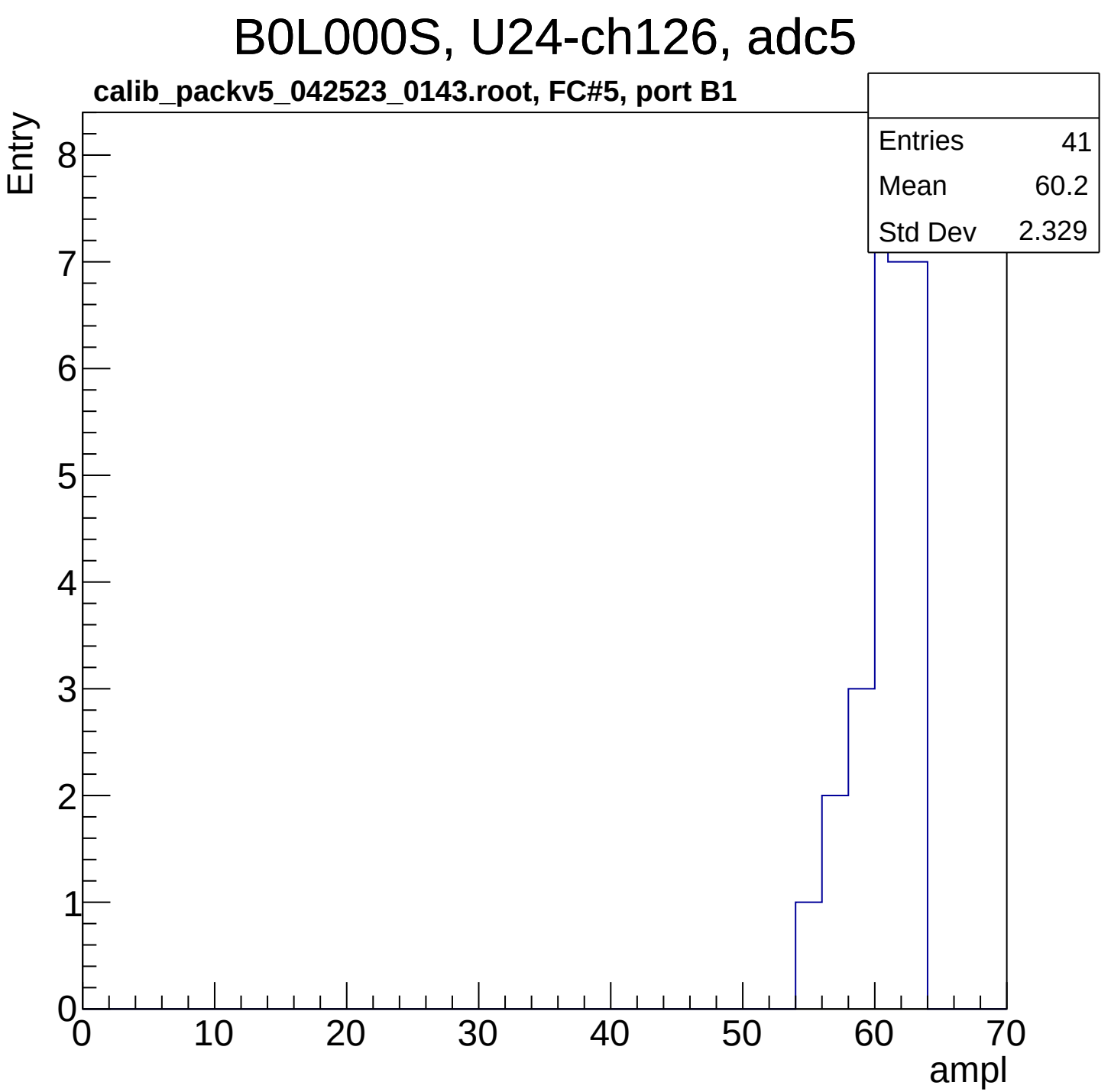

B0L000S, U24-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 54.15
Std Dev 7.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 60.2
Std Dev 2.329
Entry
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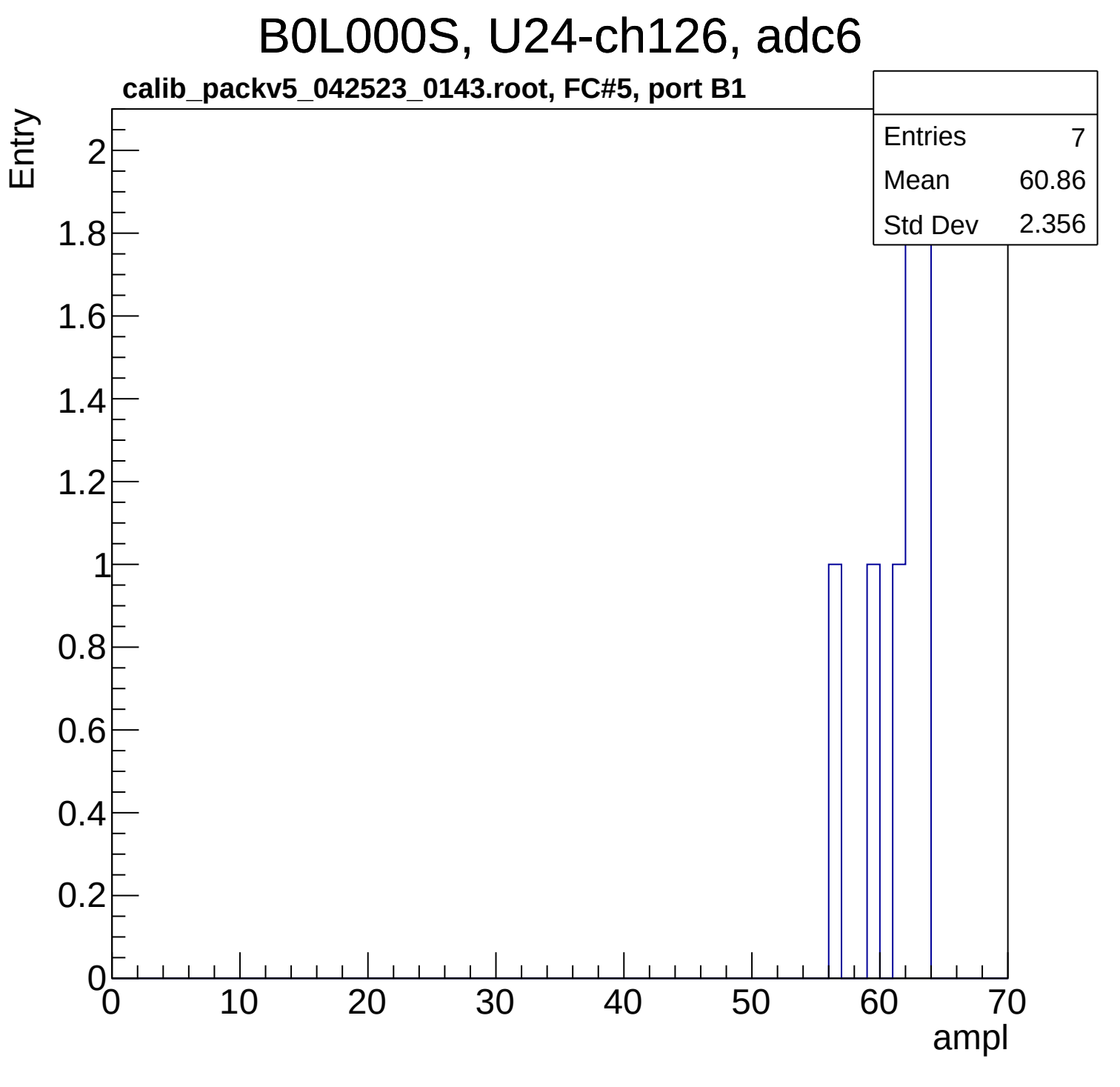
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 60.86
Std Dev 2.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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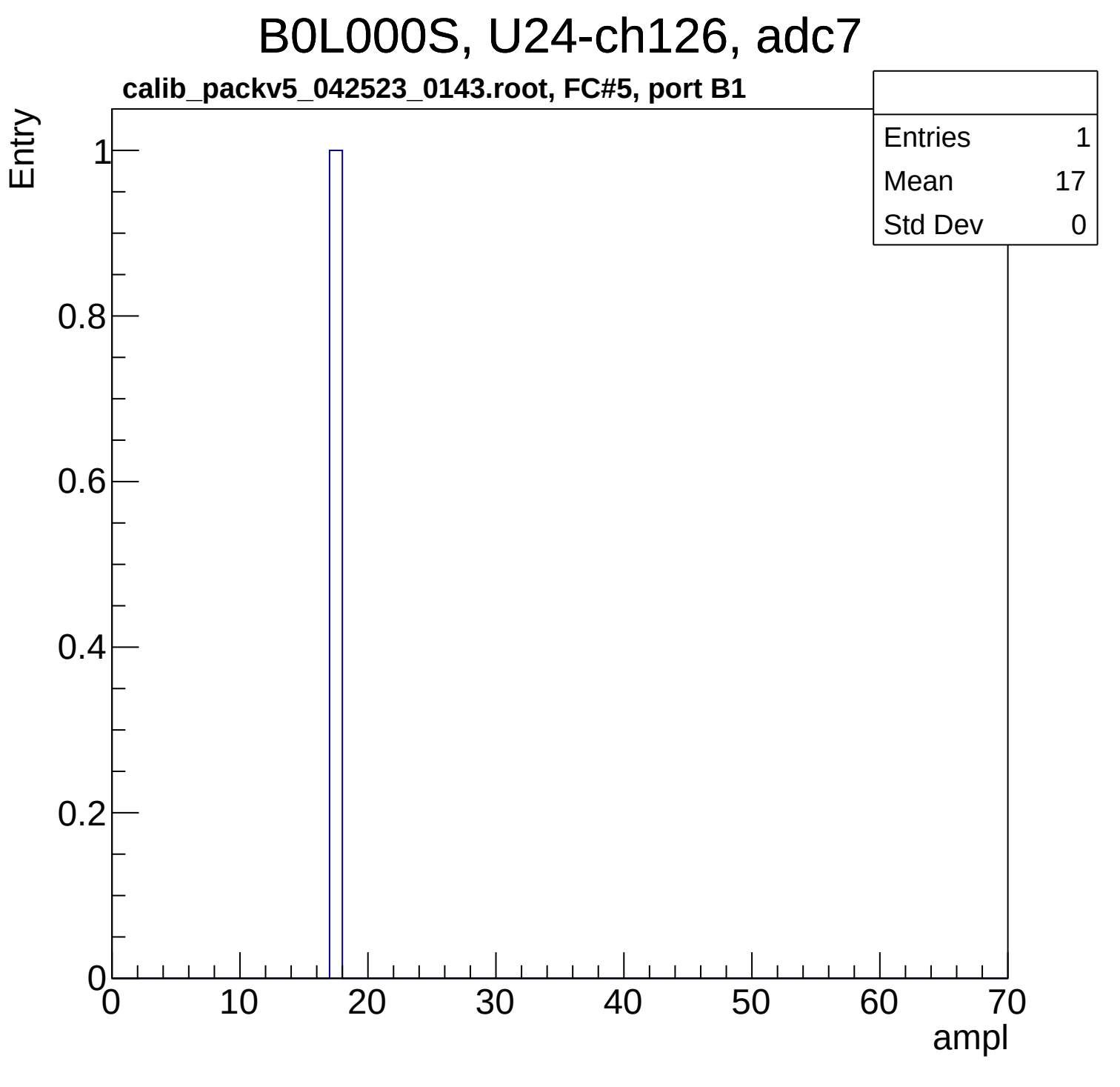
40
50
60
70


B0L000S, U24-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 17
Std Dev 0
Entry
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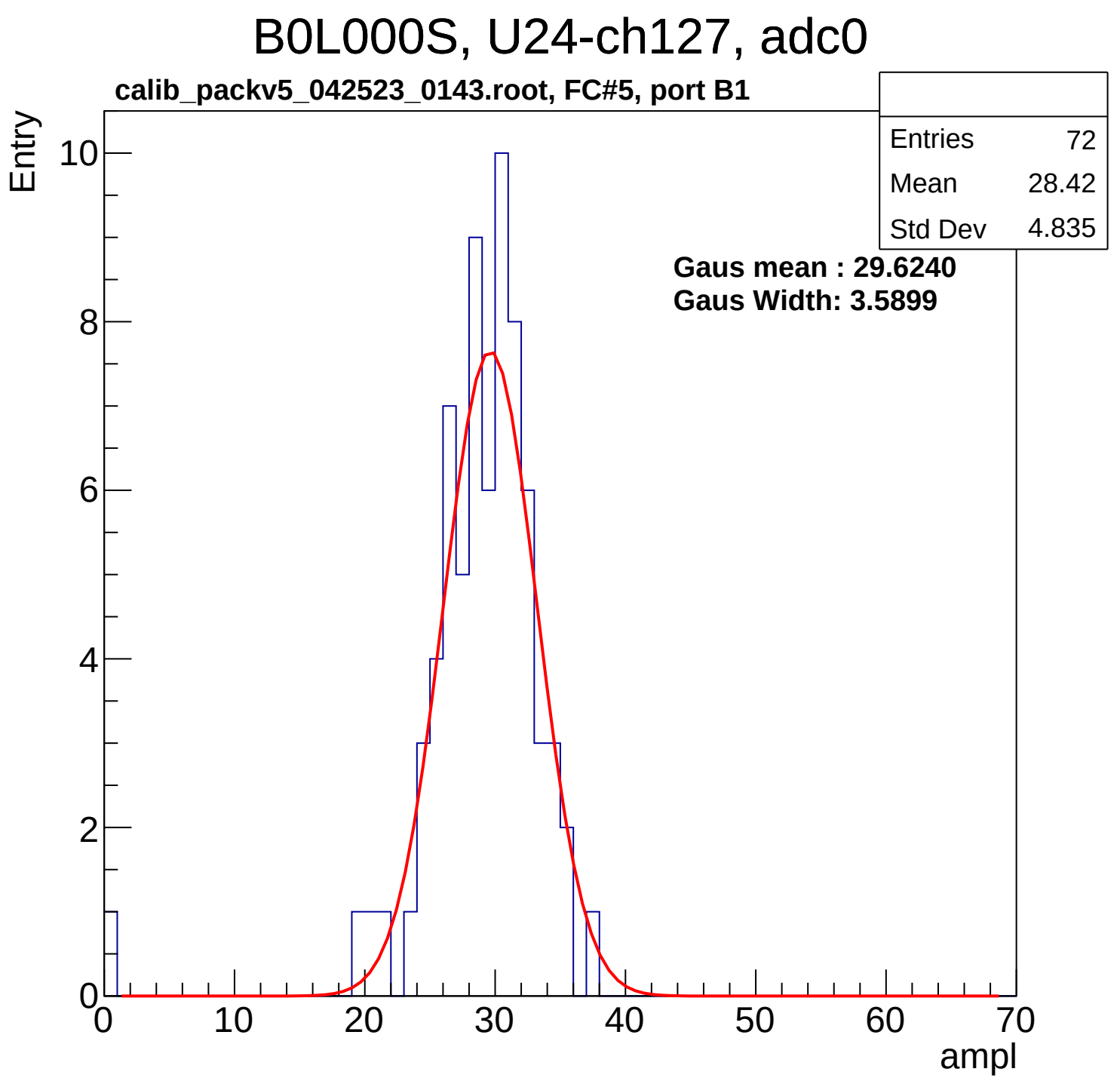
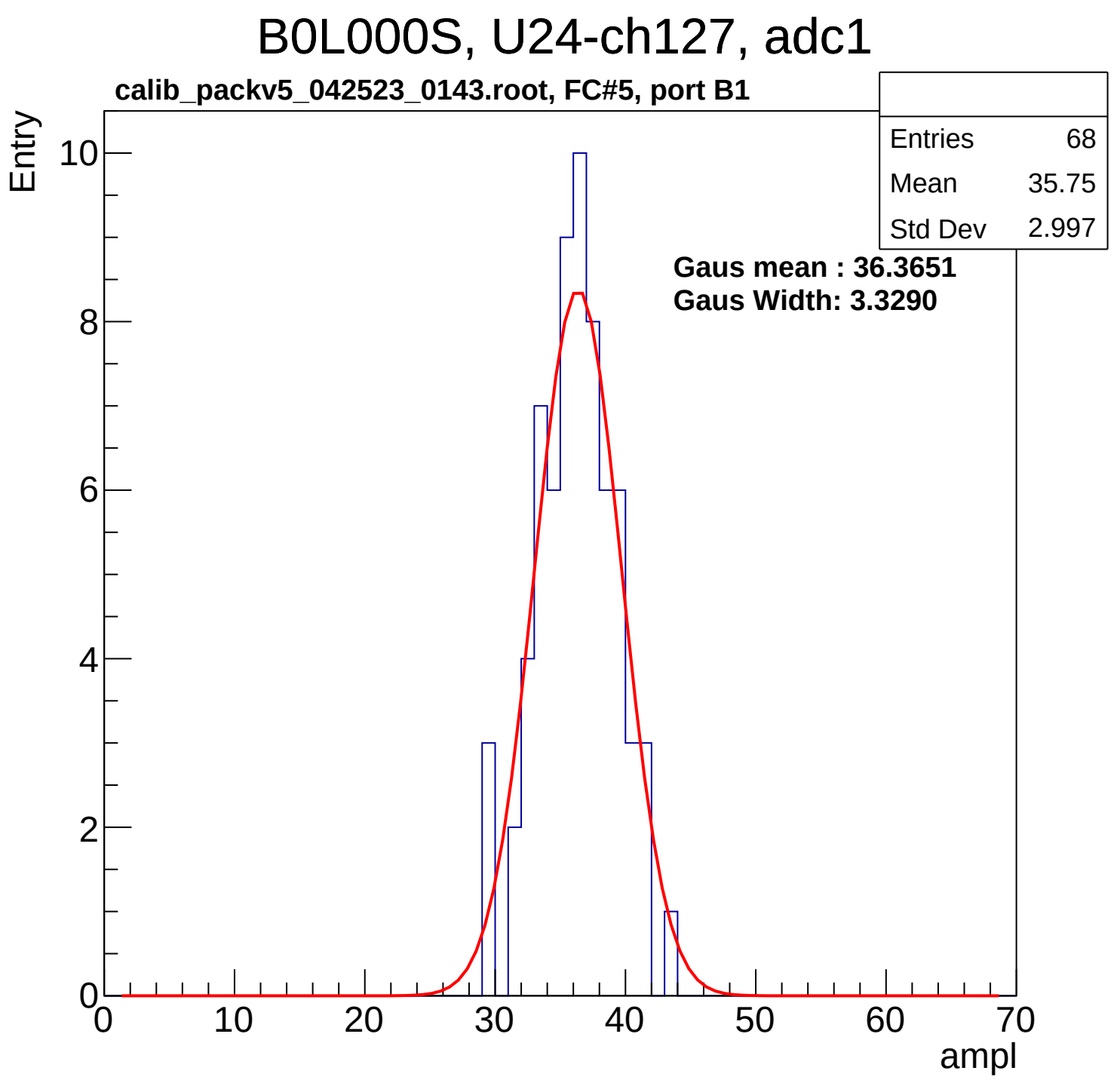
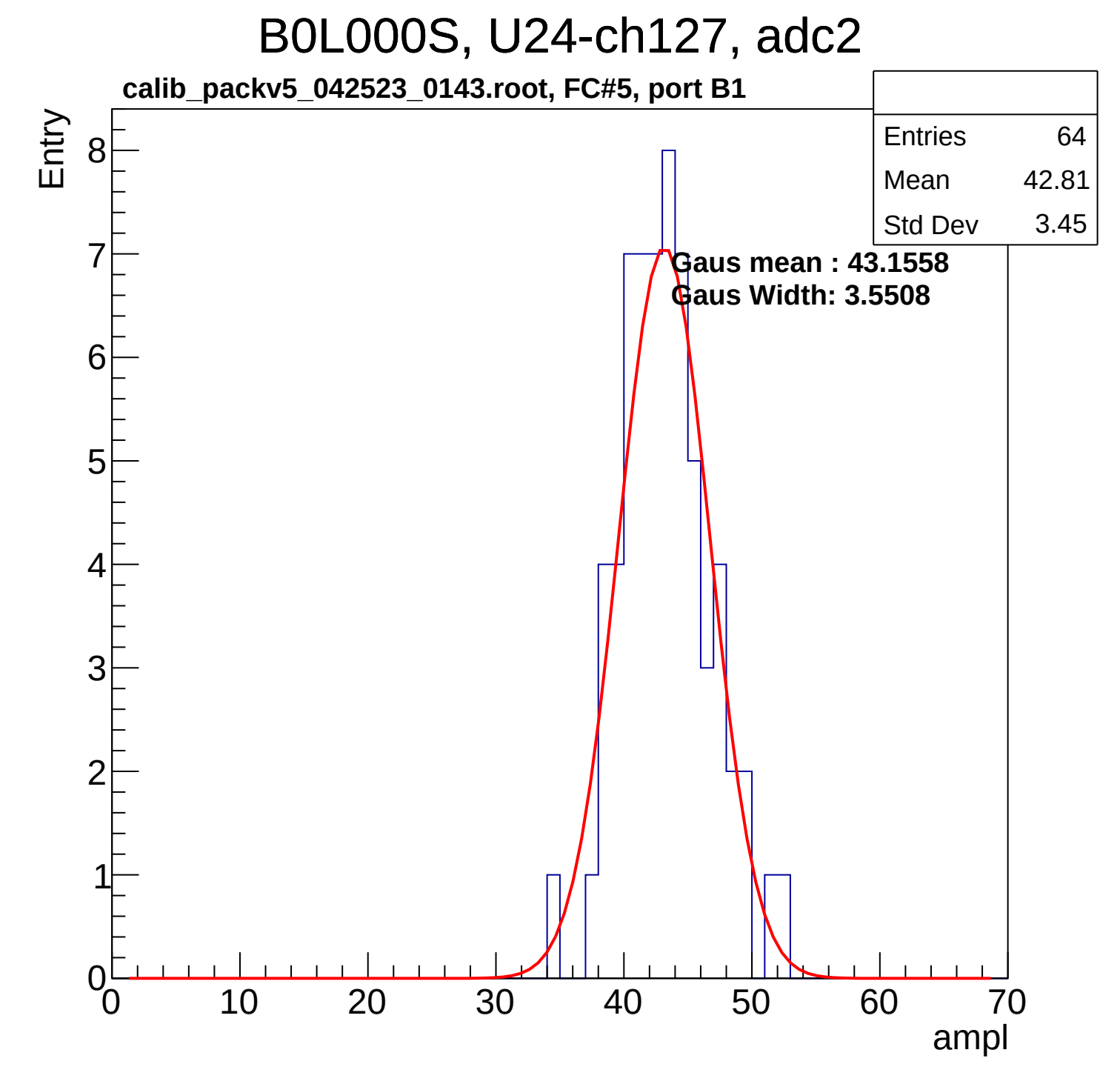
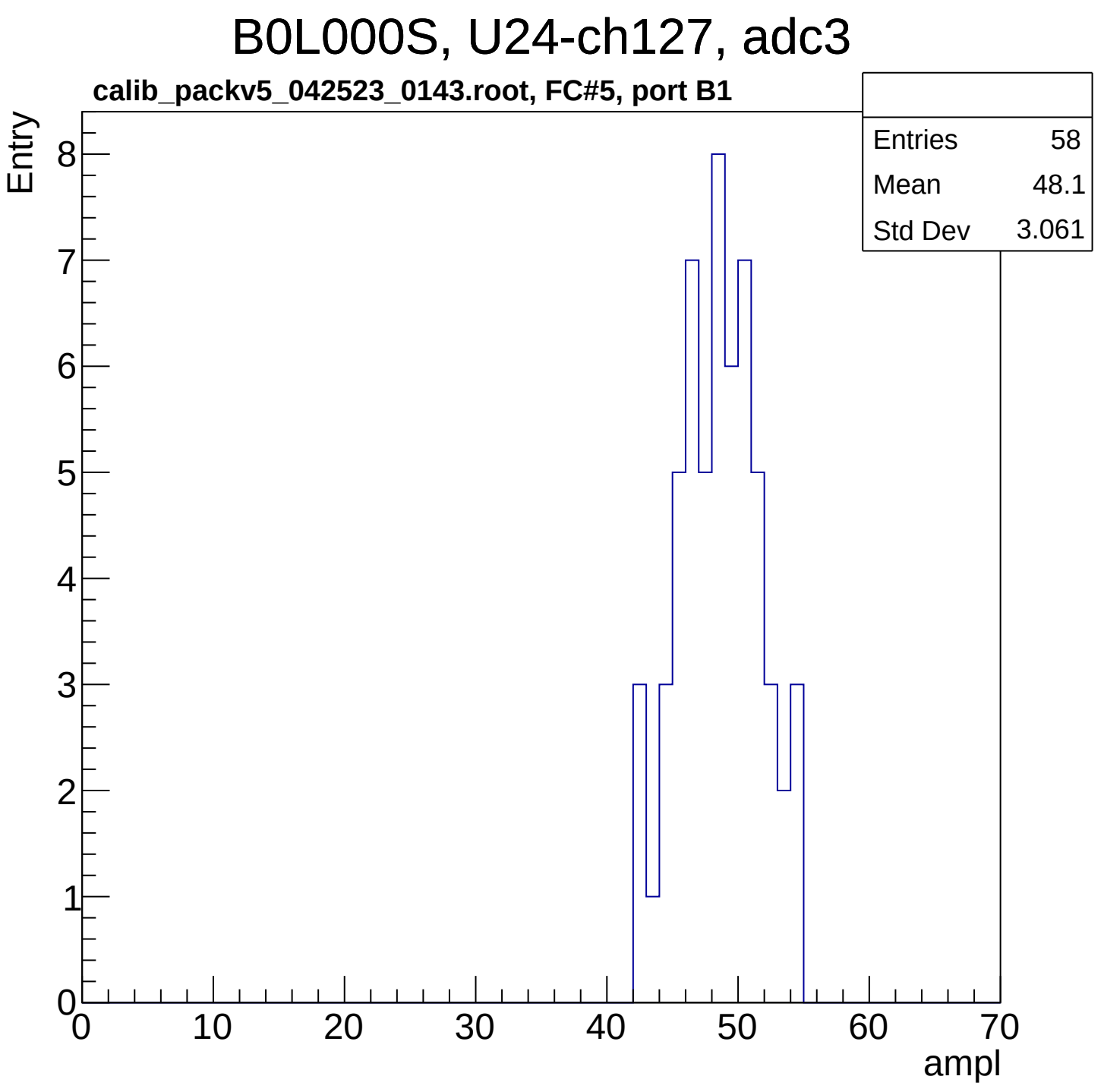
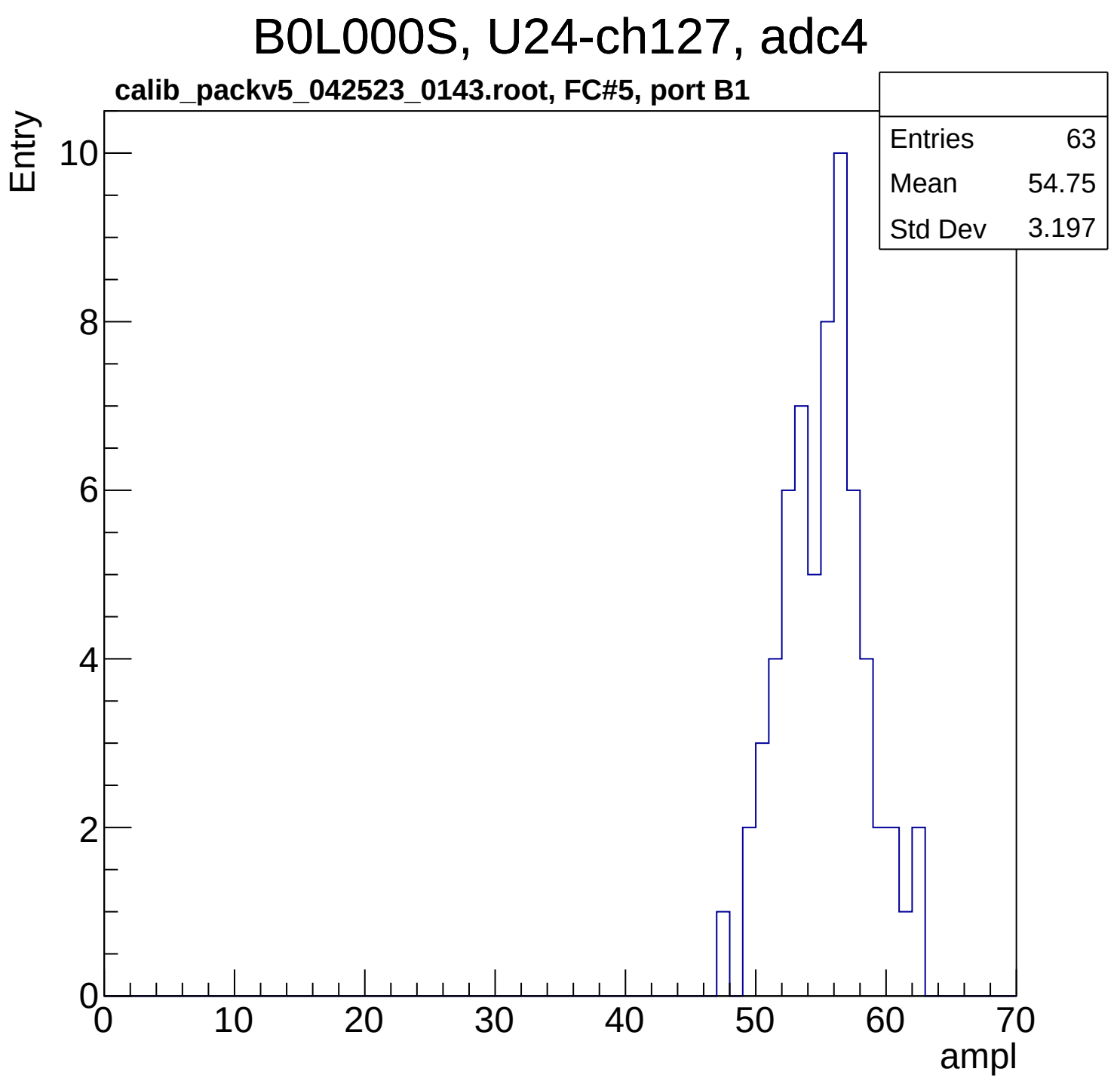
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

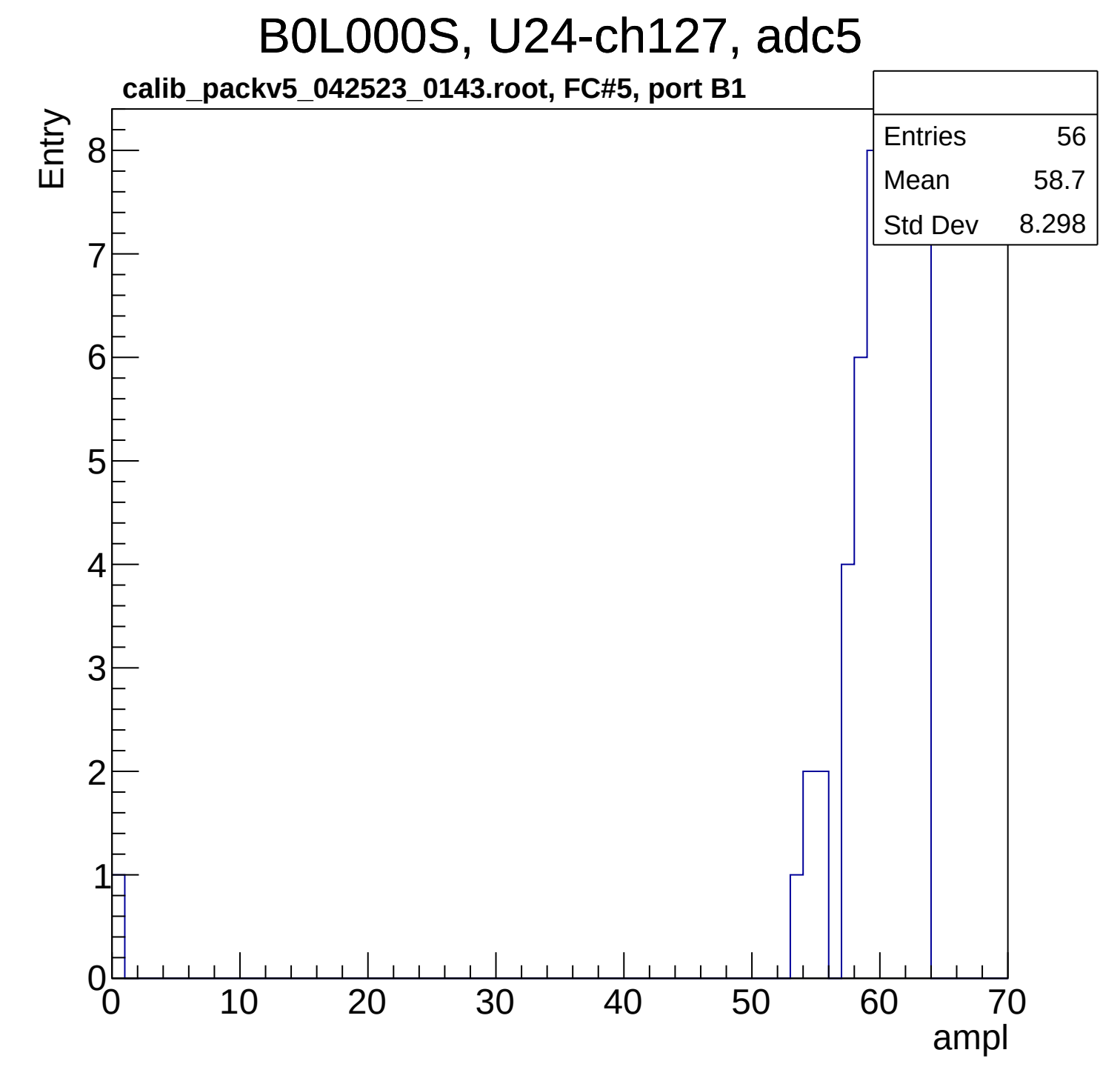

B0L000S, U24-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.42
Std Dev 4.835
Gaus mean : 29.6240
Gaus Width: 3.5899
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

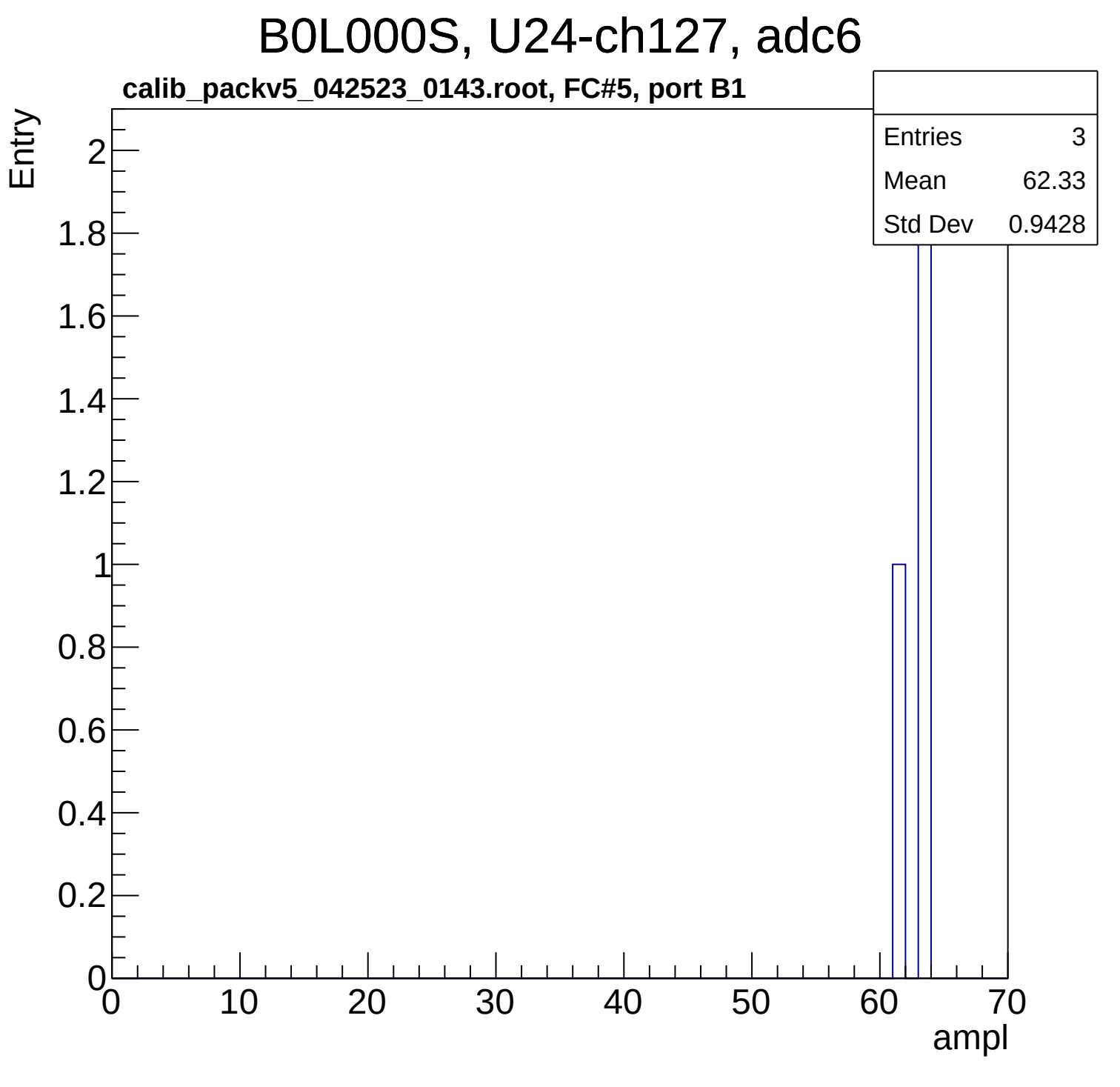

B0L000S, U24-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.75
Std Dev 2.997
Gaus mean : 36.3651
Gaus Width: 3.3290
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

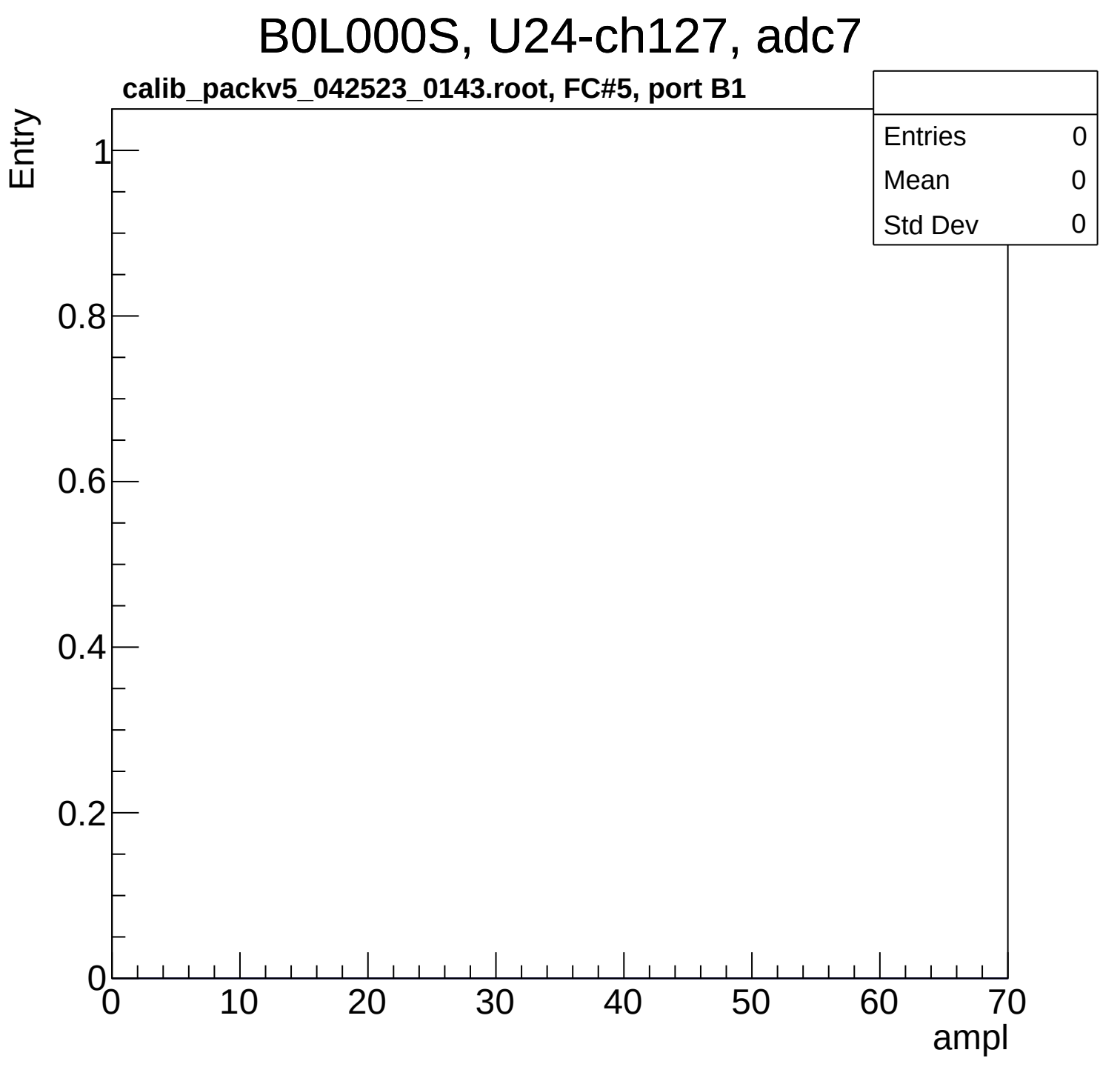

B0L000S, U24-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.81
Std Dev 3.45
Gaus mean : 43.1558
Gaus Width: 3.5508
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

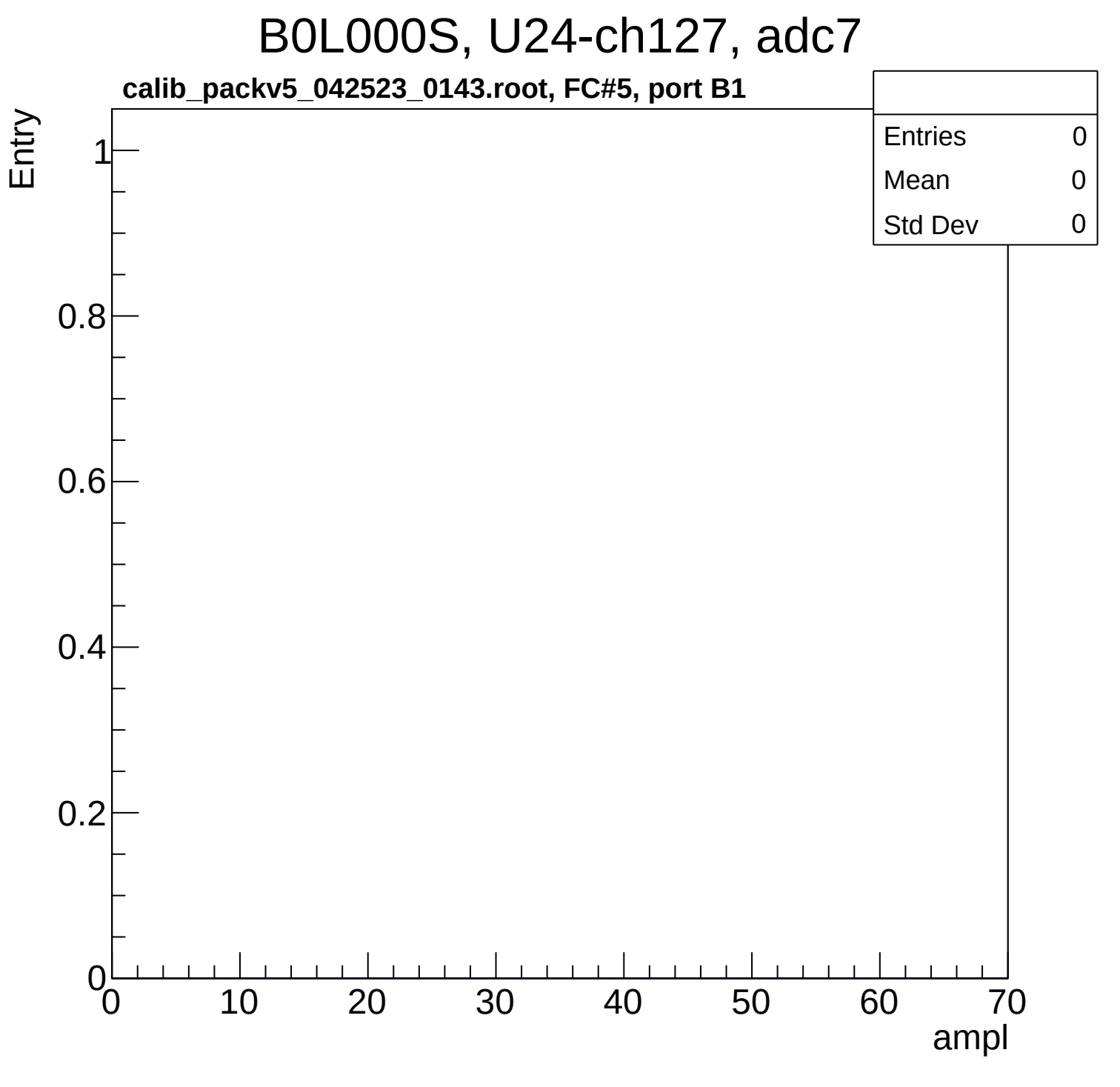

B0L000S, U24-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.1
Std Dev 3.061
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.75
Std Dev 3.197
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.7
Std Dev 8.298
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U24-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl