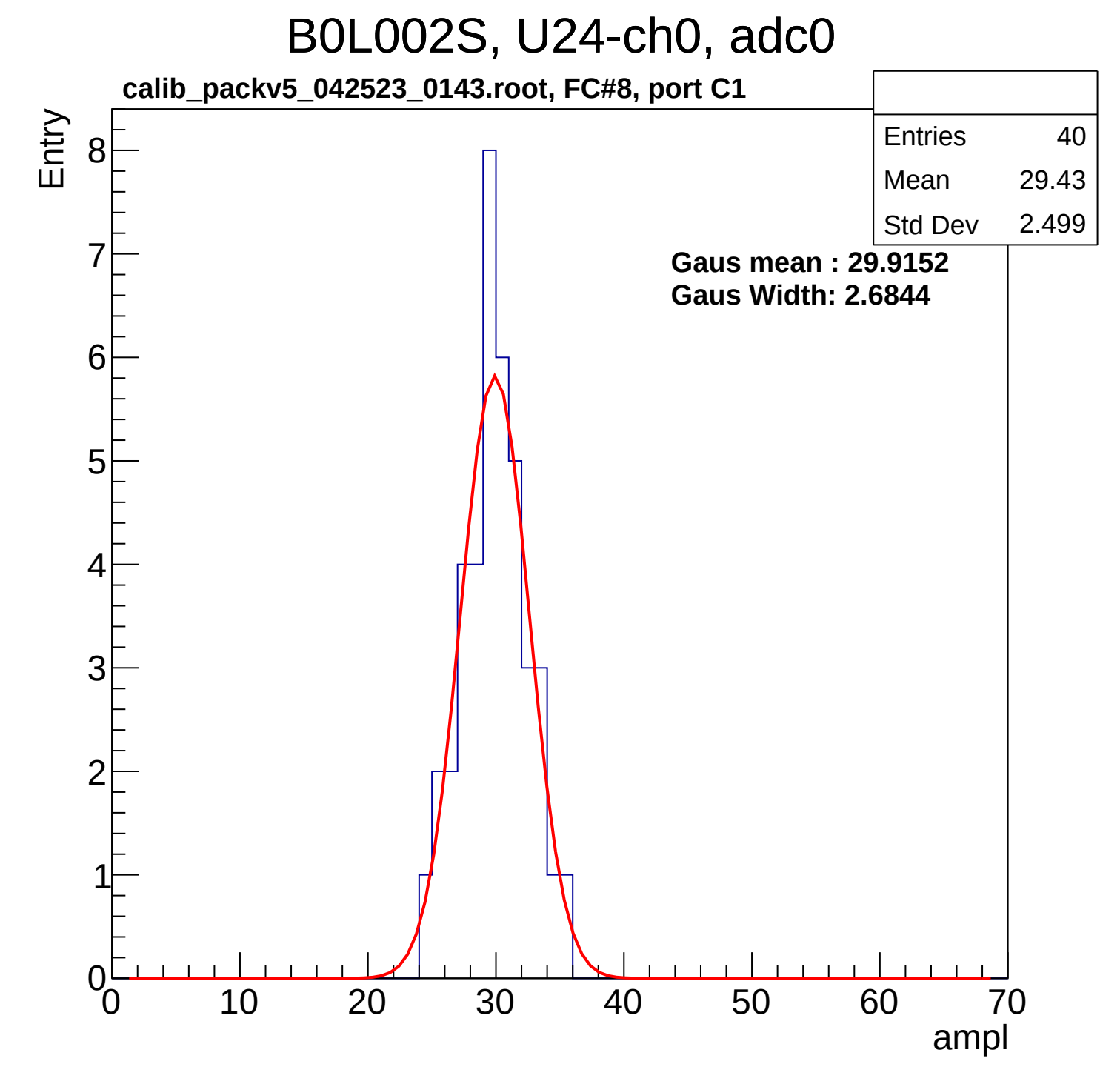
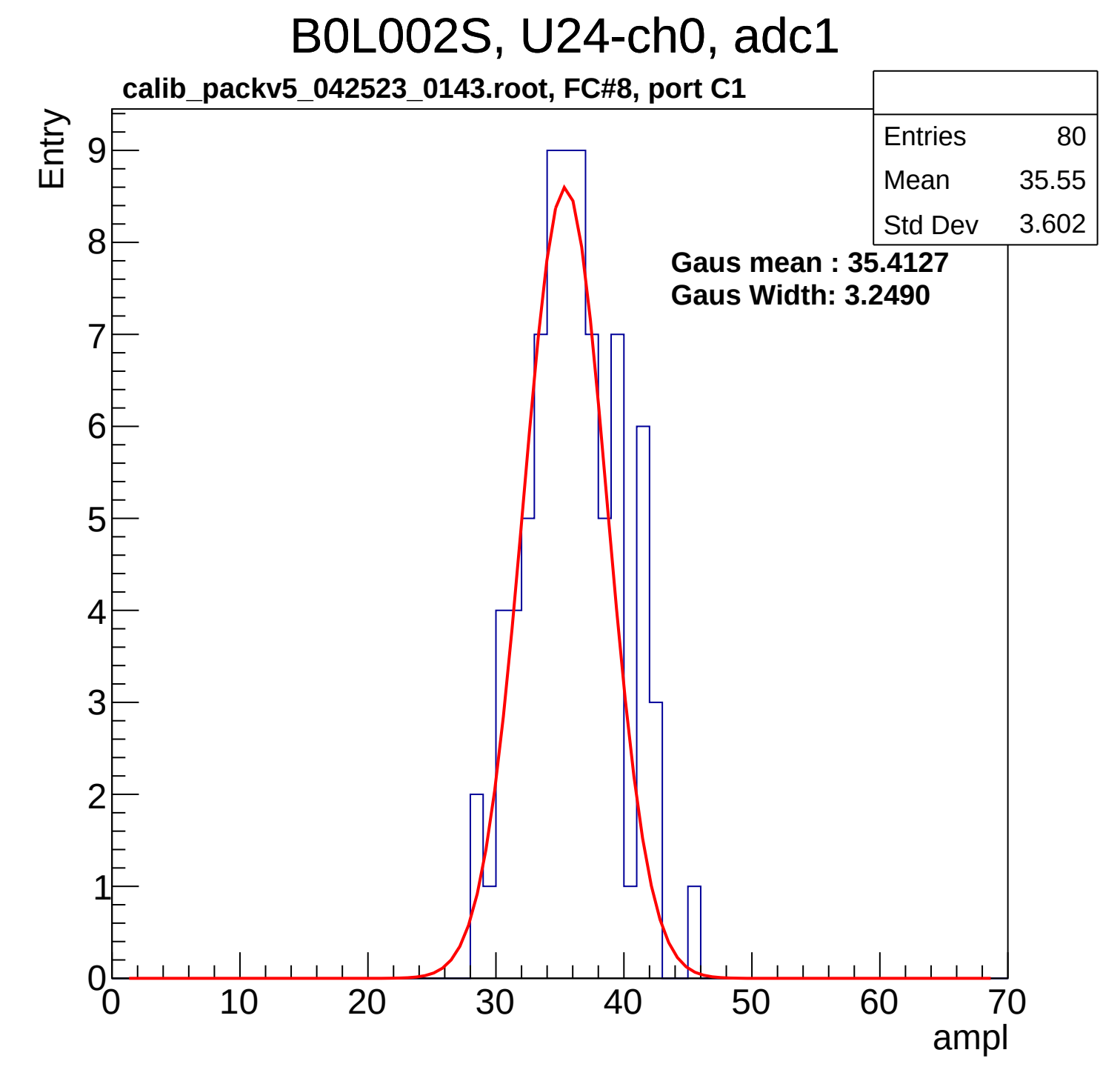

B0L002S, U24-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 29.43
Std Dev 2.499
Gaus mean : 29.9152
Gaus Width: 2.6844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 35.55
Std Dev 3.602
Gaus mean : 35.4127
Gaus Width: 3.2490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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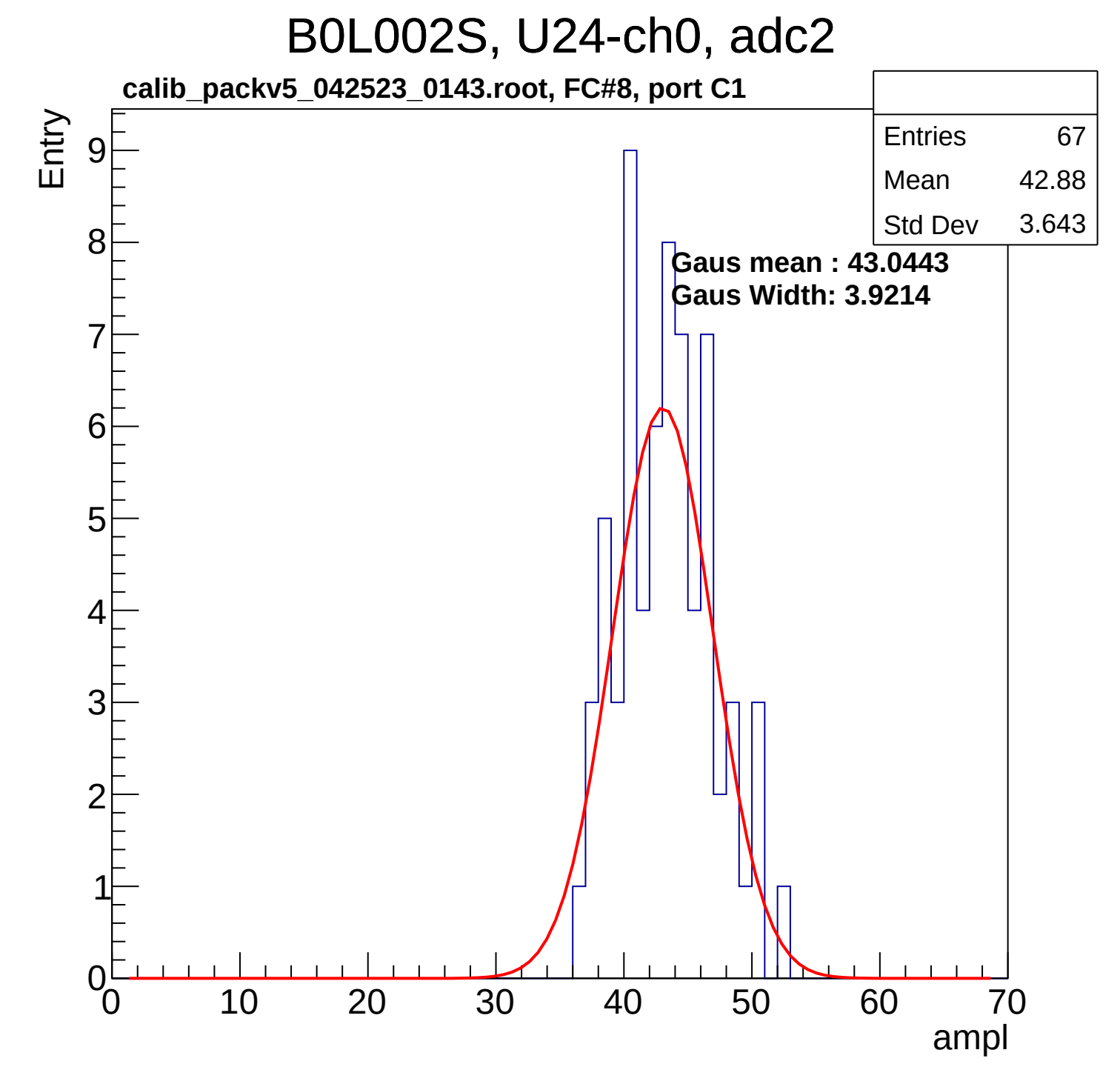
B0L002S, U24-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42.88
Std Dev 3.643
Gaus mean : 43.0443
Gaus Width: 3.9214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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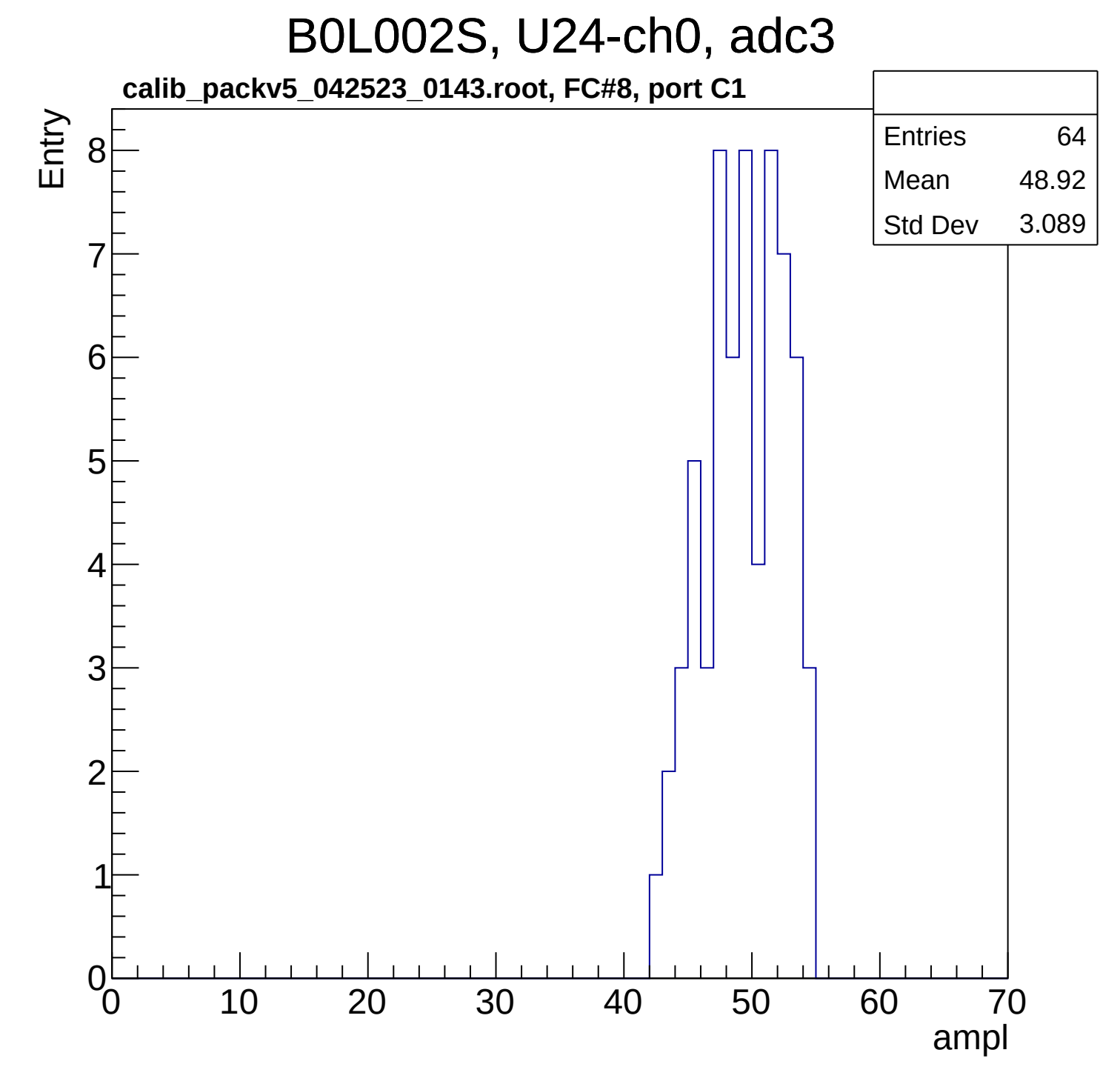
B0L002S, U24-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 48.92
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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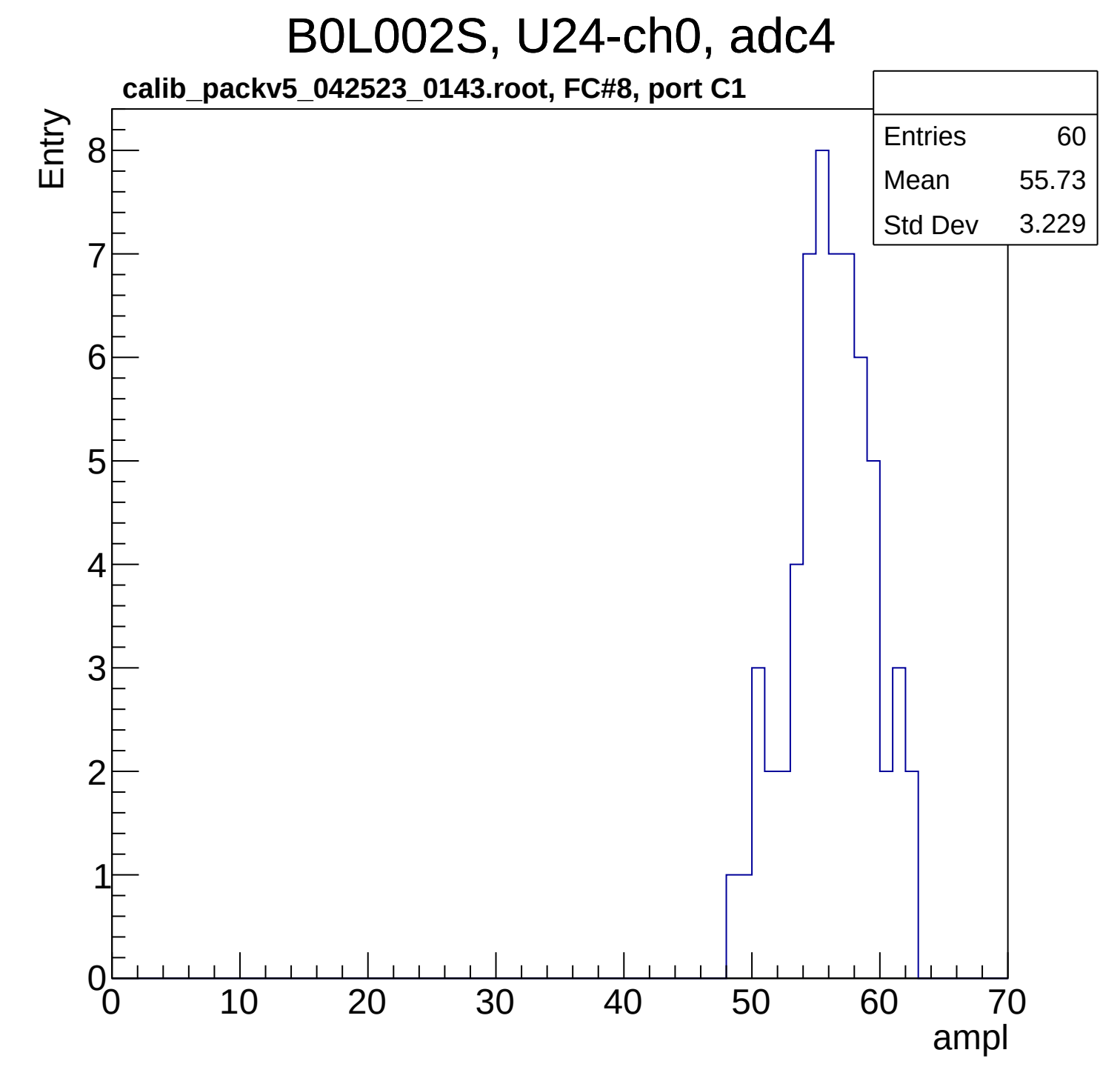
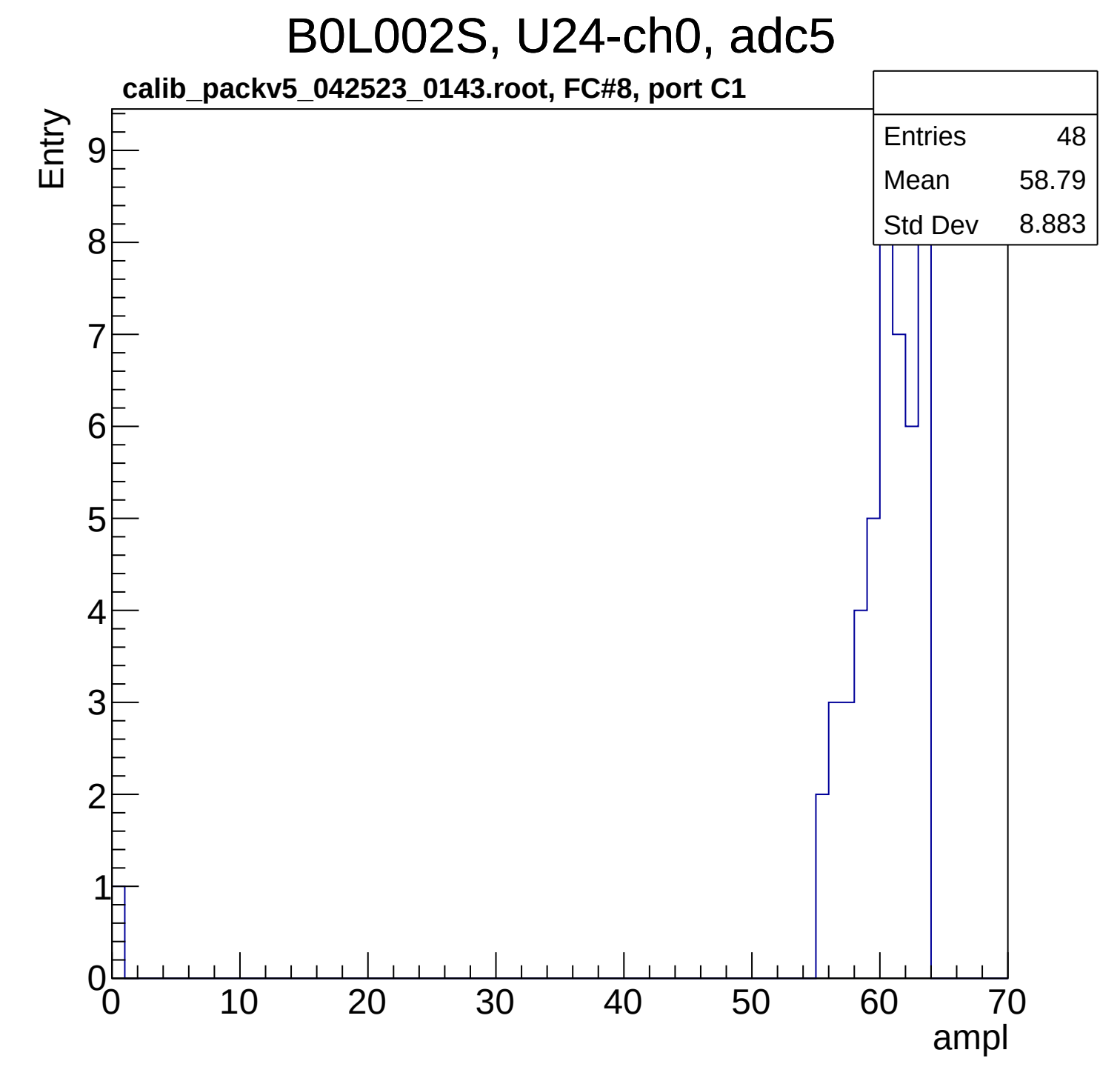

B0L002S, U24-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.73
Std Dev 3.229
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 58.79
Std Dev 8.883
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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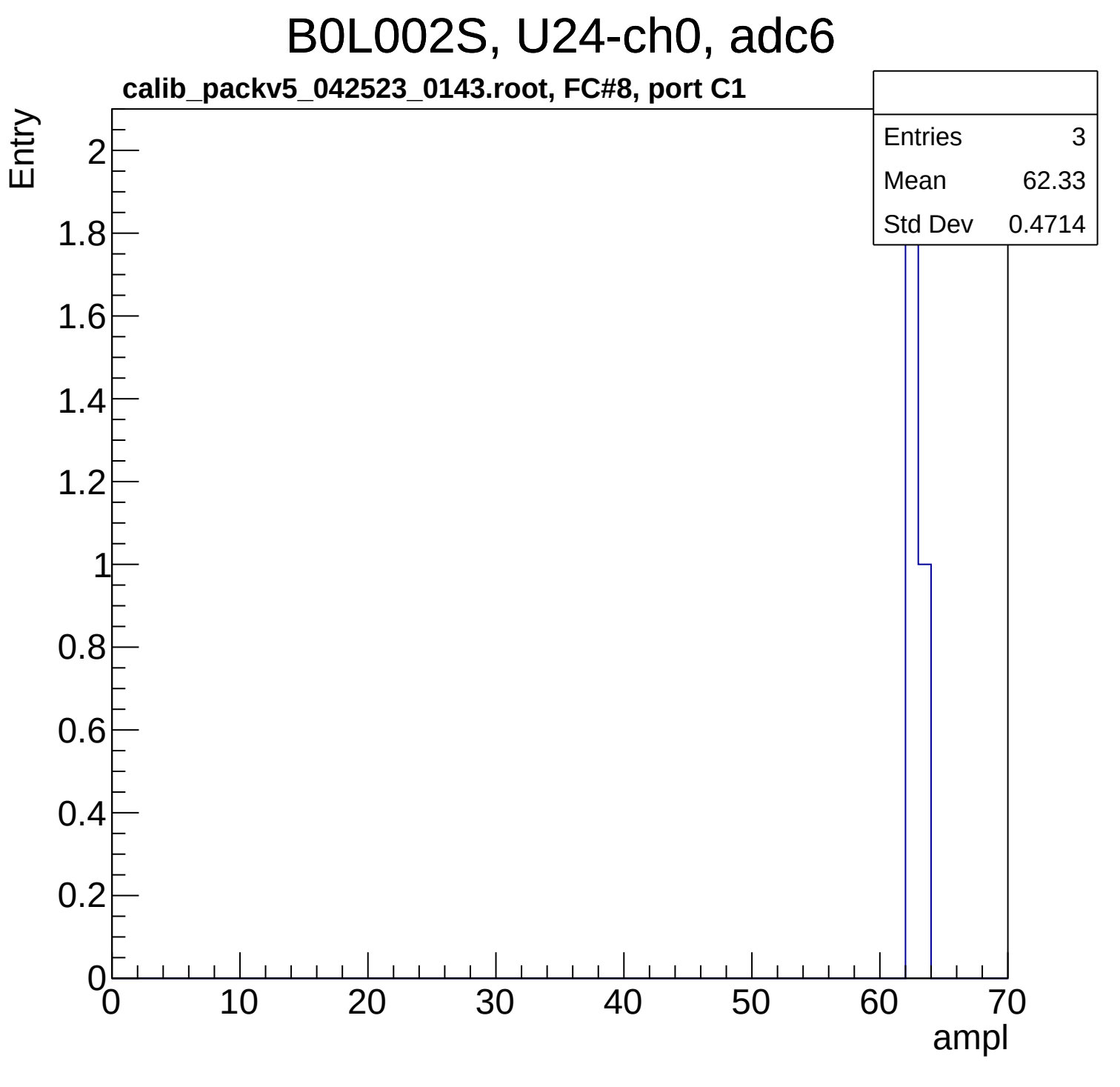
B0L002S, U24-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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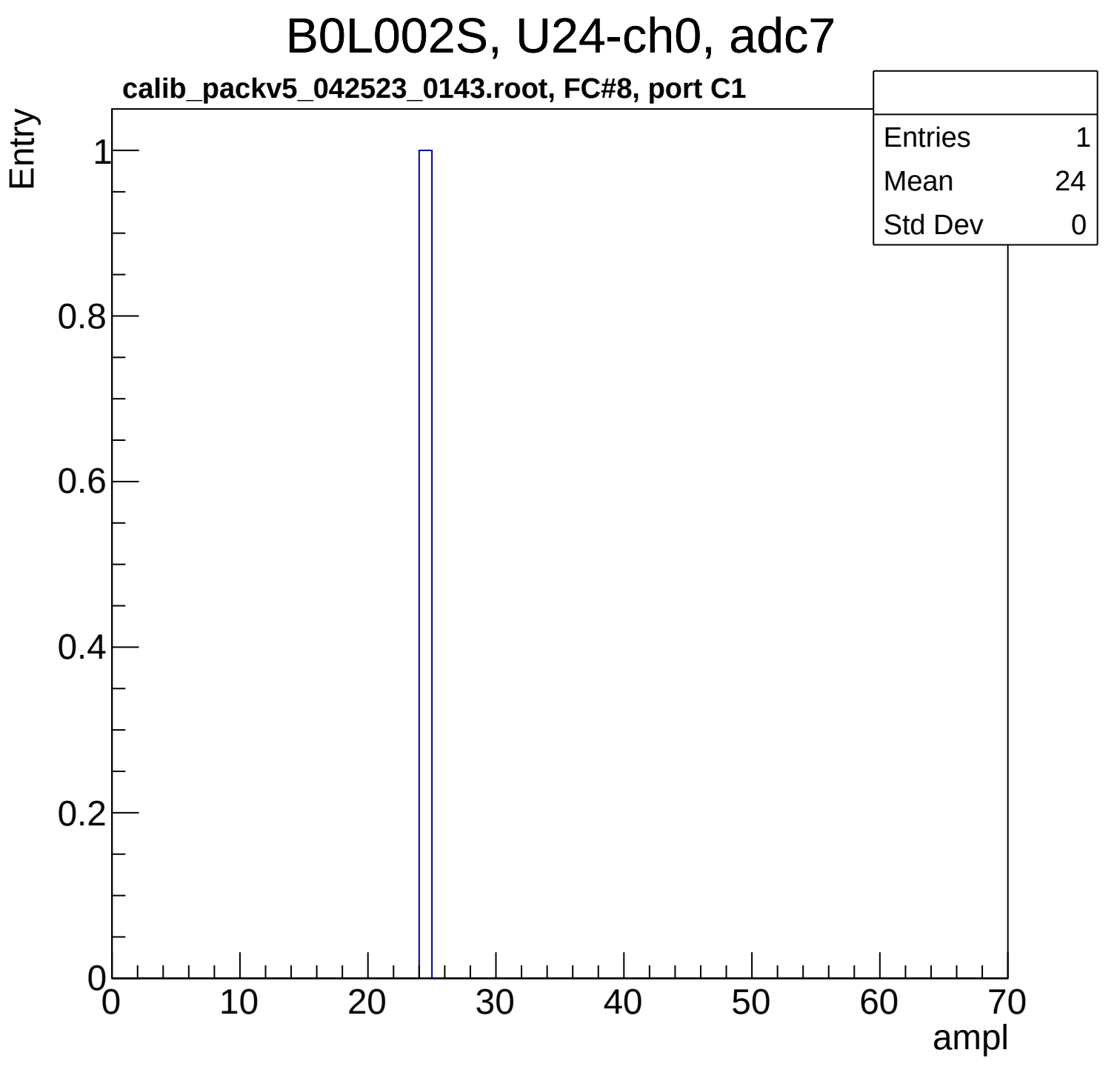
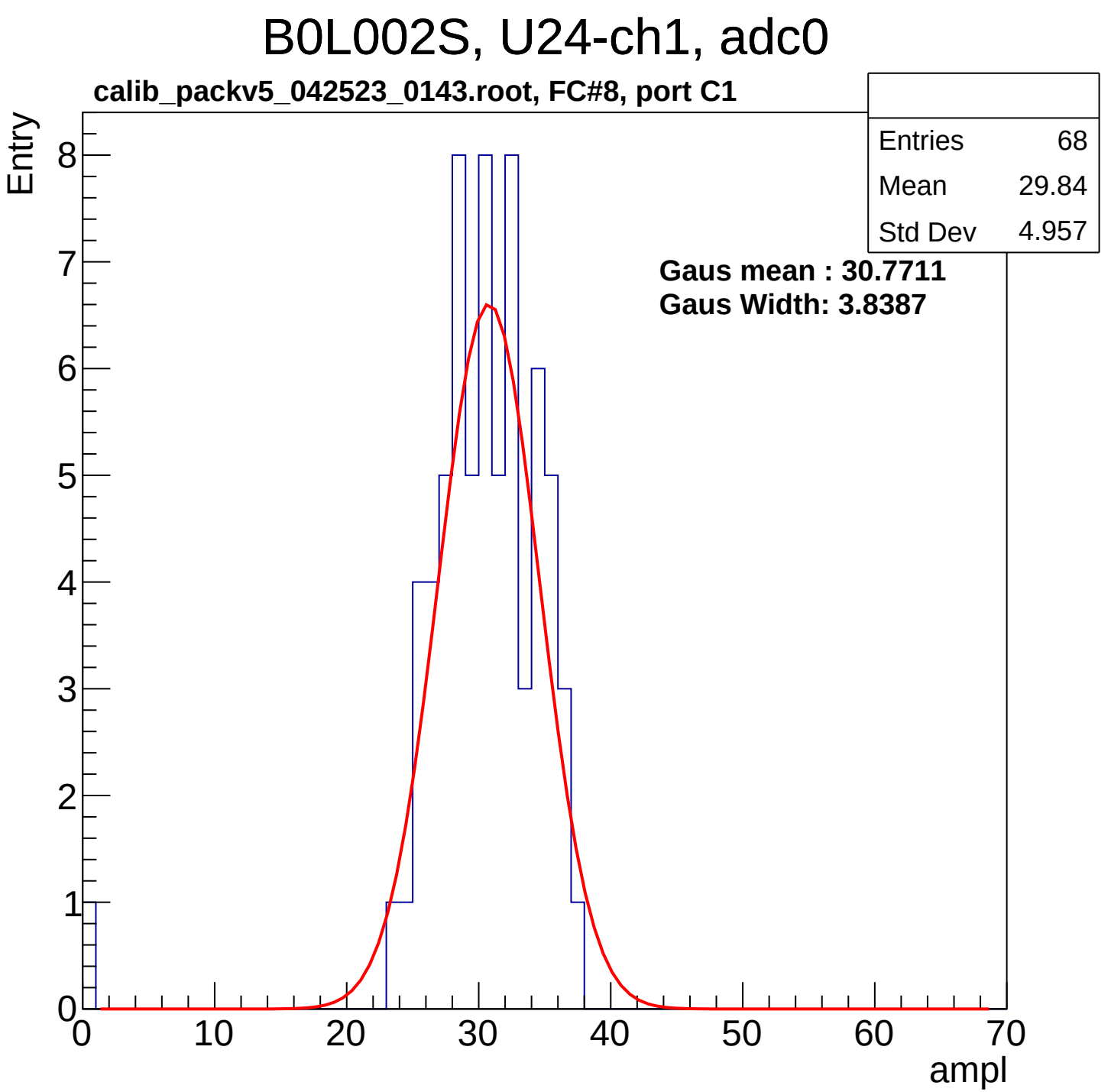

B0L002S, U24-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.84
Std Dev 4.957
Gaus mean : 30.7711
Gaus Width: 3.8387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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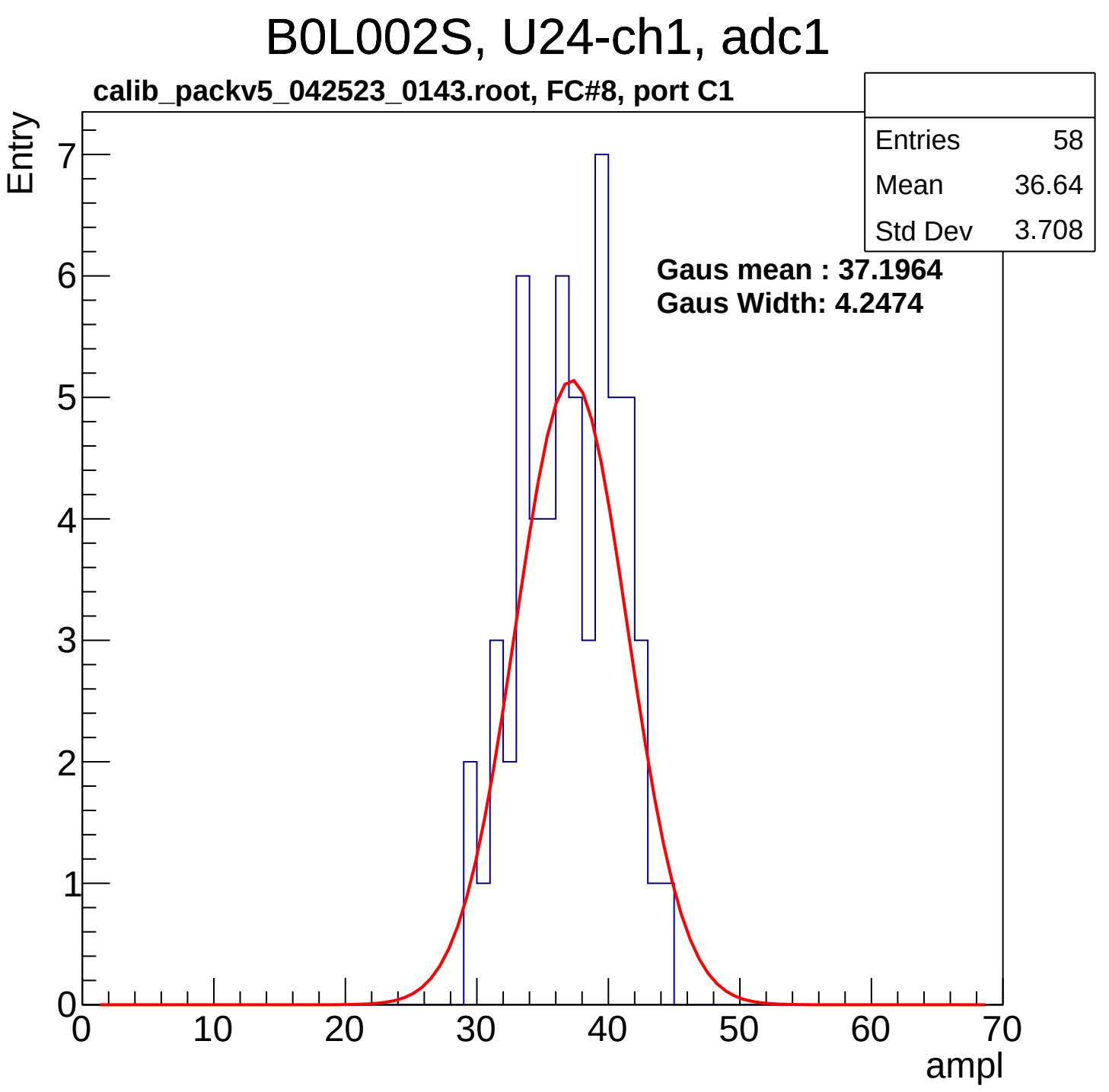
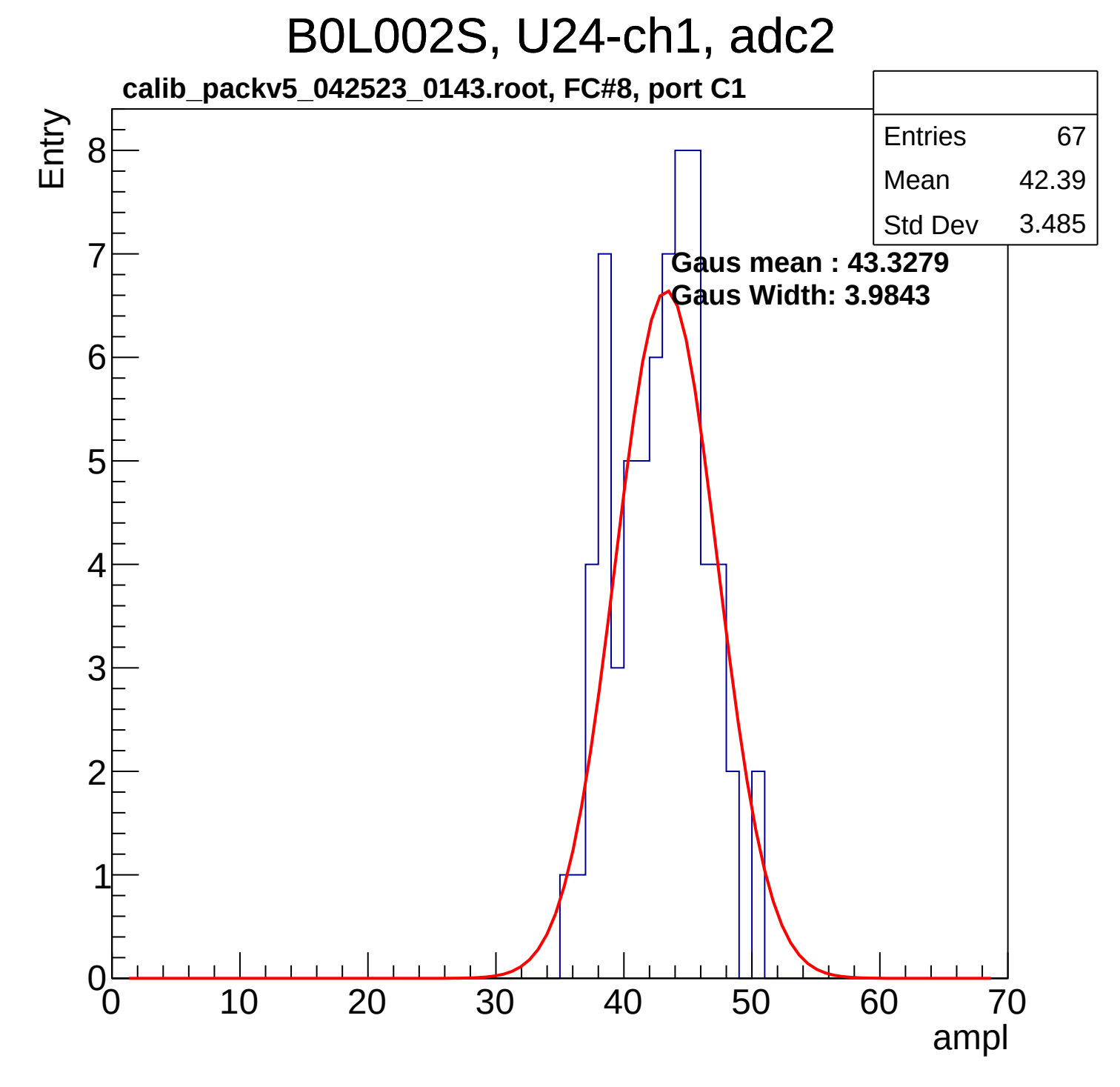

B0L002S, U24-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 36.64
Std Dev 3.708
Gaus mean : 37.1964
Gaus Width: 4.2474
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42.39
Std Dev 3.485
Gaus mean : 43.3279
Gaus Width: 3.9843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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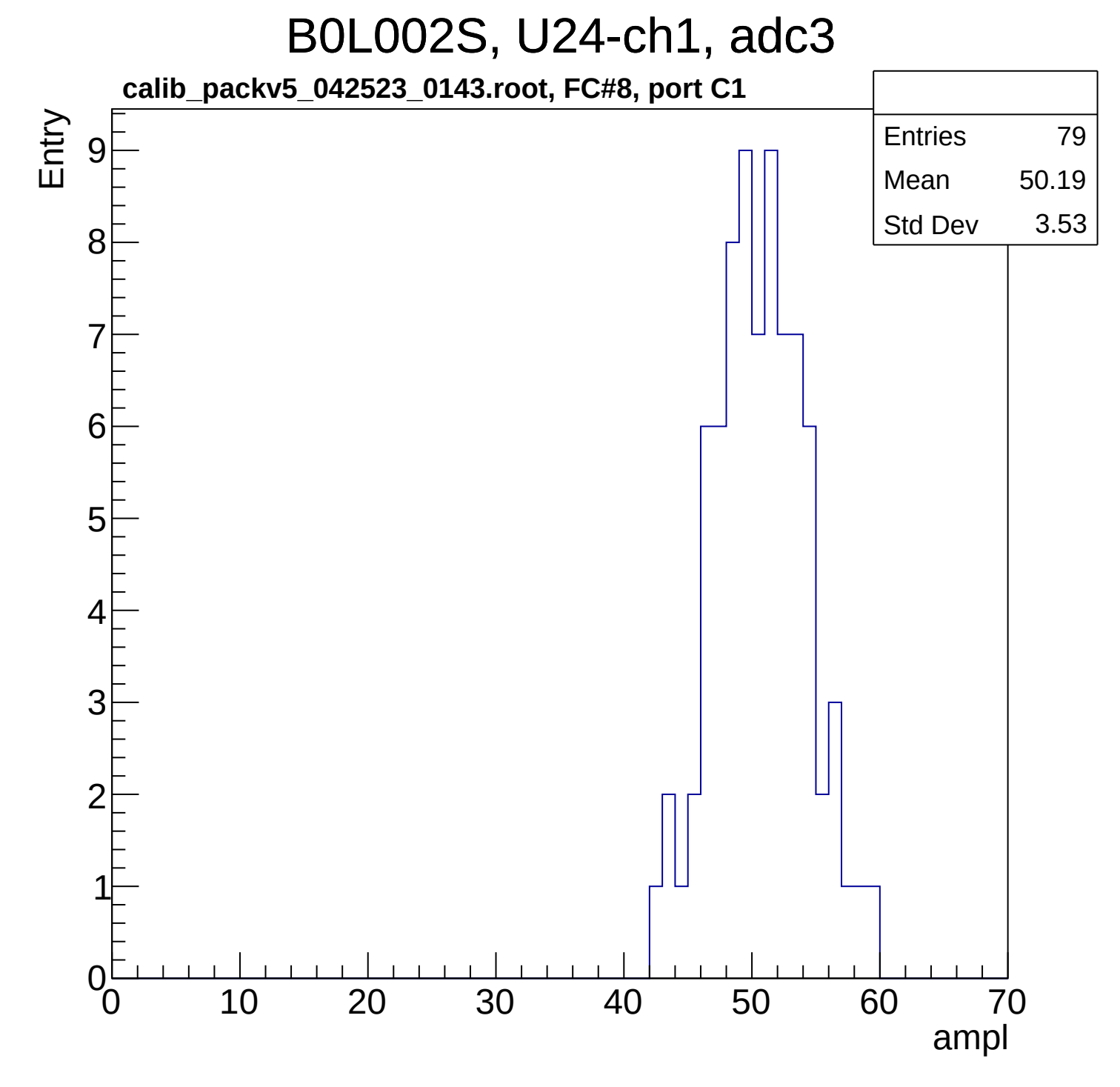
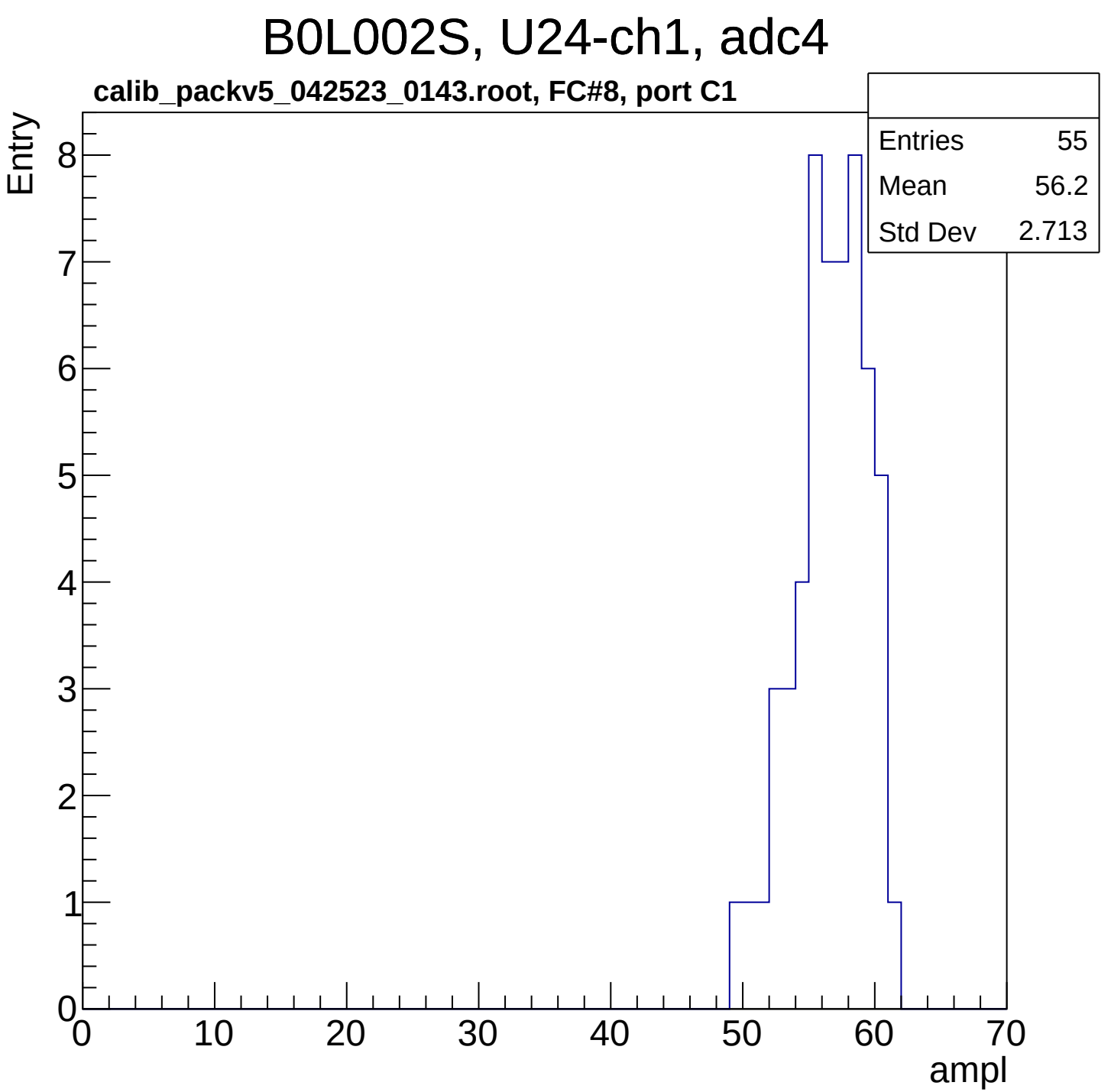

B0L002S, U24-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 50.19
Std Dev 3.53
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.2
Std Dev 2.713
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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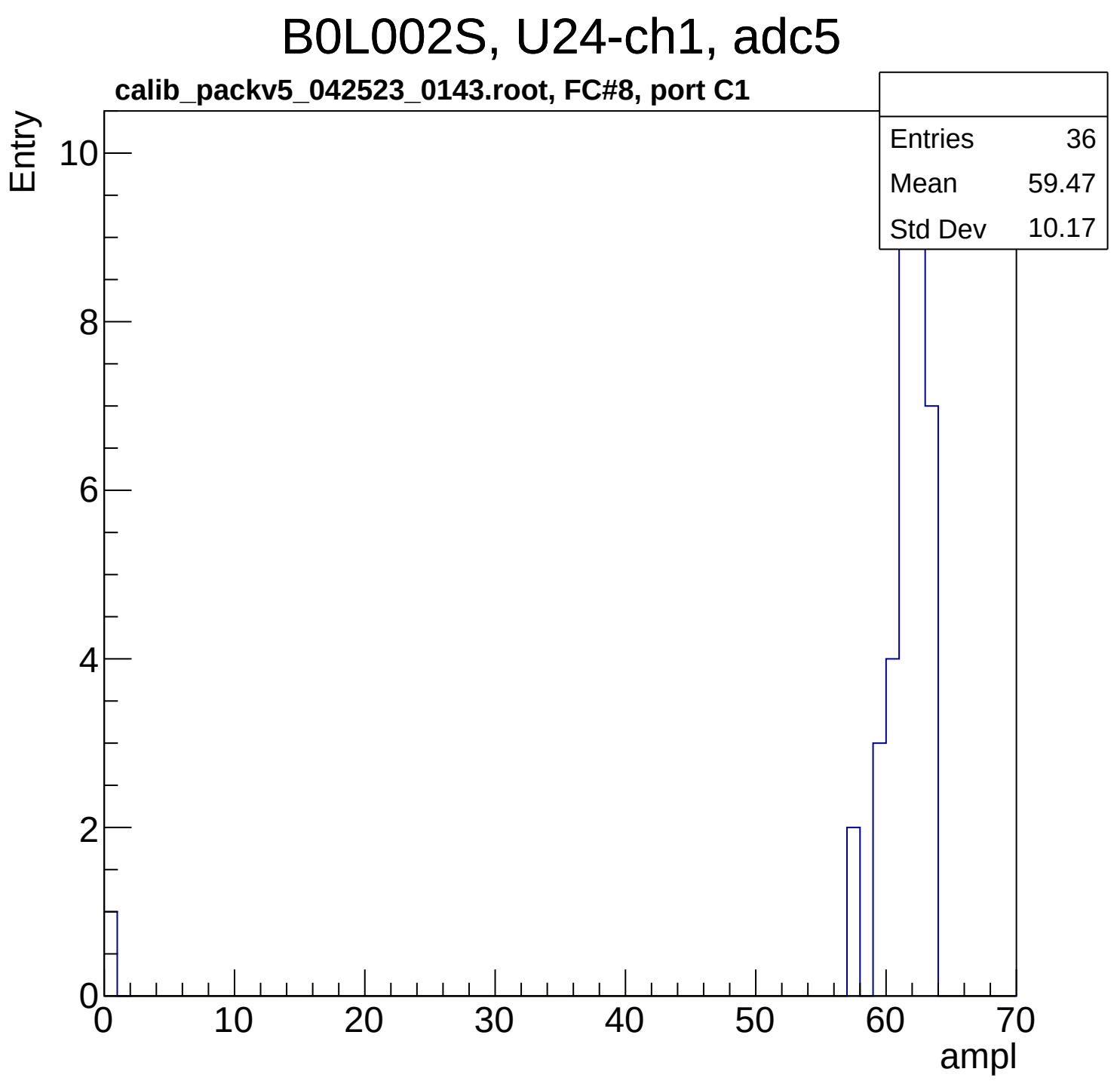
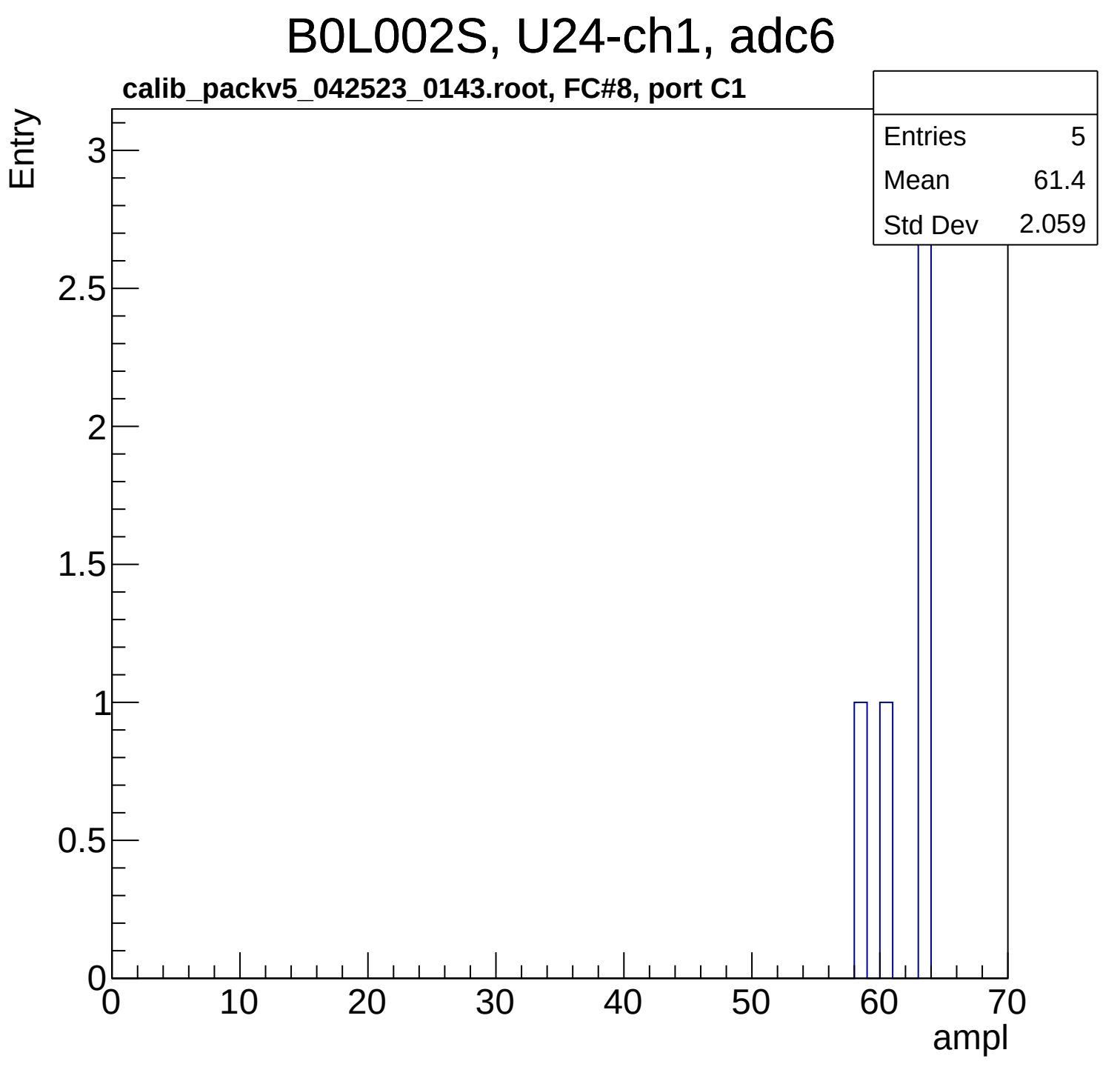
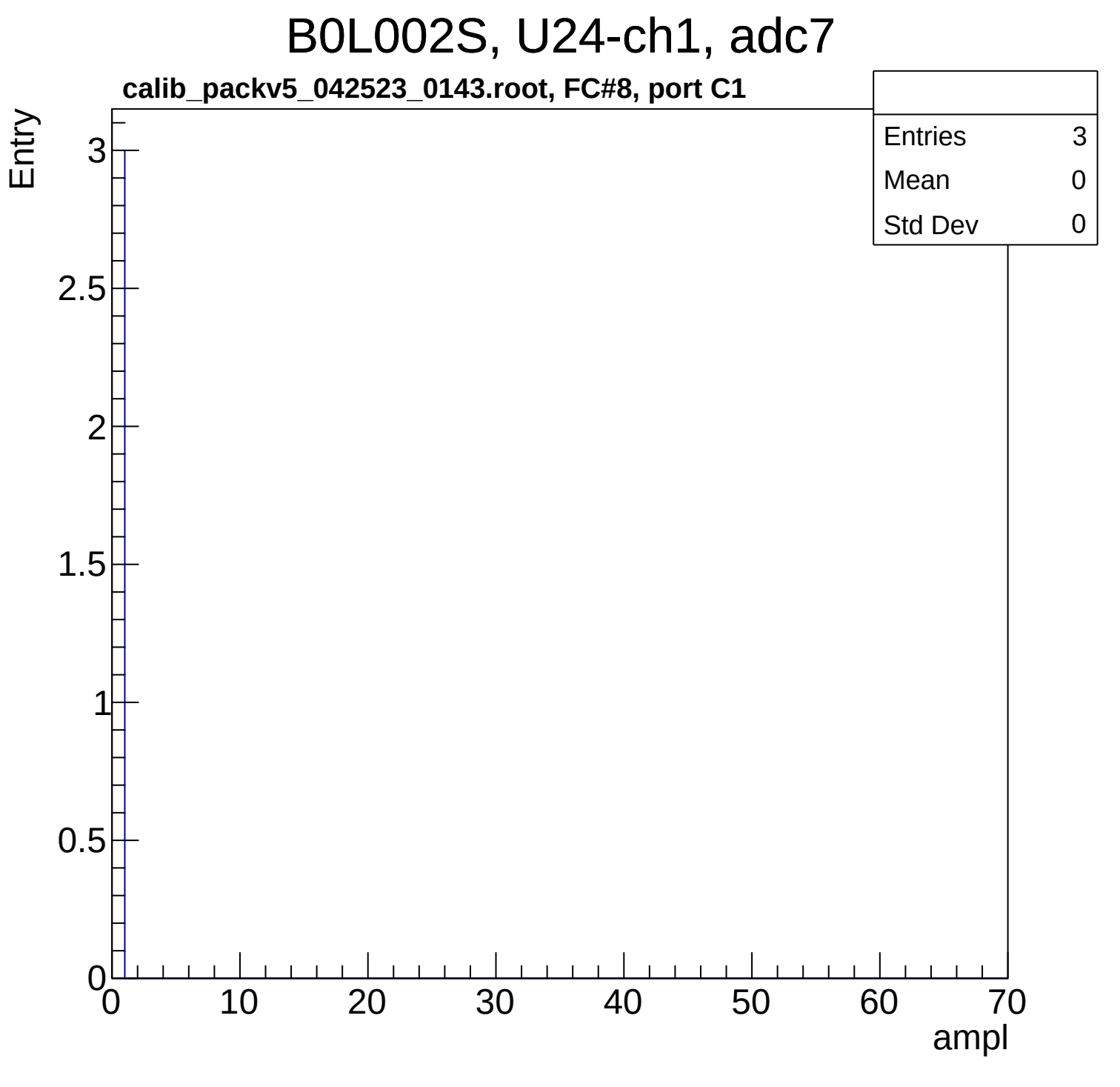
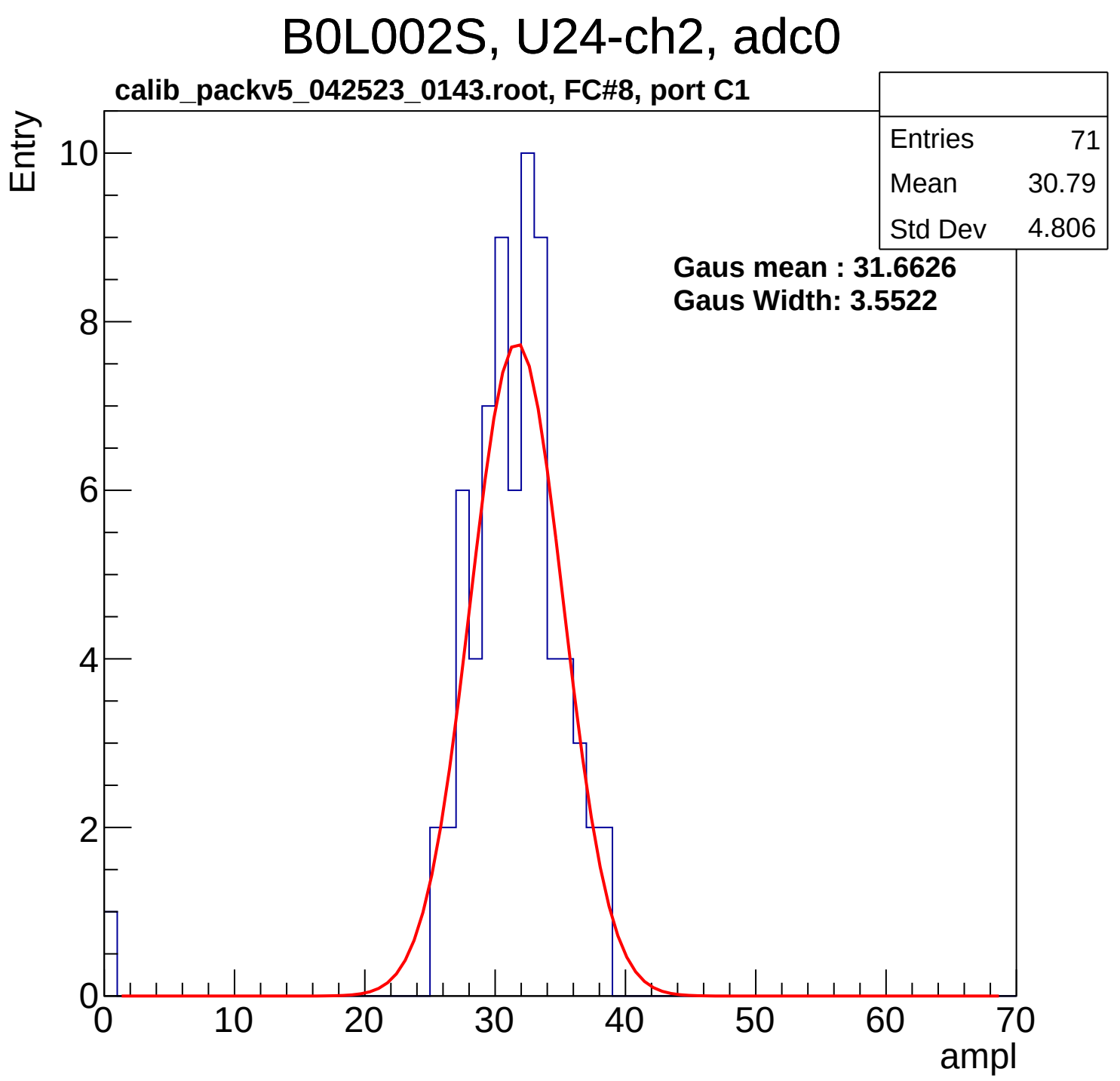
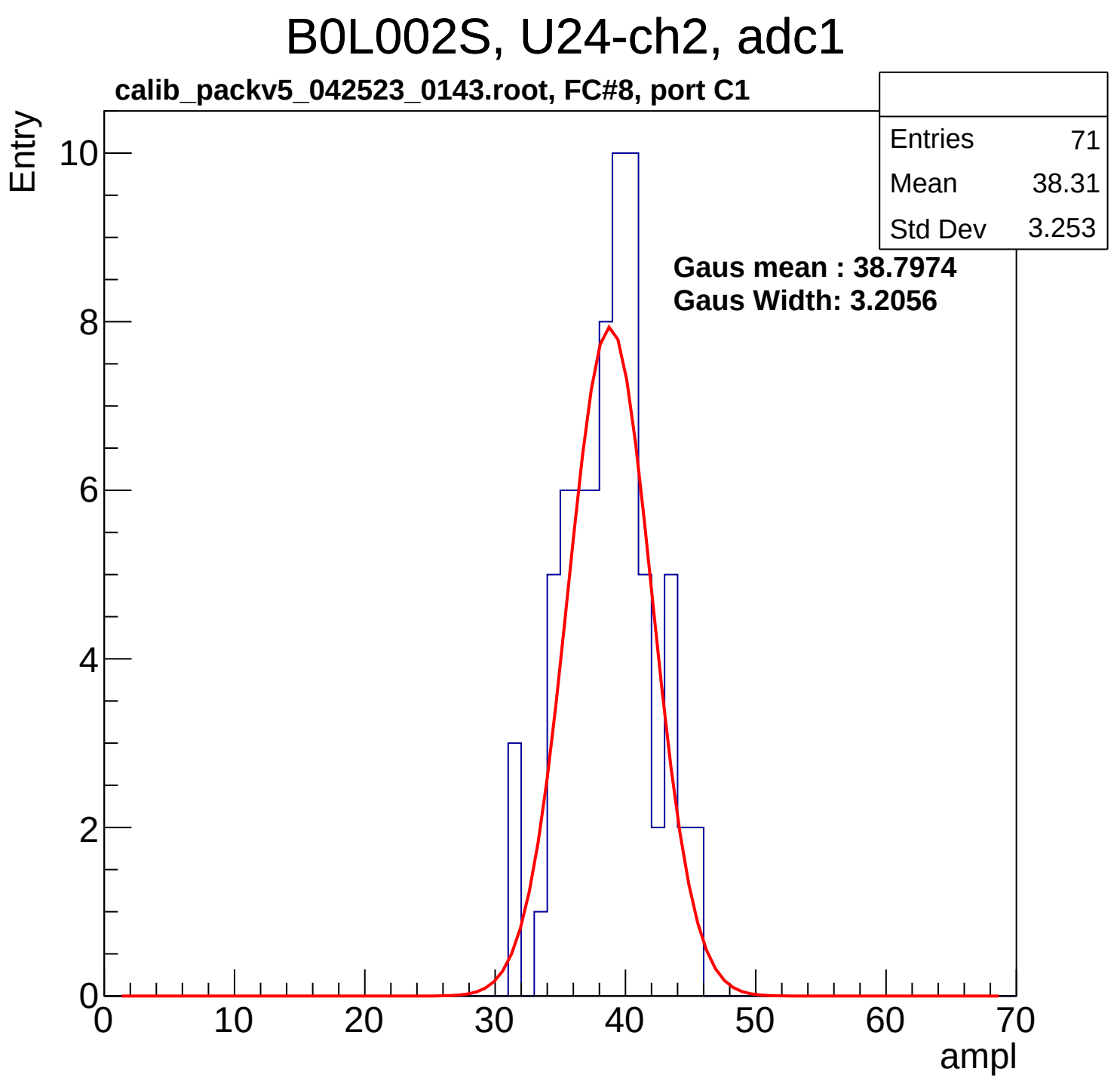
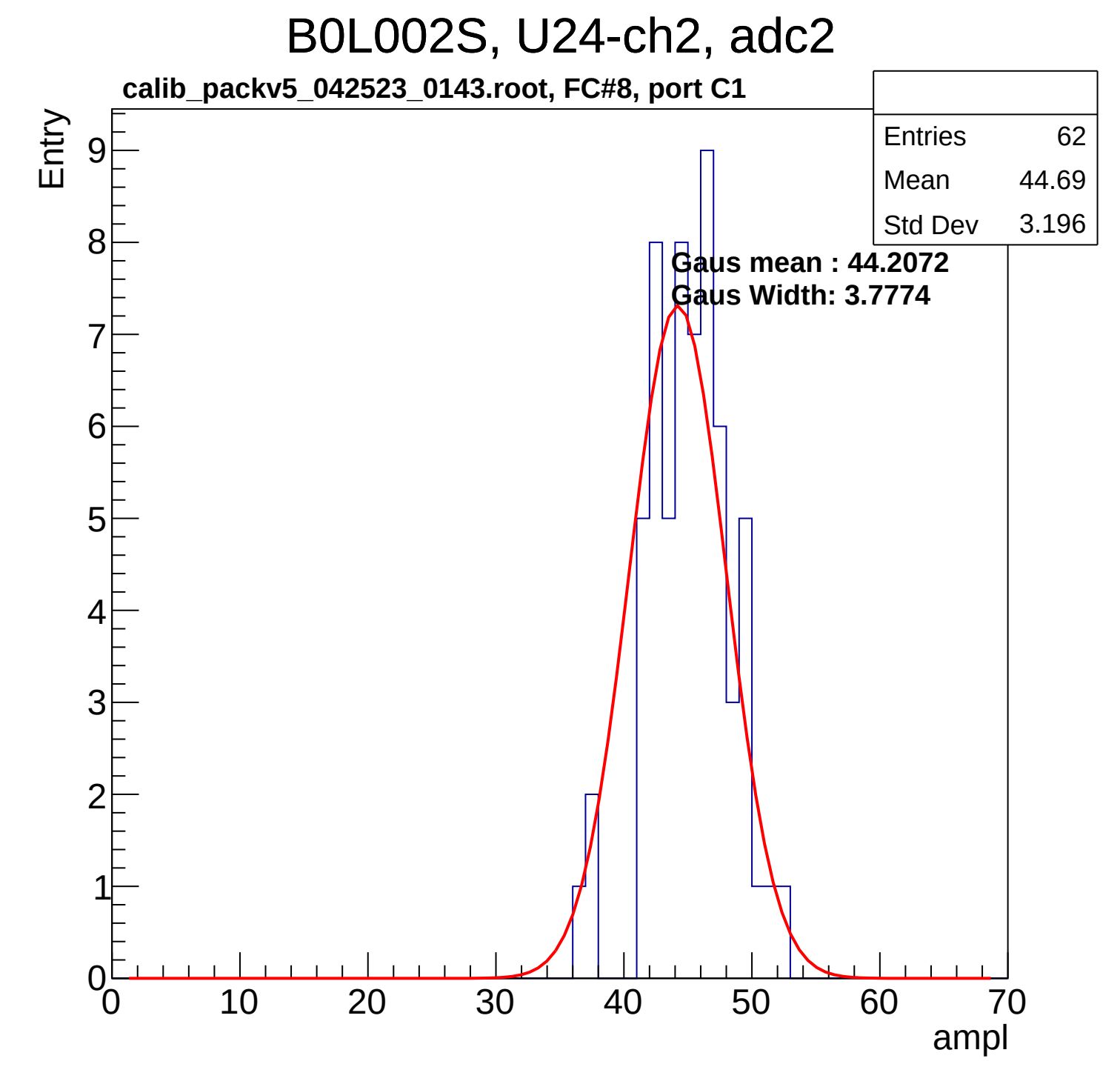

B0L002S, U24-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.47
Std Dev 10.17
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.4
Std Dev 2.059
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.79
Std Dev 4.806
Gaus mean : 31.6626
Gaus Width: 3.5522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.31
Std Dev 3.253
Gaus mean : 38.7974
Gaus Width: 3.2056
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.69
Std Dev 3.196
Gaus mean : 44.2072
Gaus Width: 3.7774
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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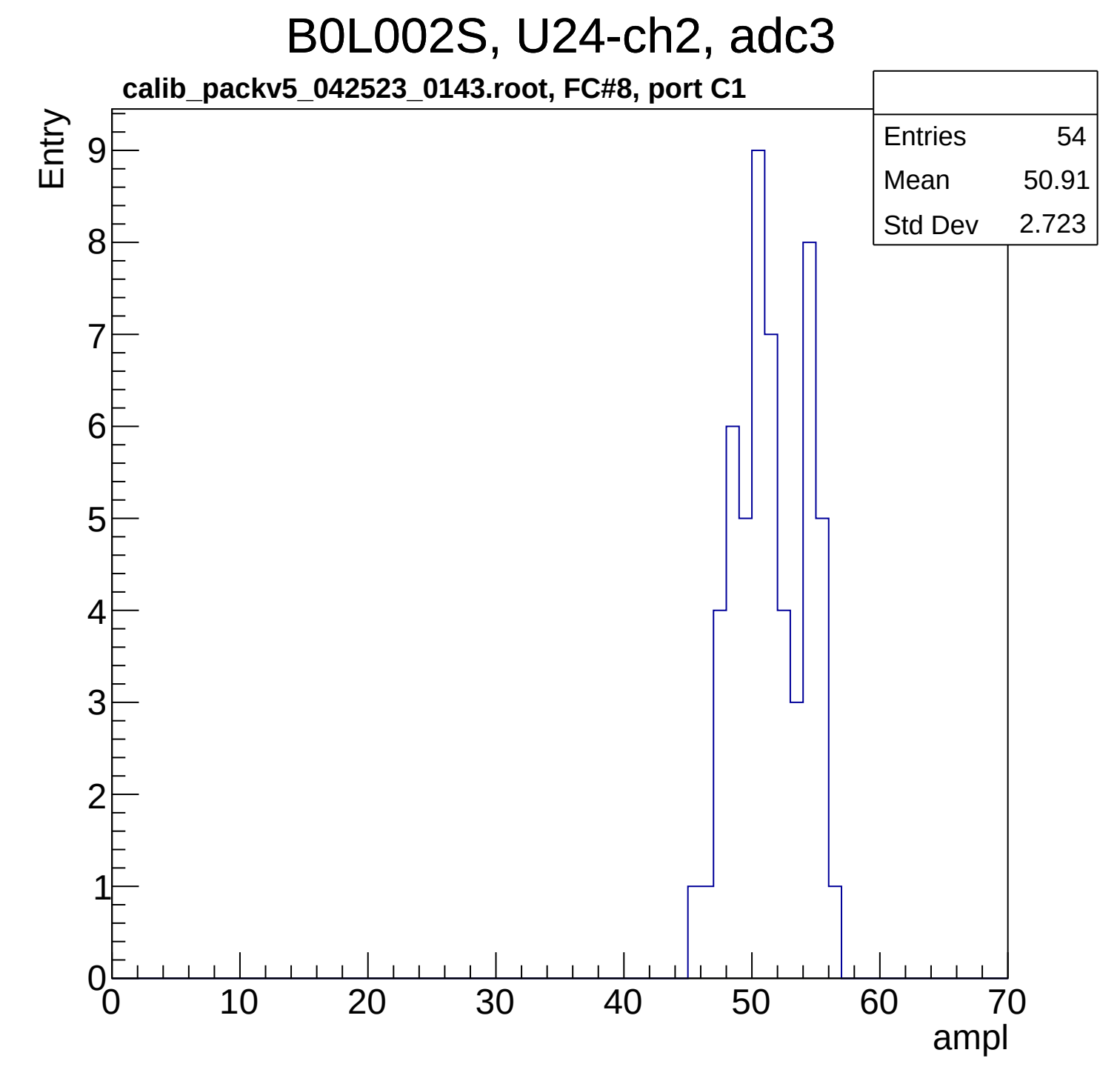
B0L002S, U24-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.91
Std Dev 2.723
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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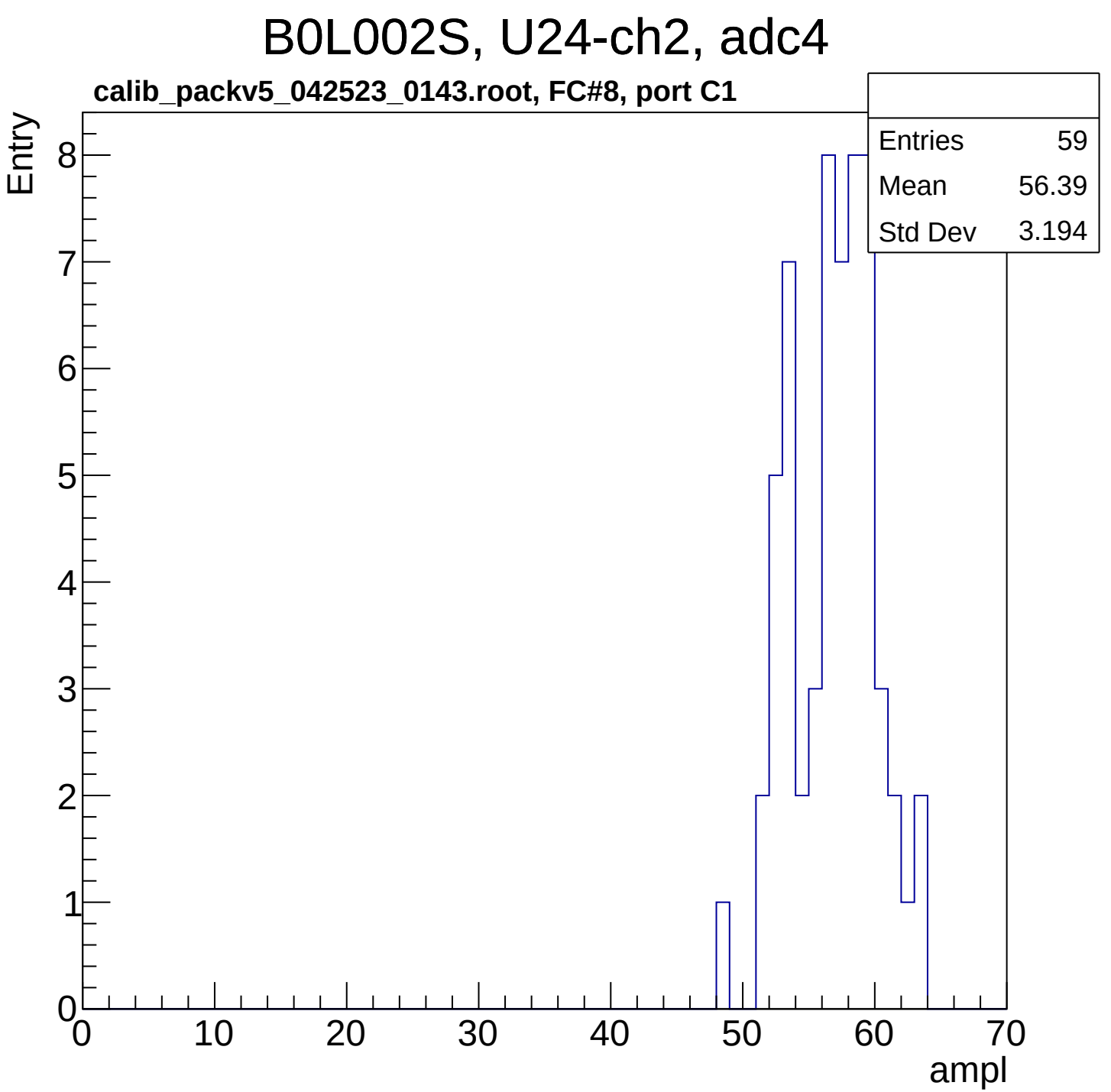
B0L002S, U24-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.39
Std Dev 3.194
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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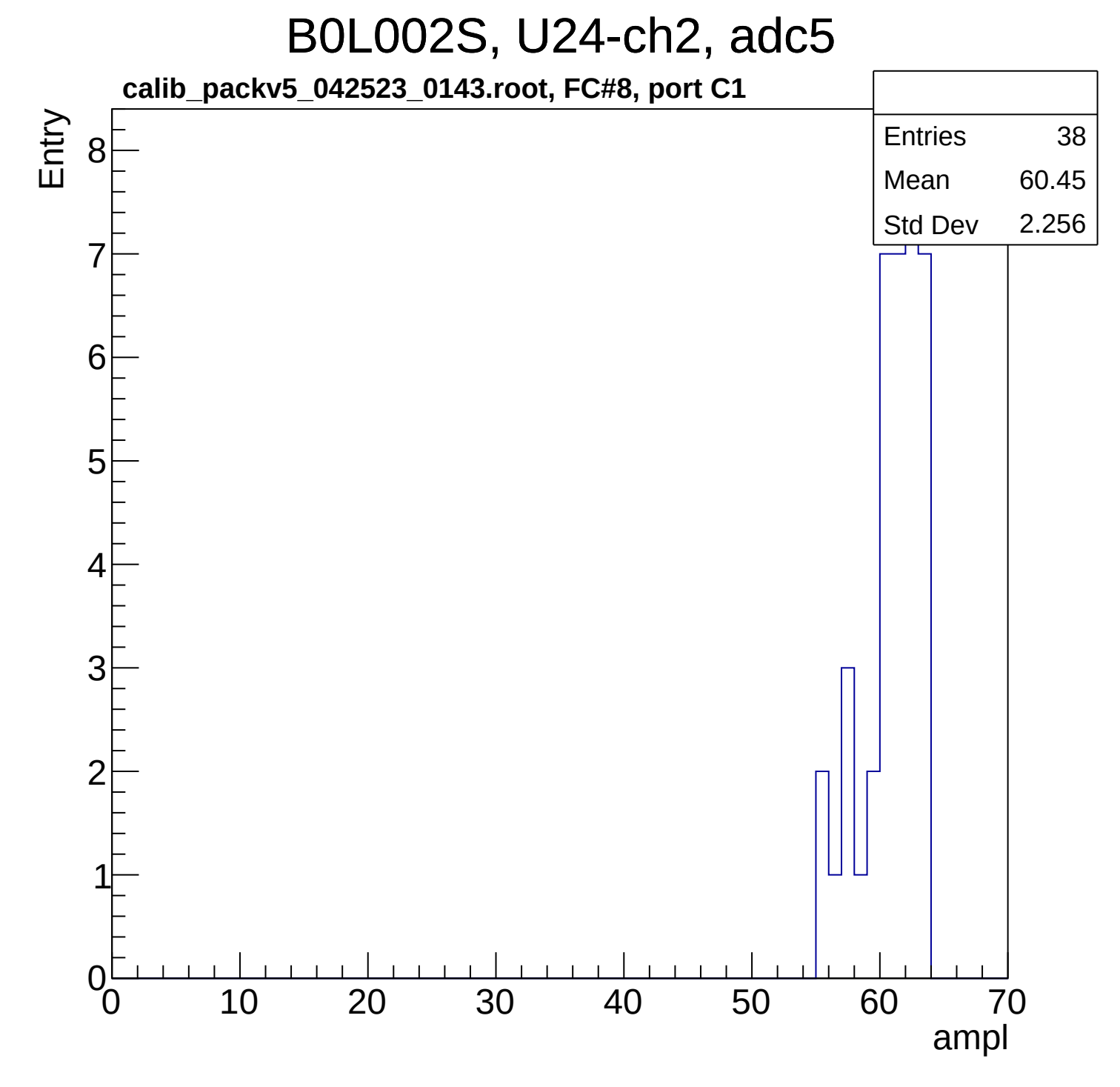
B0L002S, U24-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.45
Std Dev 2.256
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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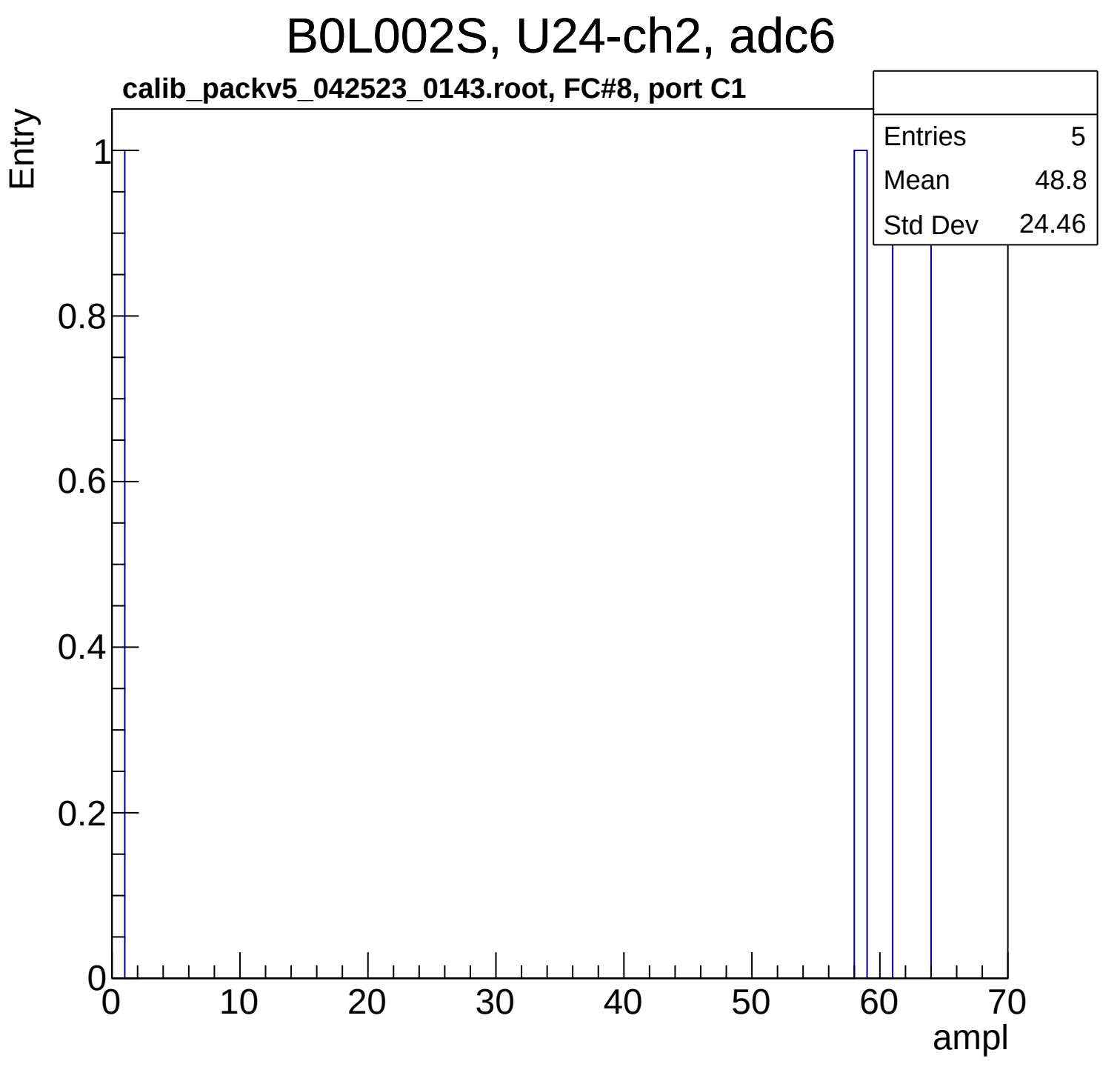
B0L002S, U24-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 48.8
Std Dev 24.46
Entry
ampl
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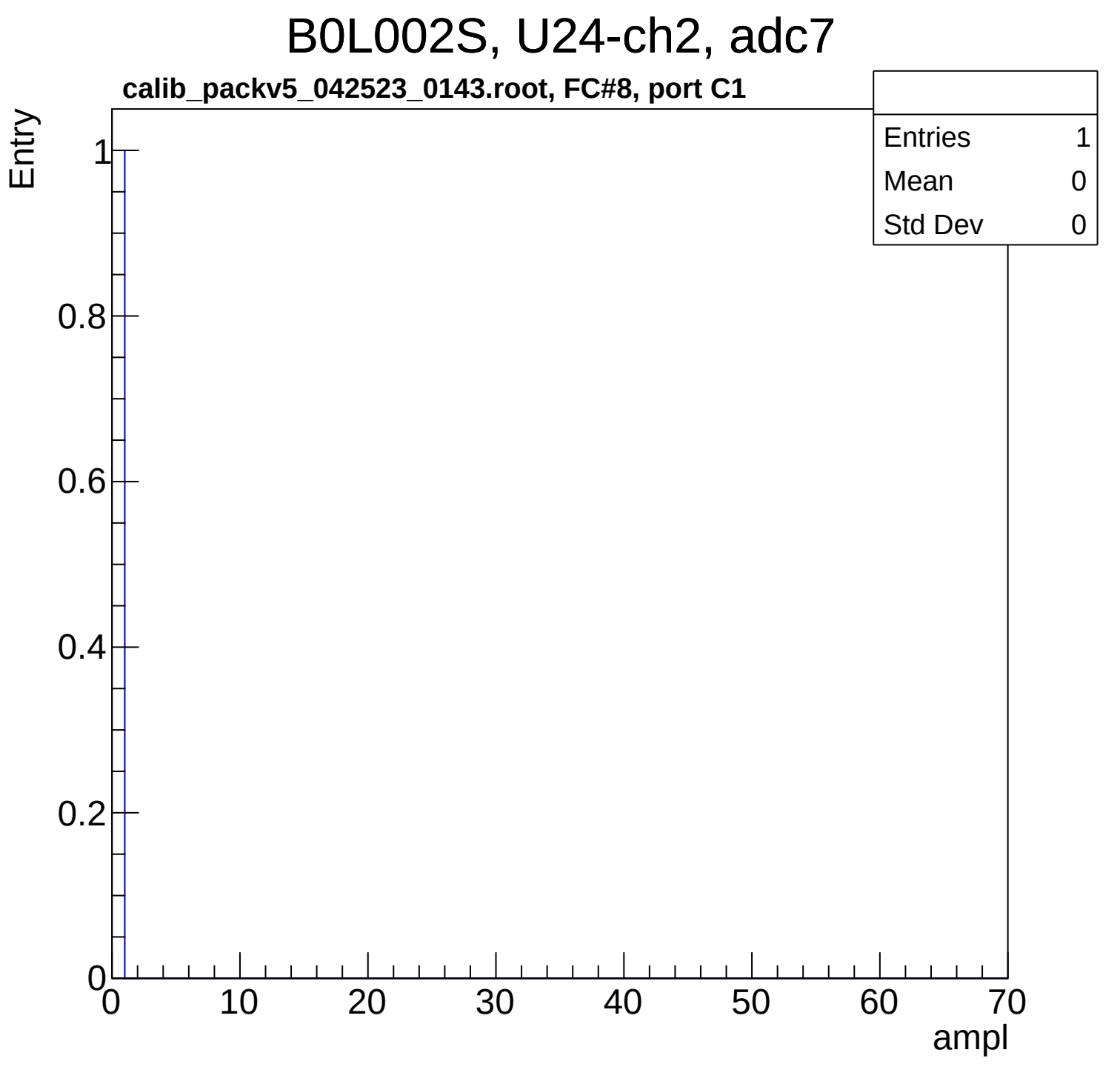
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L002S, U24-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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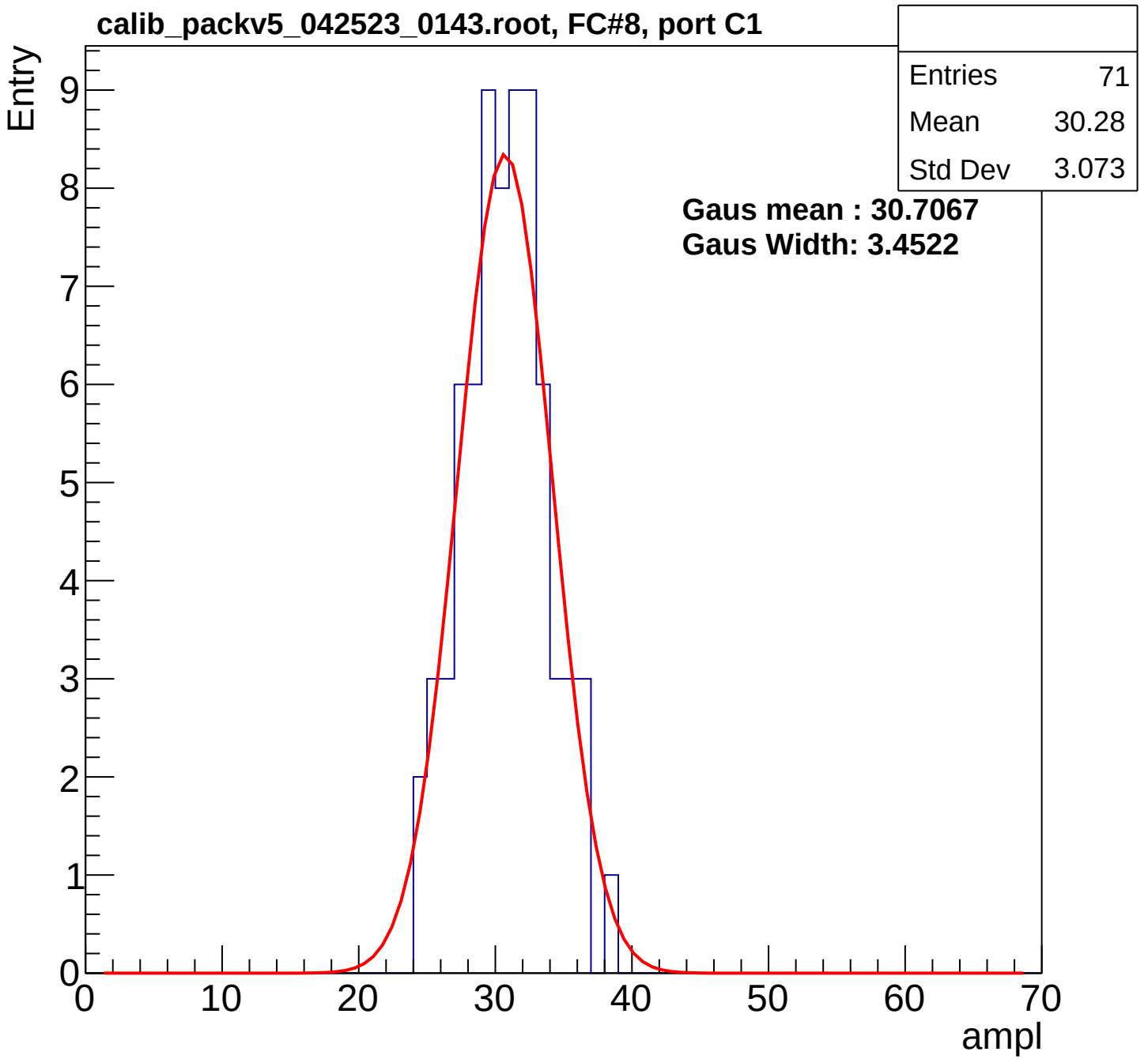
50
60
70
ampl


B0L002S, U24-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.28
Std Dev 3.073
Gaus mean : 30.7067
Gaus Width: 3.4522
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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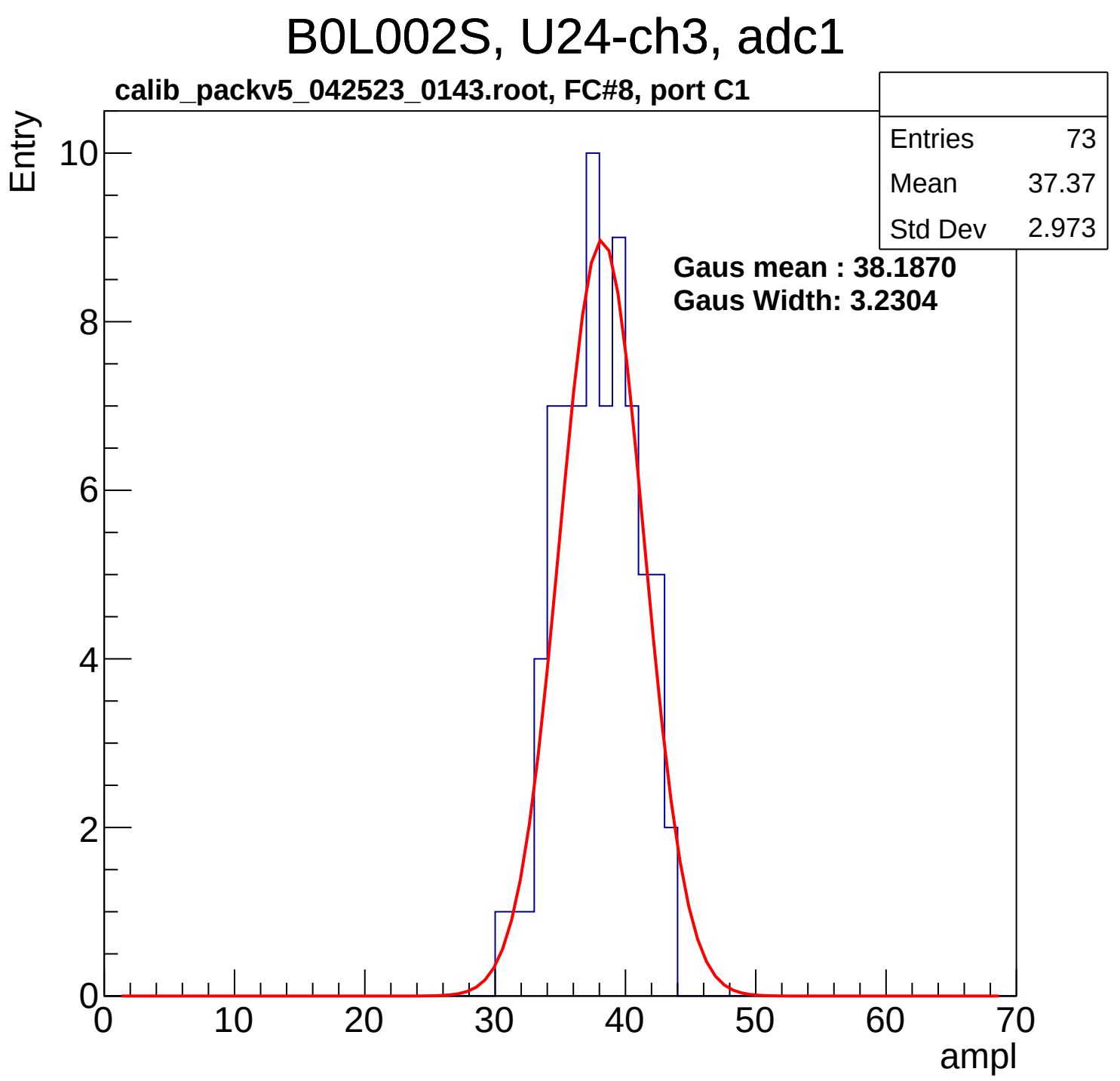
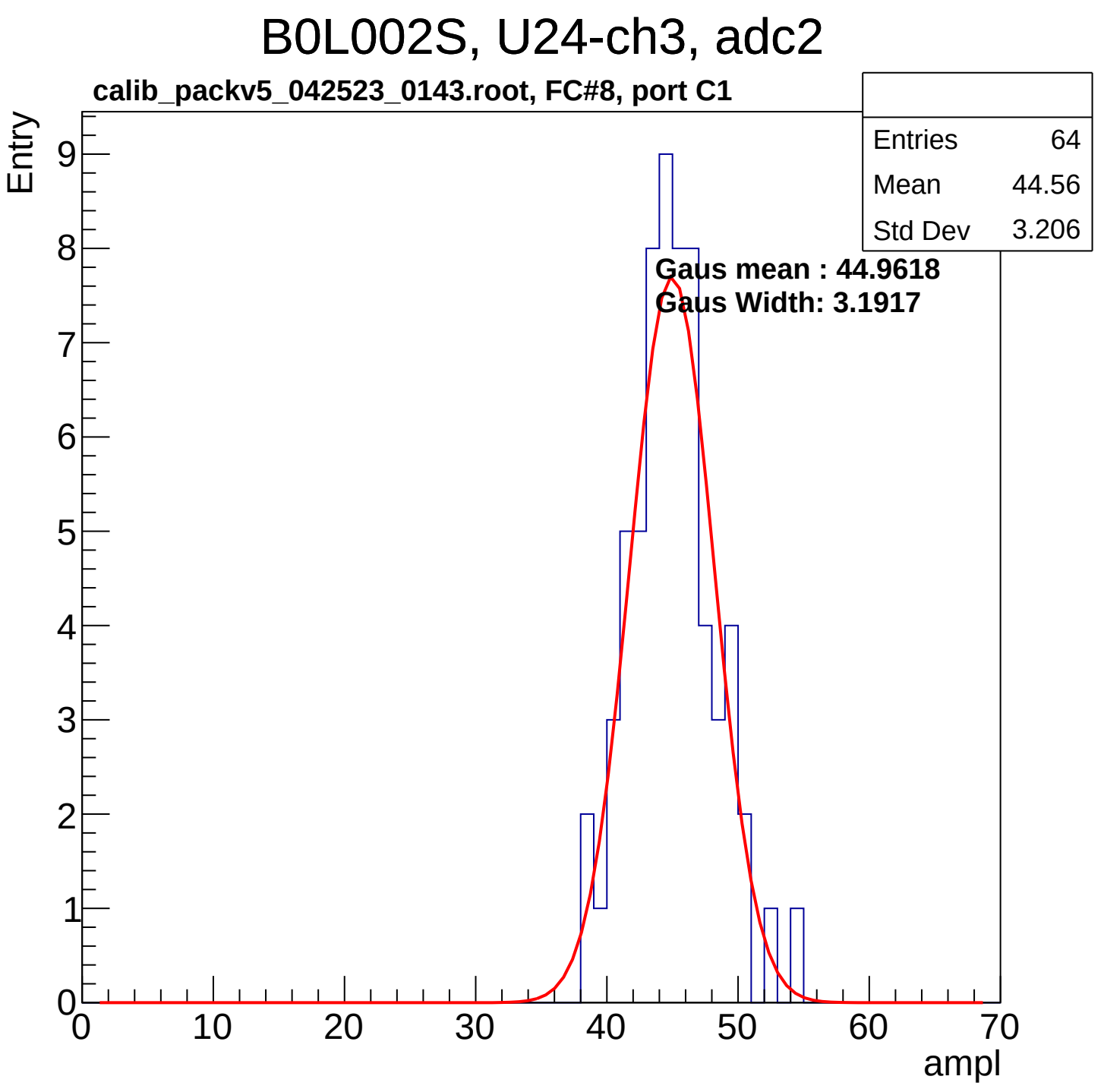

B0L002S, U24-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.37
Std Dev 2.973
Gaus mean : 38.1870
Gaus Width: 3.2304
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.56
Std Dev 3.206
Gaus mean : 44.9618
Gaus Width: 3.1917
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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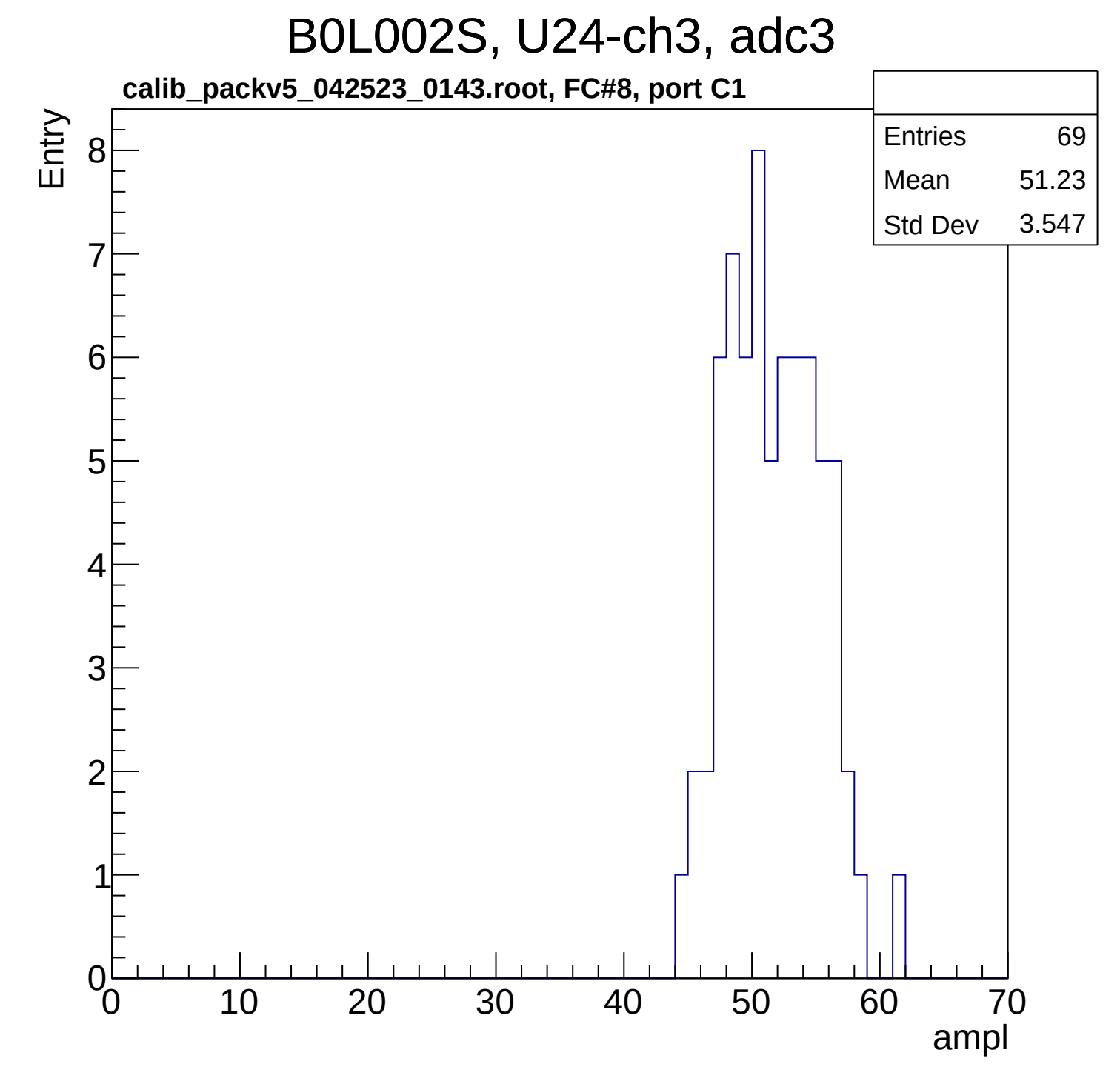
B0L002S, U24-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.23
Std Dev 3.547
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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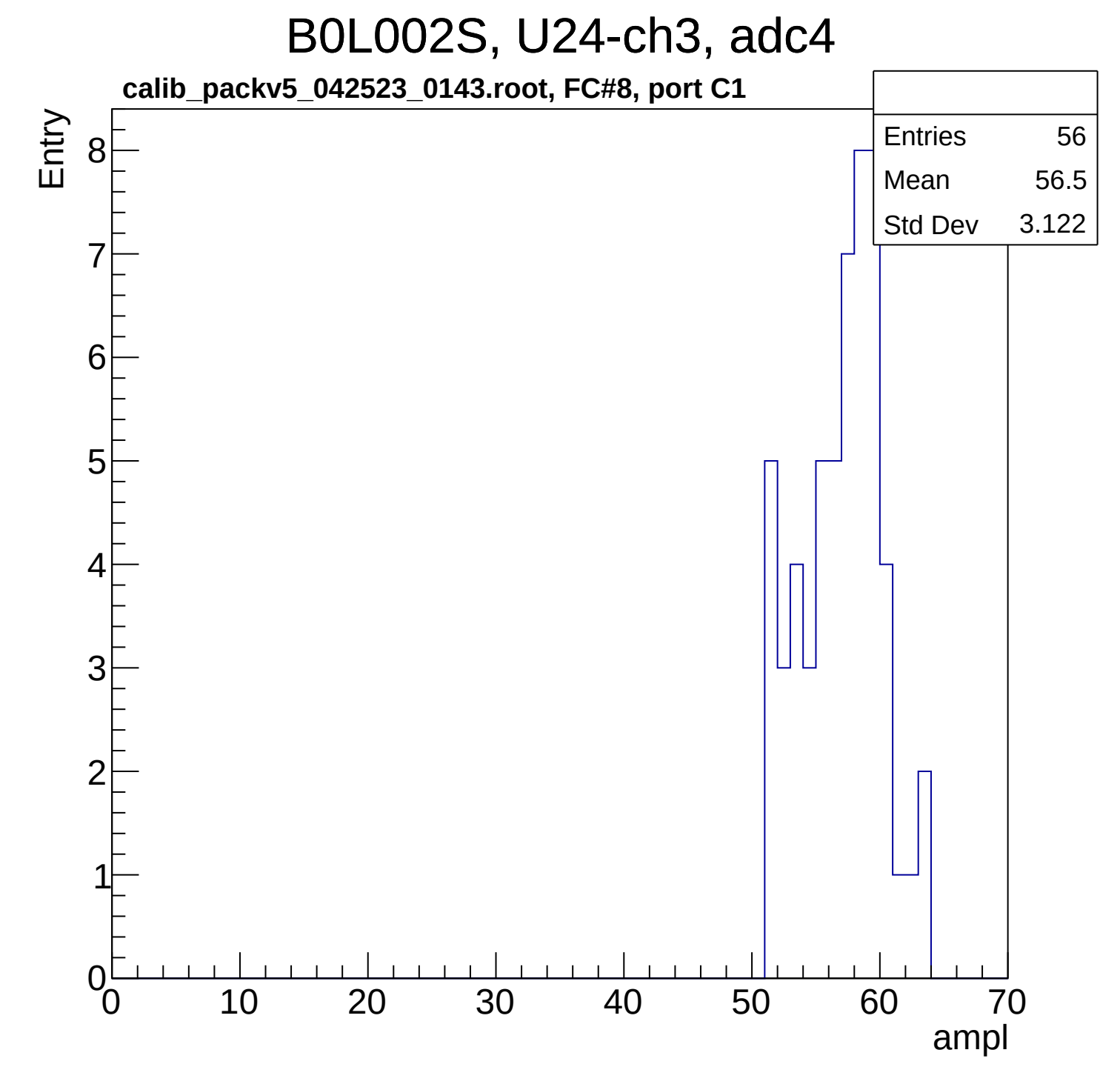
B0L002S, U24-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.5
Std Dev 3.122
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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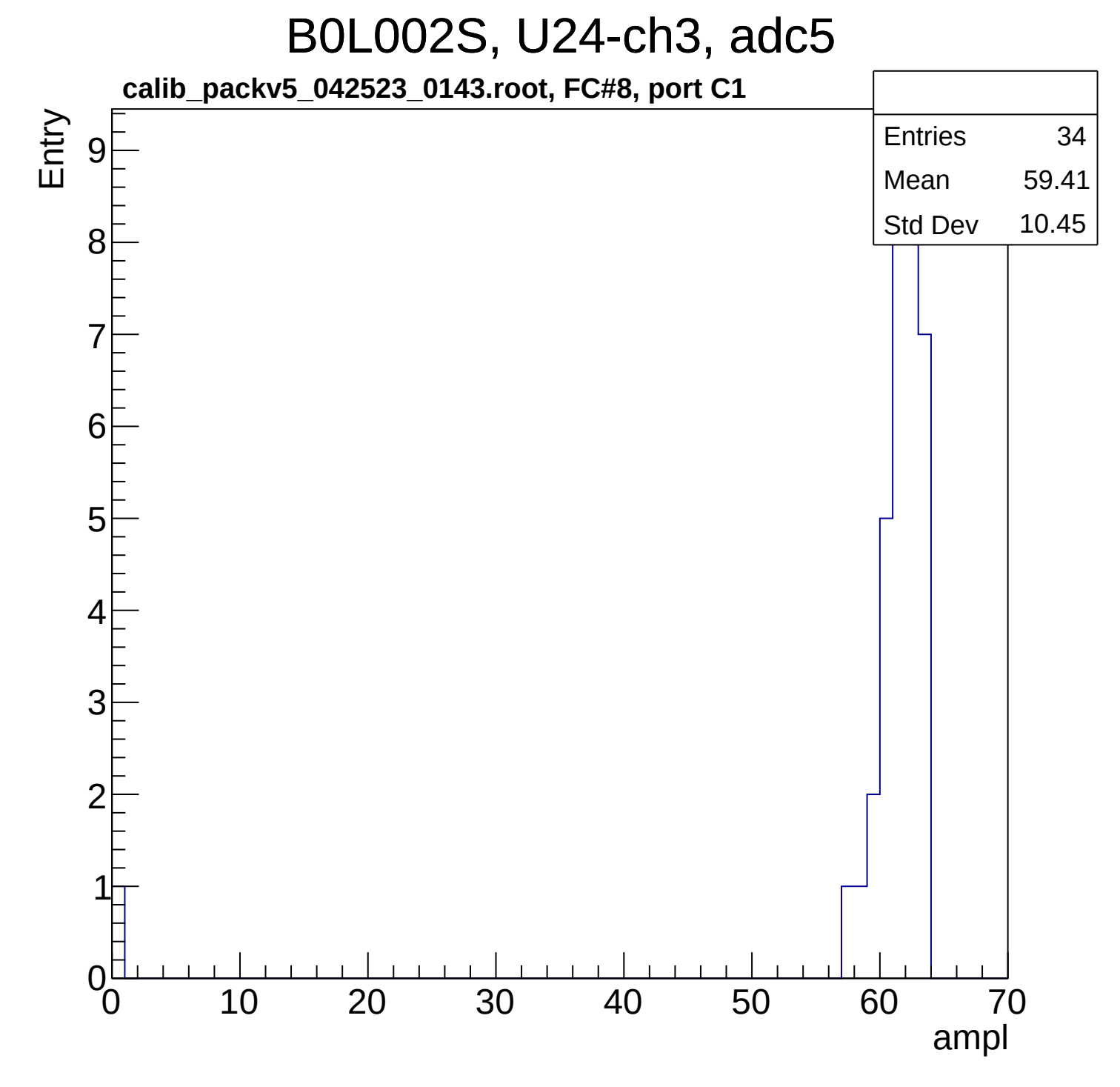
B0L002S, U24-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.41
Std Dev 10.45
Entry
ampl
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3
4
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0
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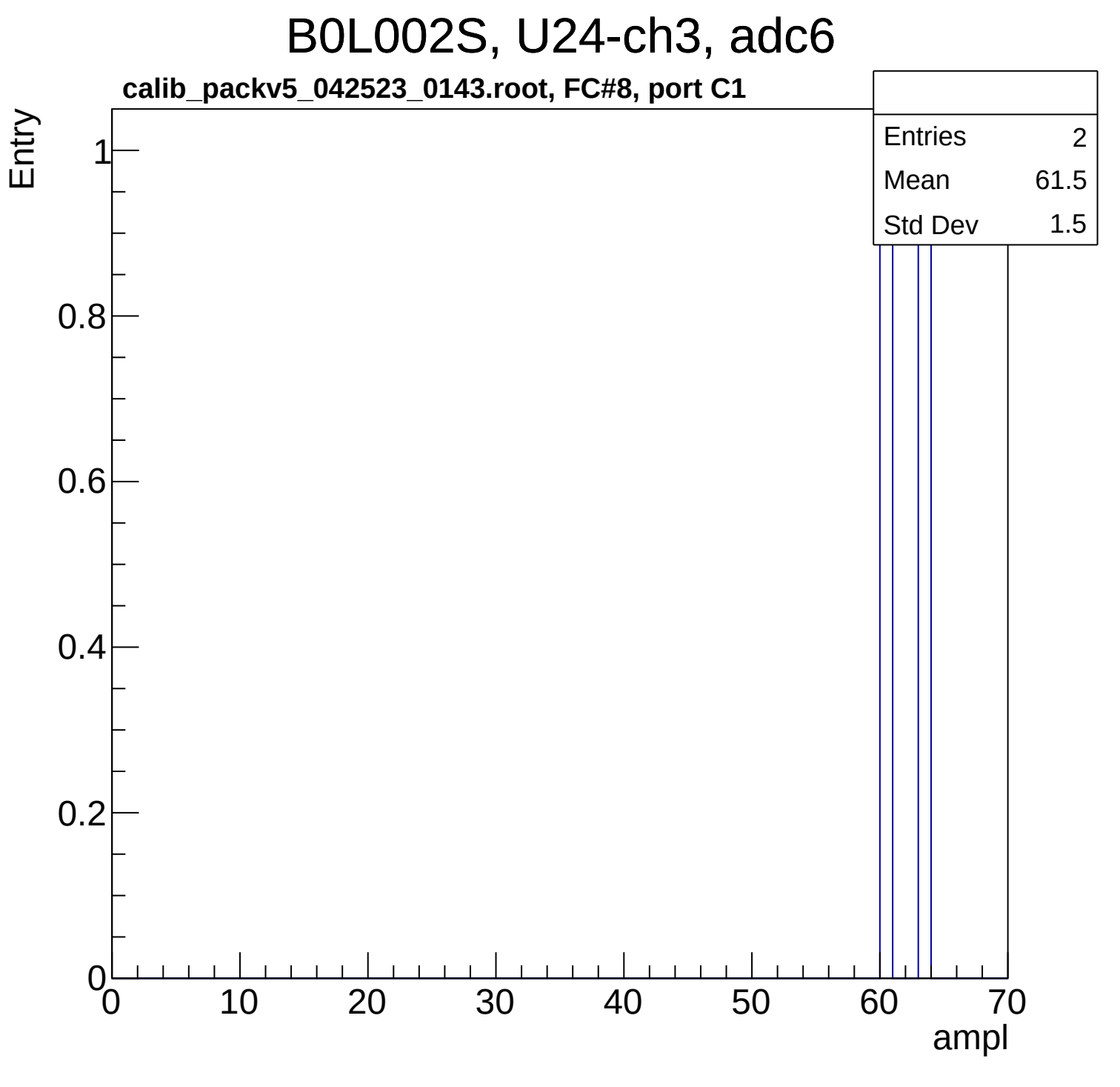
B0L002S, U24-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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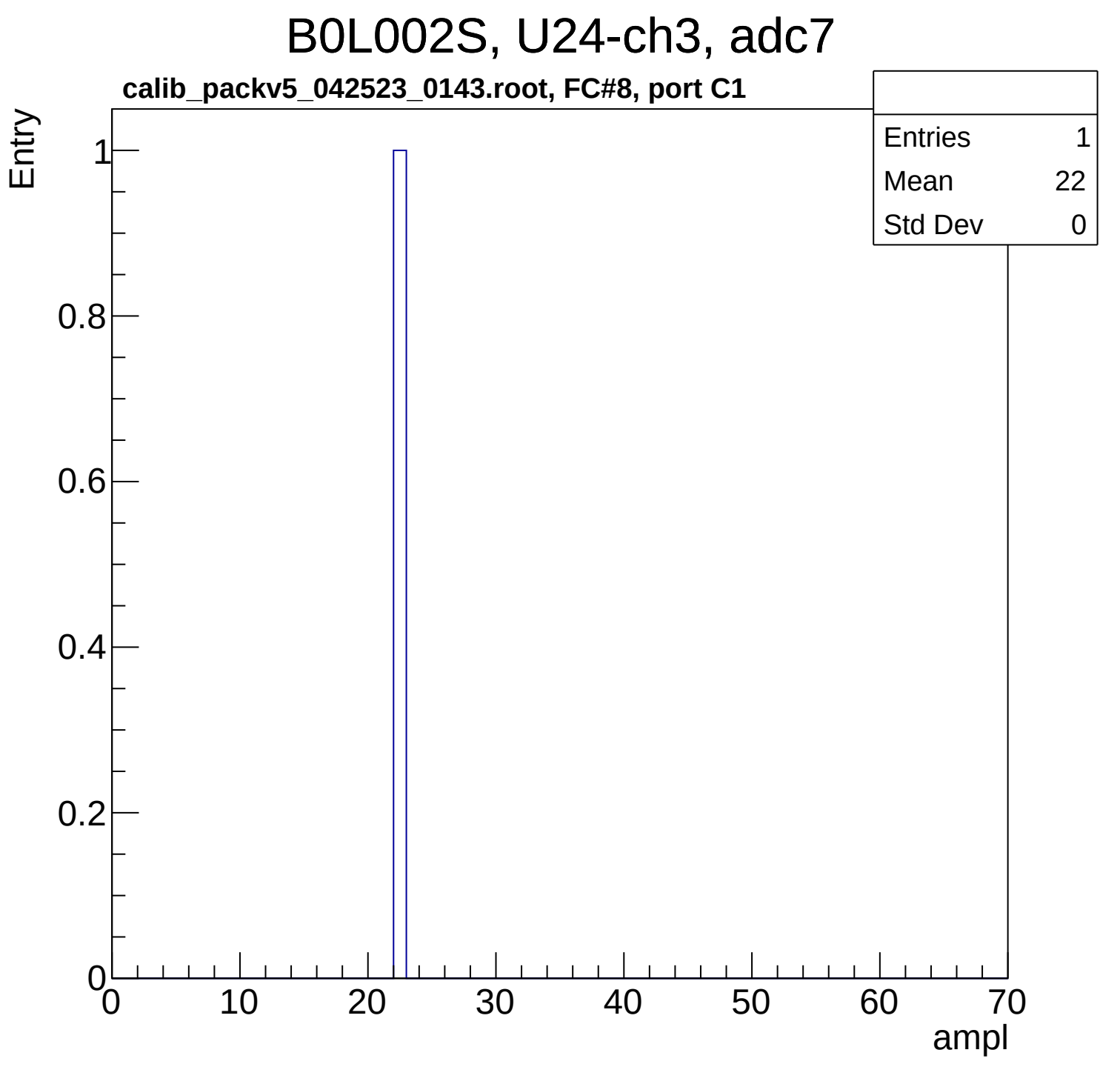
50
60
70
ampl


B0L002S, U24-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
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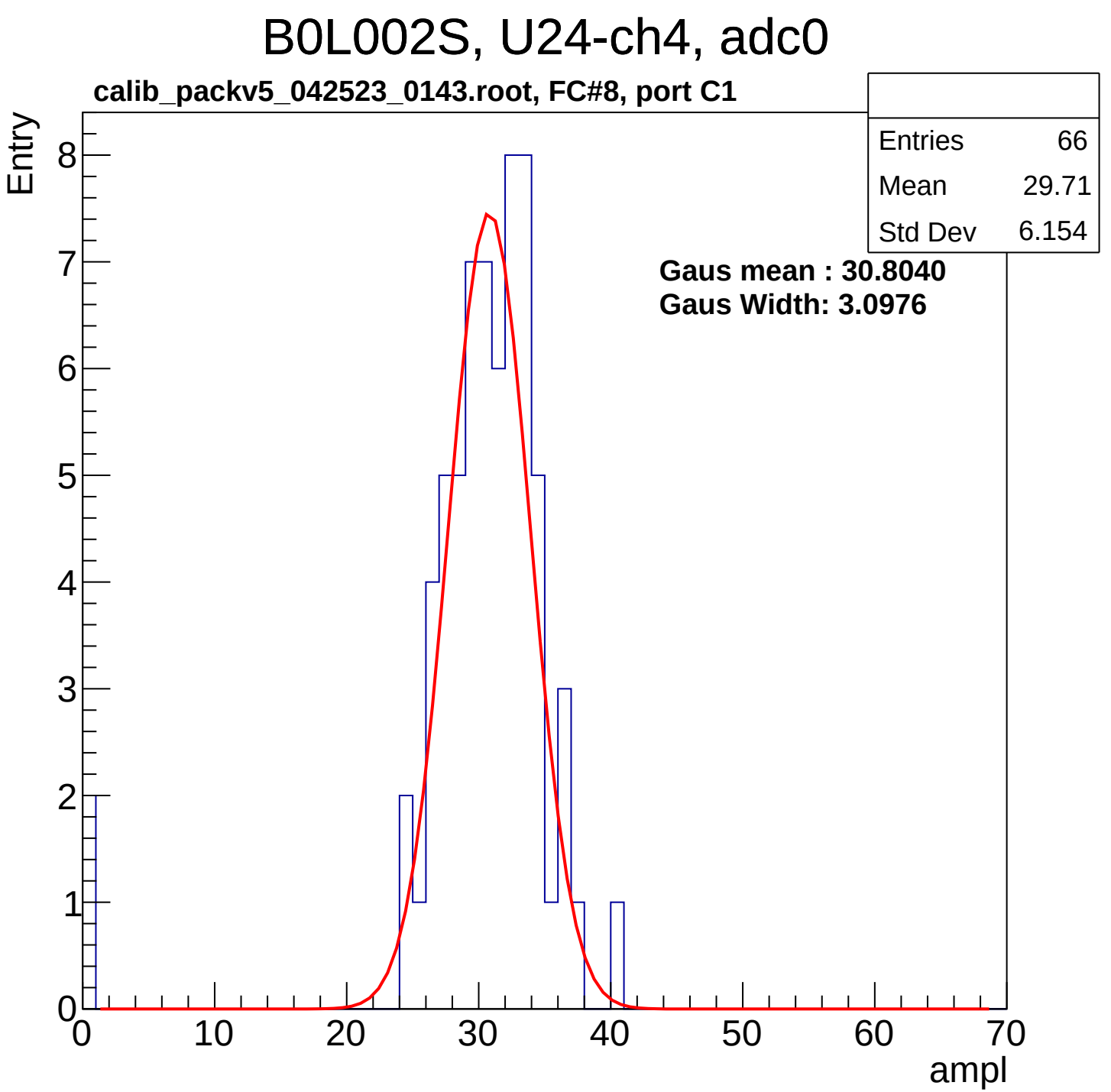
50
60
70
ampl


B0L002S, U24-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.71
Std Dev 6.154
Gaus mean : 30.8040
Gaus Width: 3.0976
Entry
ampl
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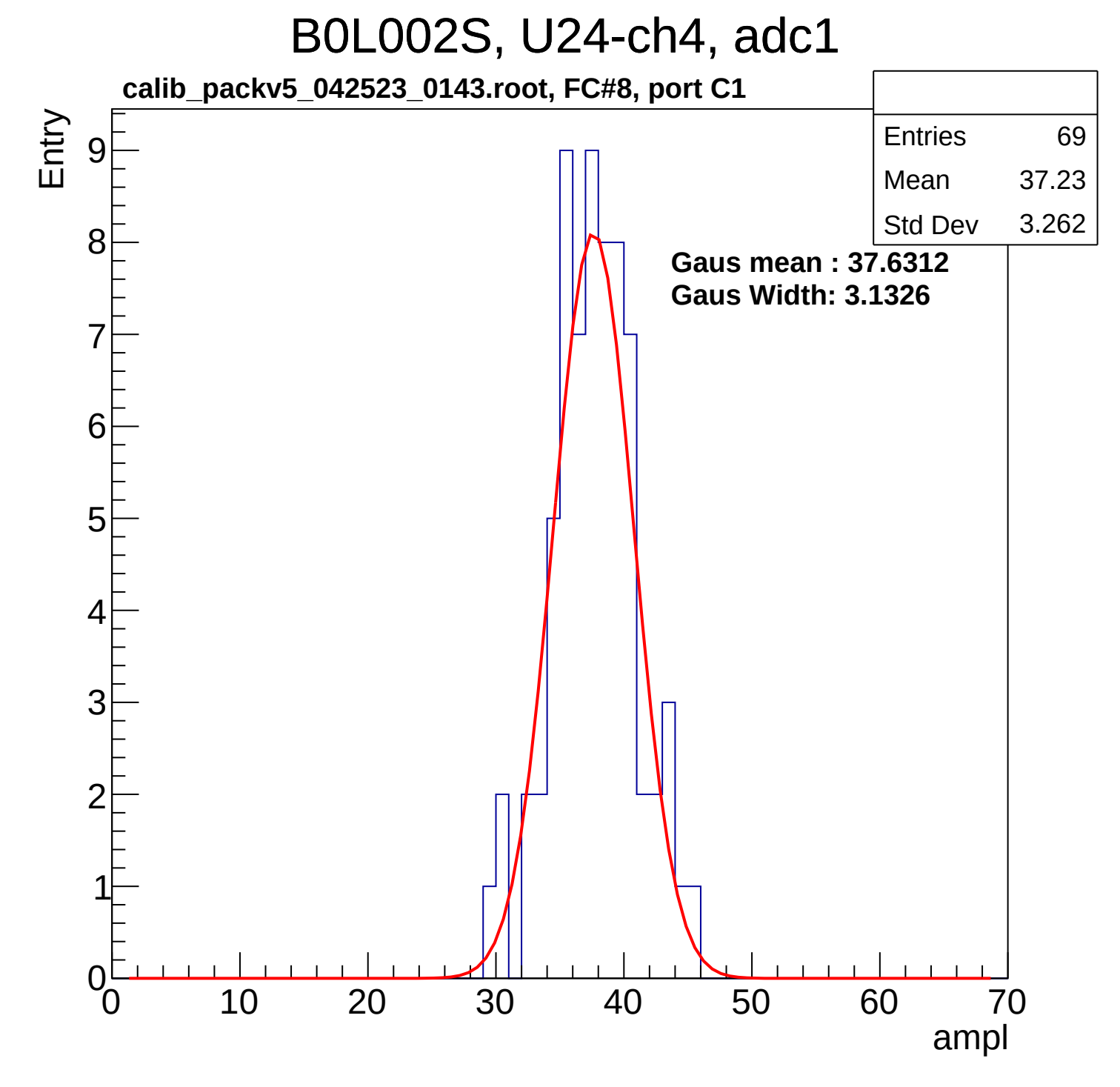
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.23
Std Dev 3.262
Gaus mean : 37.6312
Gaus Width: 3.1326
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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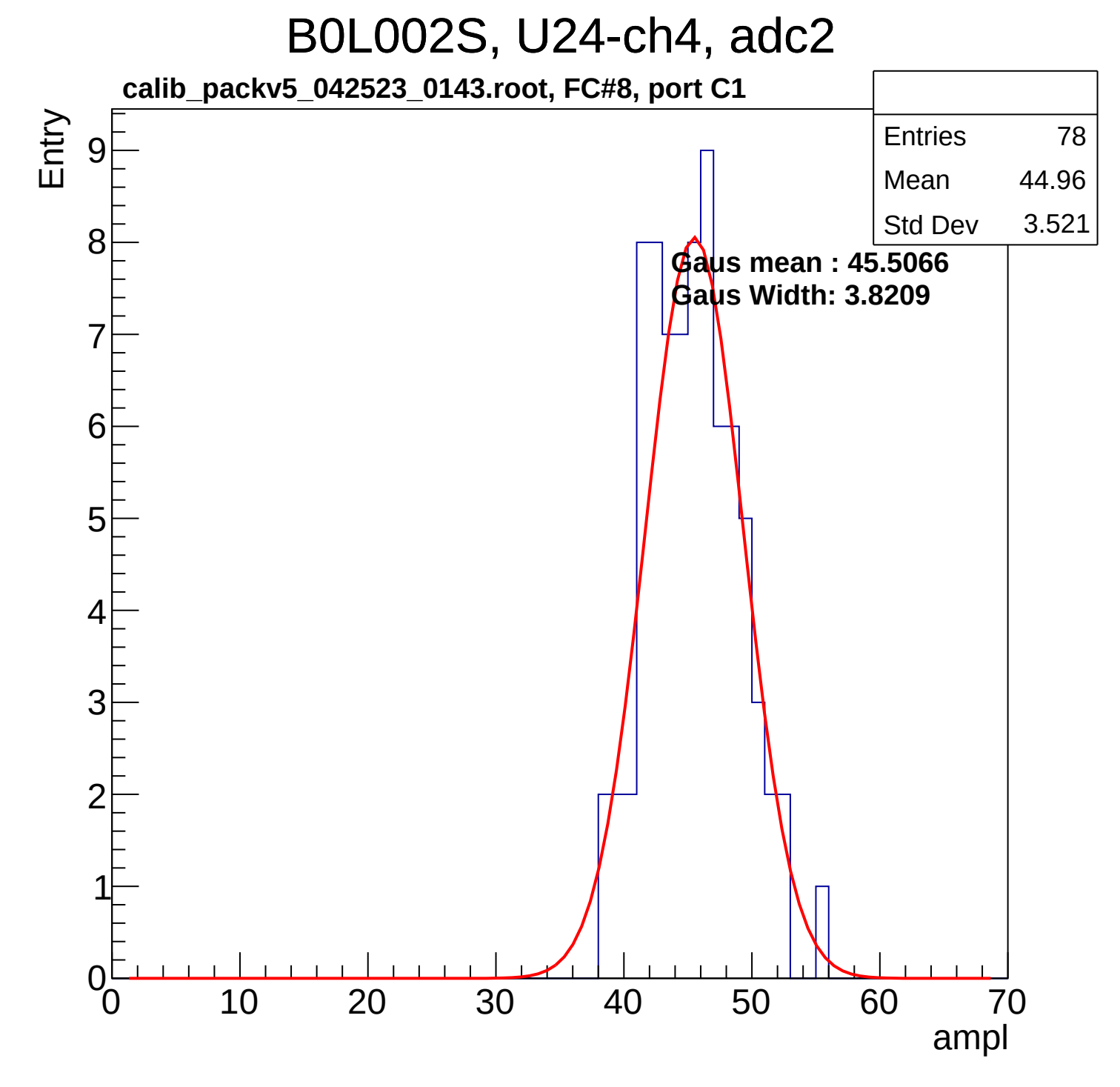
B0L002S, U24-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 44.96
Std Dev 3.521
Gaus mean : 45.5066
Gaus Width: 3.8209
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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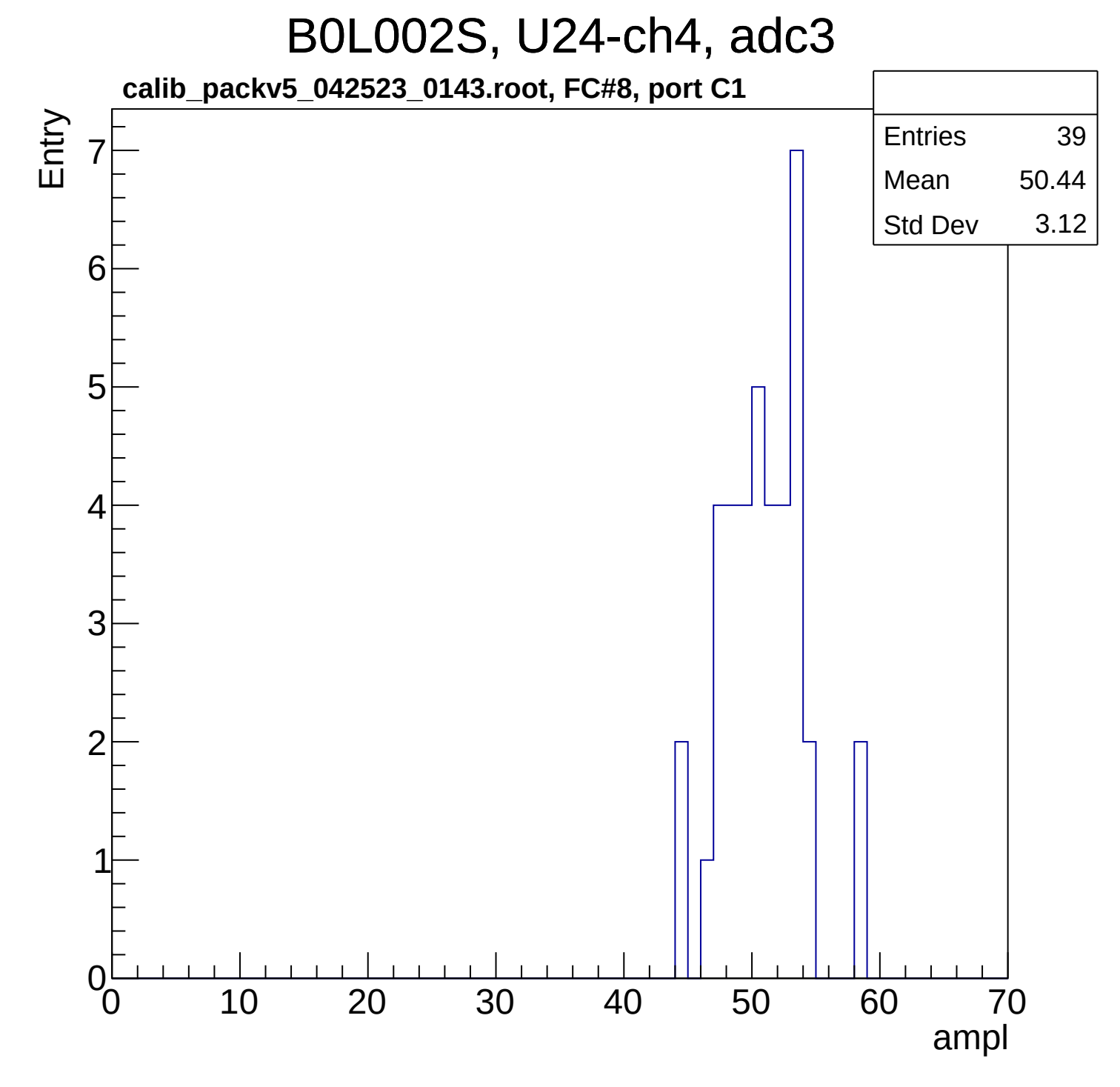
B0L002S, U24-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 50.44
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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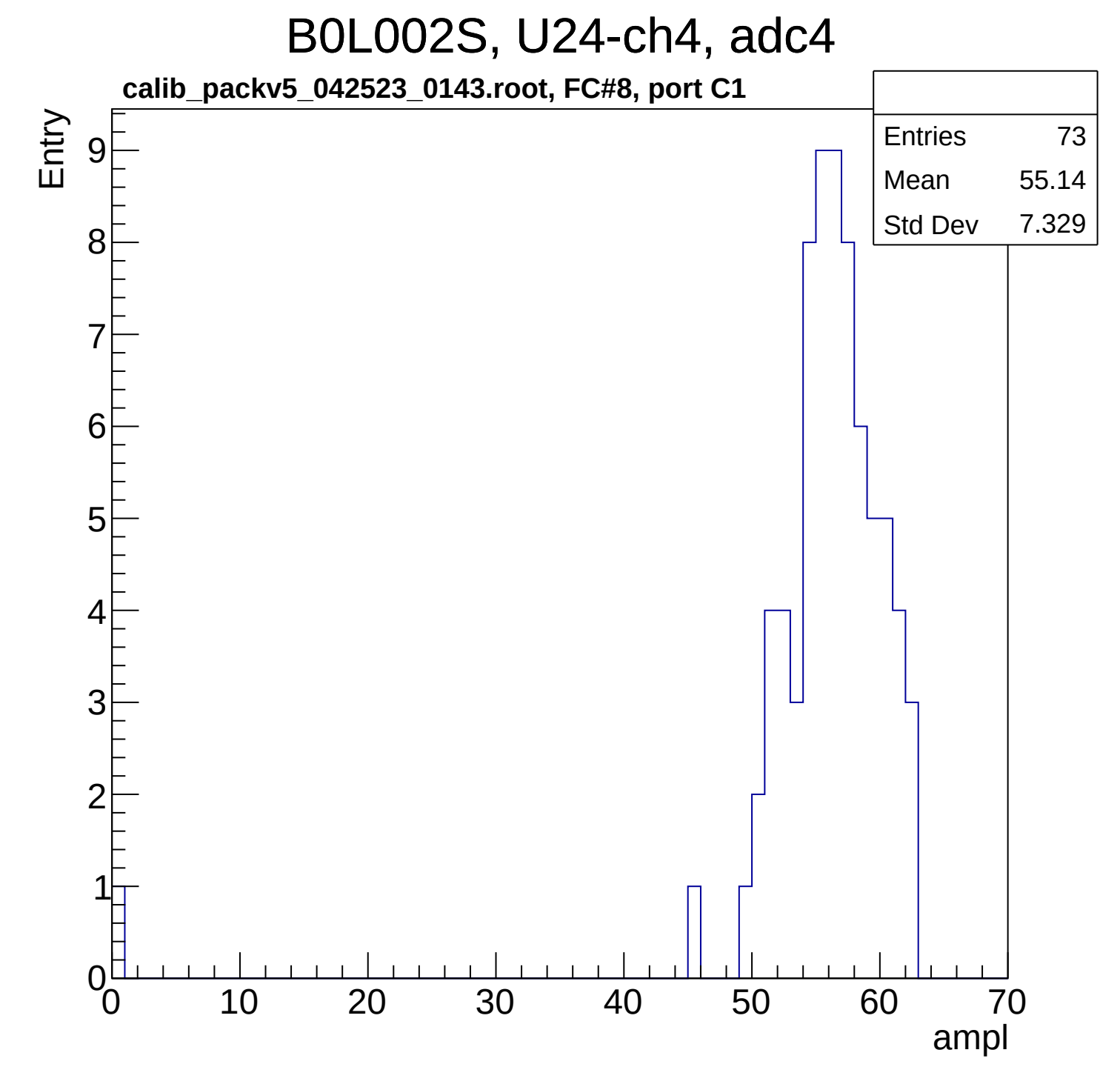
B0L002S, U24-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 55.14
Std Dev 7.329
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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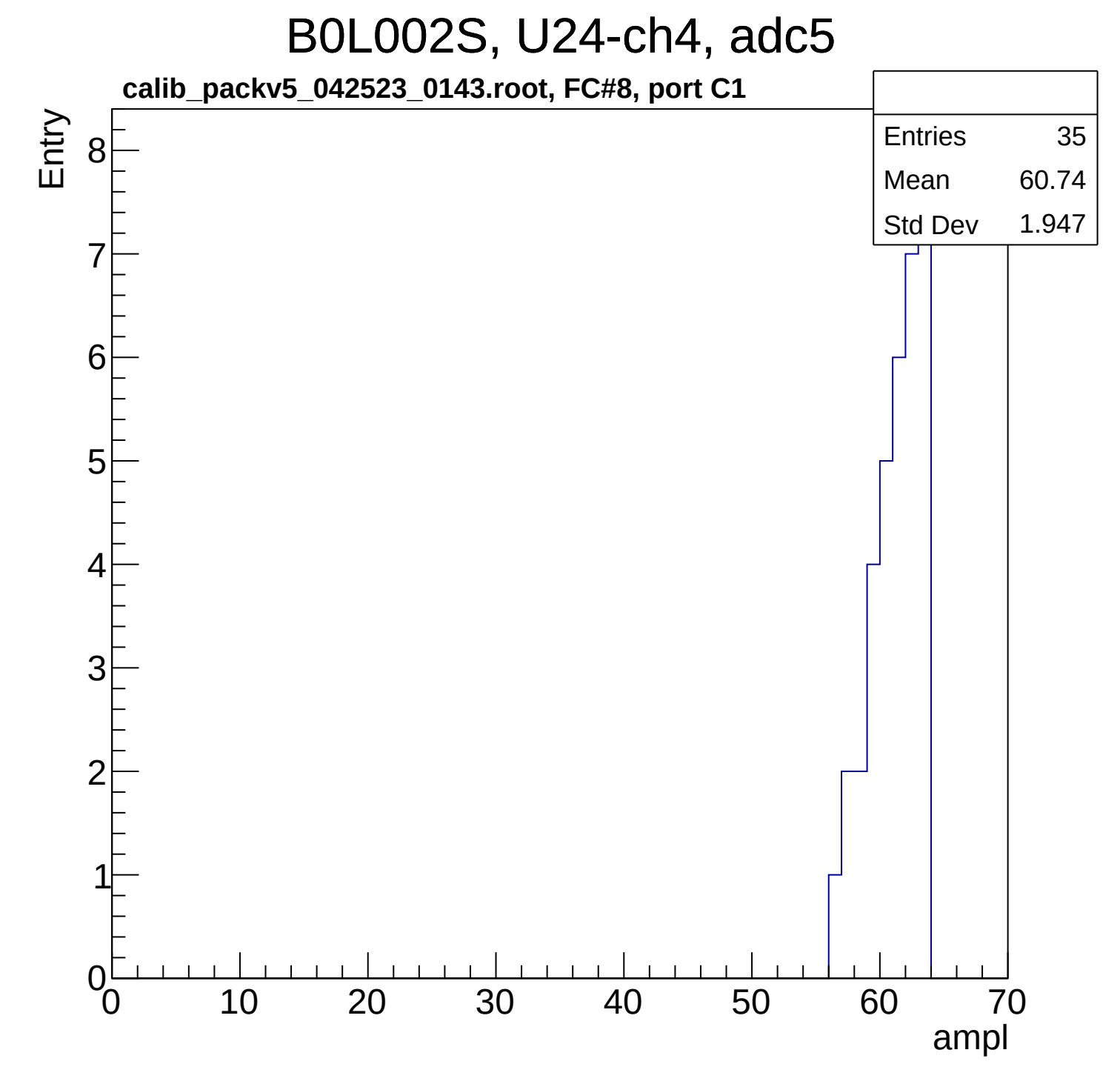
B0L002S, U24-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.74
Std Dev 1.947
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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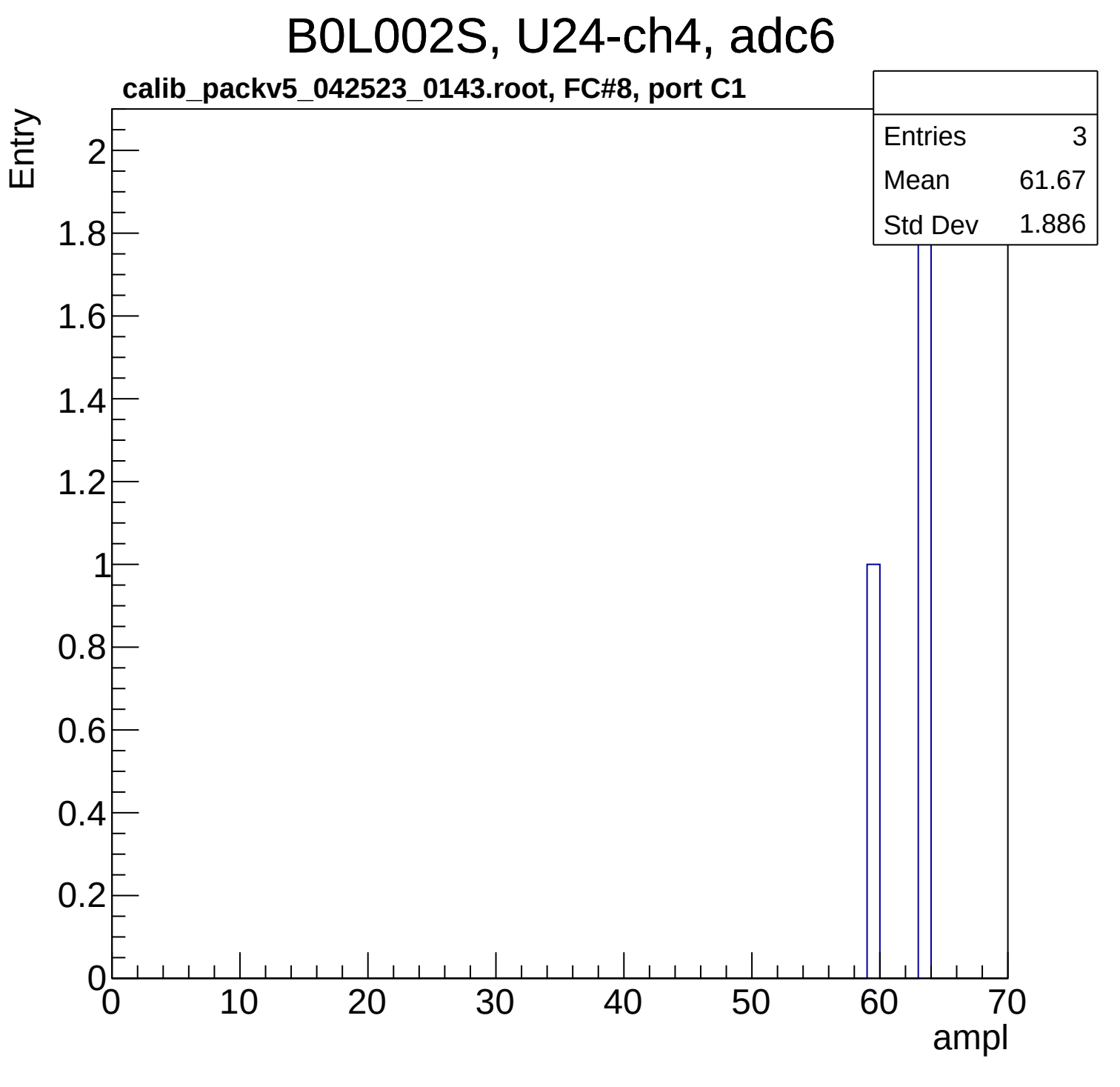
B0L002S, U24-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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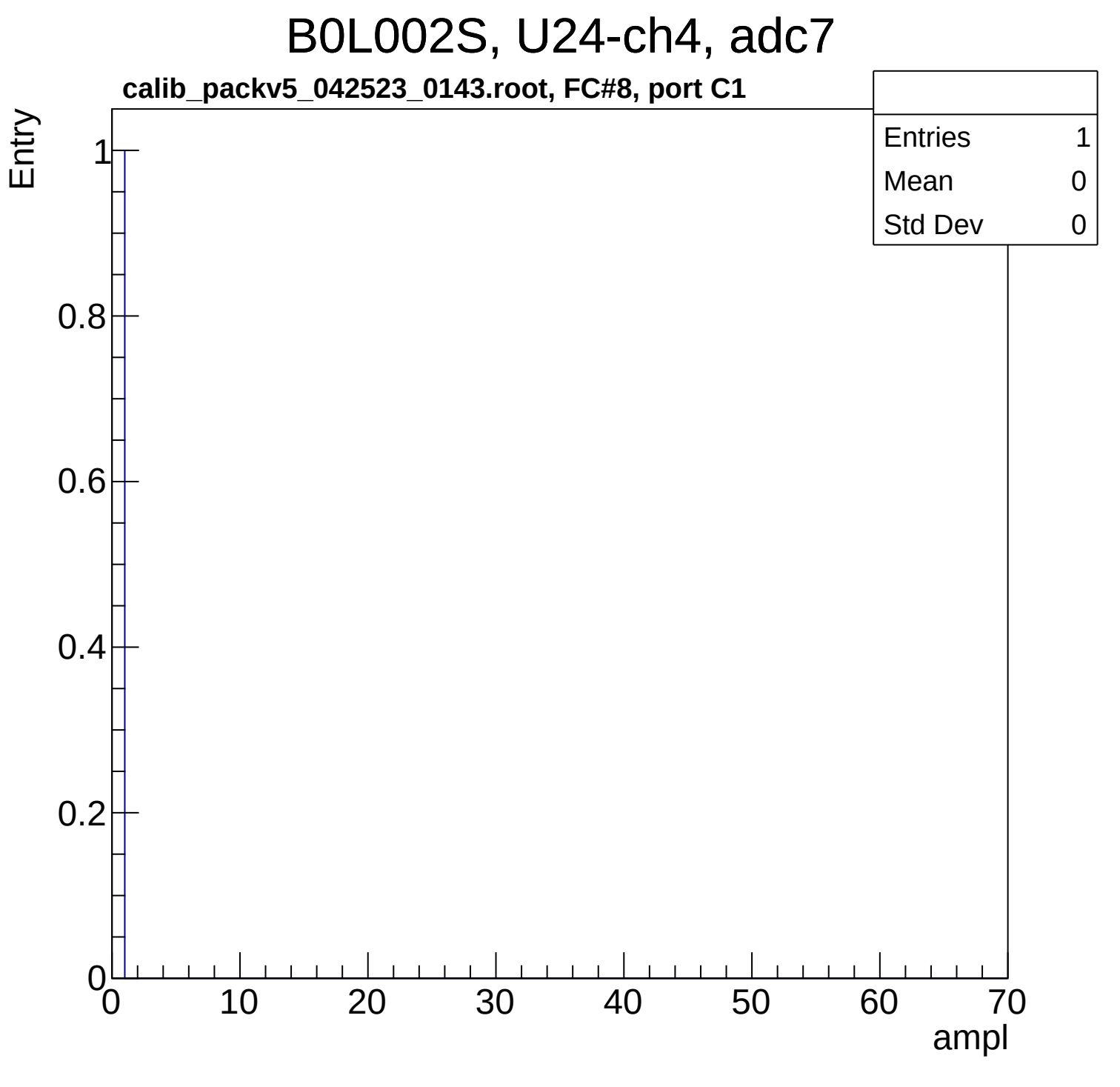
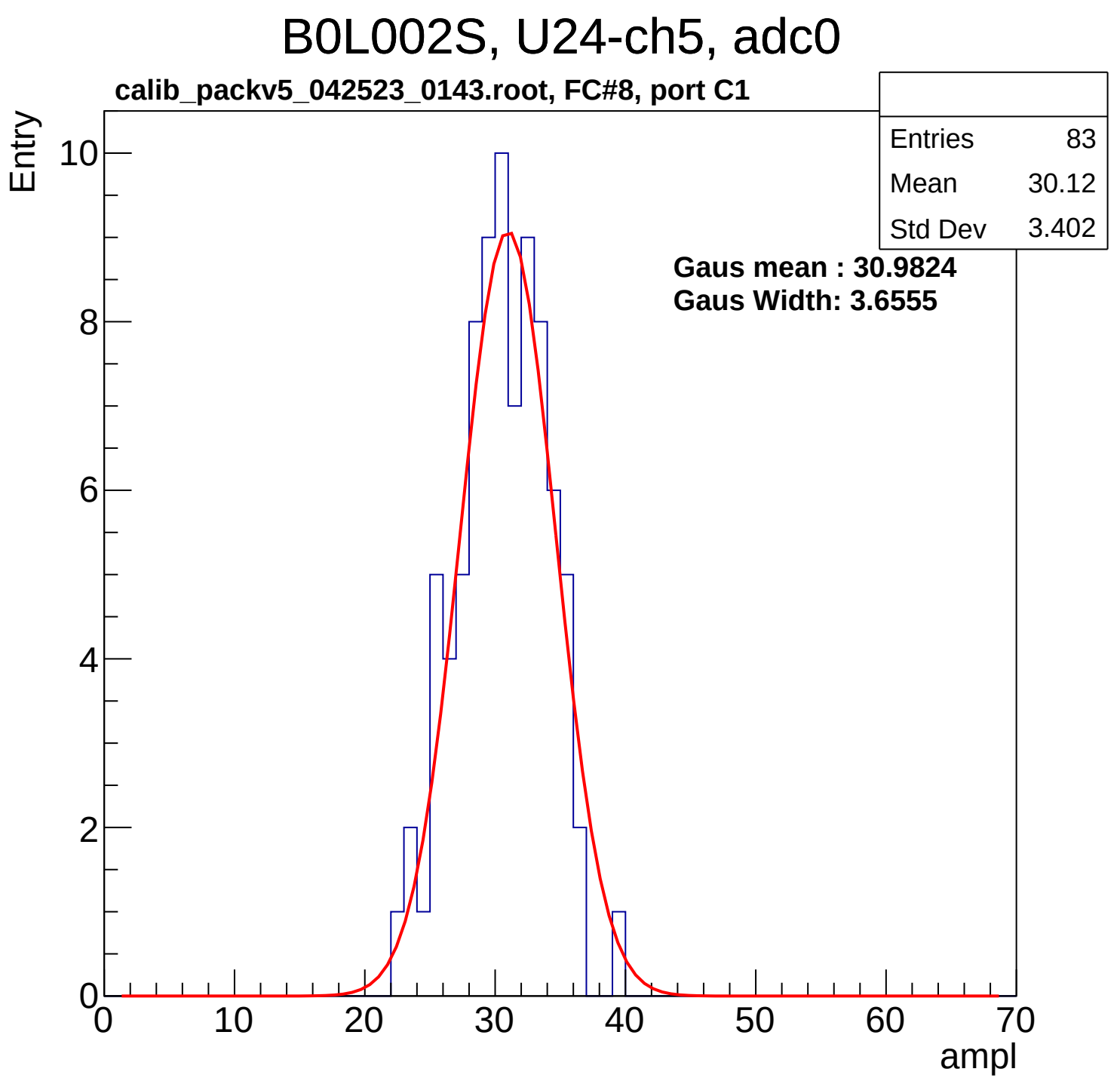
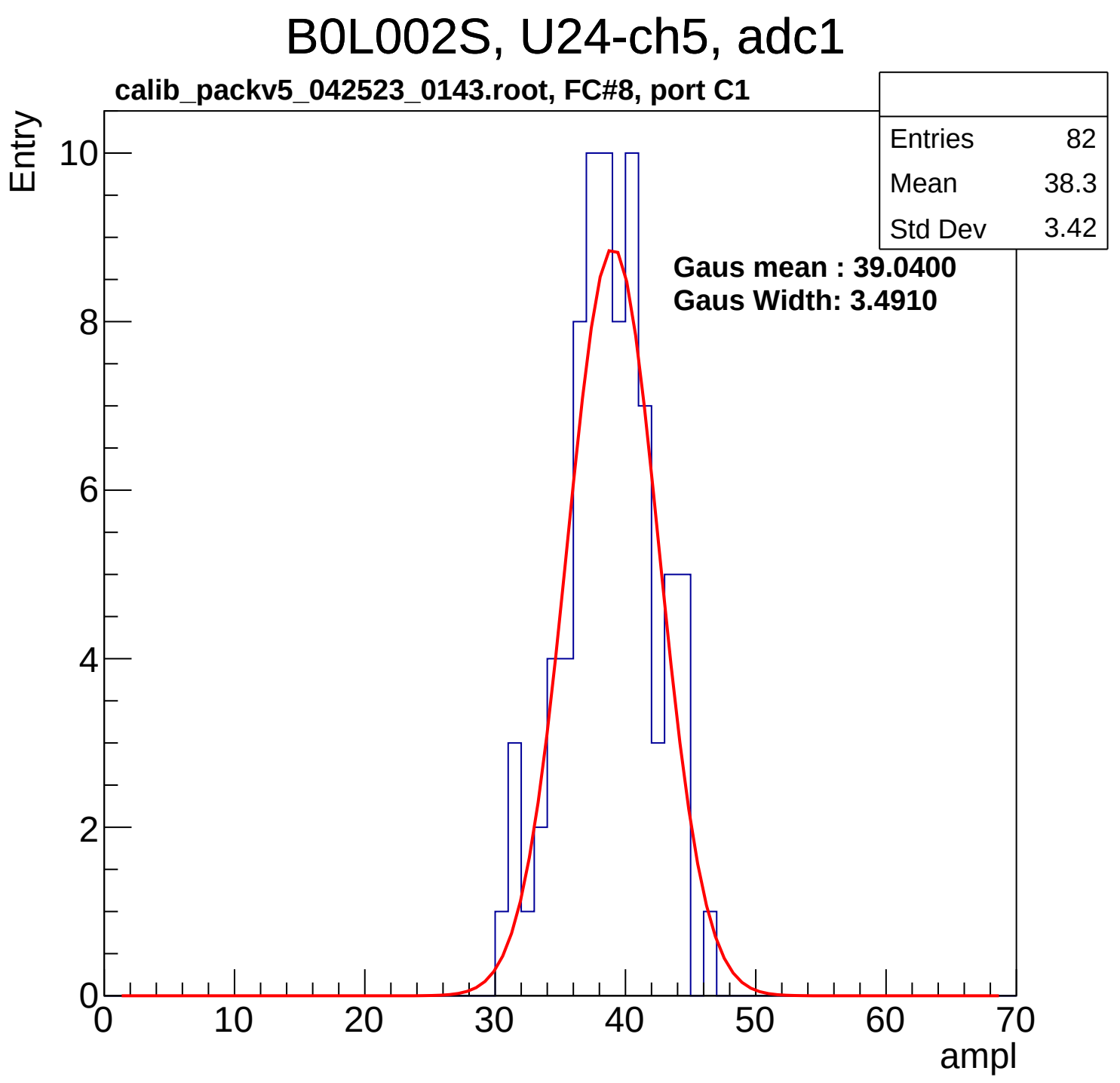

B0L002S, U24-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 30.12
Std Dev 3.402
Gaus mean : 30.9824
Gaus Width: 3.6555
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 38.3
Std Dev 3.42
Gaus mean : 39.0400
Gaus Width: 3.4910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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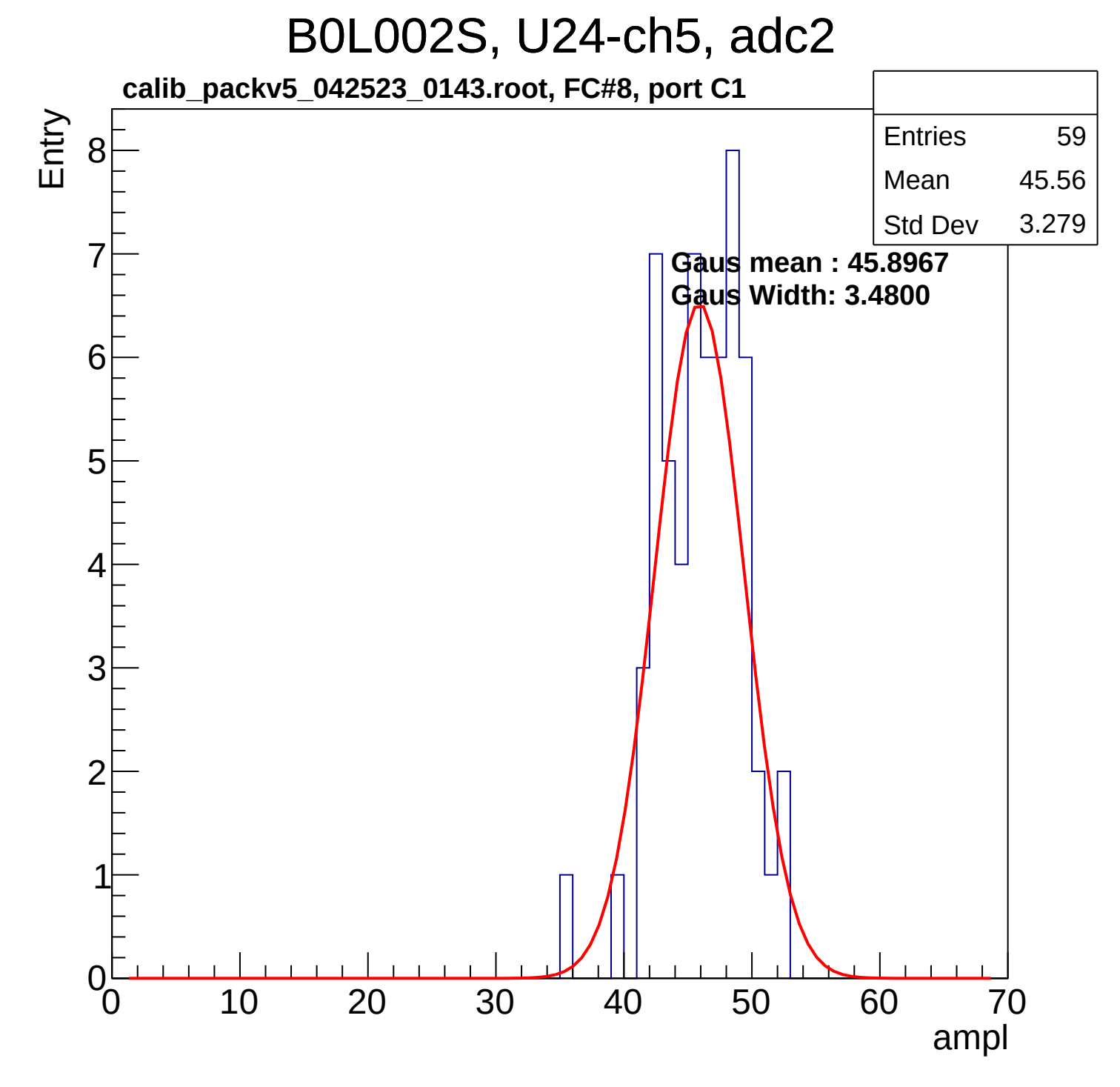
B0L002S, U24-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 45.56
Std Dev 3.279
Gaus mean : 45.8967
Gaus Width: 3.4800
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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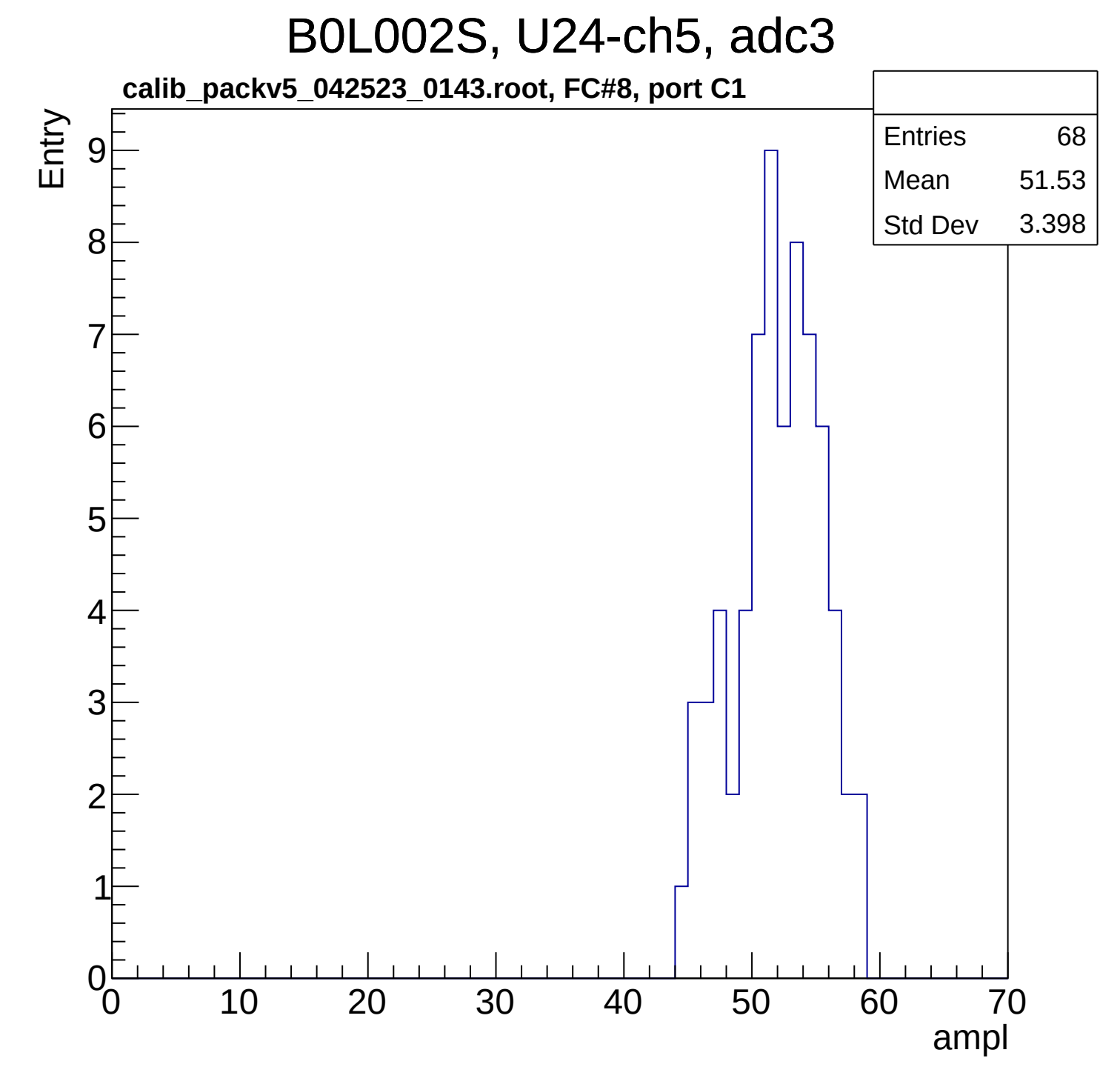
B0L002S, U24-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.53
Std Dev 3.398
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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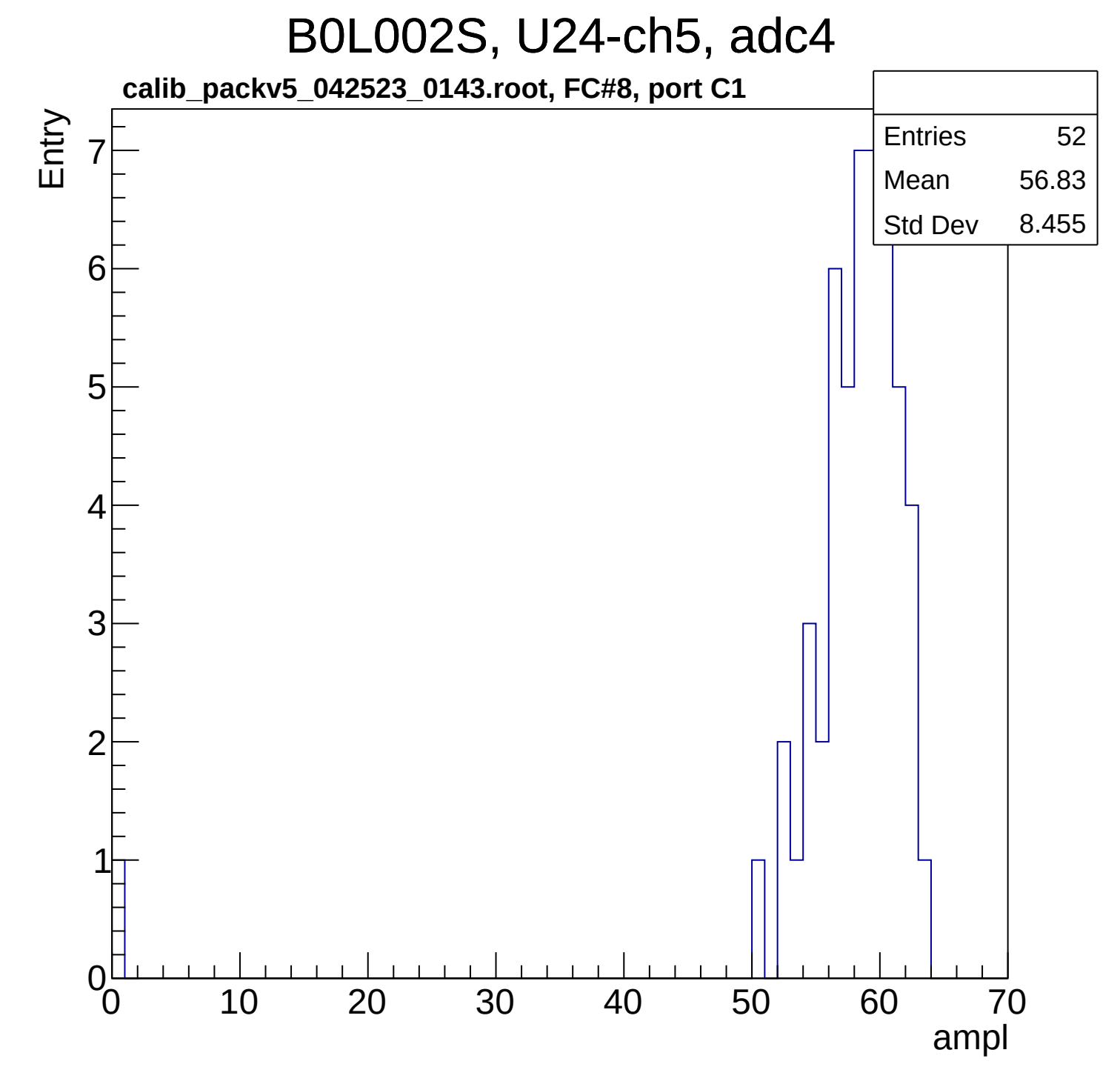
B0L002S, U24-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.83
Std Dev 8.455
Entry
ampl
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1
2
3
4
5
6
7
0
10
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40
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60
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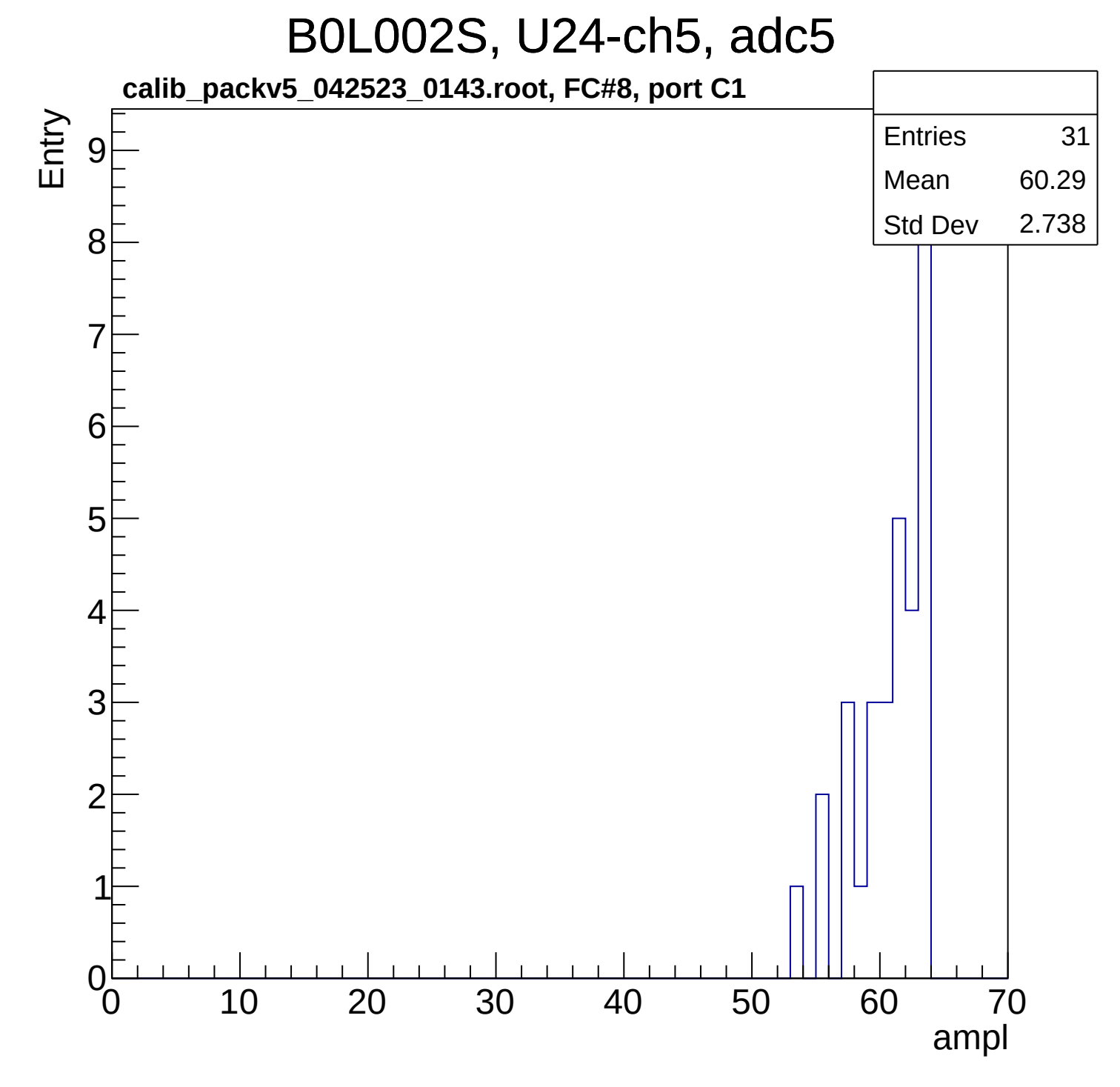
B0L002S, U24-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 60.29
Std Dev 2.738
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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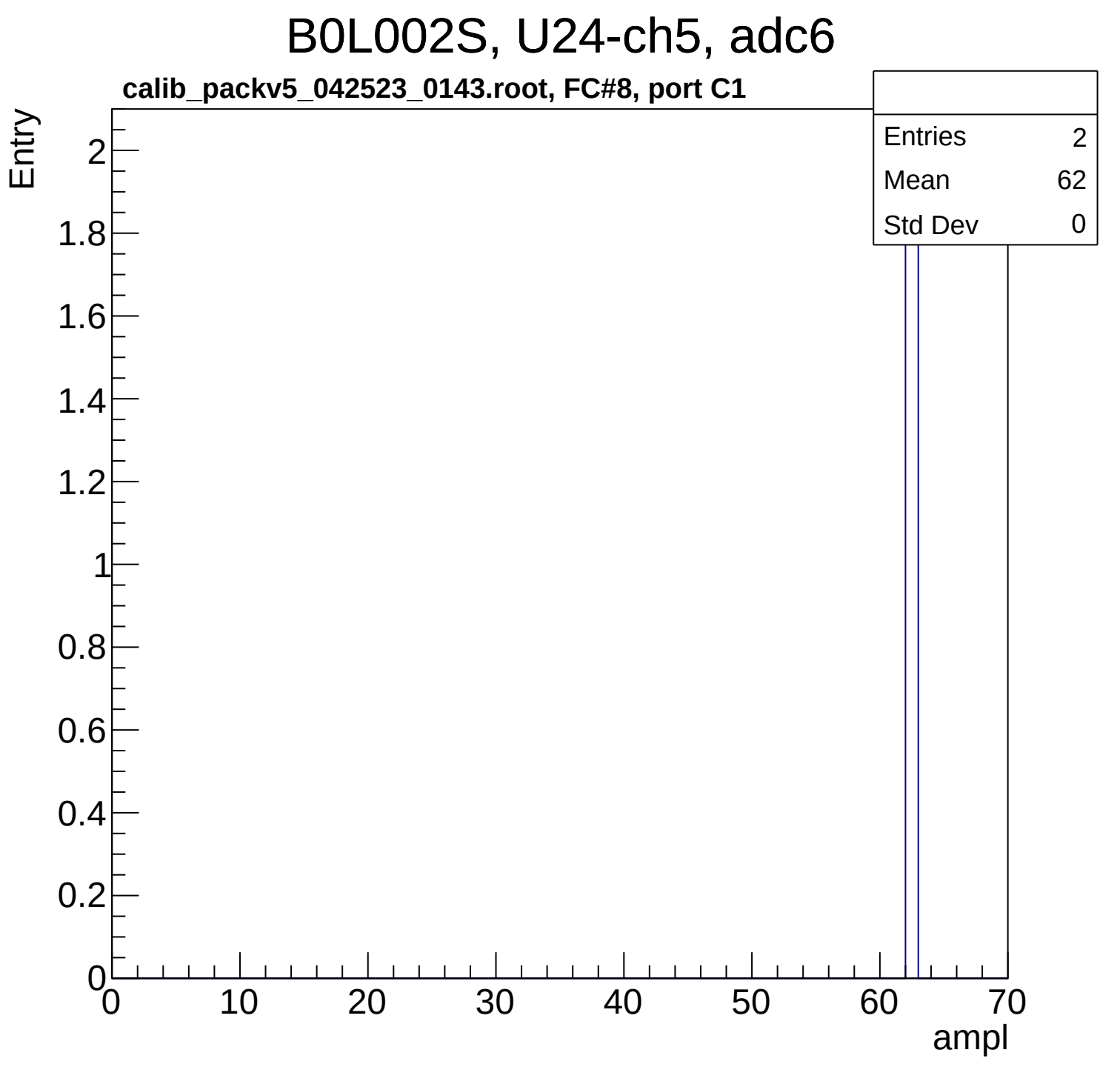
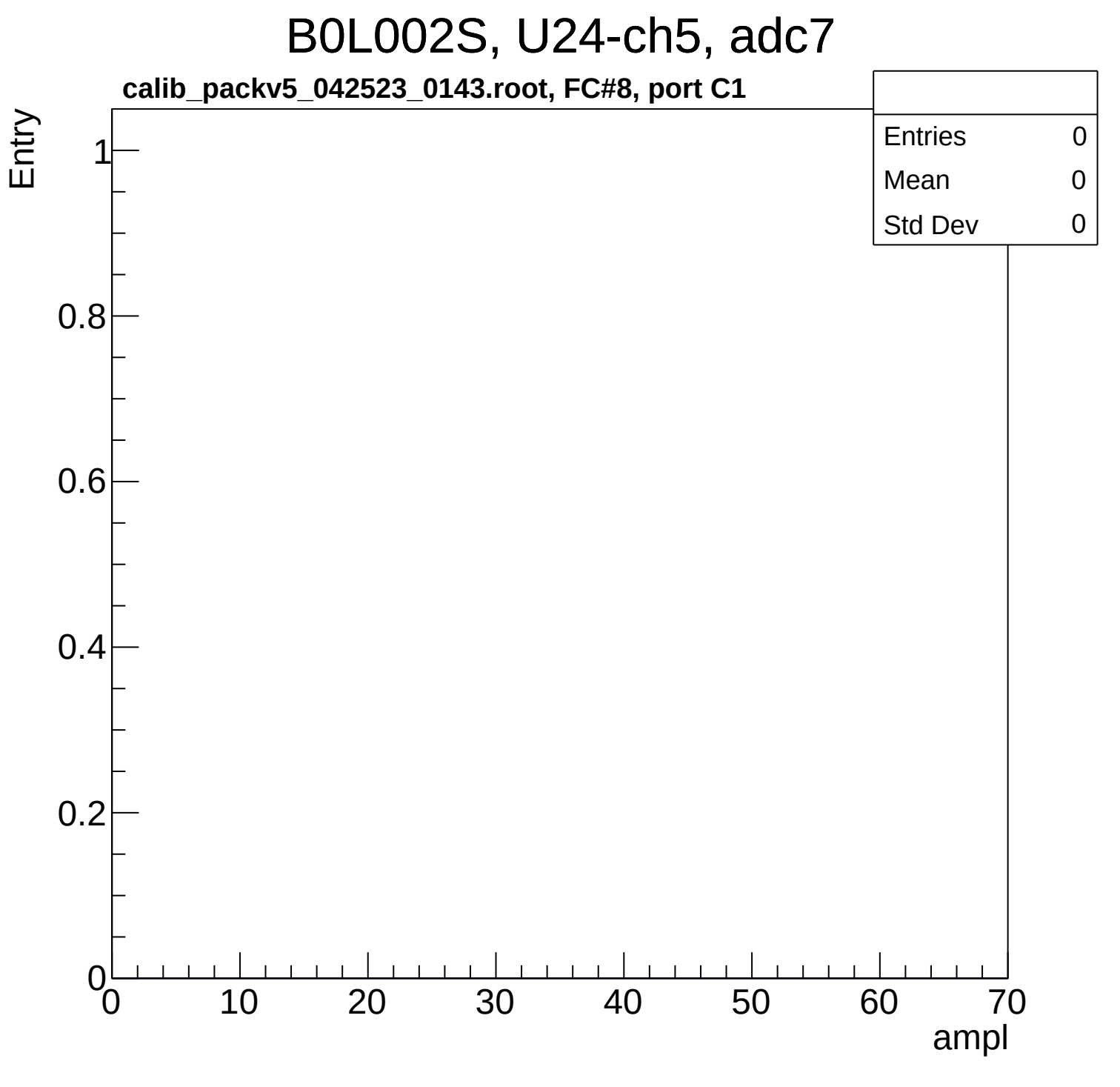

B0L002S, U24-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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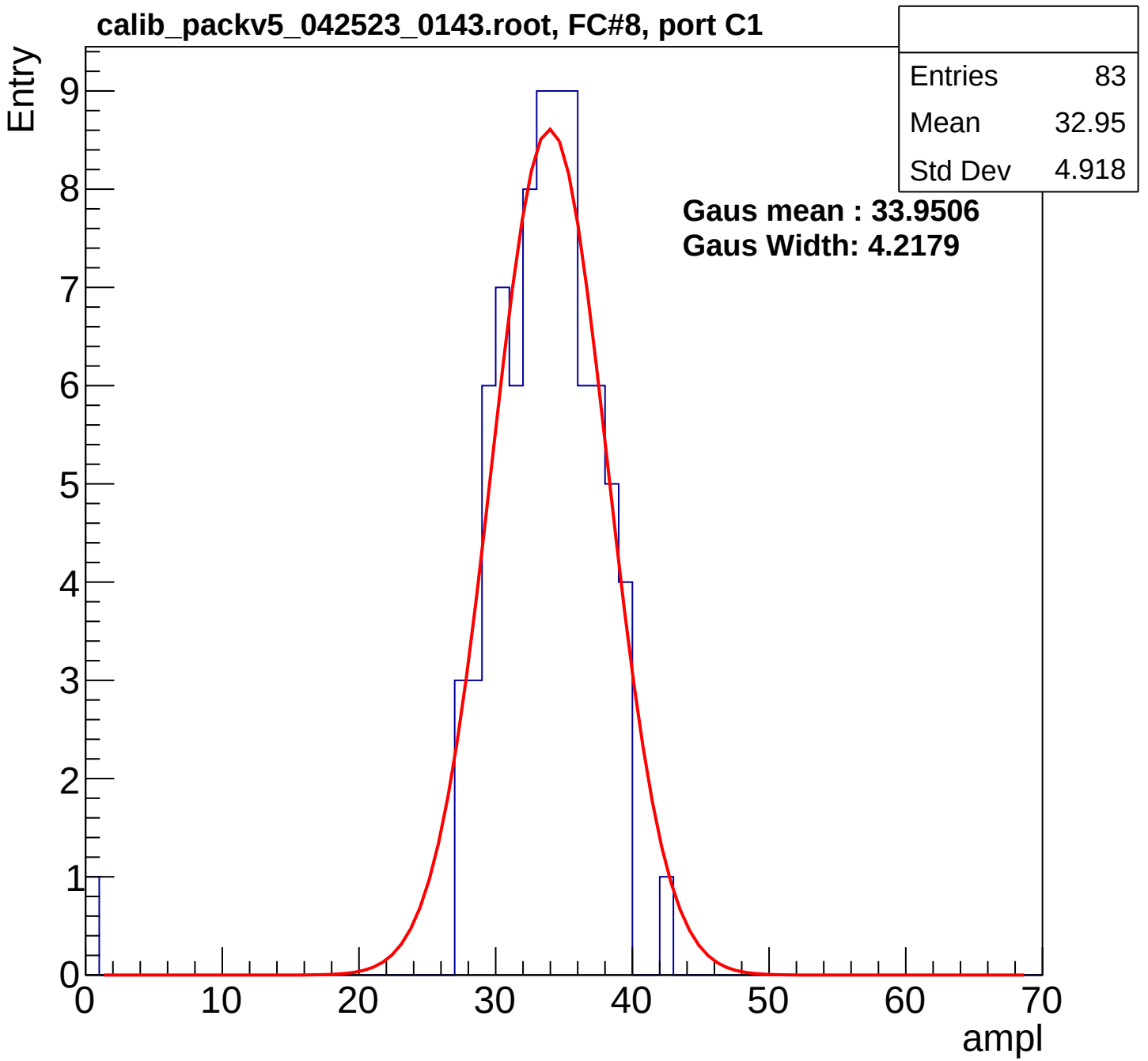
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 32.95
Std Dev 4.918
Gaus mean : 33.9506
Gaus Width: 4.2179
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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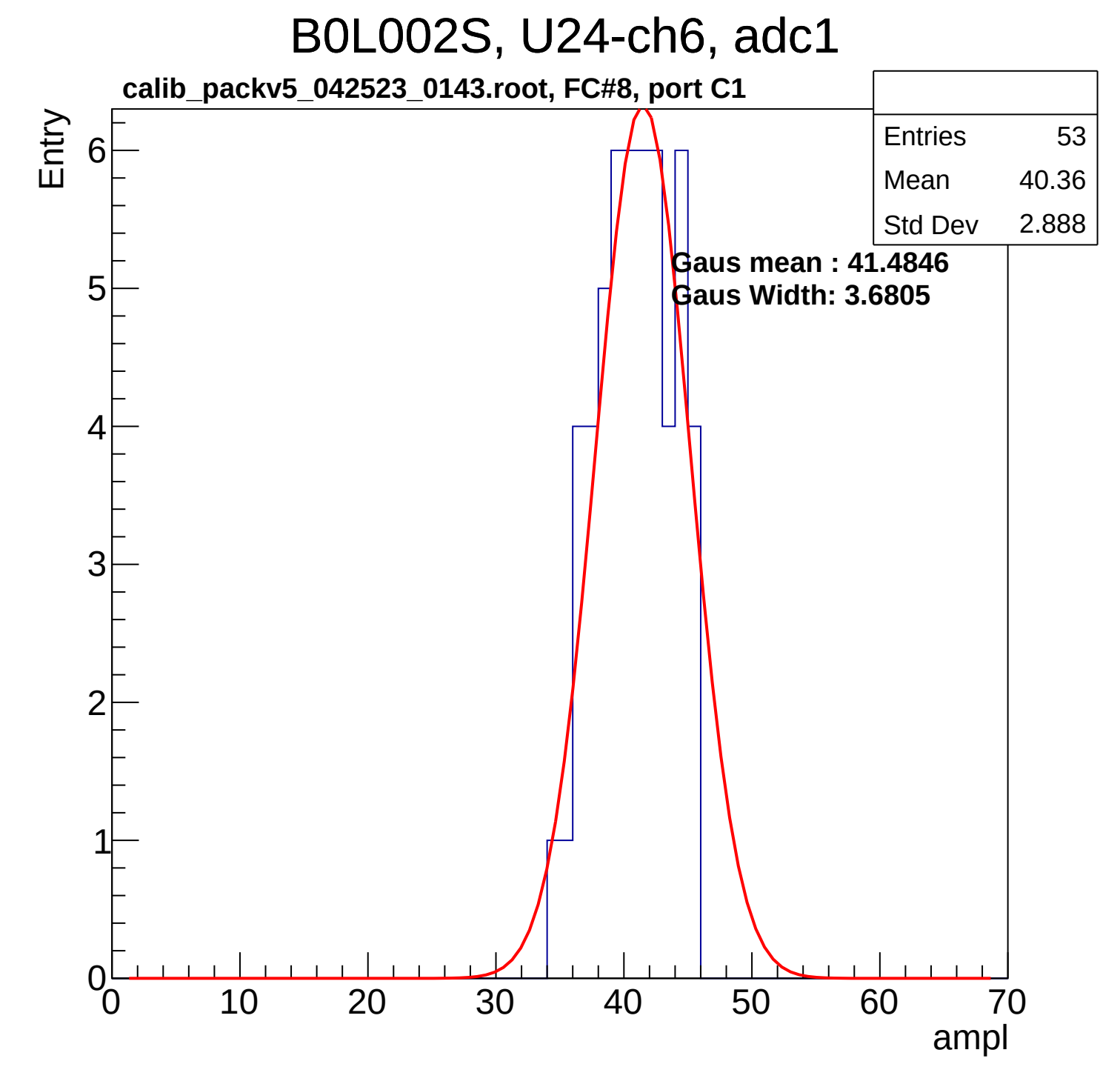
B0L002S, U24-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 40.36
Std Dev 2.888
Gaus mean : 41.4846
Gaus Width: 3.6805
Entry
ampl
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2
3
4
5
6
0
10
20
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40
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60
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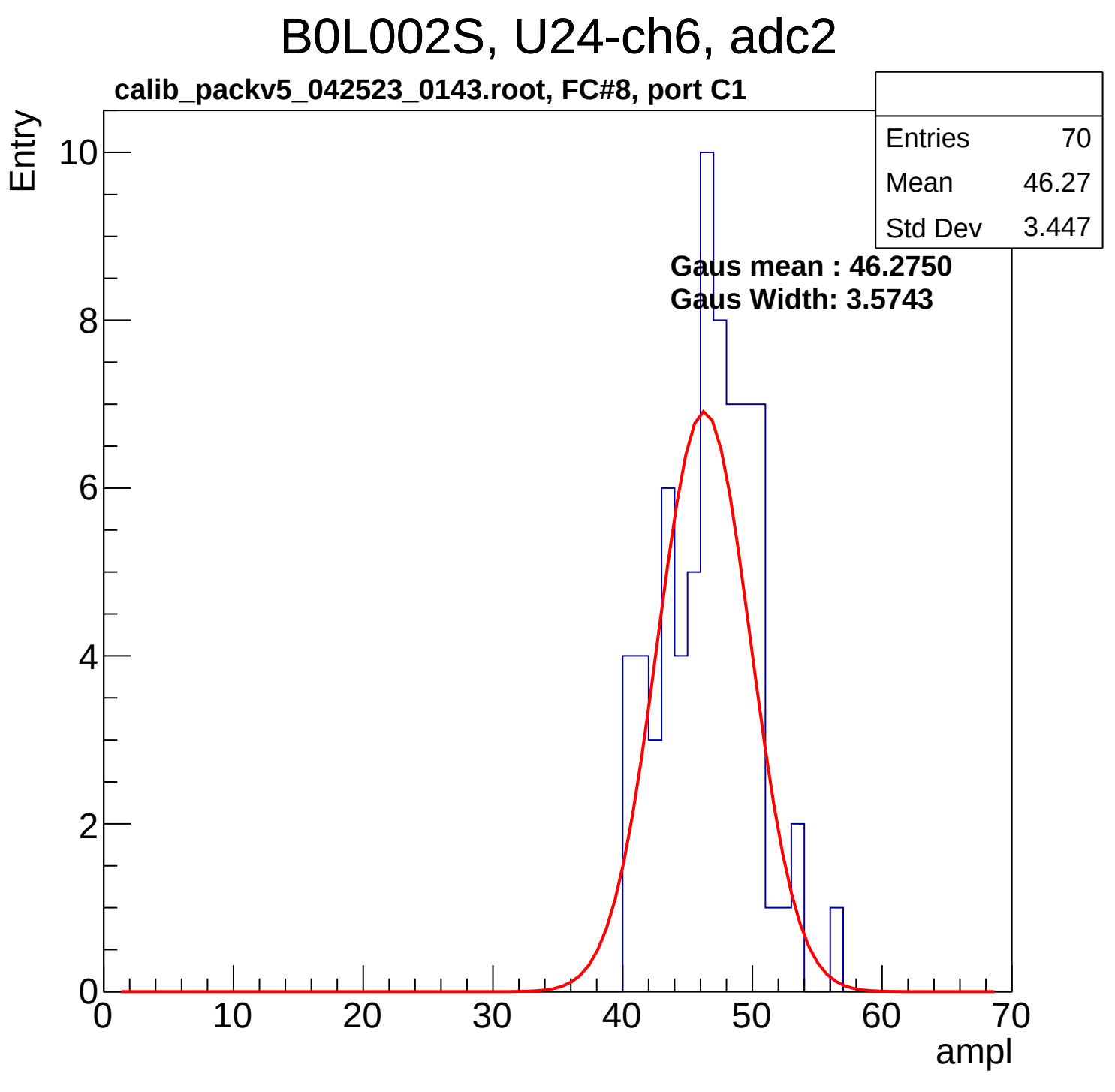
B0L002S, U24-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 46.27
Std Dev 3.447
Gaus mean : 46.2750
Gaus Width: 3.5743
Entry
ampl
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4
6
8
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0
10
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60
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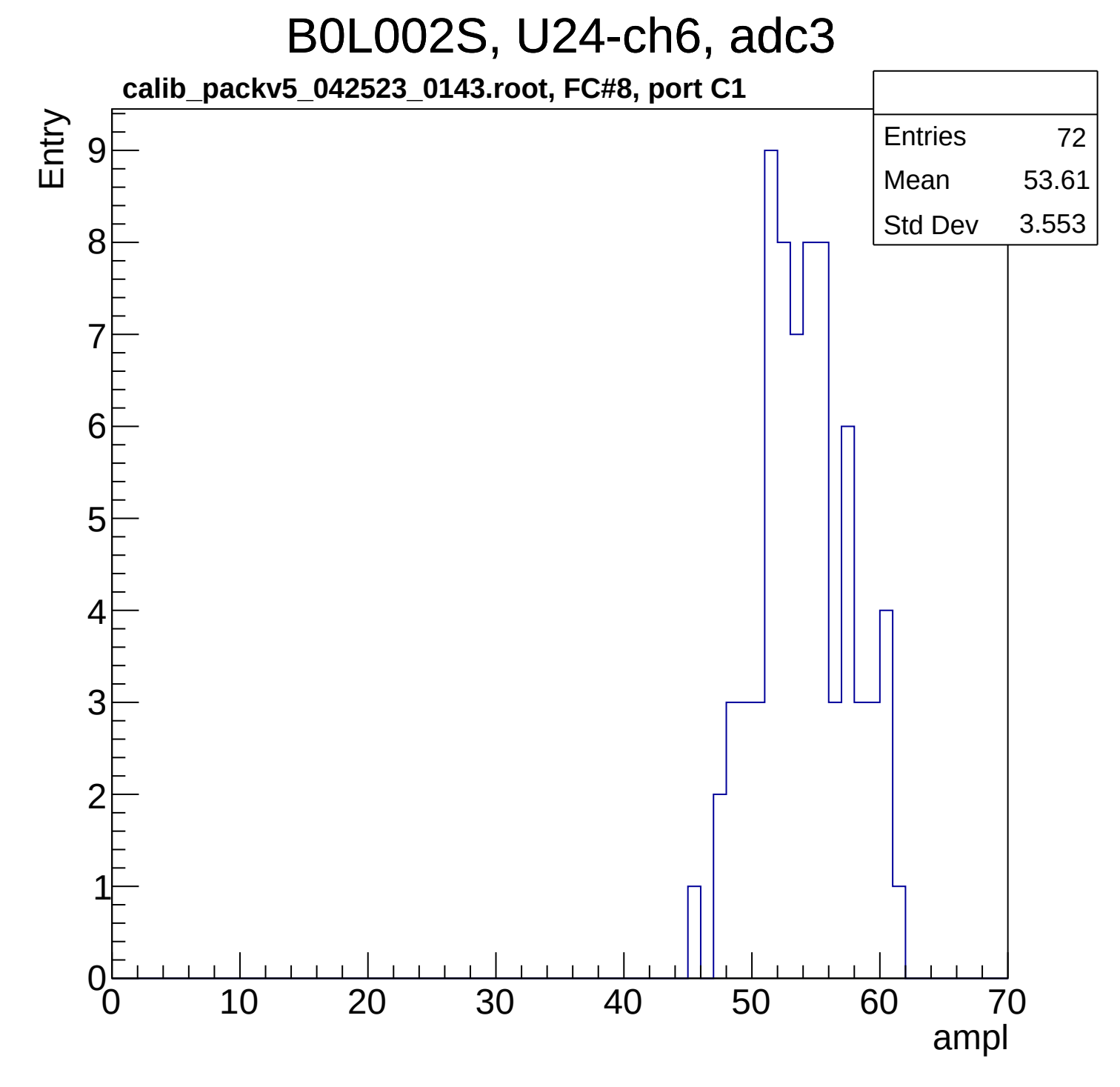
B0L002S, U24-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 53.61
Std Dev 3.553
Entry
ampl
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3
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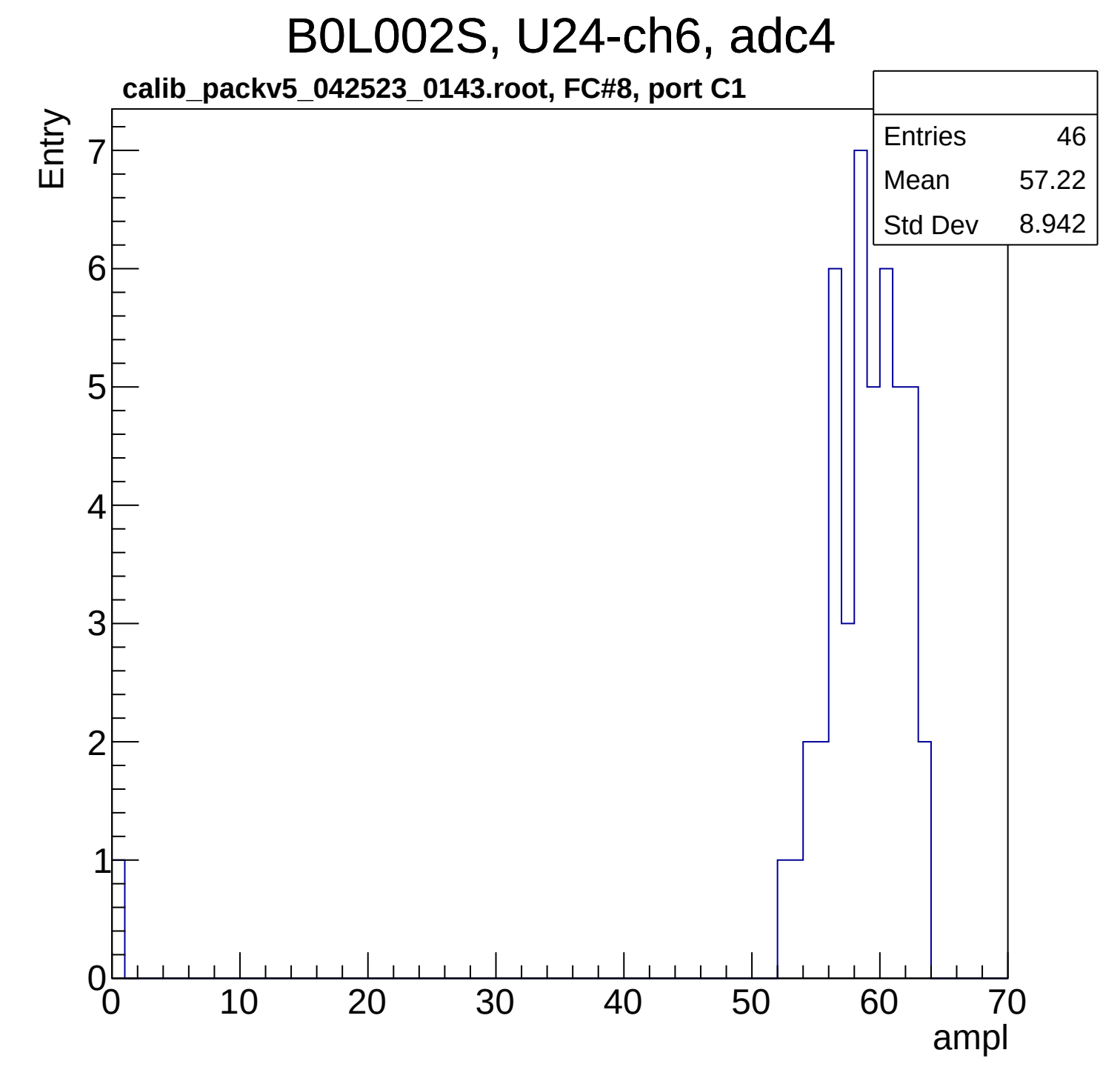
B0L002S, U24-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 57.22
Std Dev 8.942
Entry
ampl
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0
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60
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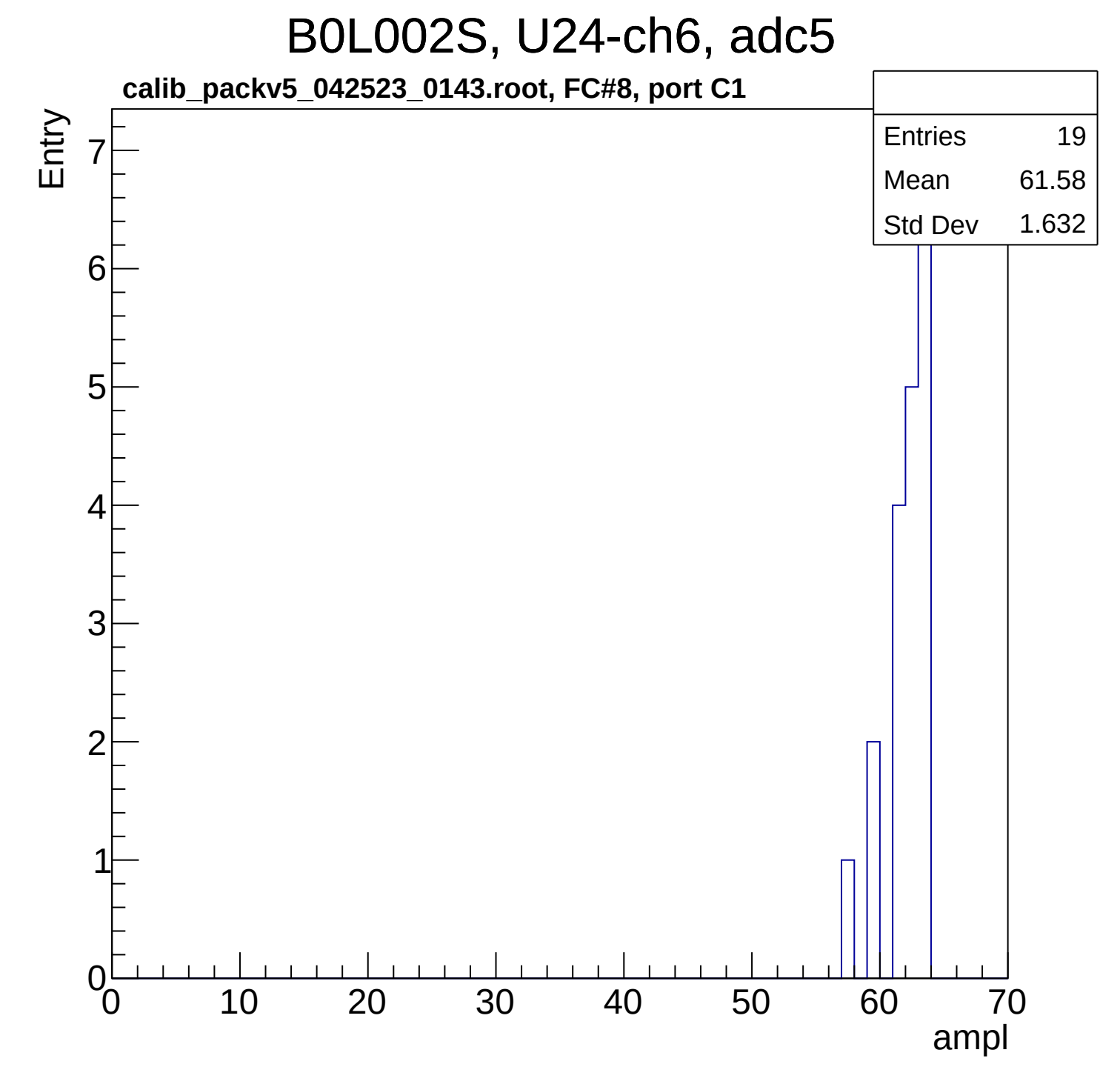
B0L002S, U24-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.58
Std Dev 1.632
Entry
ampl
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3
4
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6
7
0
10
20
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40
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60
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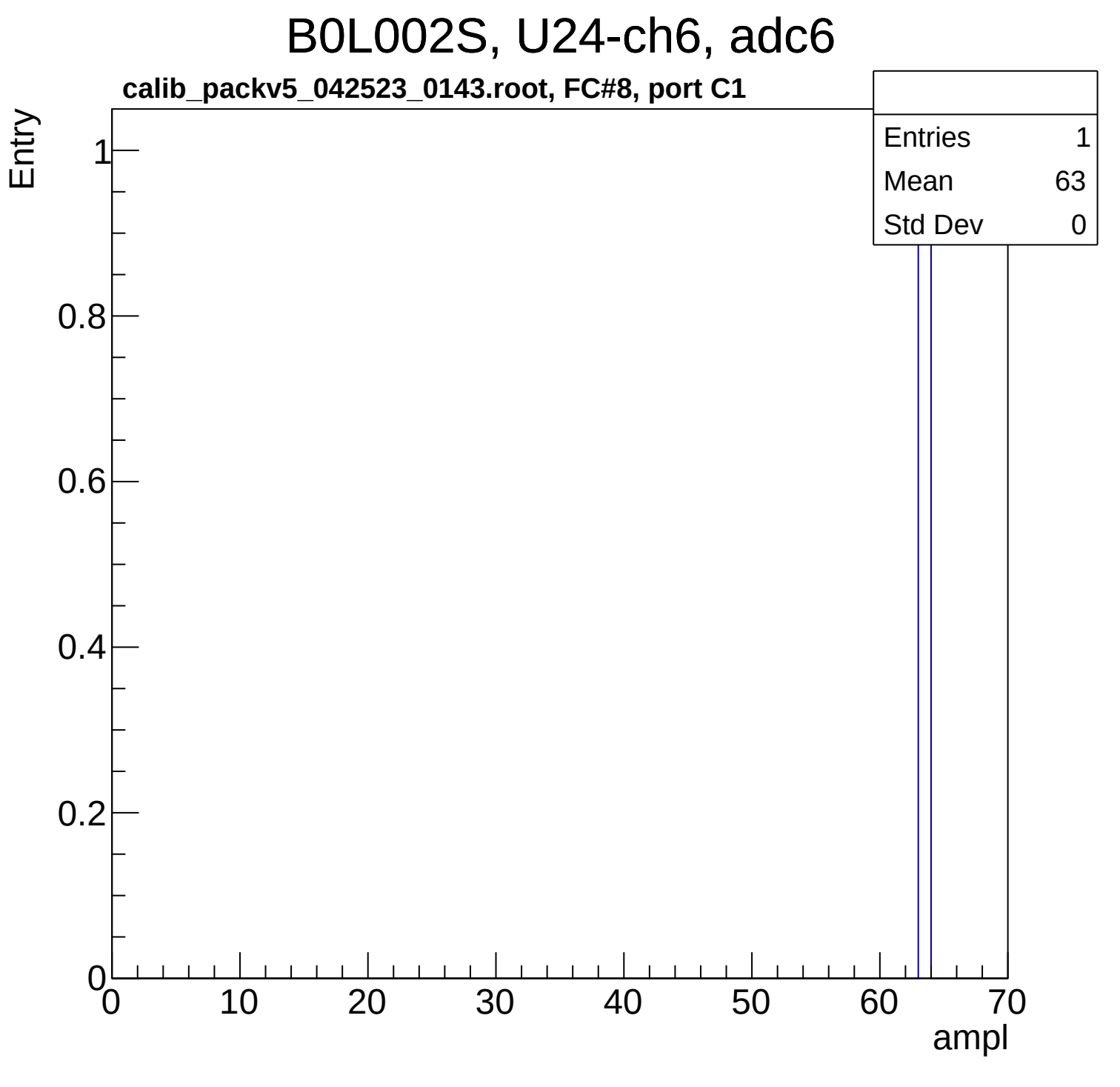
B0L002S, U24-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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0.4
0.2
0
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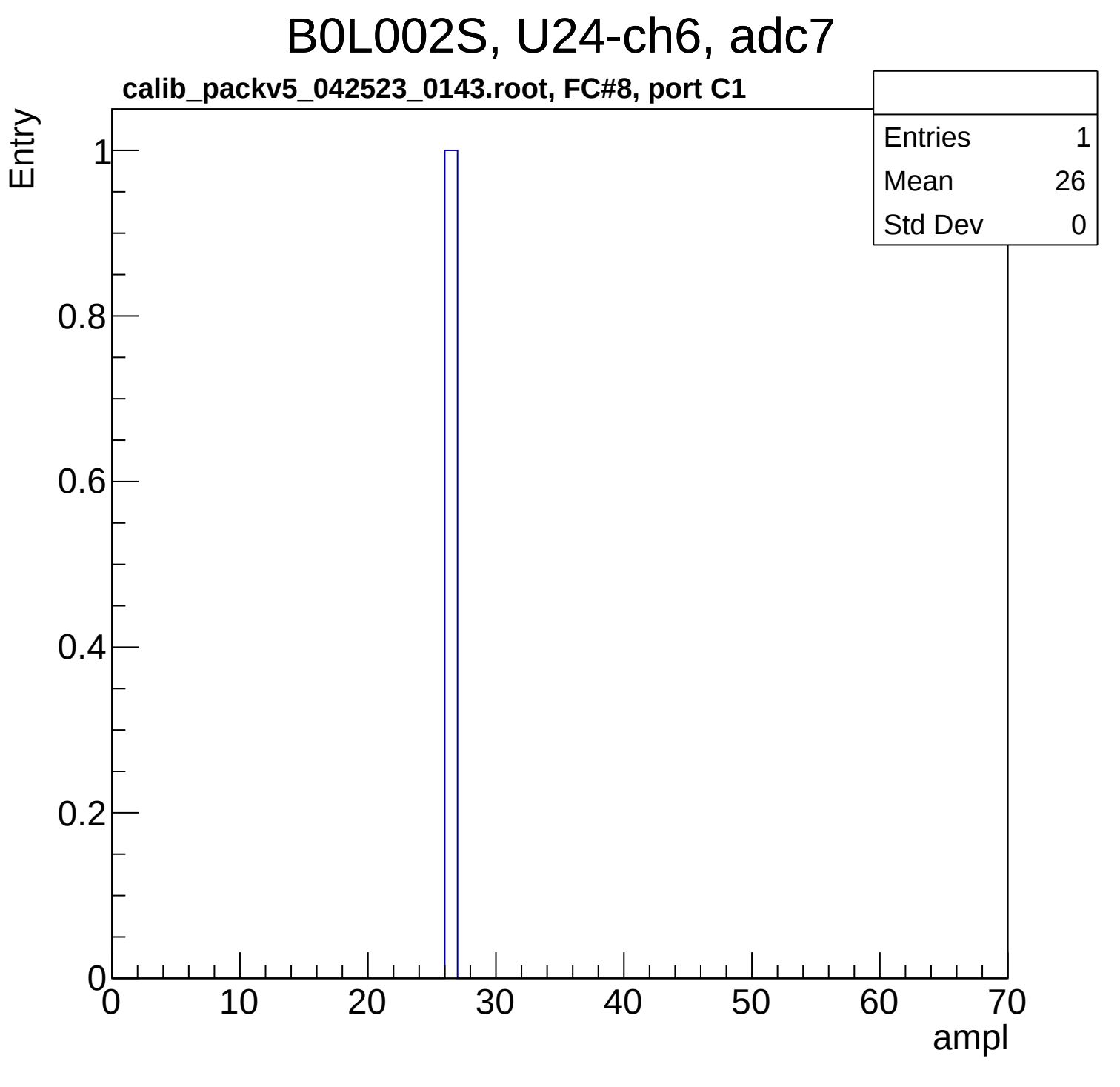
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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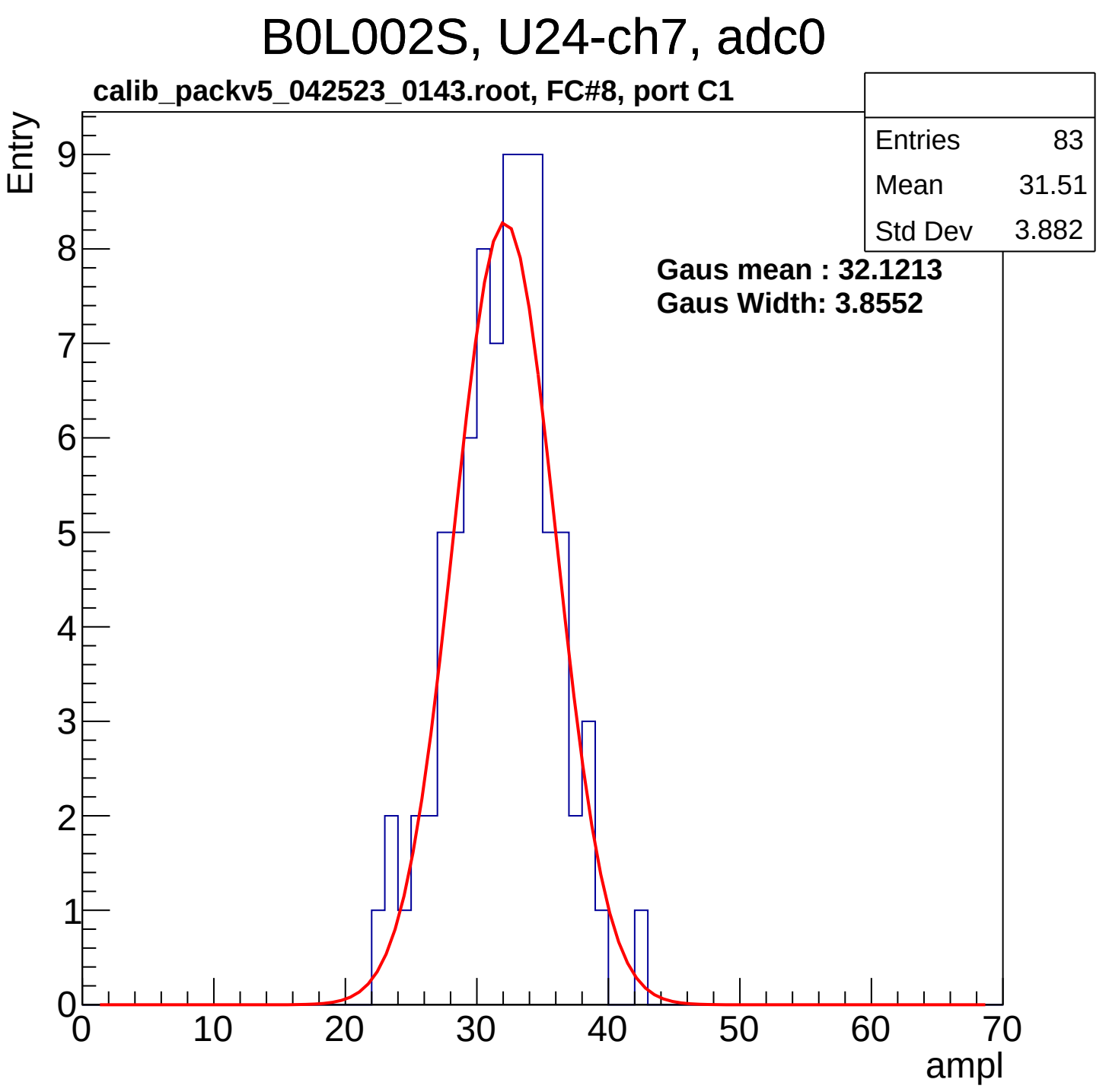
50
60
70
ampl


B0L002S, U24-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 31.51
Std Dev 3.882
Gaus mean : 32.1213
Gaus Width: 3.8552
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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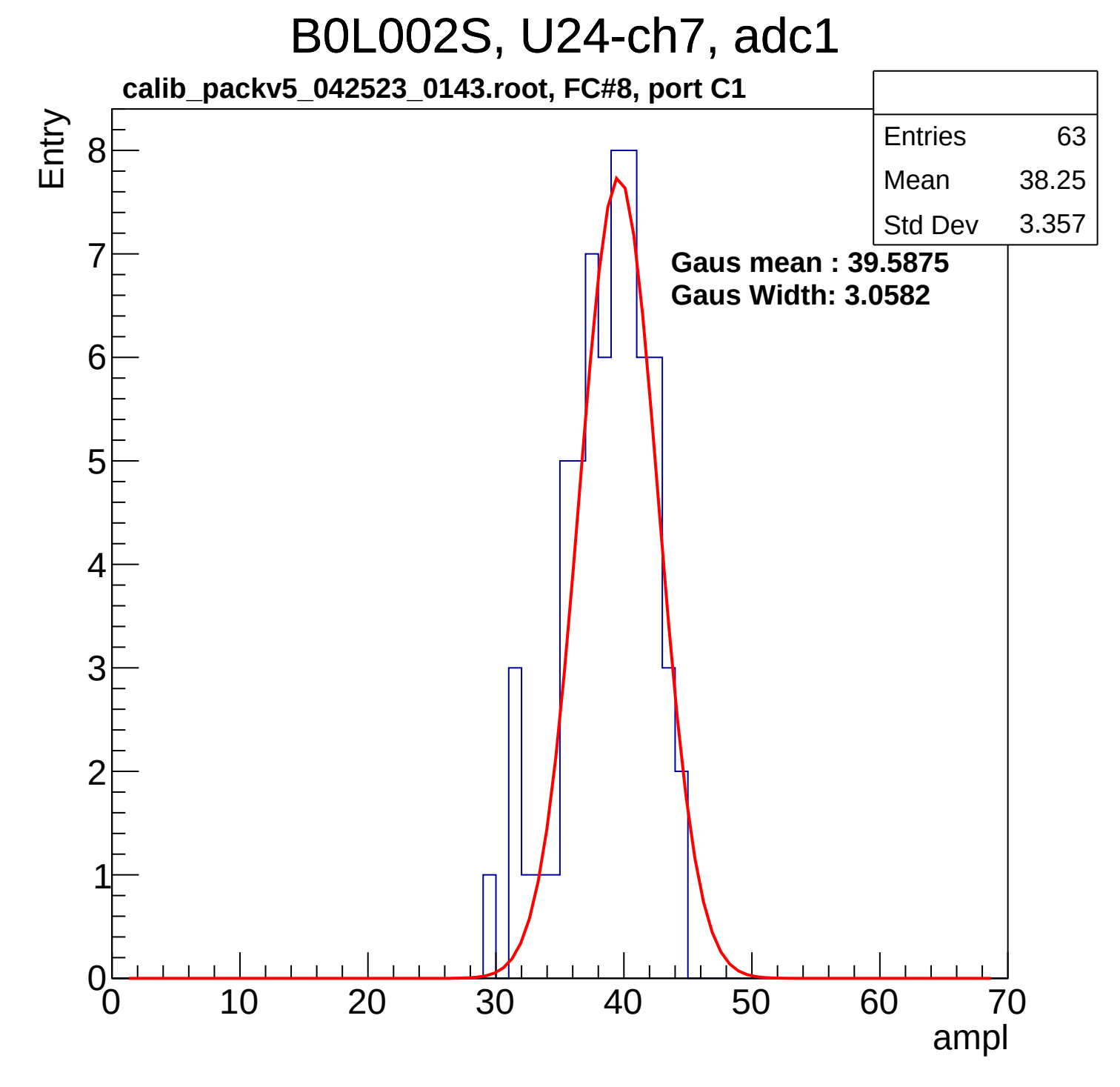
B0L002S, U24-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.25
Std Dev 3.357
Gaus mean : 39.5875
Gaus Width: 3.0582
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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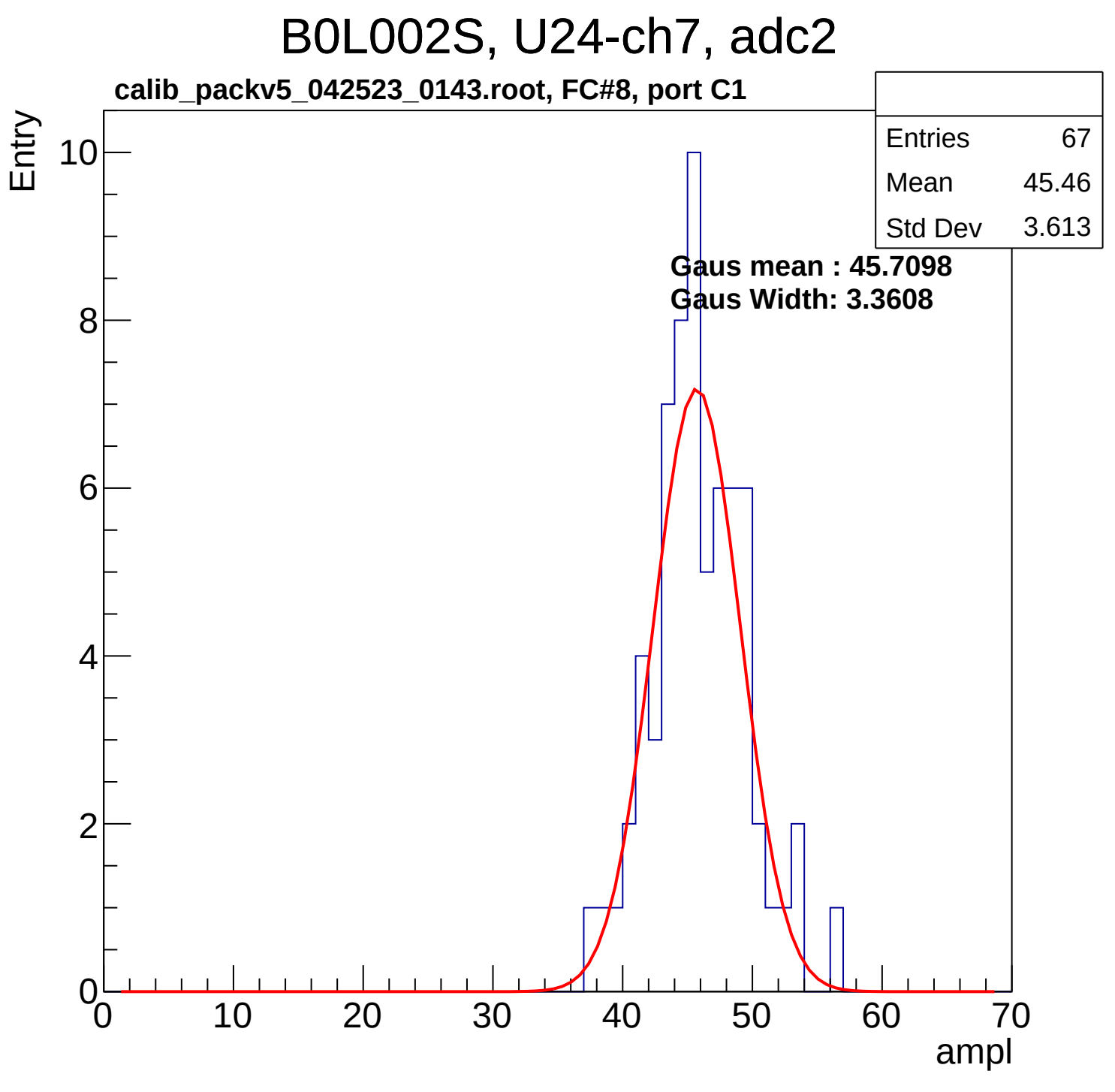
B0L002S, U24-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.46
Std Dev 3.613
Gaus mean : 45.7098
Gaus Width: 3.3608
Entry
ampl
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0
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40
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60
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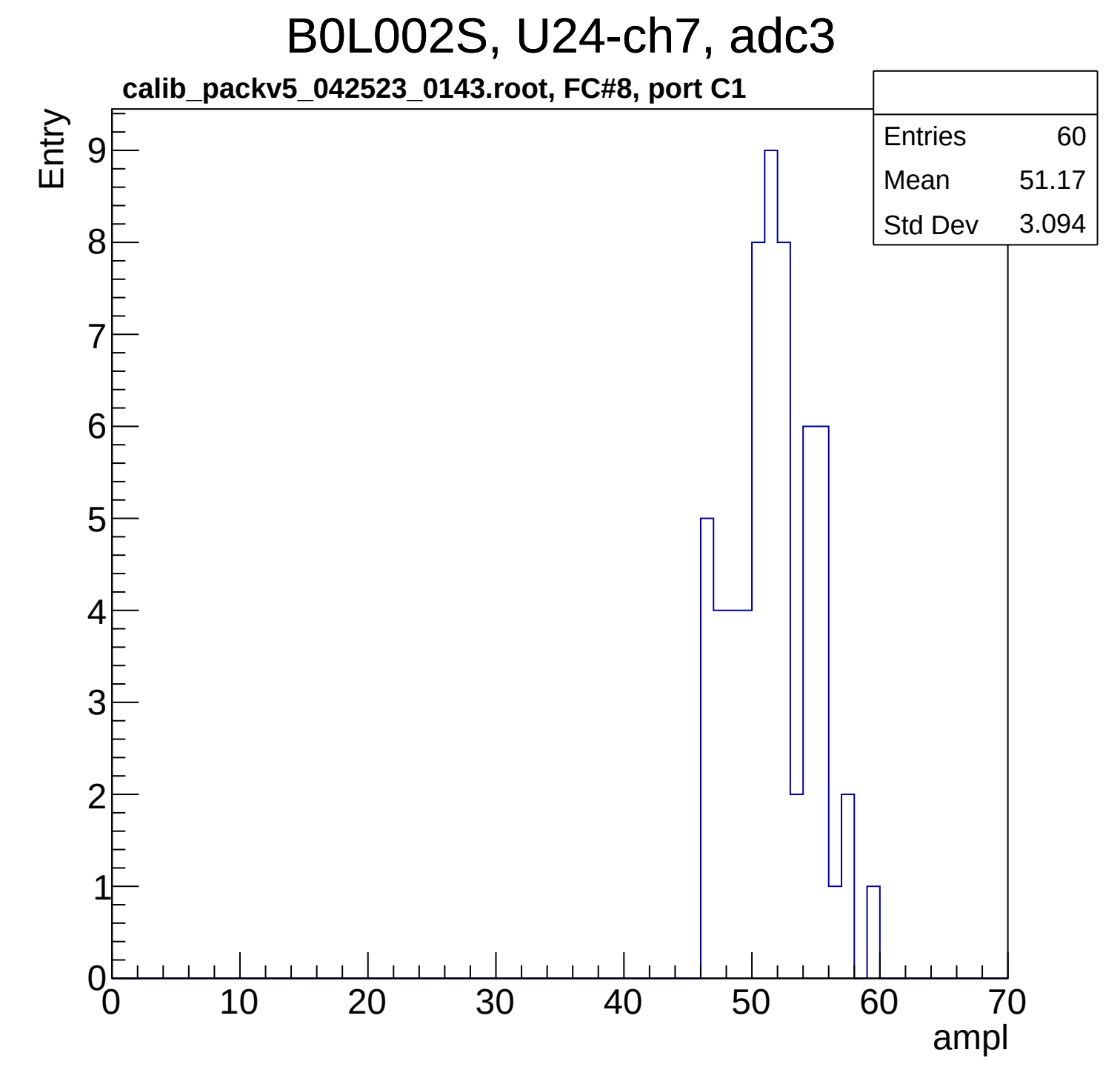
B0L002S, U24-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.17
Std Dev 3.094
Entry
ampl
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3
4
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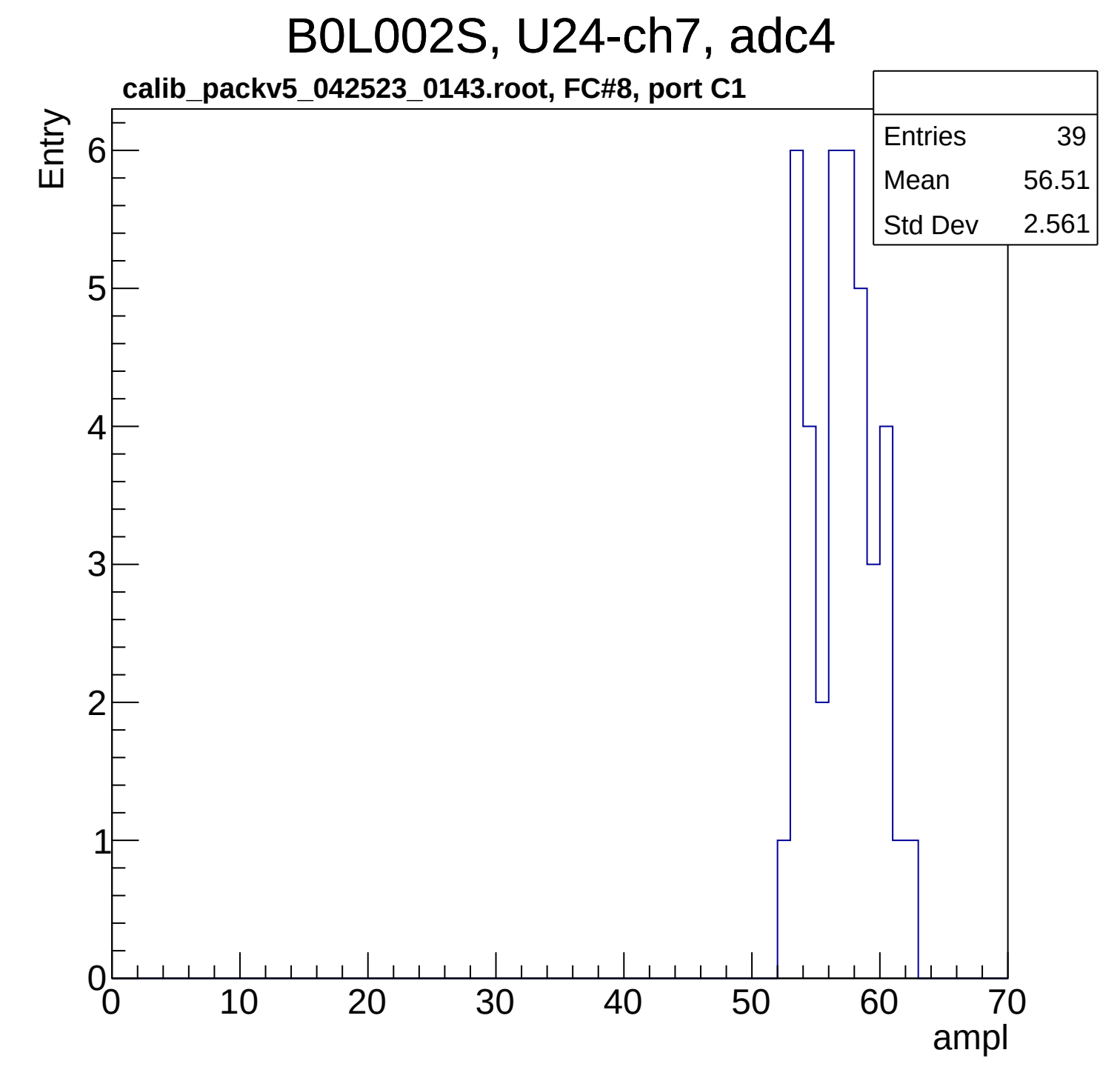
B0L002S, U24-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 56.51
Std Dev 2.561
Entry
ampl
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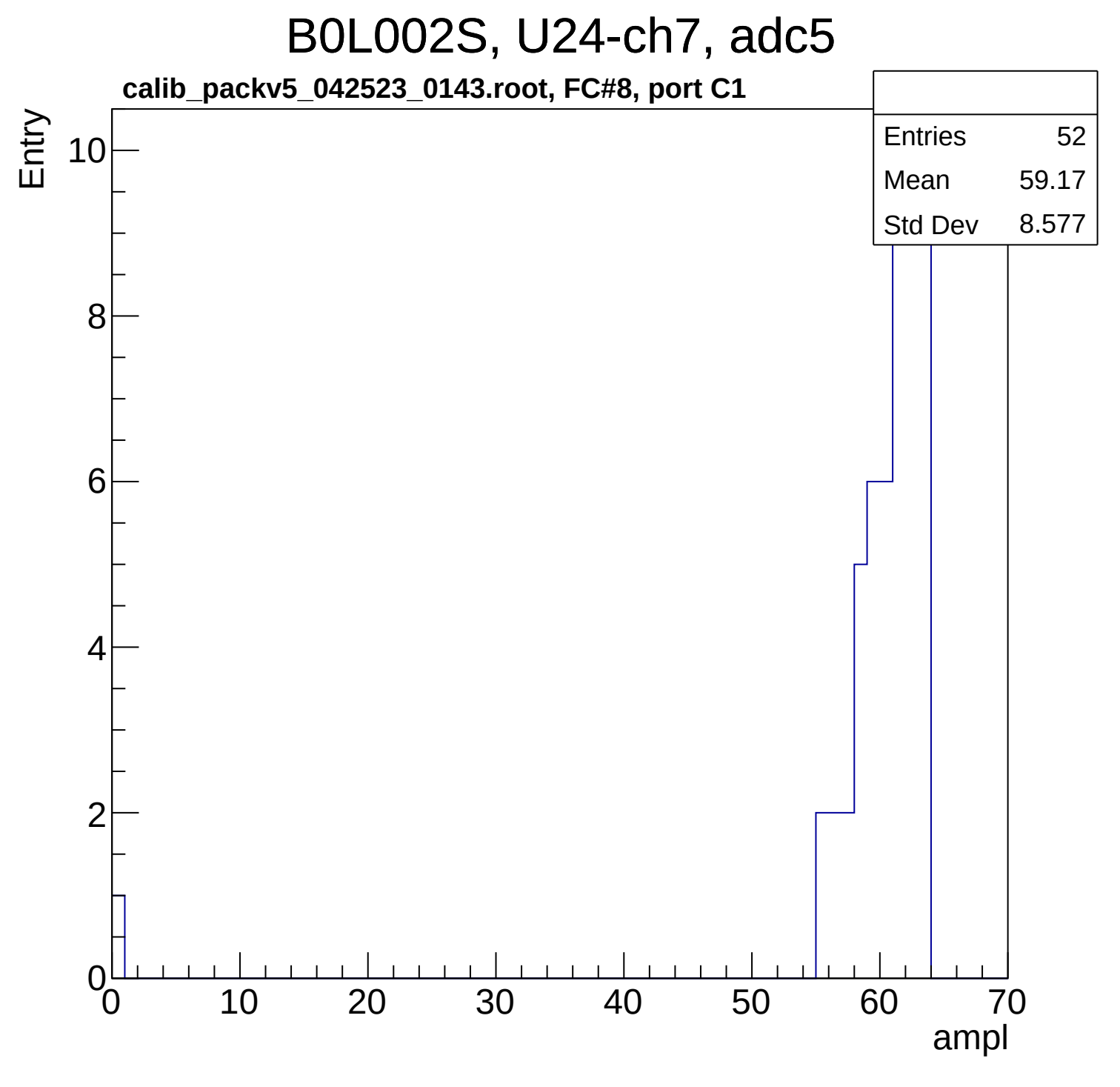
B0L002S, U24-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 59.17
Std Dev 8.577
Entry
0
2
4
6
8
10
0
10
20
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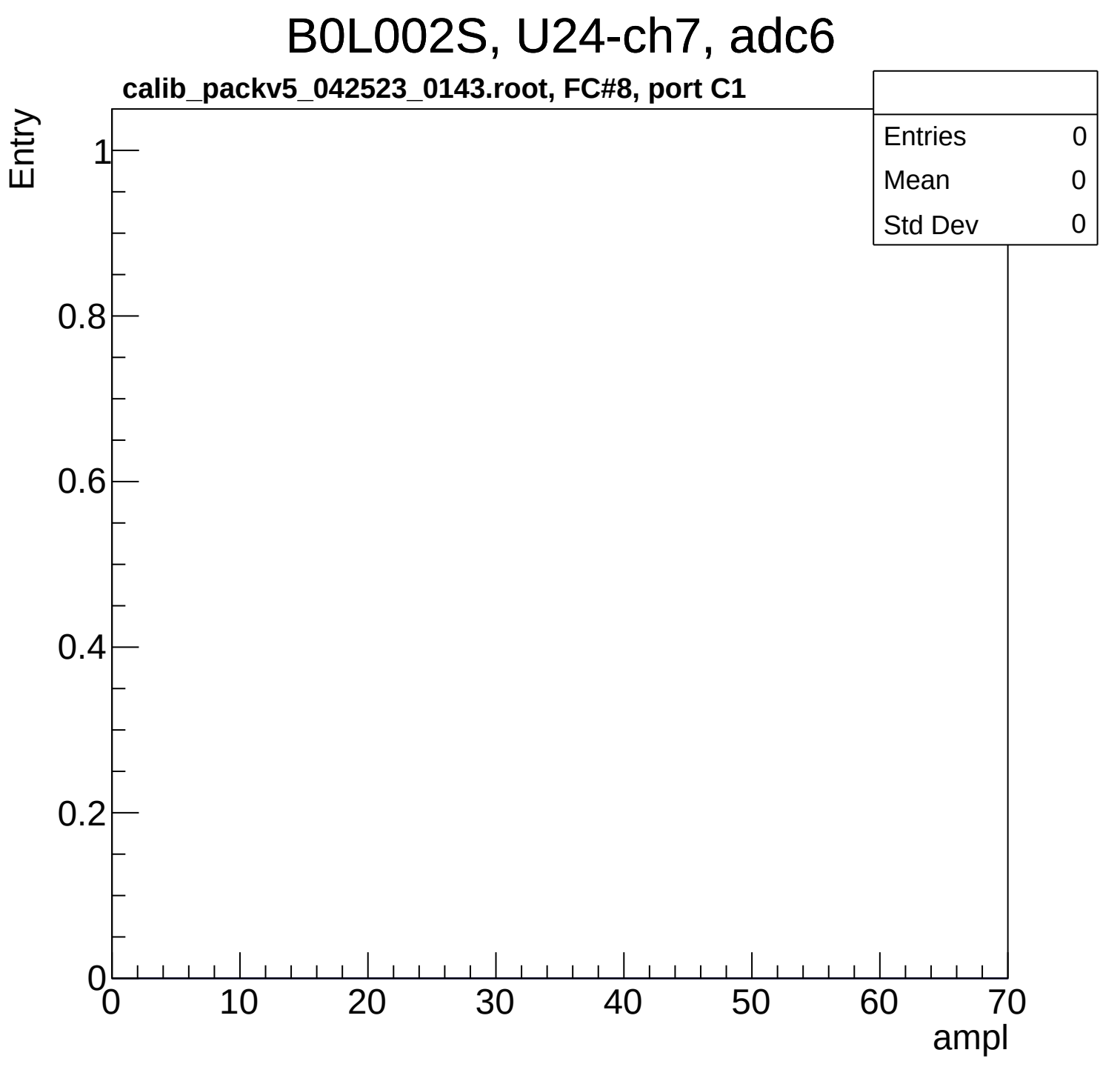
50
60
70
ampl


B0L002S, U24-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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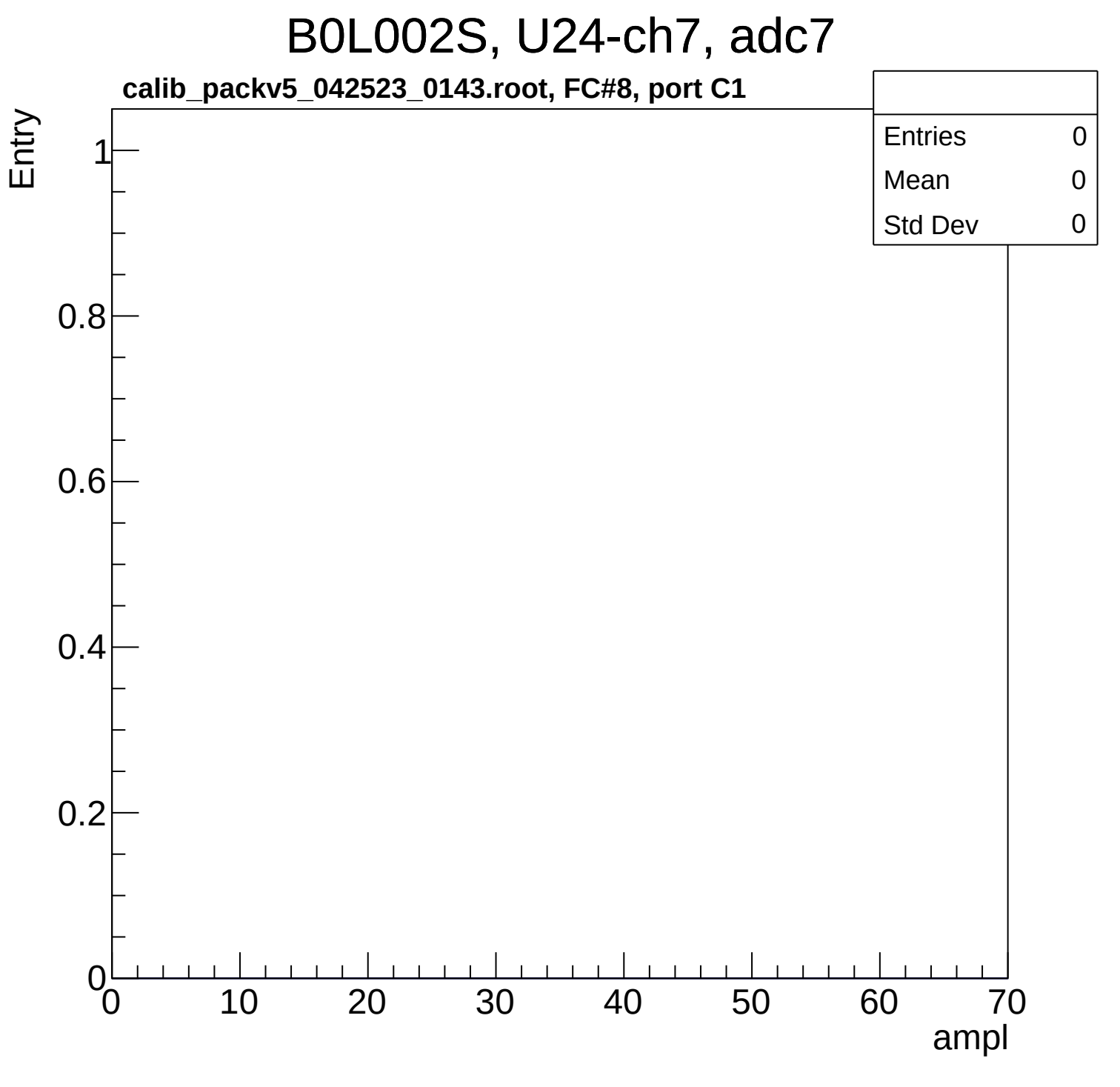
70
ampl


B0L002S, U24-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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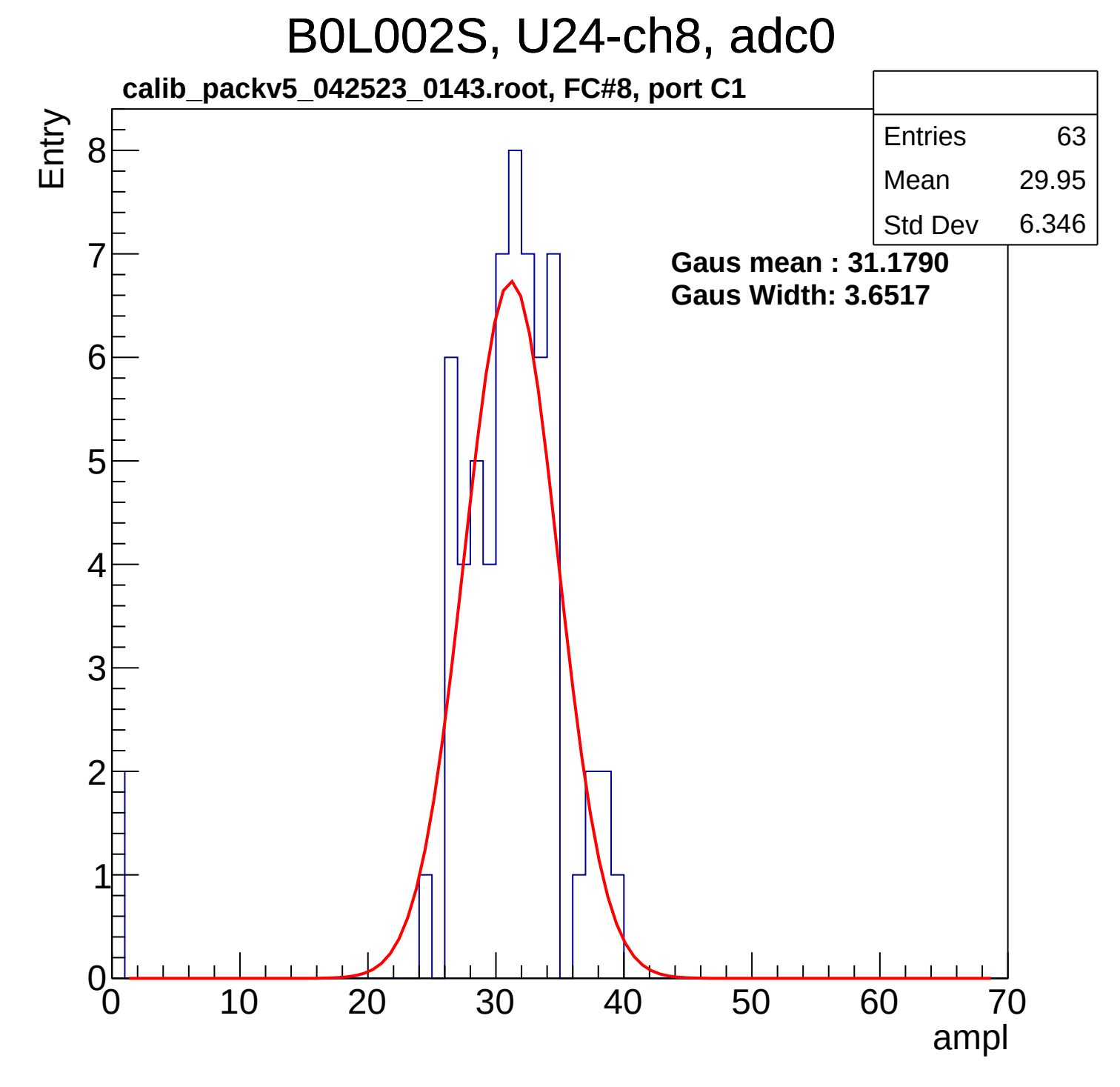
ampl


B0L002S, U24-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.95
Std Dev 6.346
Gaus mean : 31.1790
Gaus Width: 3.6517
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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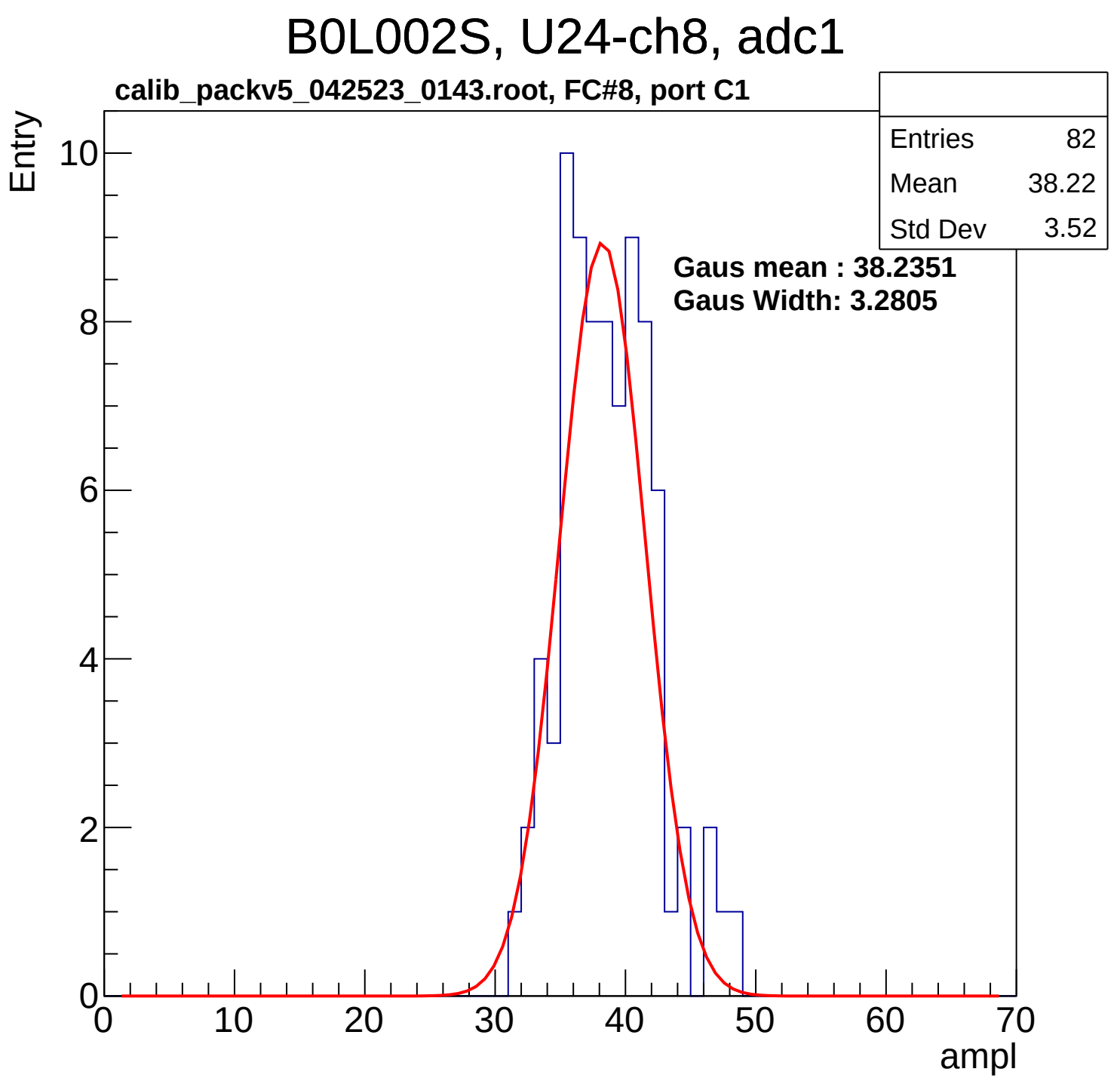
B0L002S, U24-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 38.22
Std Dev 3.52
Gaus mean : 38.2351
Gaus Width: 3.2805
Entry
ampl
0
2
4
6
8
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0
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40
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60
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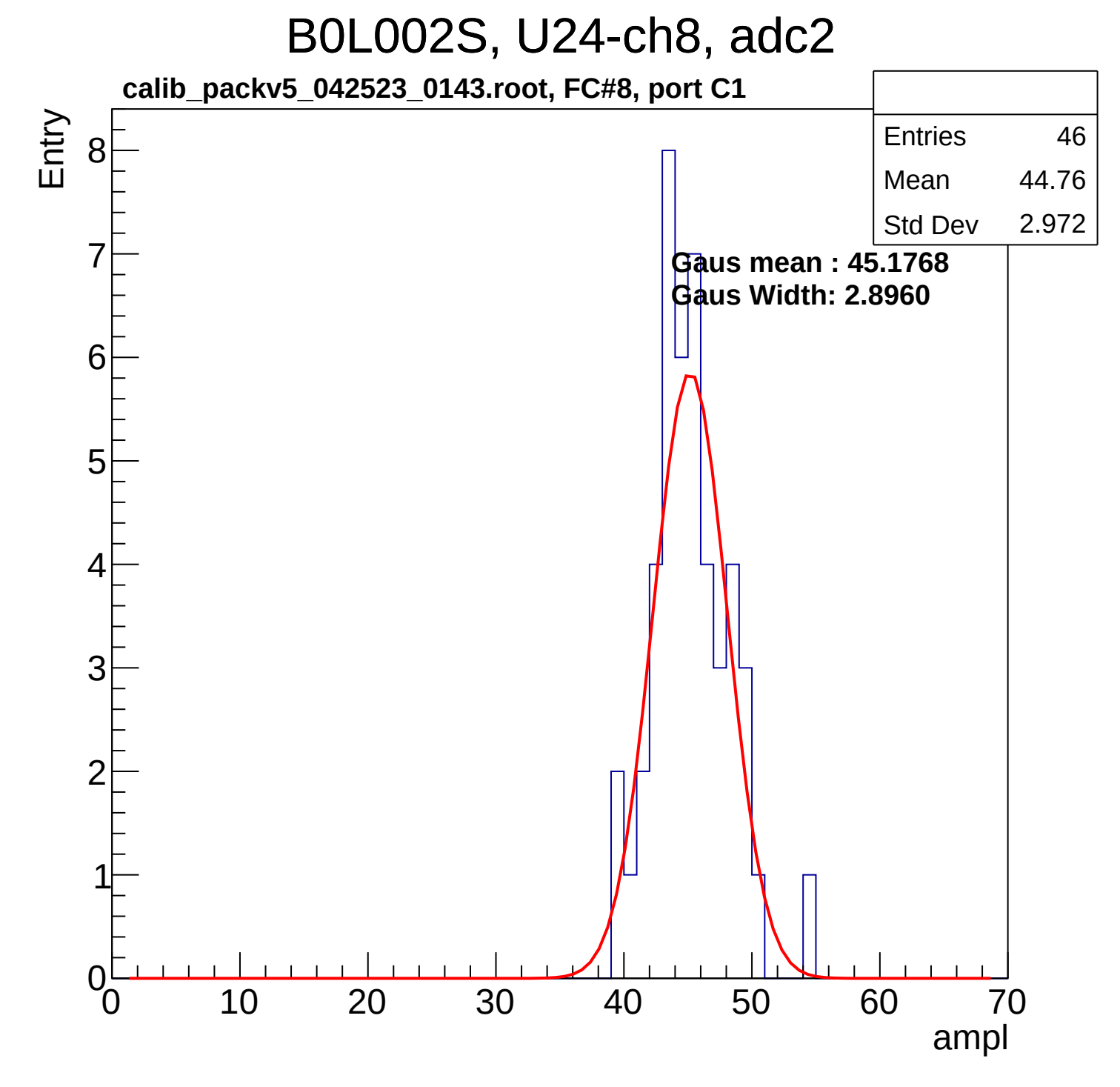
B0L002S, U24-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 44.76
Std Dev 2.972
Gaus mean : 45.1768
Gaus Width: 2.8960
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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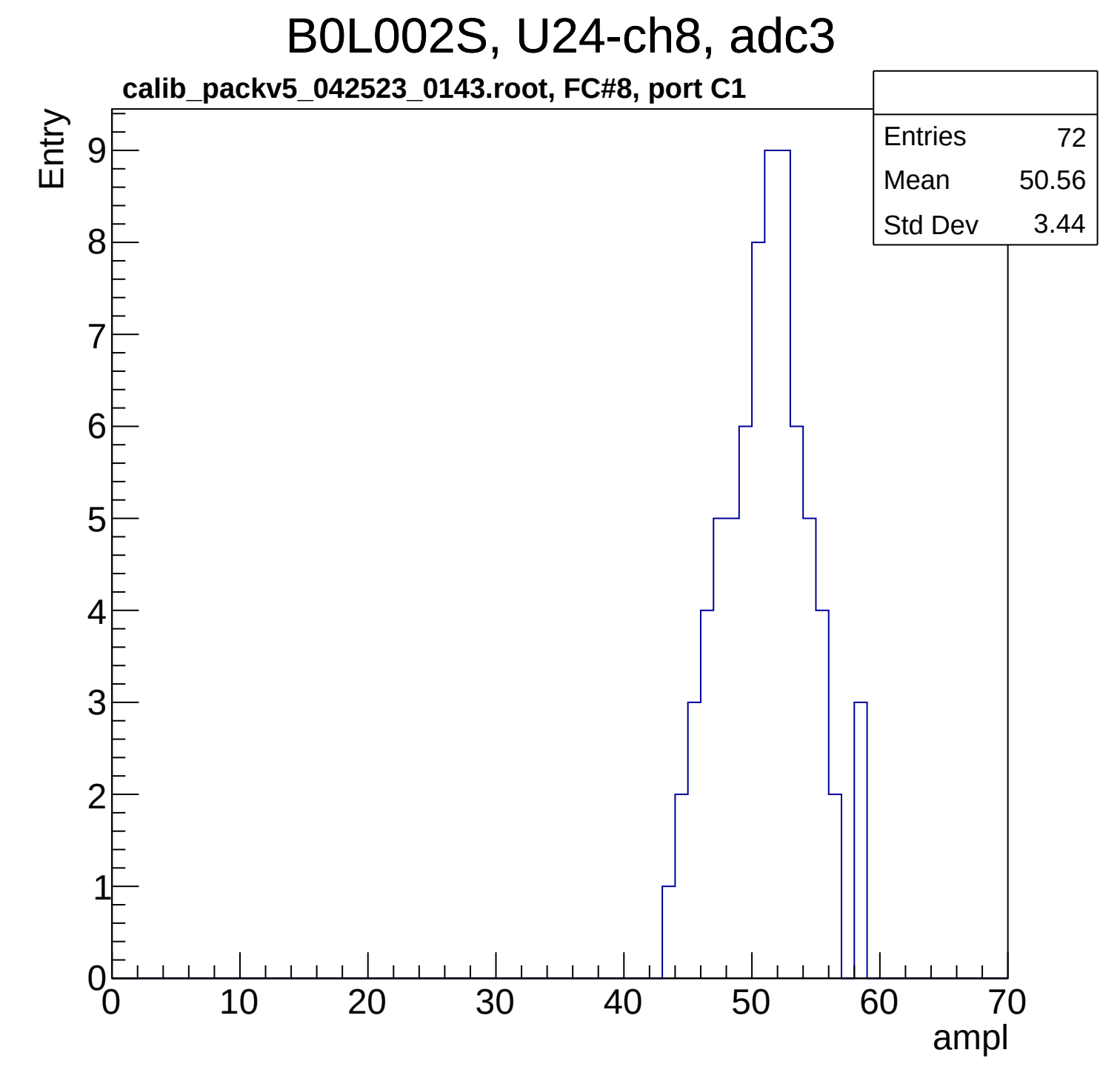
B0L002S, U24-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.56
Std Dev 3.44
Entry
ampl
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2
3
4
5
6
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0
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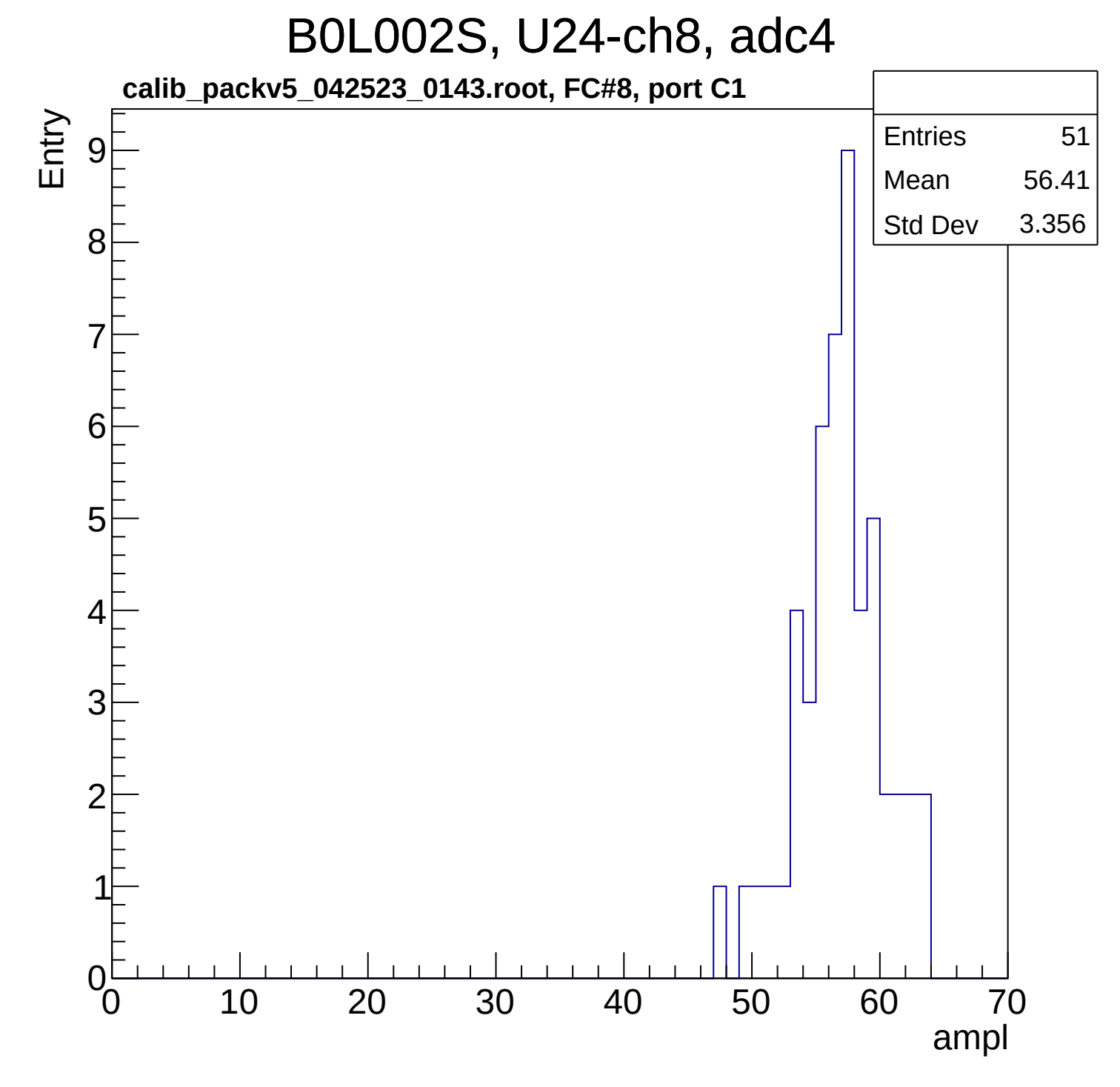
B0L002S, U24-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.41
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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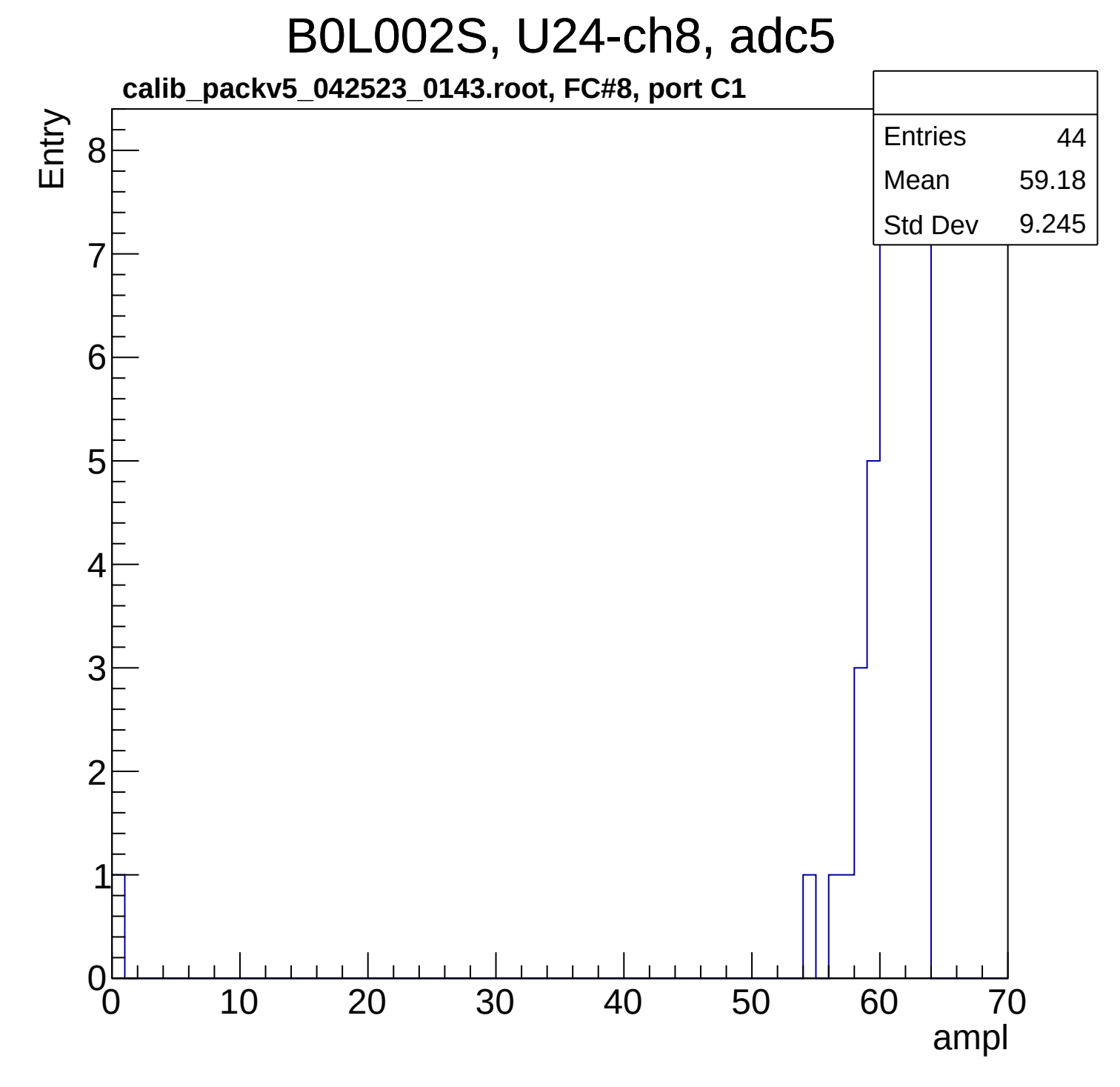
B0L002S, U24-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.18
Std Dev 9.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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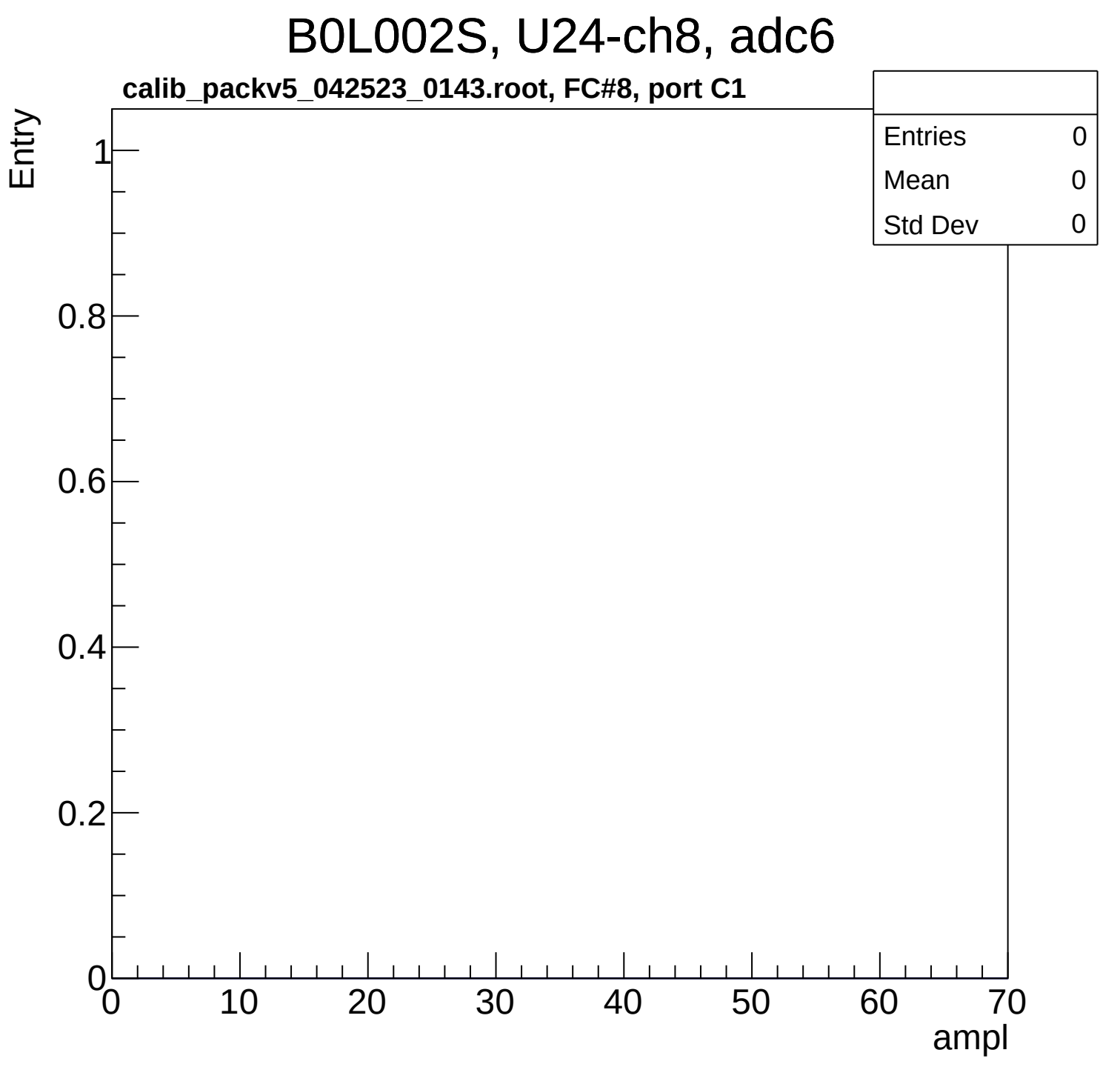
B0L002S, U24-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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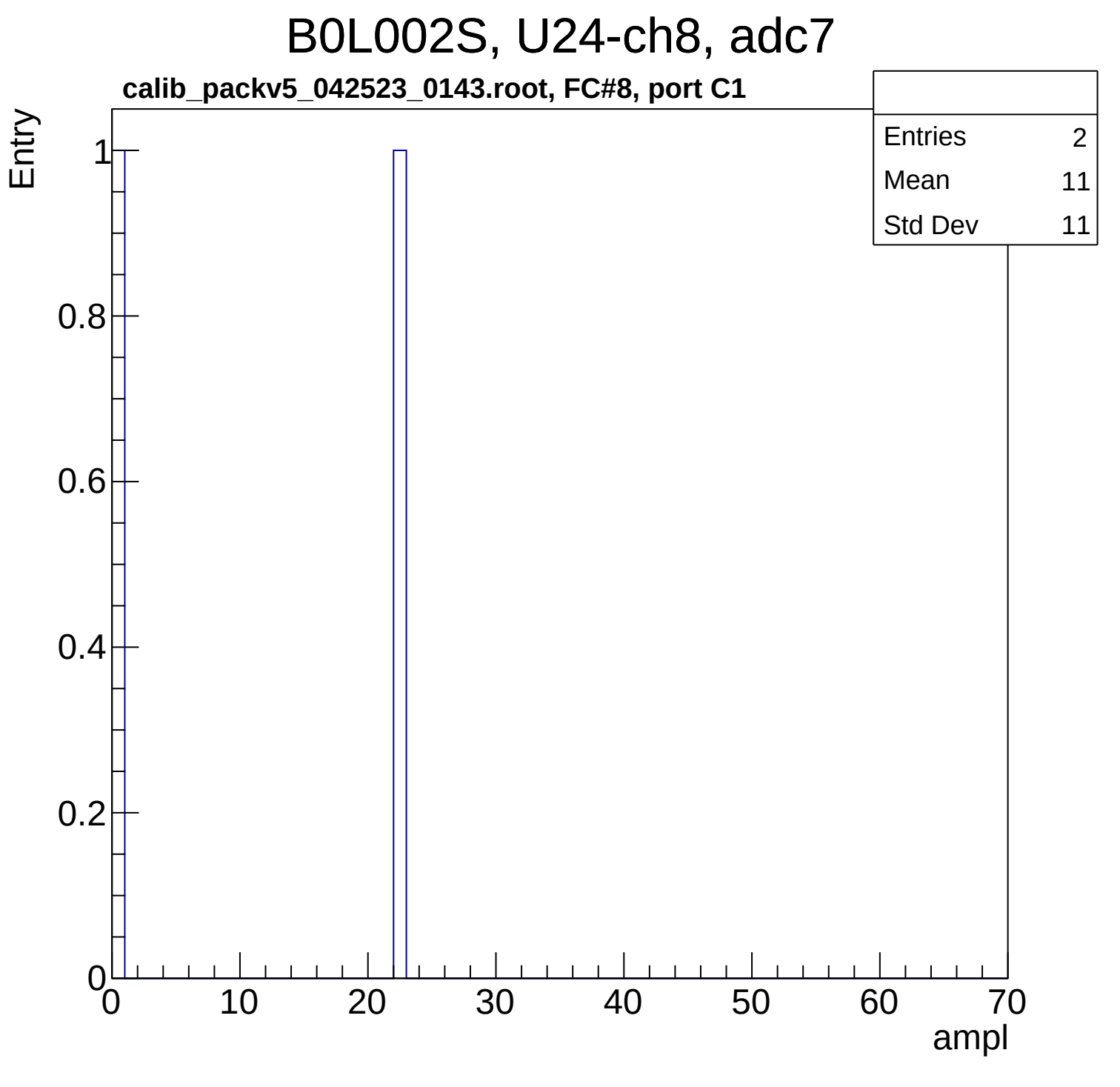
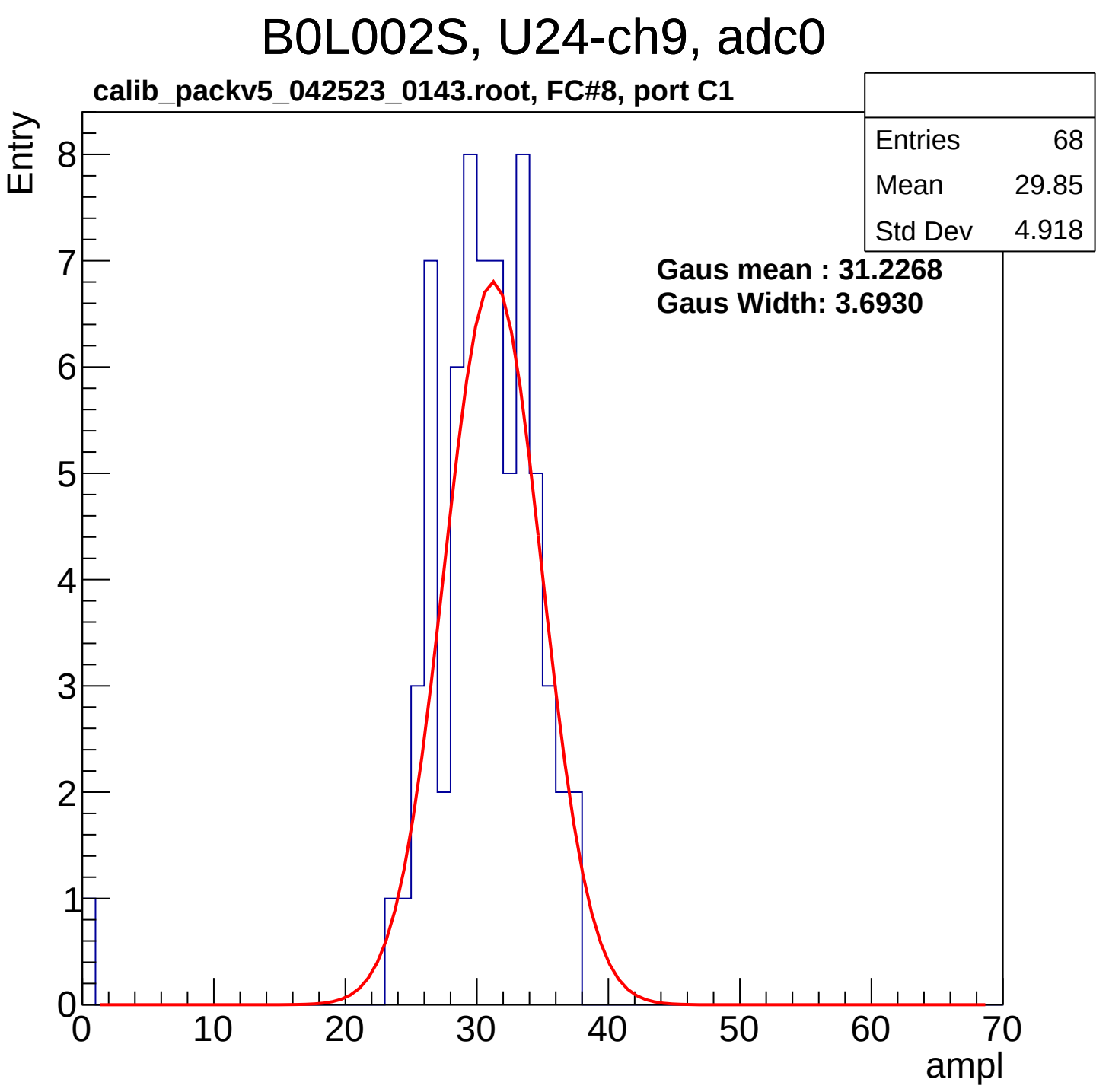
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.85
Std Dev 4.918
Gaus mean : 31.2268
Gaus Width: 3.6930
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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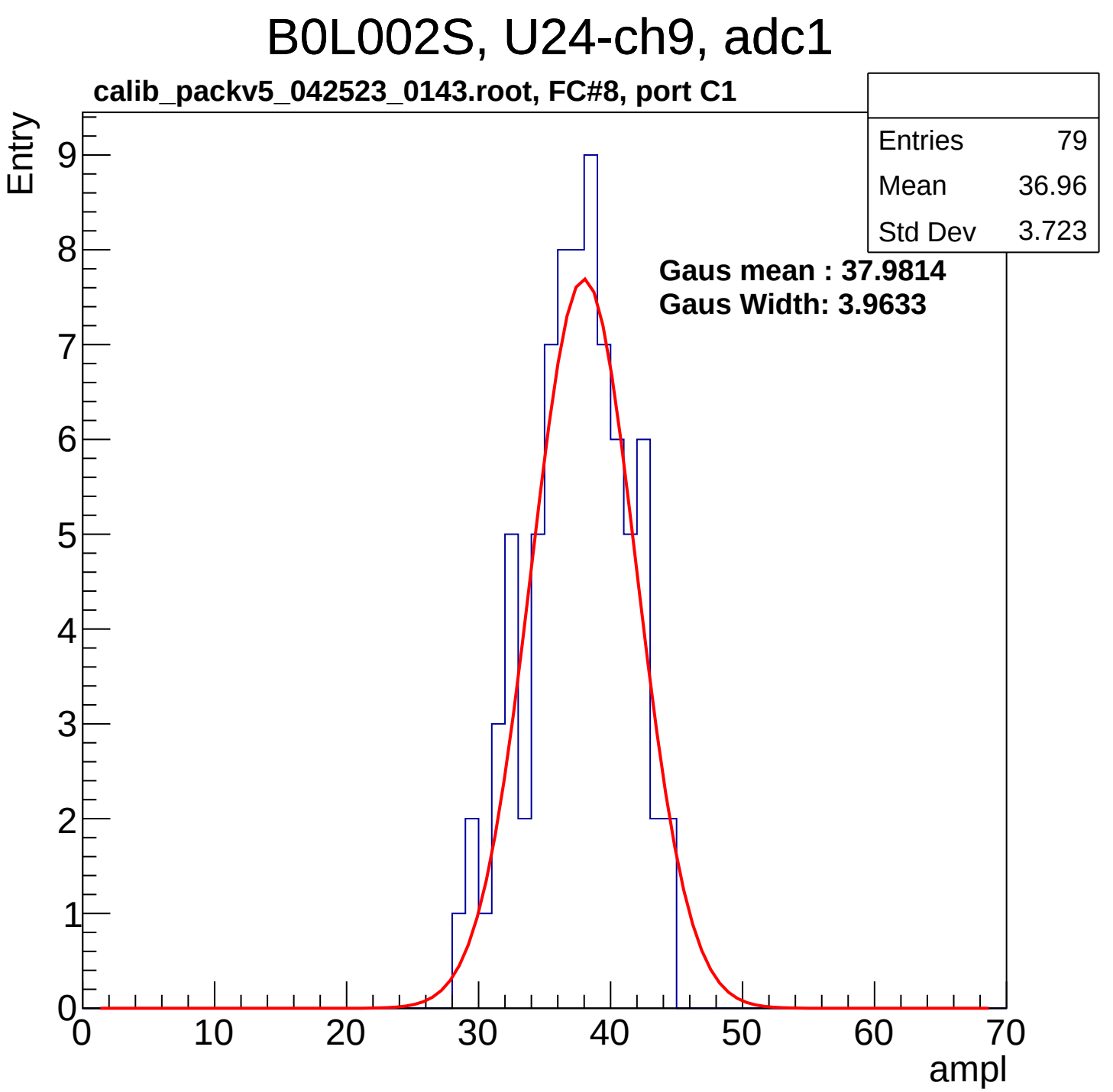
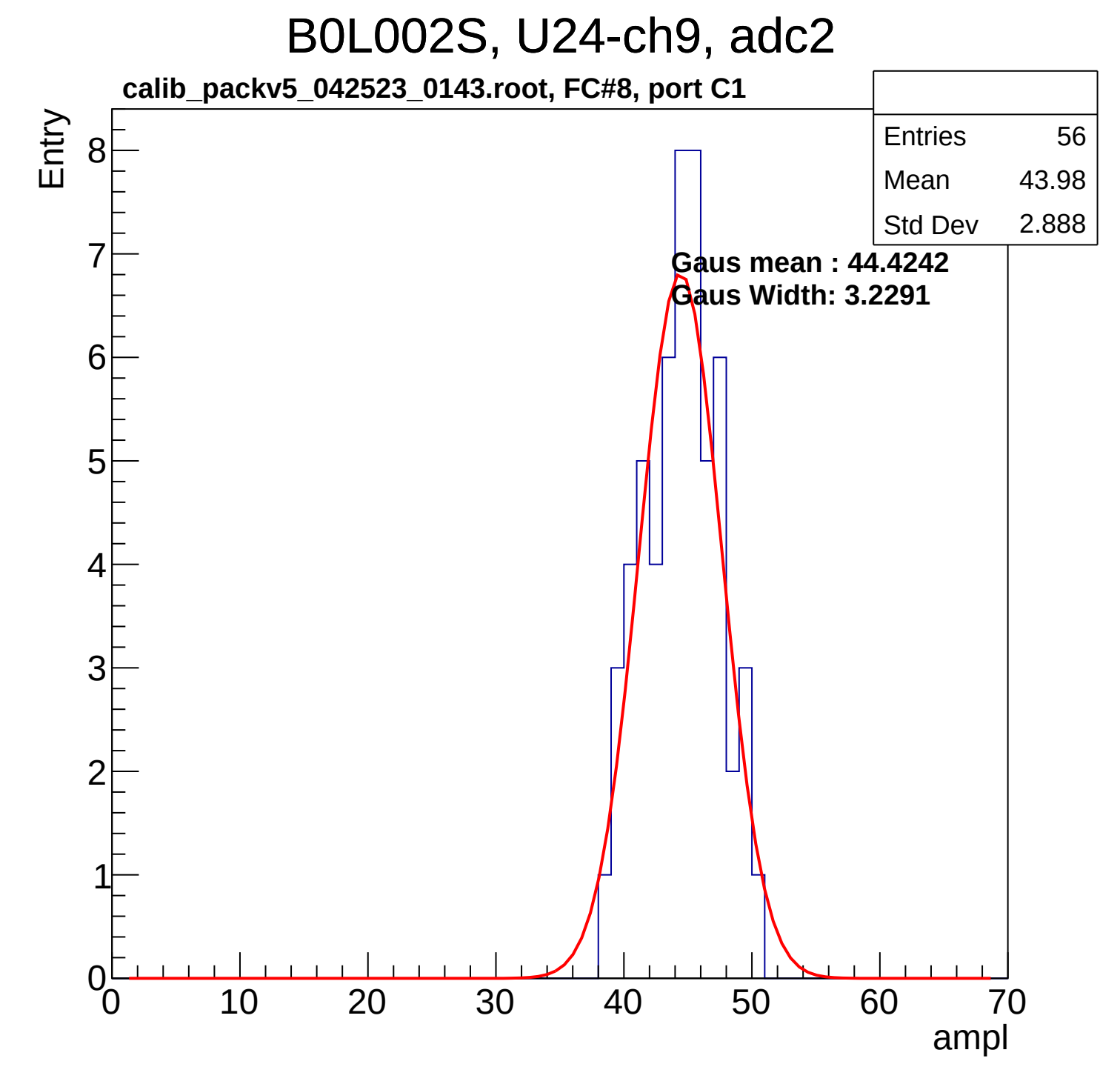

B0L002S, U24-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 36.96
Std Dev 3.723
Gaus mean : 37.9814
Gaus Width: 3.9633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 43.98
Std Dev 2.888
Gaus mean : 44.4242
Gaus Width: 3.2291
Entry
ampl
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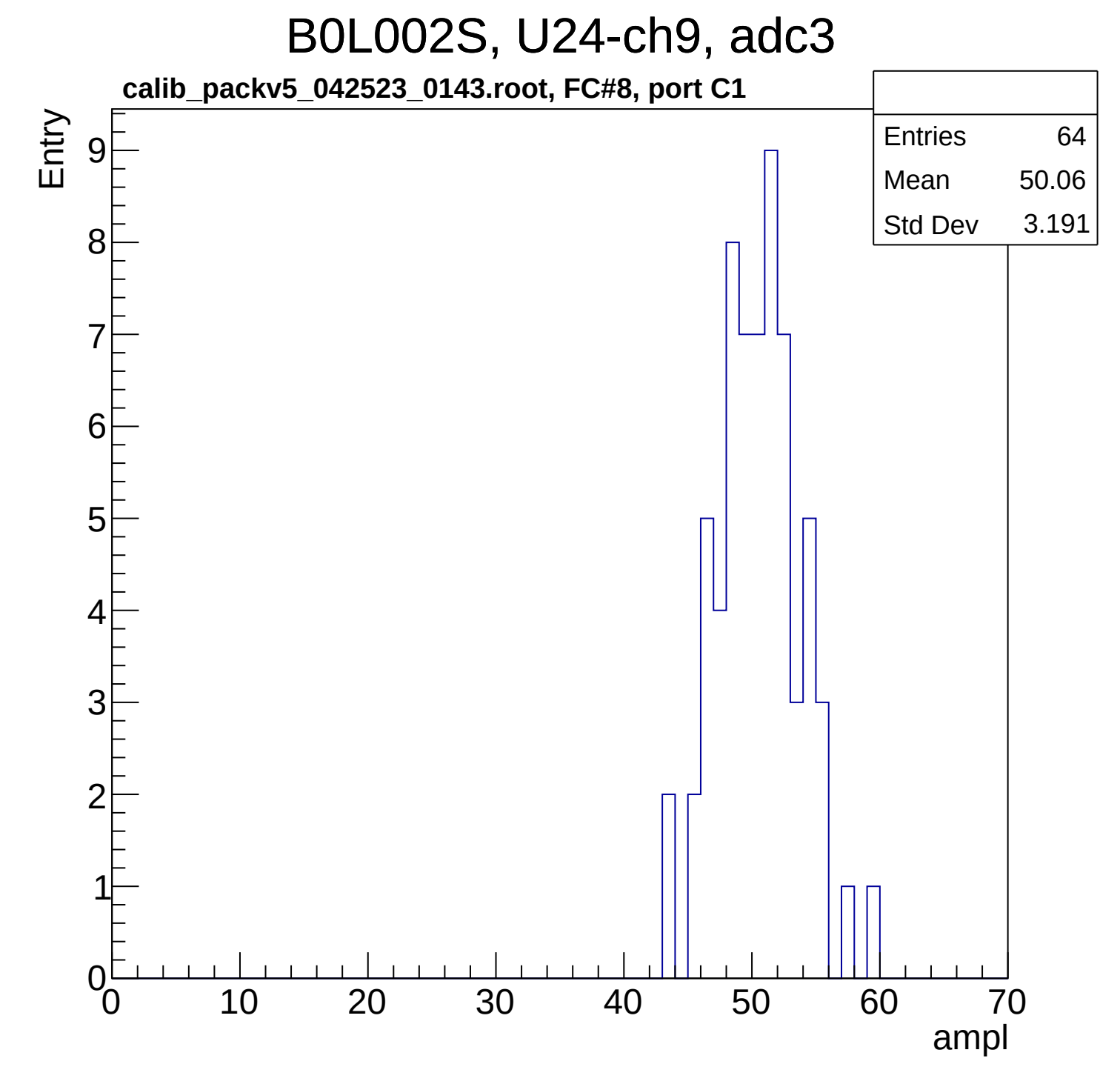
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.06
Std Dev 3.191
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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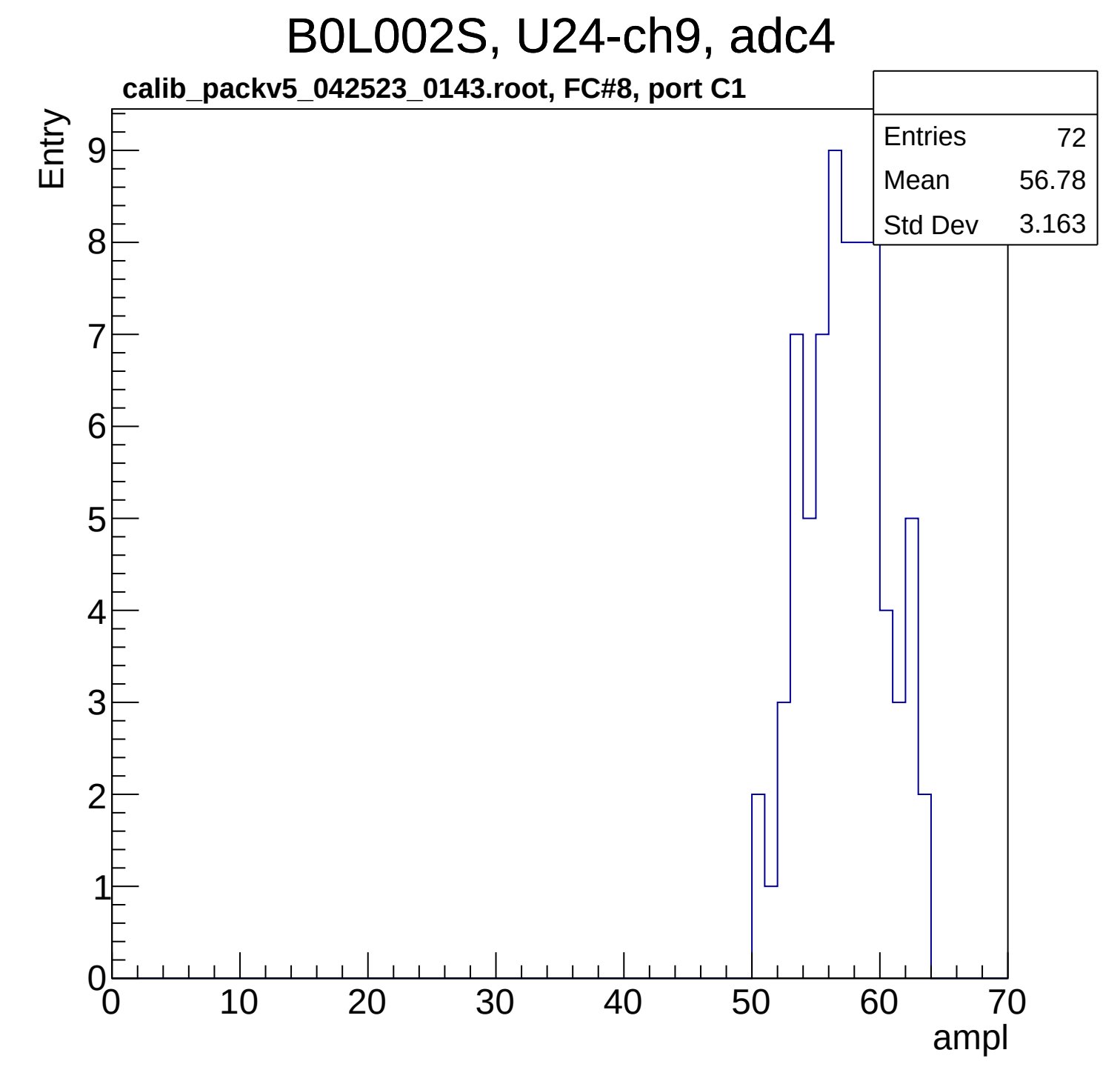
B0L002S, U24-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 56.78
Std Dev 3.163
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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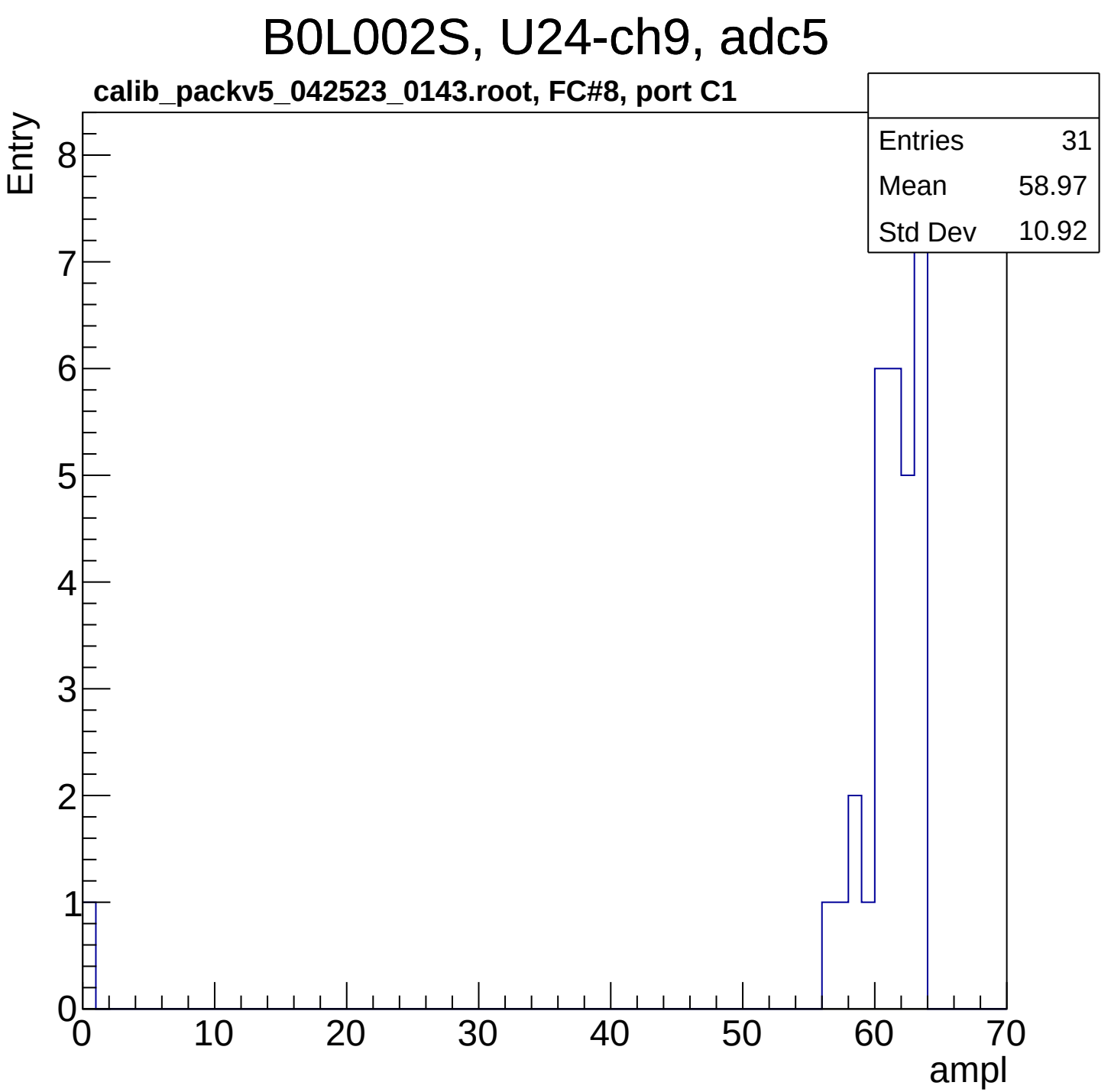
B0L002S, U24-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 58.97
Std Dev 10.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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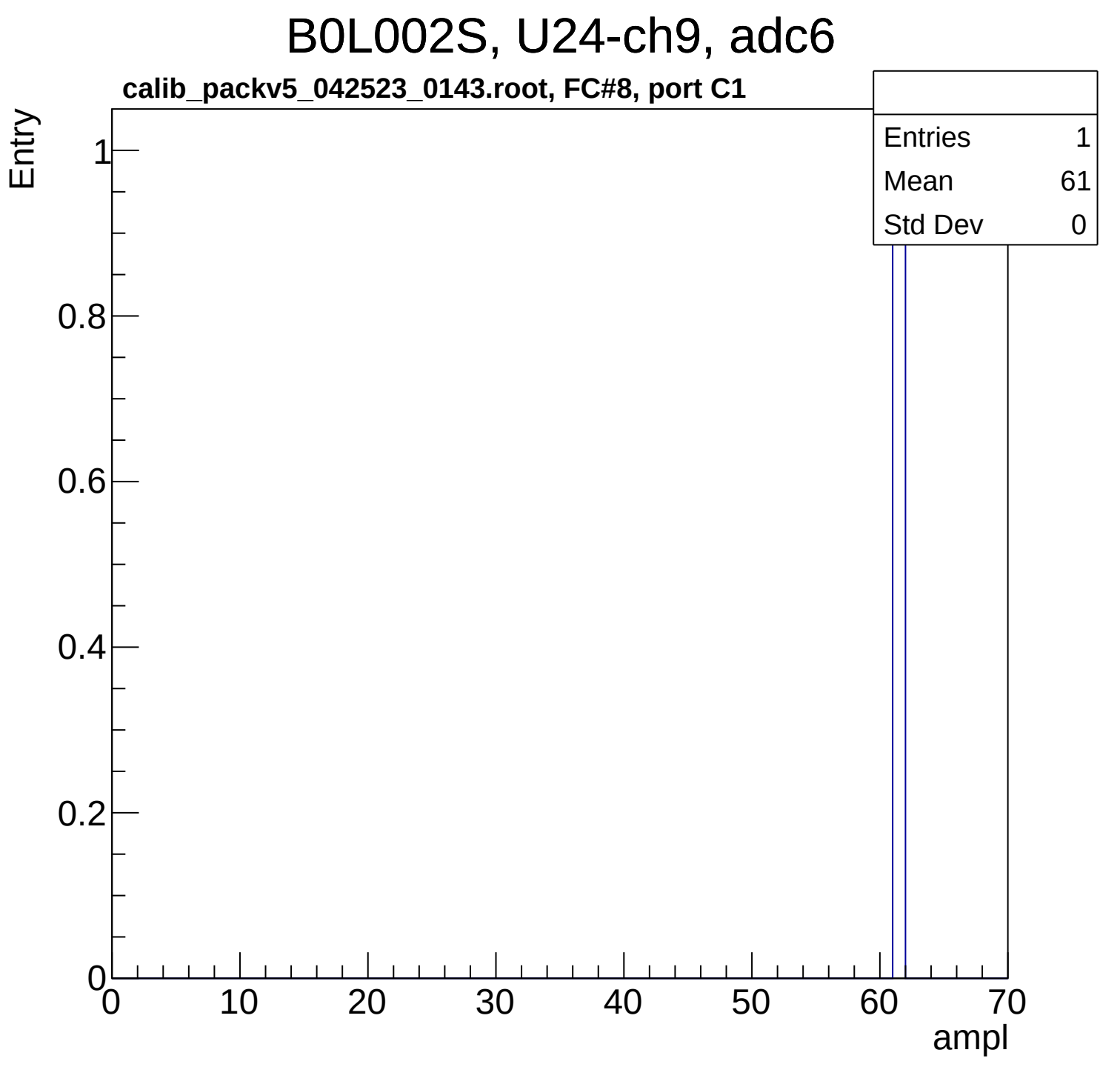
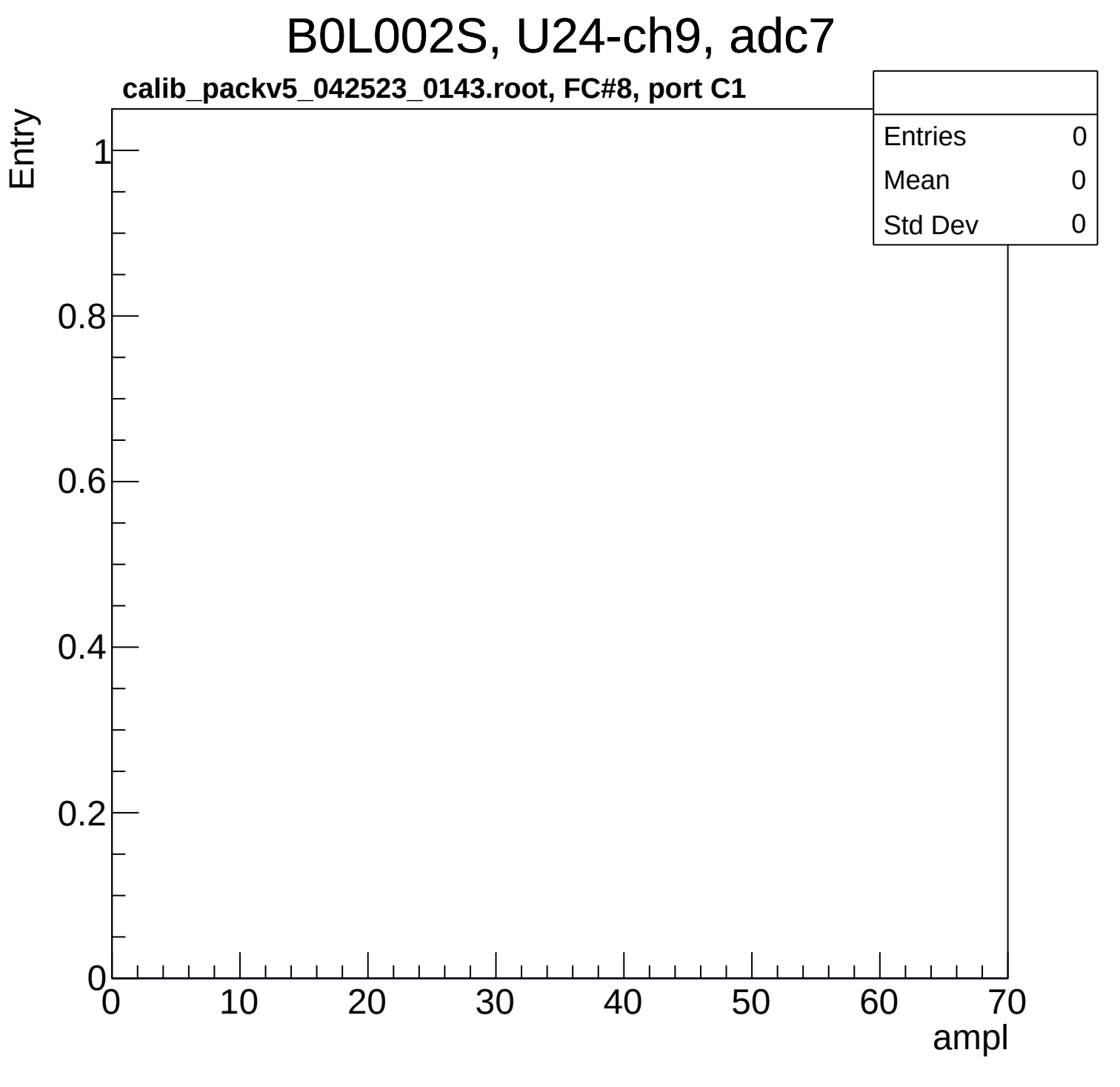
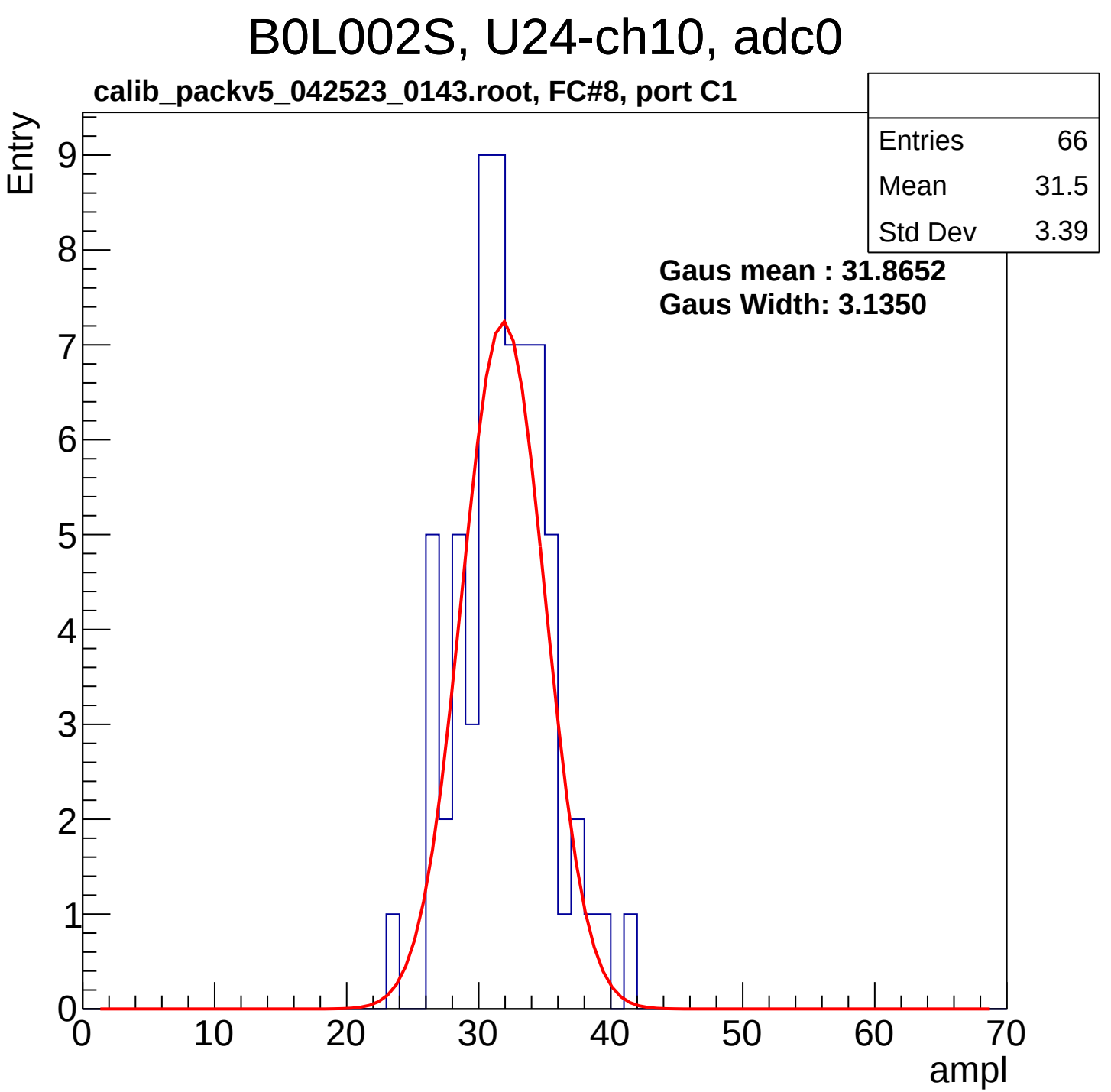

B0L002S, U24-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.5
Std Dev 3.39
Gaus mean : 31.8652
Gaus Width: 3.1350
Entry
ampl
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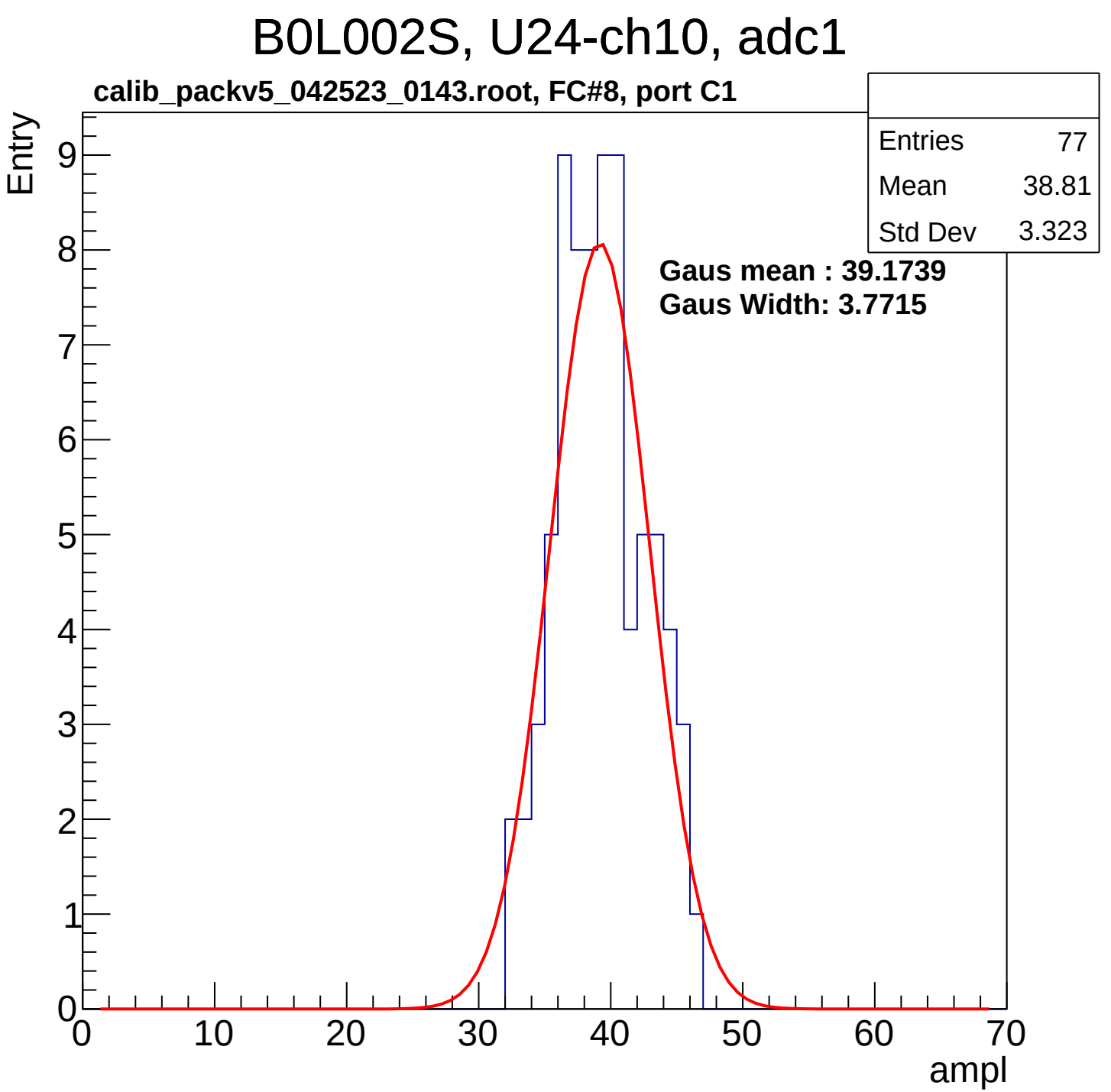
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.81
Std Dev 3.323
Gaus mean : 39.1739
Gaus Width: 3.7715
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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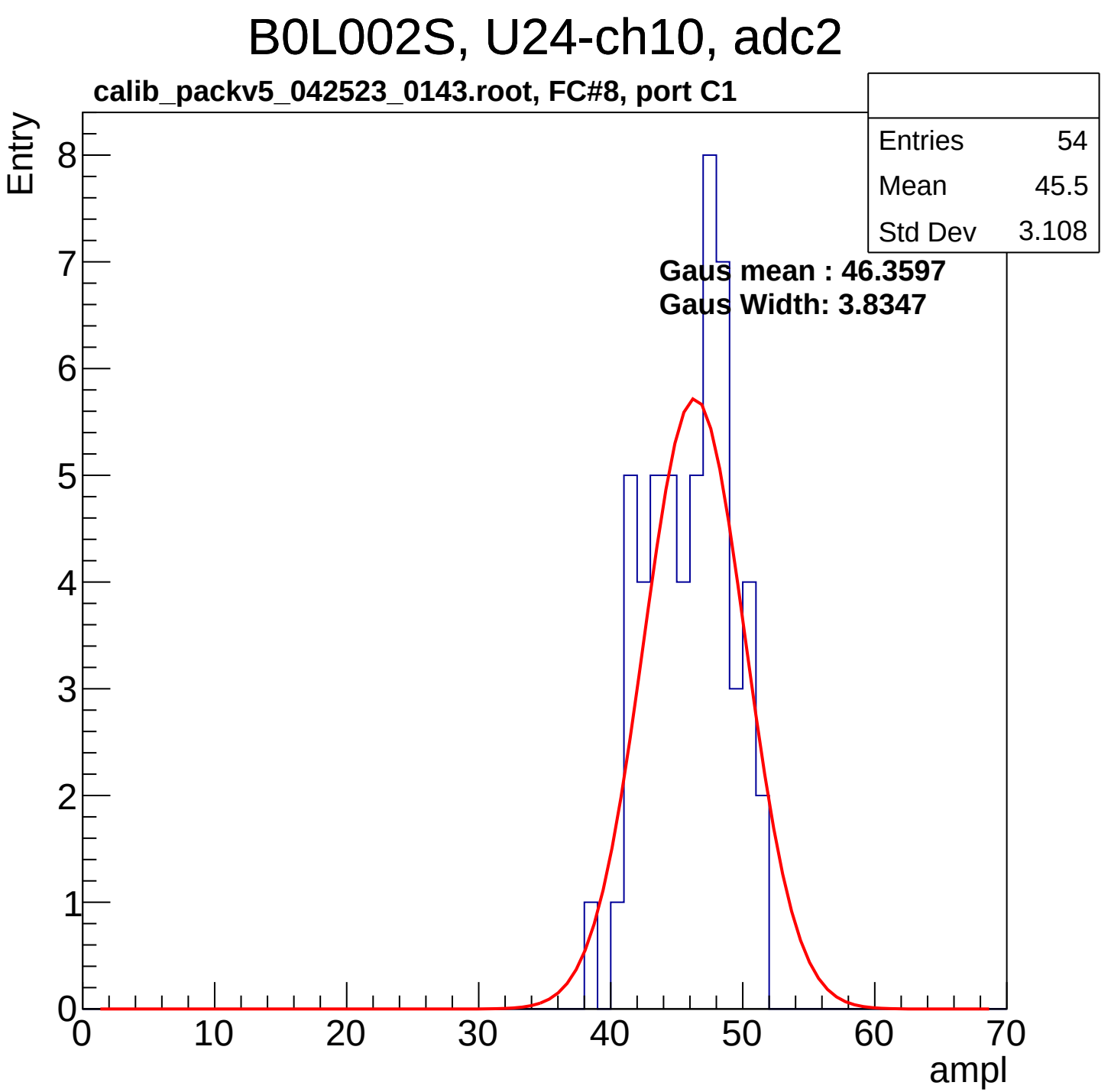
B0L002S, U24-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 45.5
Std Dev 3.108
Gaus mean : 46.3597
Gaus Width: 3.8347
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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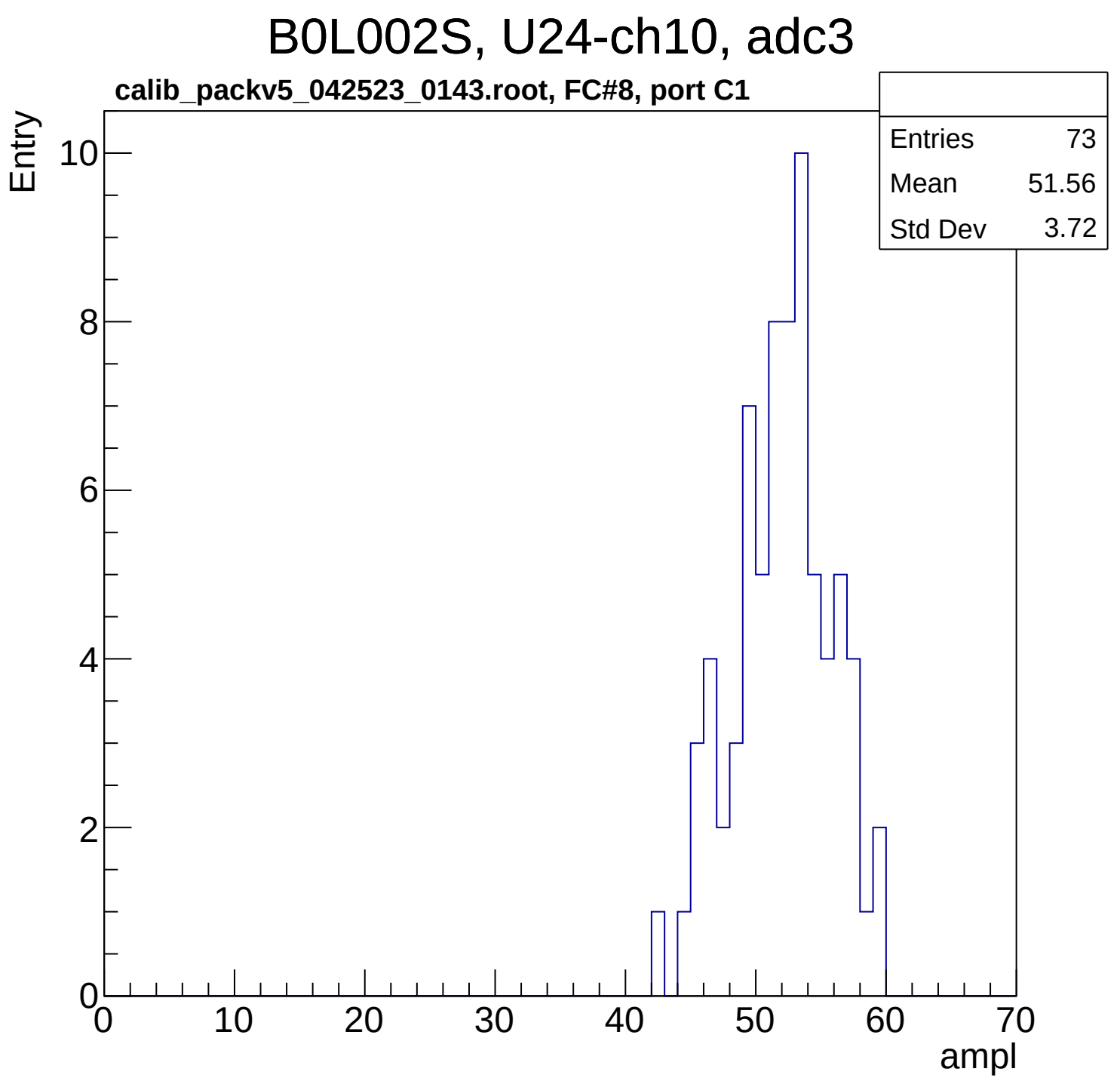
B0L002S, U24-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.56
Std Dev 3.72
Entry
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2
4
6
8
10
0
10
20
30
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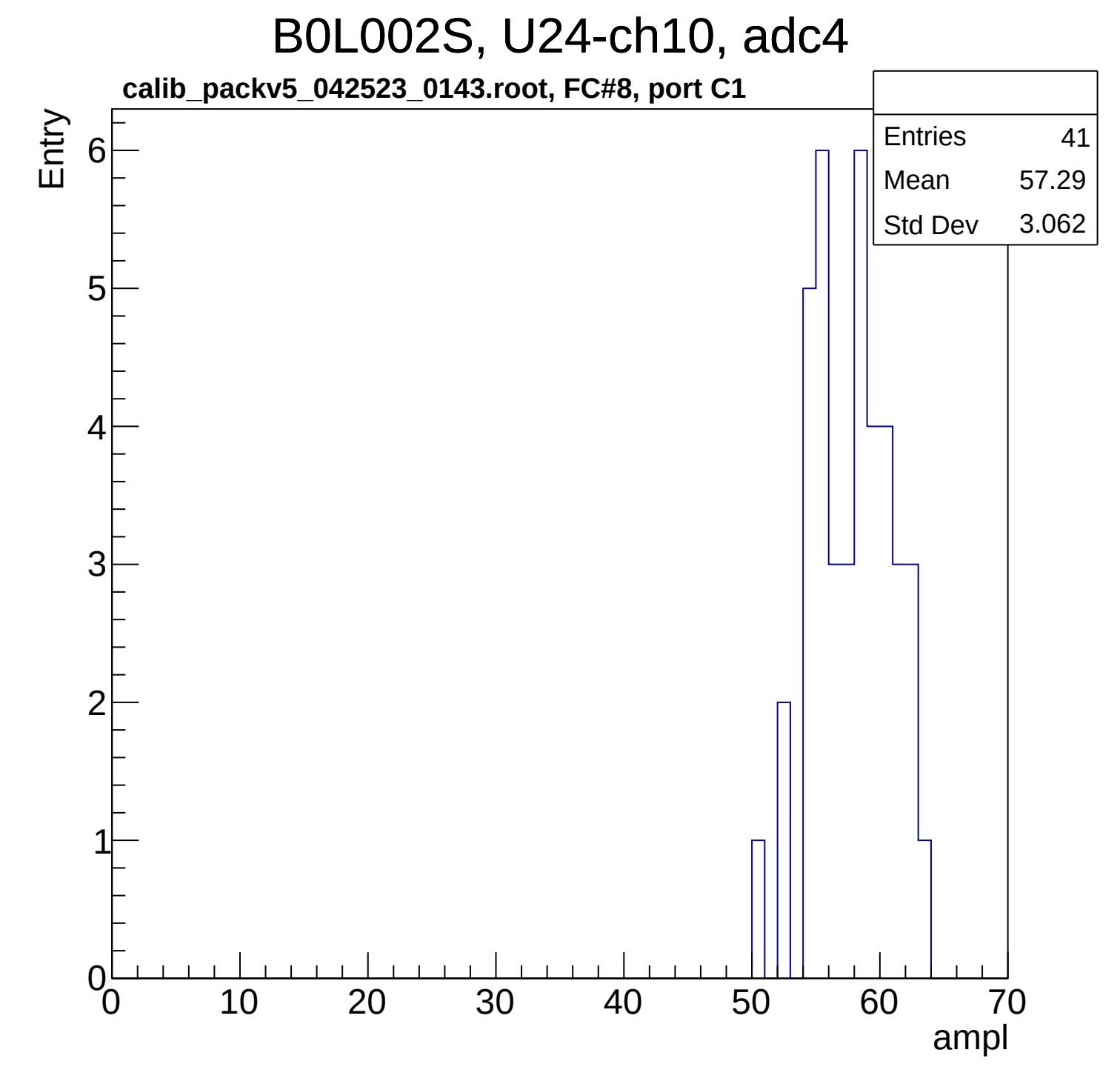
50
60
70
ampl


B0L002S, U24-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 57.29
Std Dev 3.062
Entry
0
1
2
3
4
5
6
0
10
20
30
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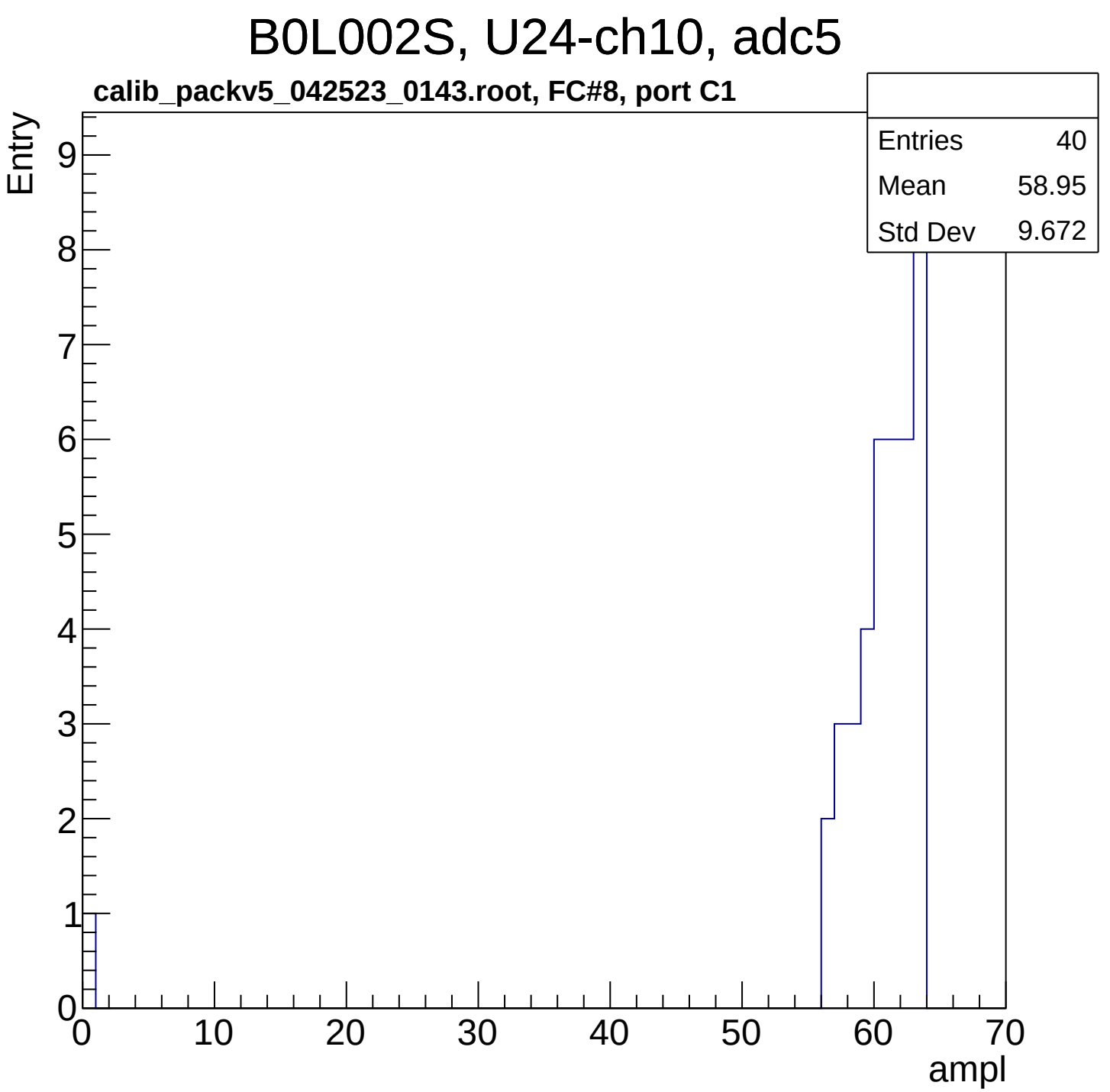
50
60
70
ampl


B0L002S, U24-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.95
Std Dev 9.672
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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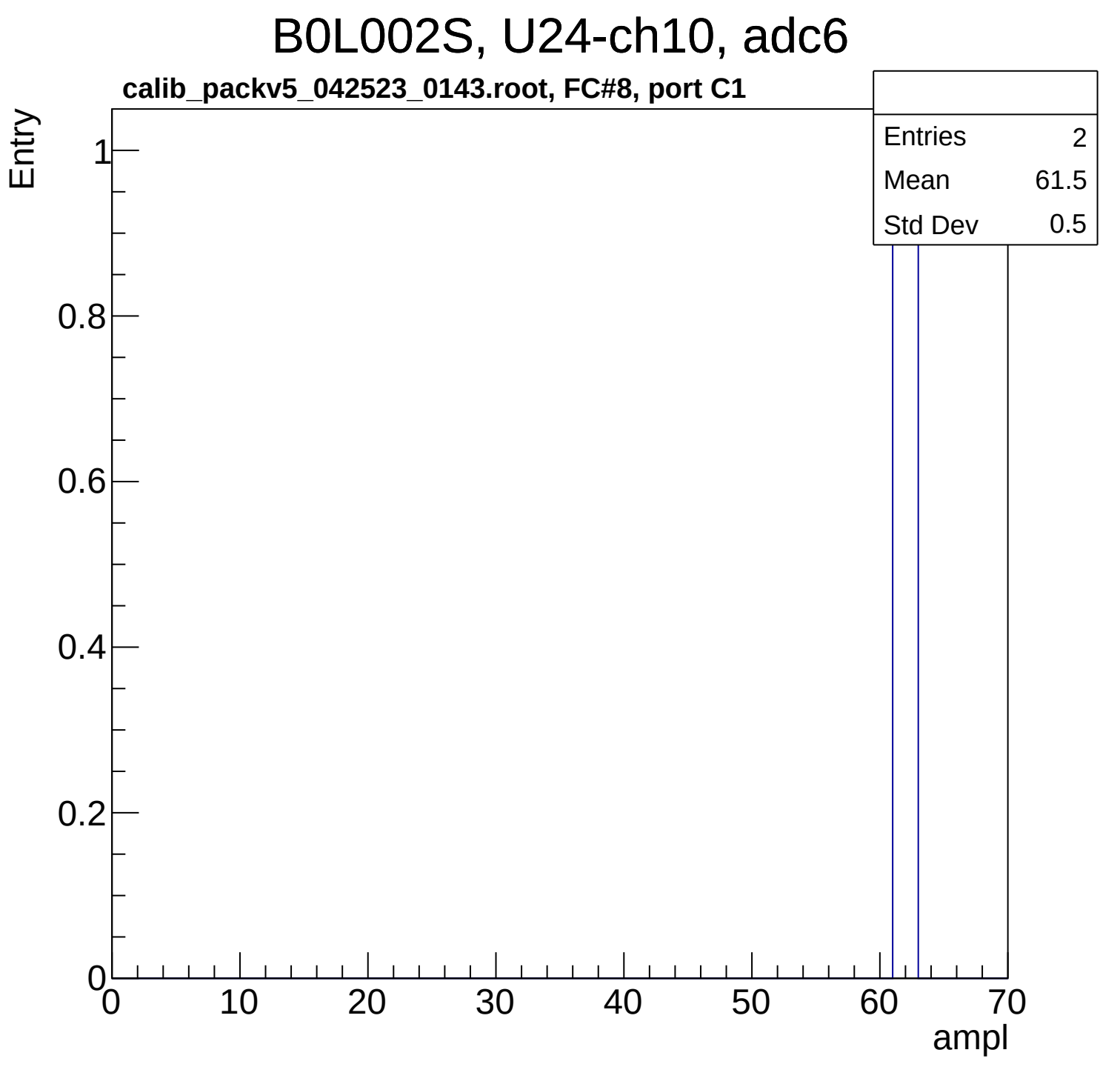
B0L002S, U24-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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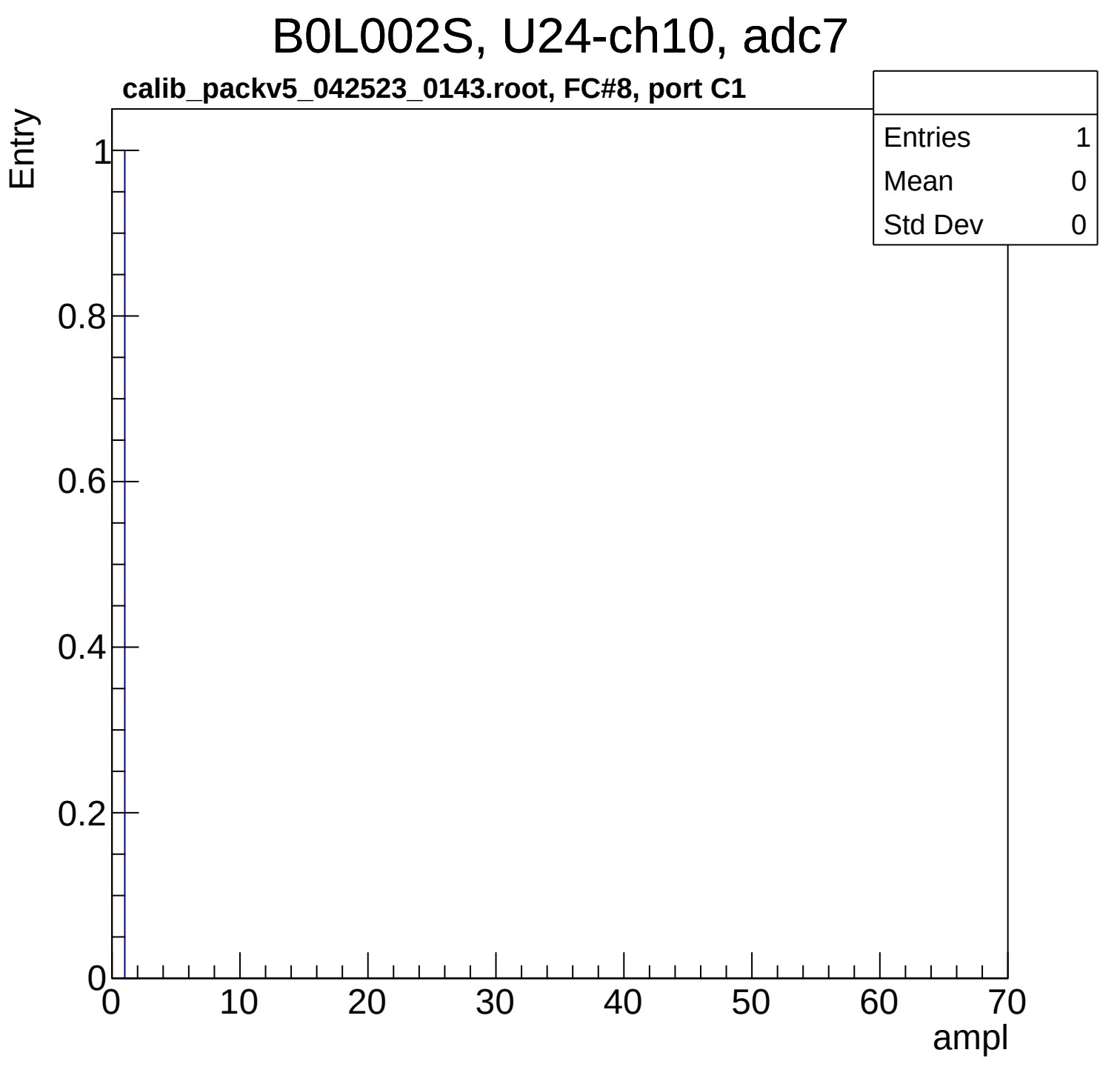
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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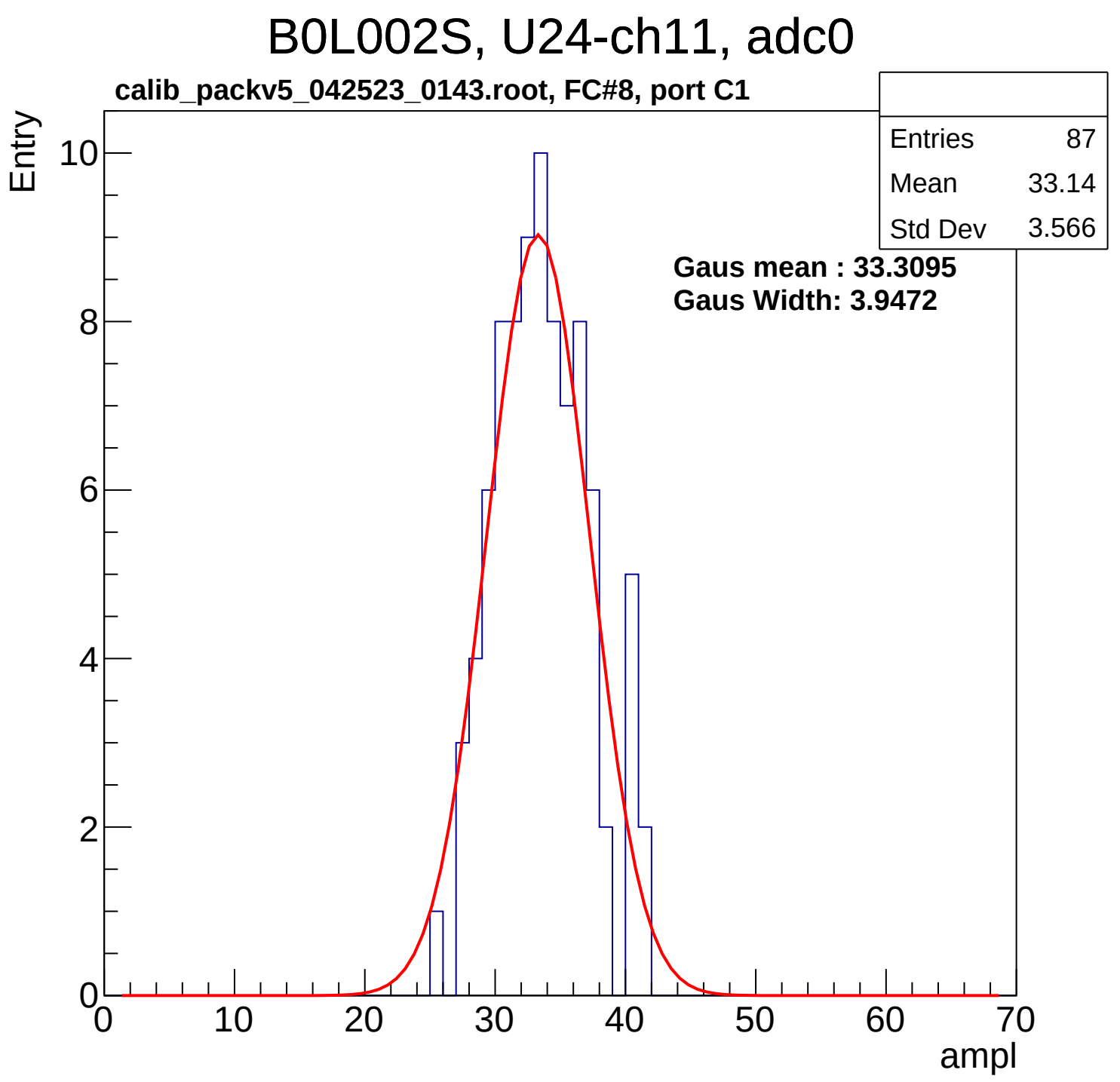
50
60
70
ampl


B0L002S, U24-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 33.14
Std Dev 3.566
Gaus mean : 33.3095
Gaus Width: 3.9472
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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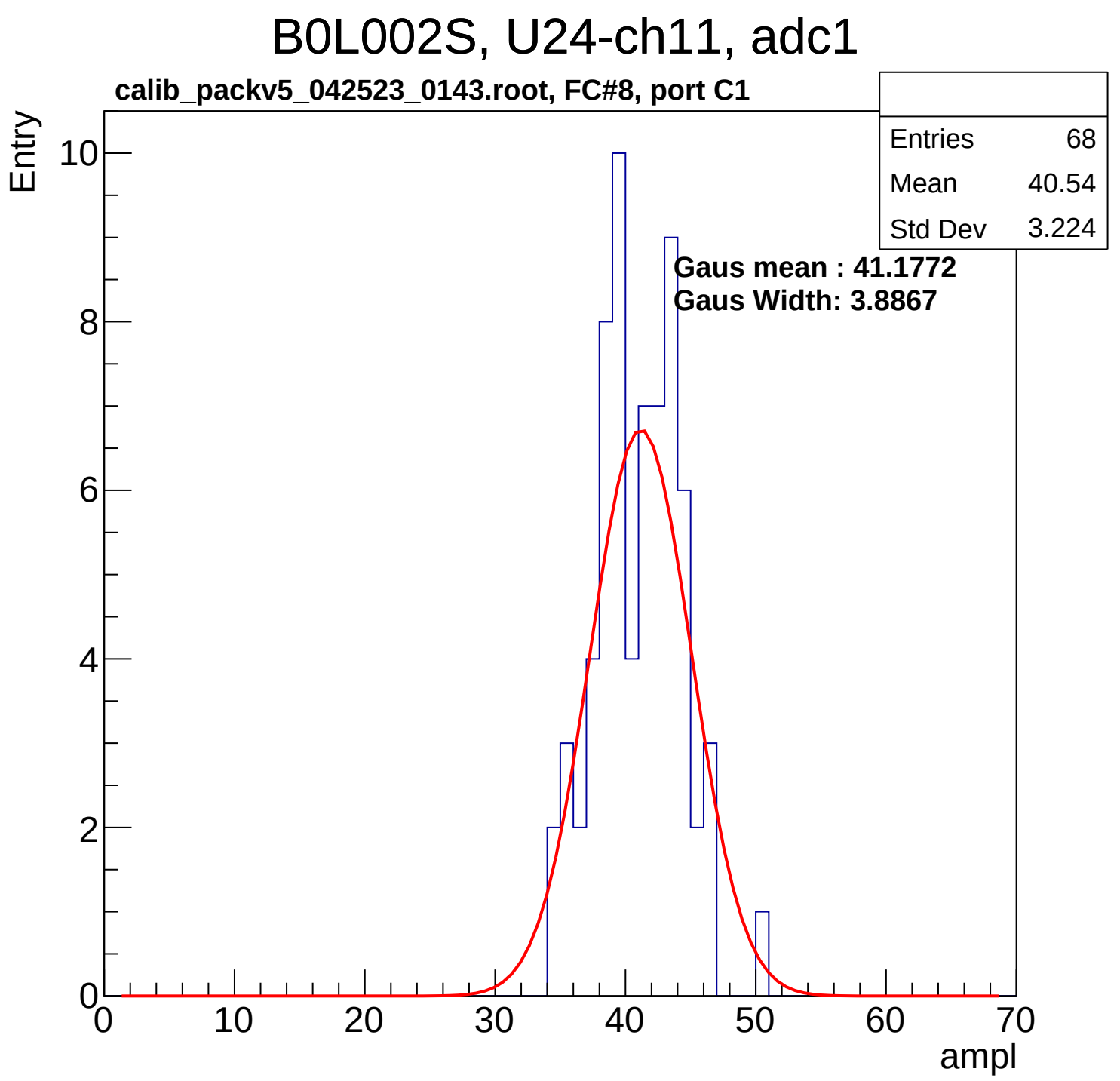
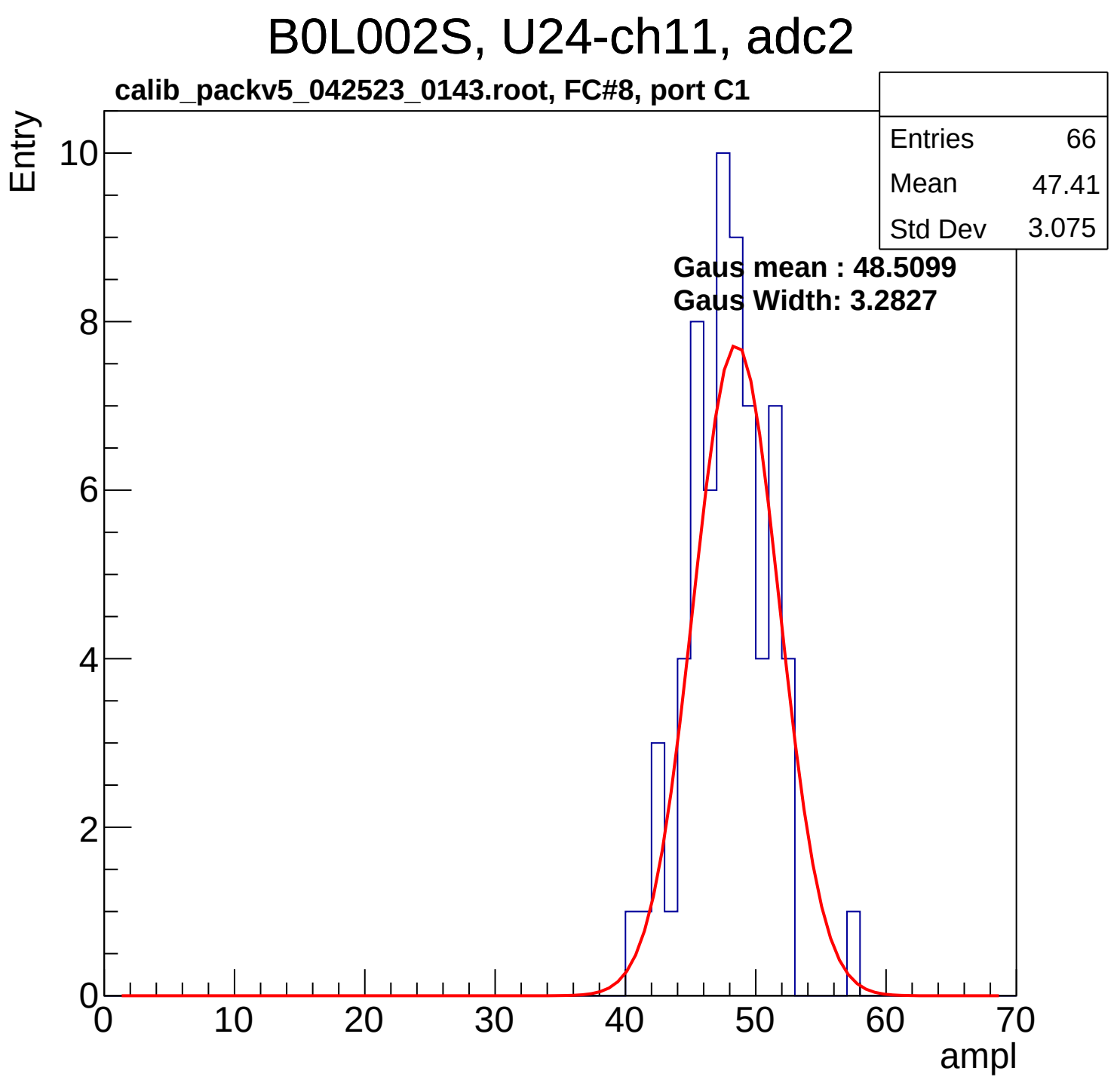

B0L002S, U24-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 40.54
Std Dev 3.224
Gaus mean : 41.1772
Gaus Width: 3.8867
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 47.41
Std Dev 3.075
Gaus mean : 48.5099
Gaus Width: 3.2827
Entry
ampl
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4
6
8
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0
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40
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60
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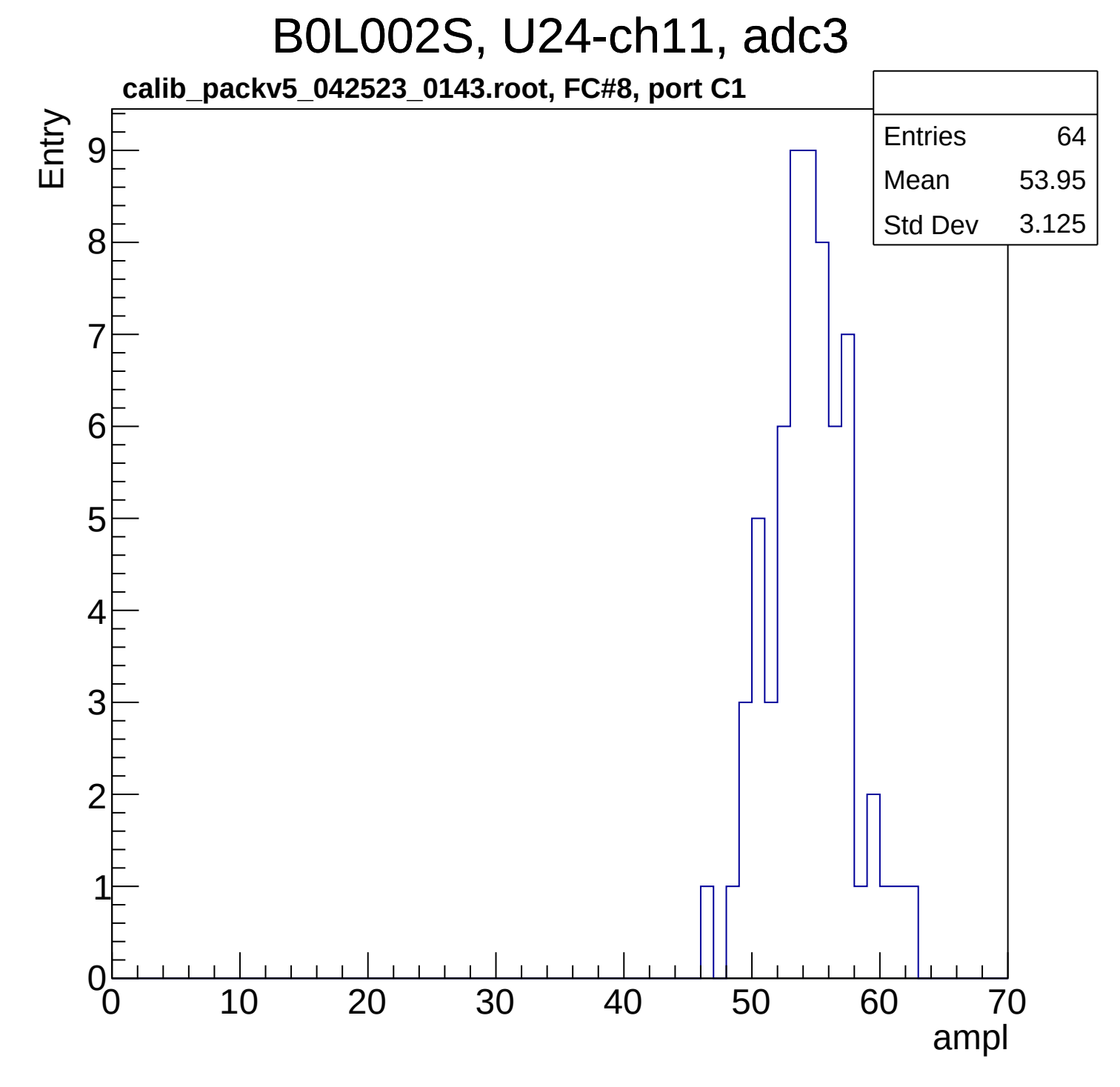
B0L002S, U24-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 53.95
Std Dev 3.125
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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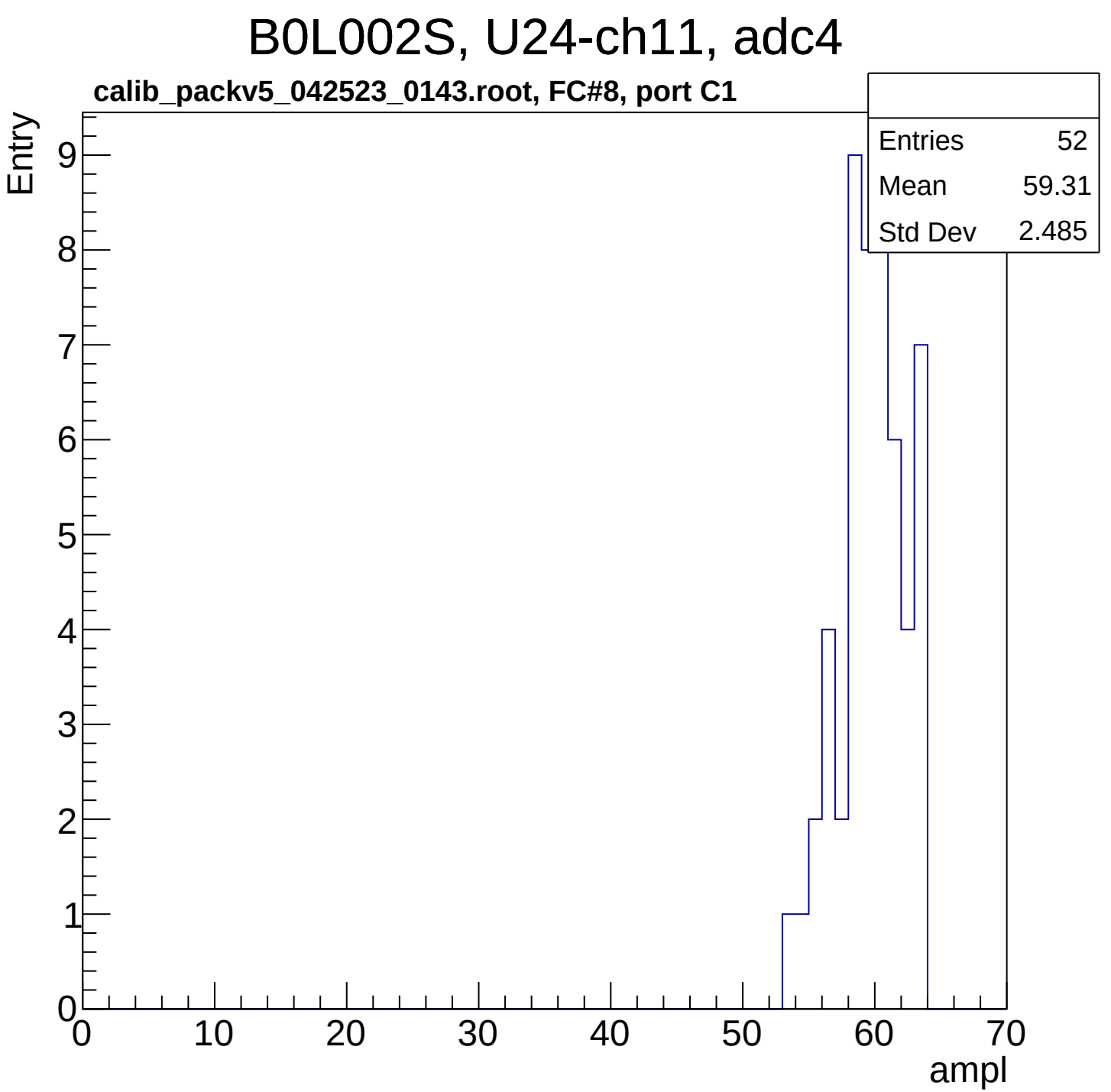
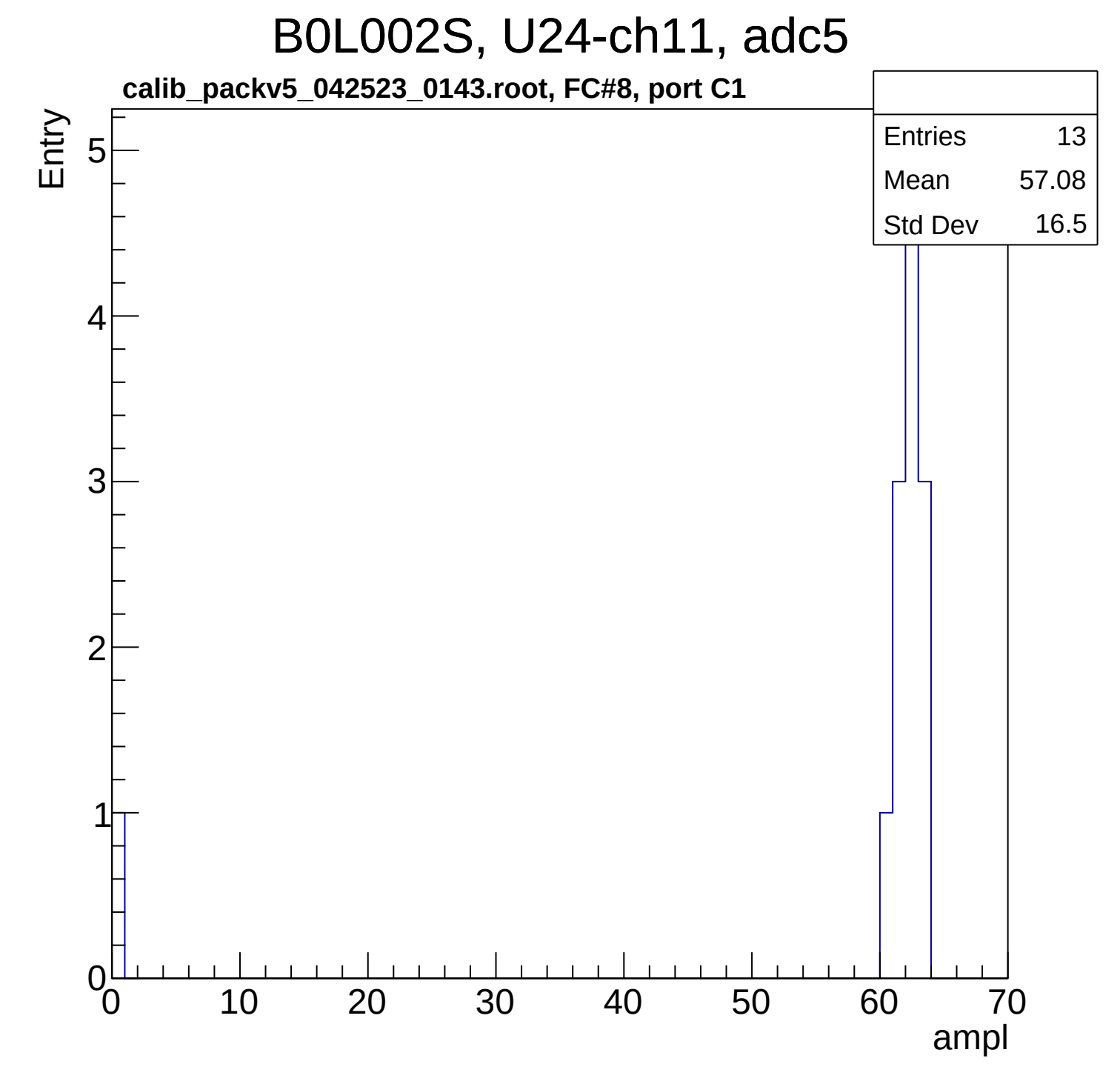

B0L002S, U24-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 59.31
Std Dev 2.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 13
Mean 57.08
Std Dev 16.5
Entry
0
1
2
3
4
5
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10
20
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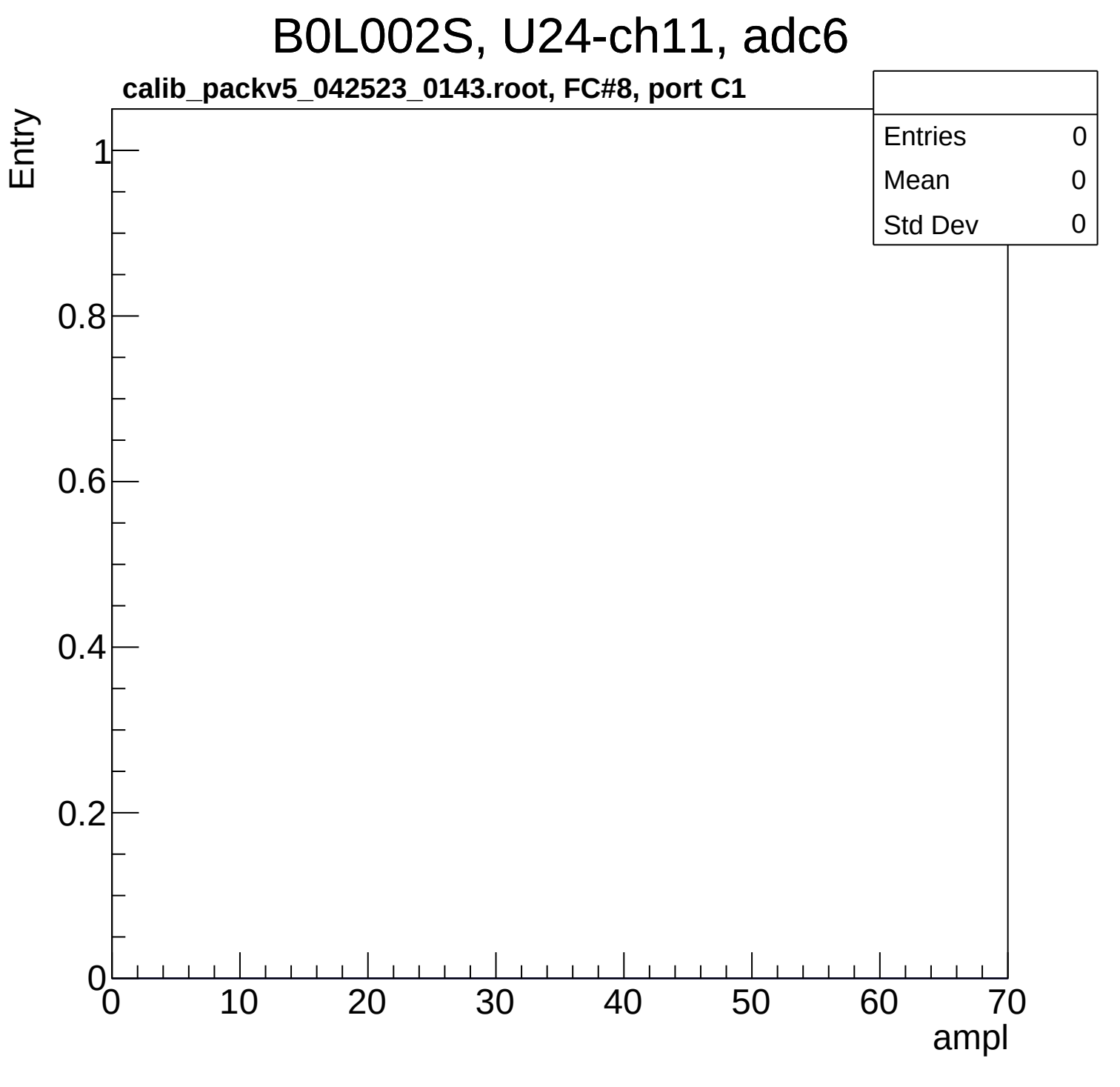
50
60
70
ampl


B0L002S, U24-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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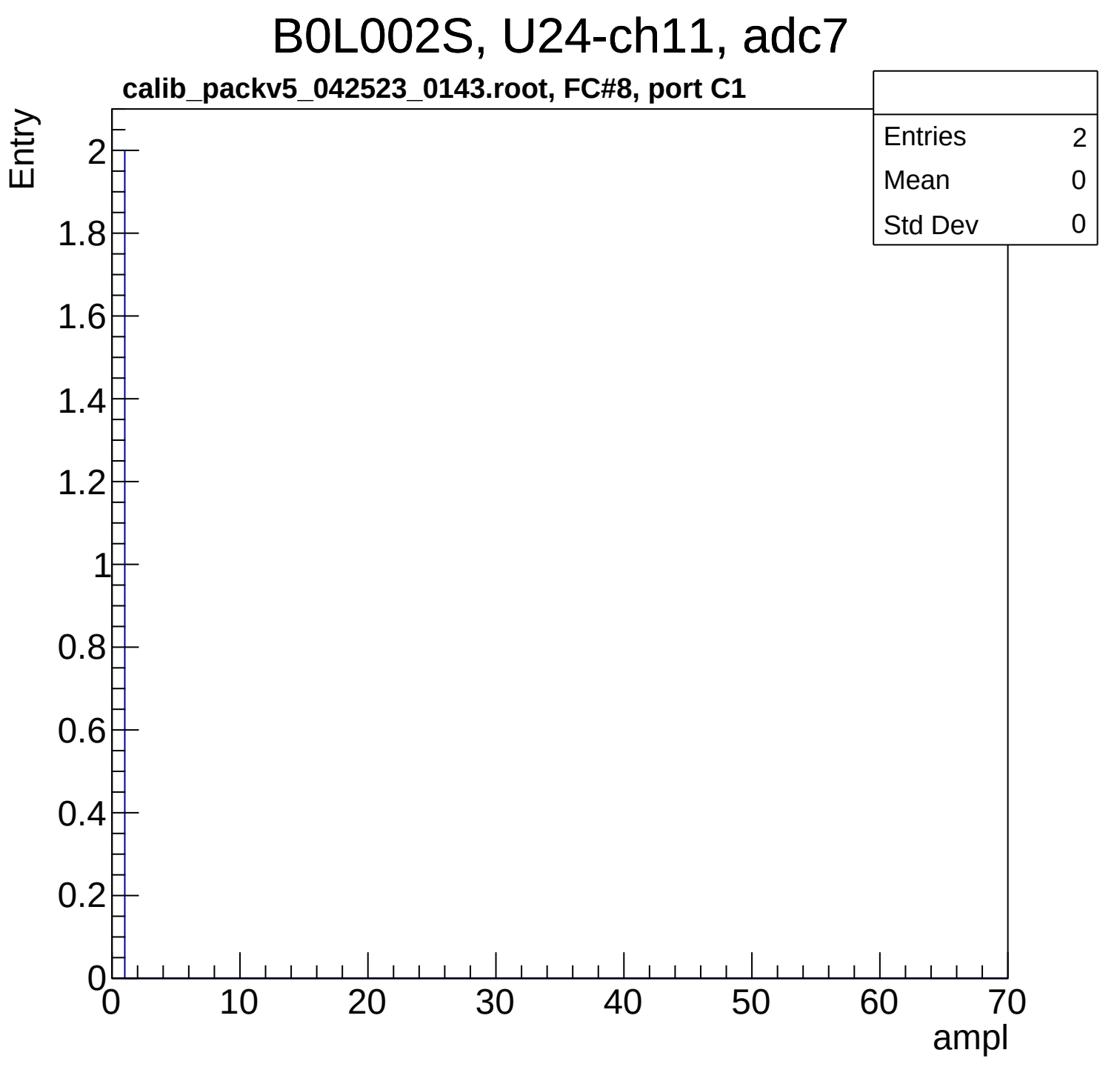
40
50
60
70
ampl


B0L002S, U24-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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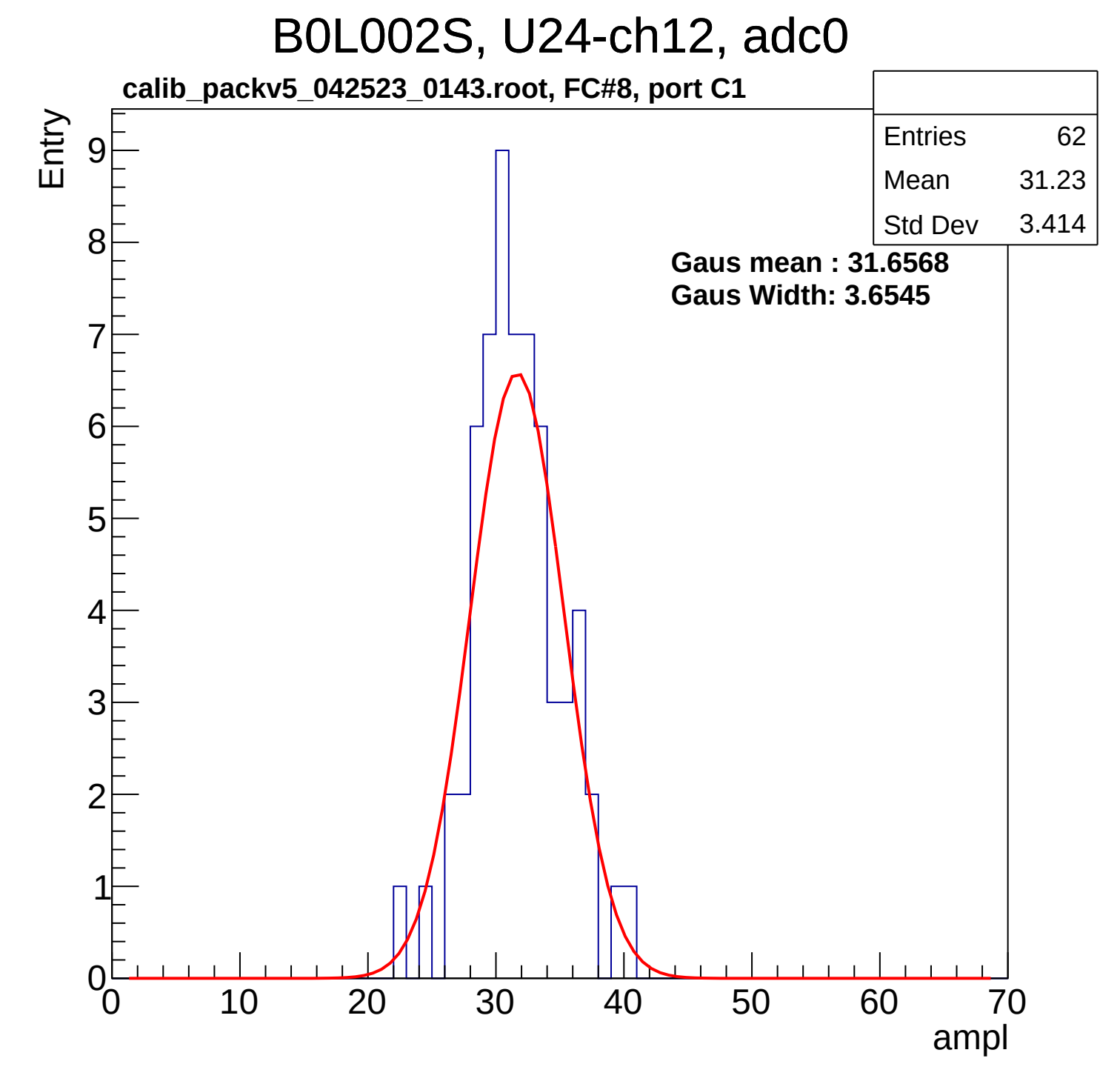
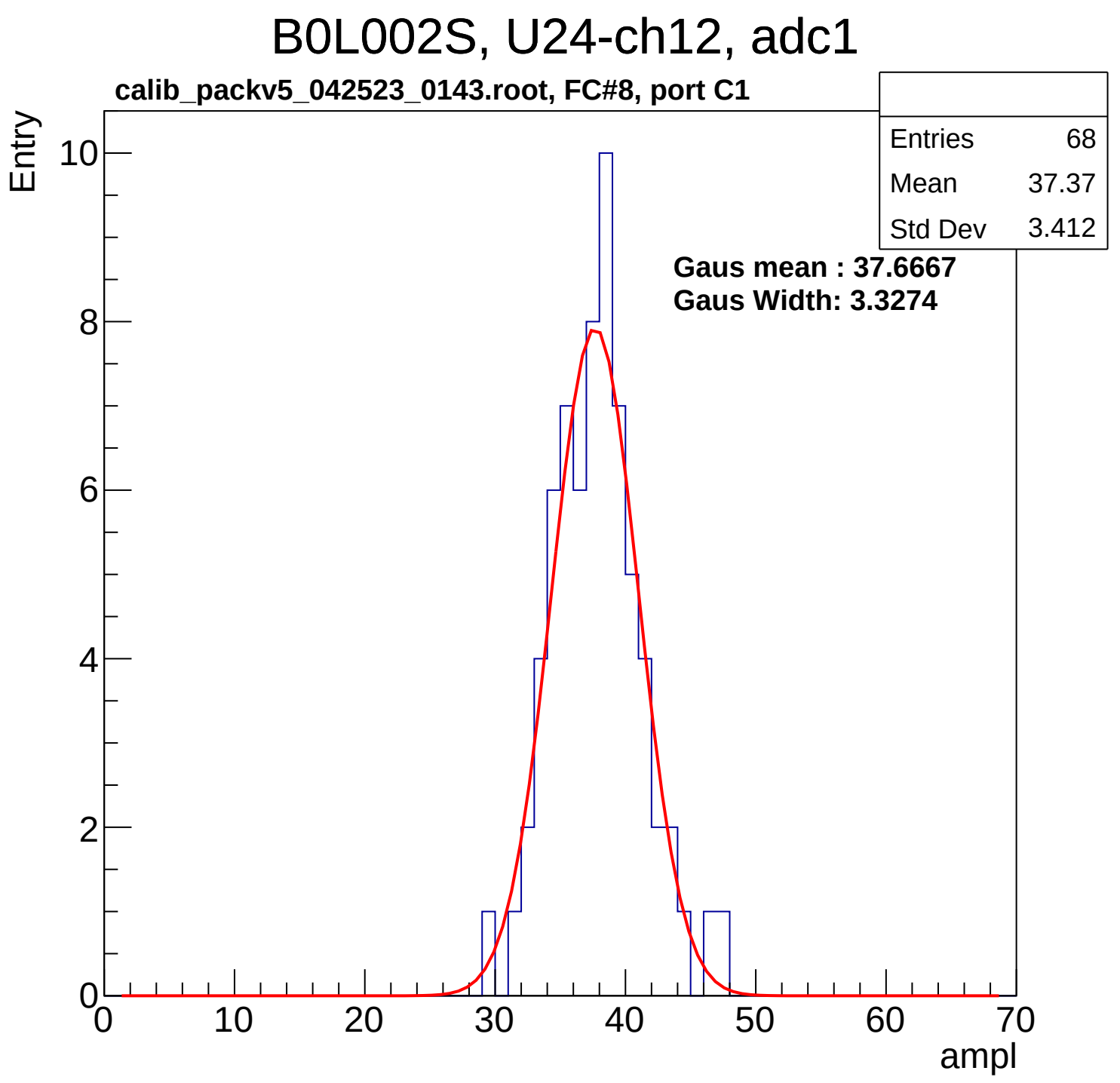
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.23
Std Dev 3.414
Gaus mean : 31.6568
Gaus Width: 3.6545
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U24-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.37
Std Dev 3.412
Gaus mean : 37.6667
Gaus Width: 3.3274
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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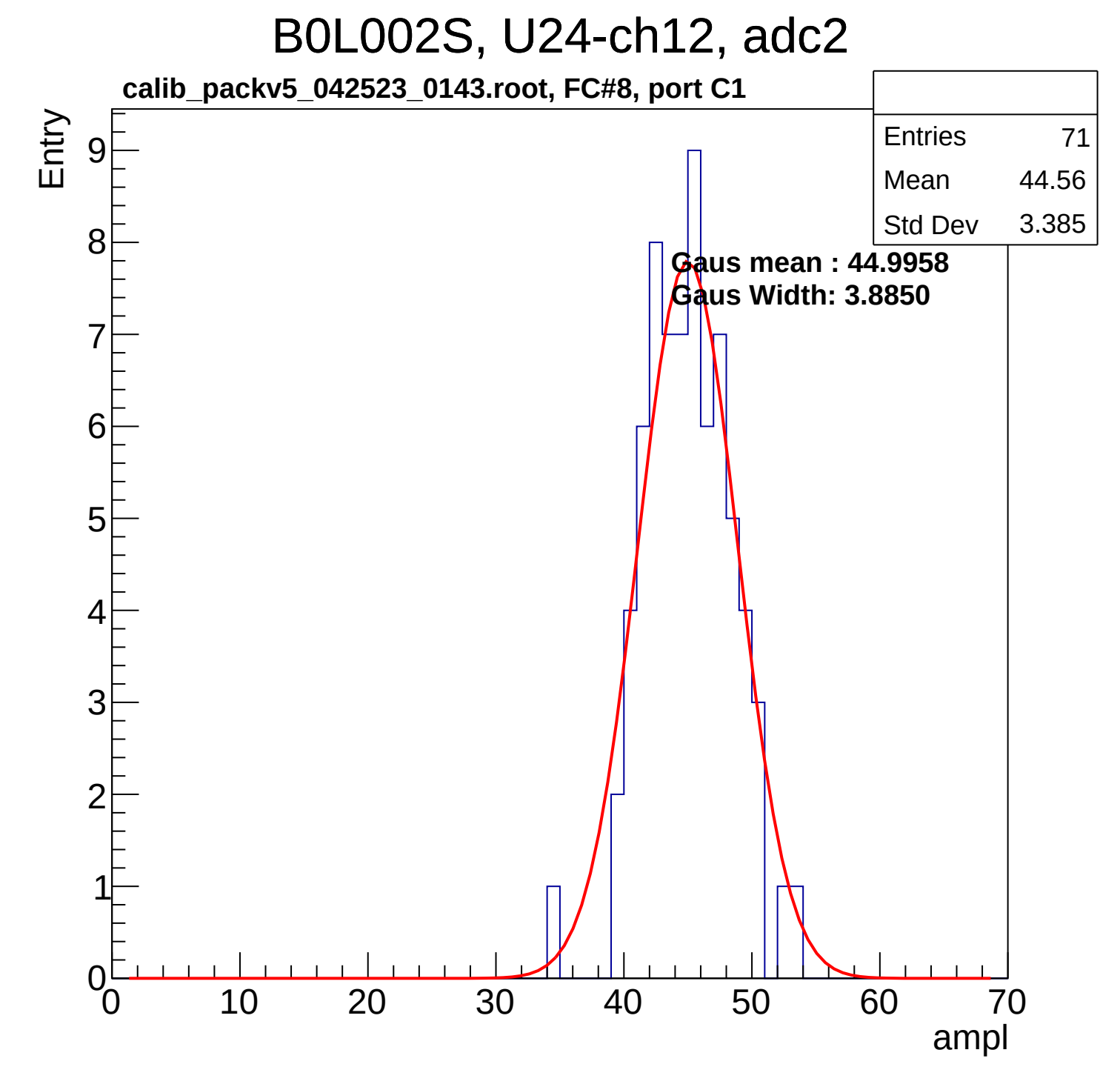
B0L002S, U24-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 44.56
Std Dev 3.385
Gaus mean : 44.9958
Gaus Width: 3.8850
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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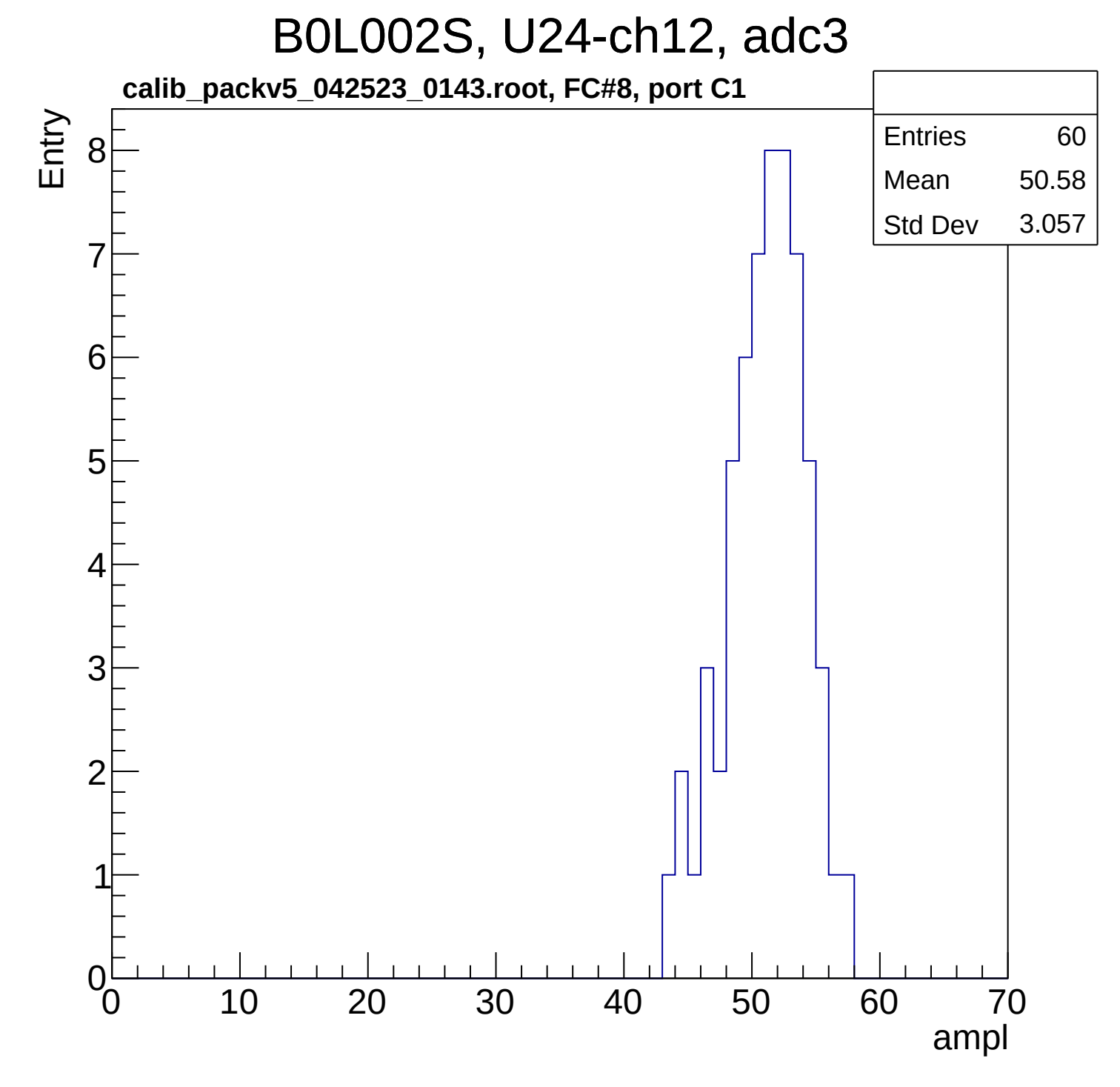
B0L002S, U24-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.58
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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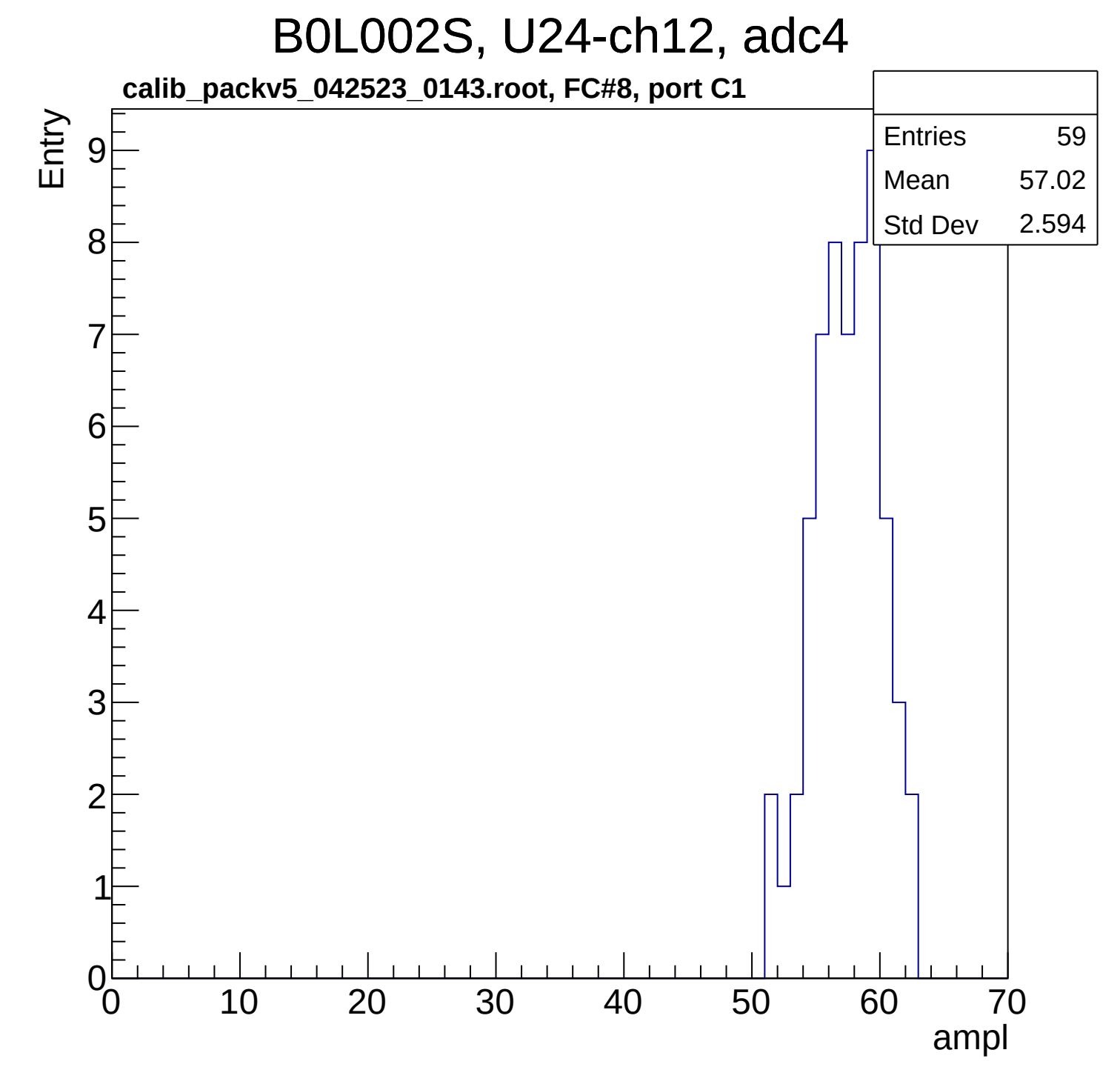
B0L002S, U24-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.02
Std Dev 2.594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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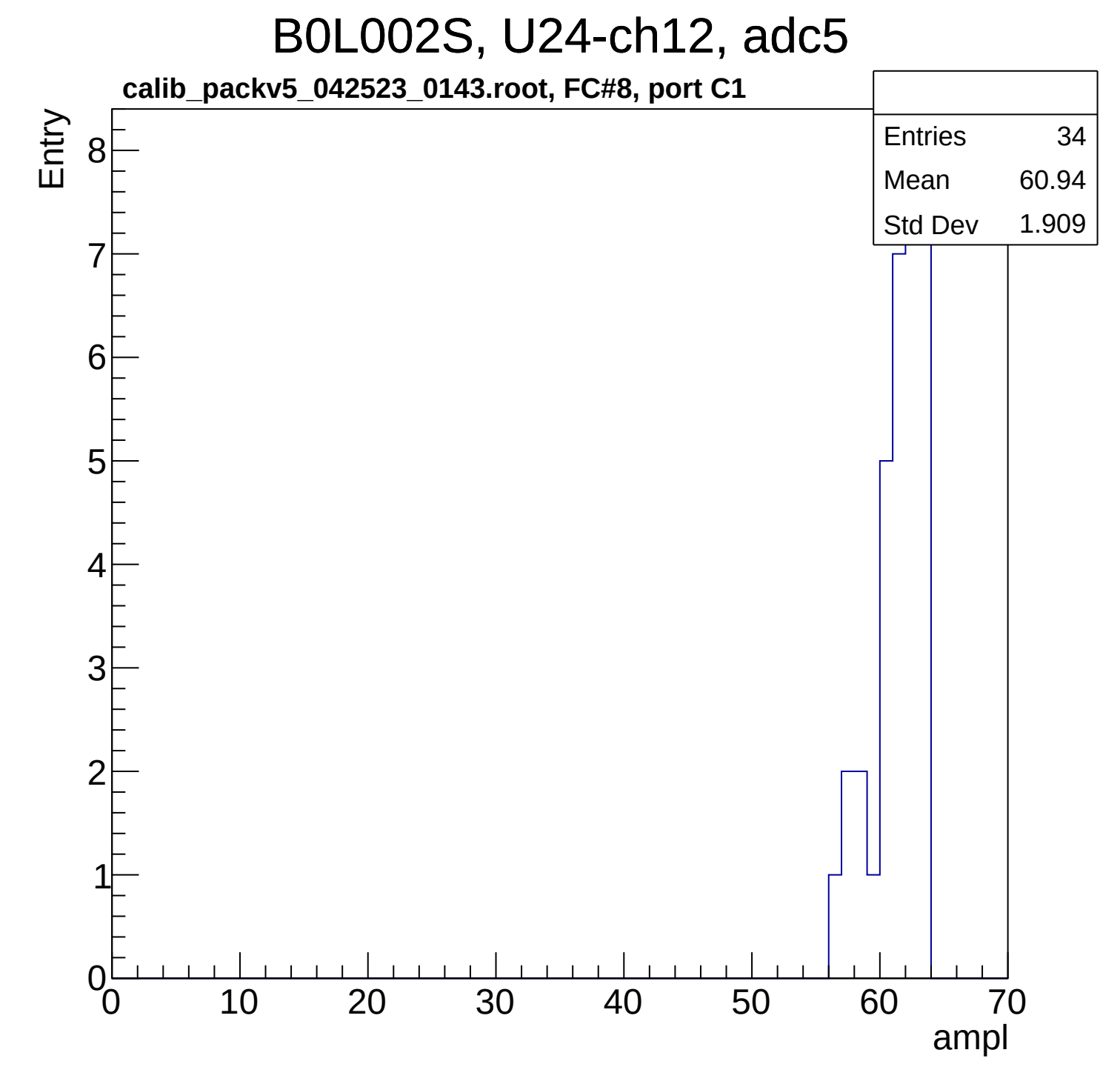
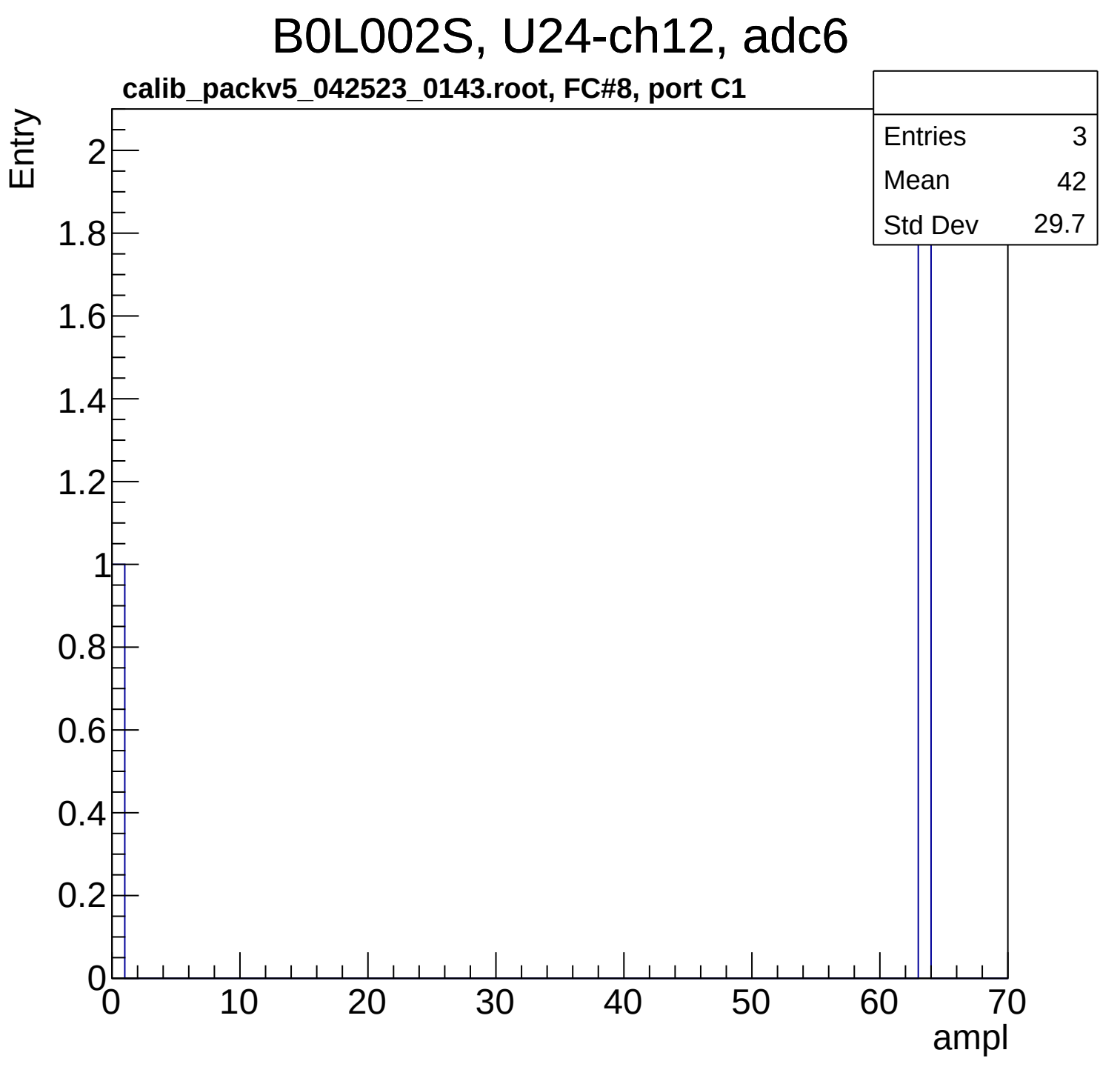
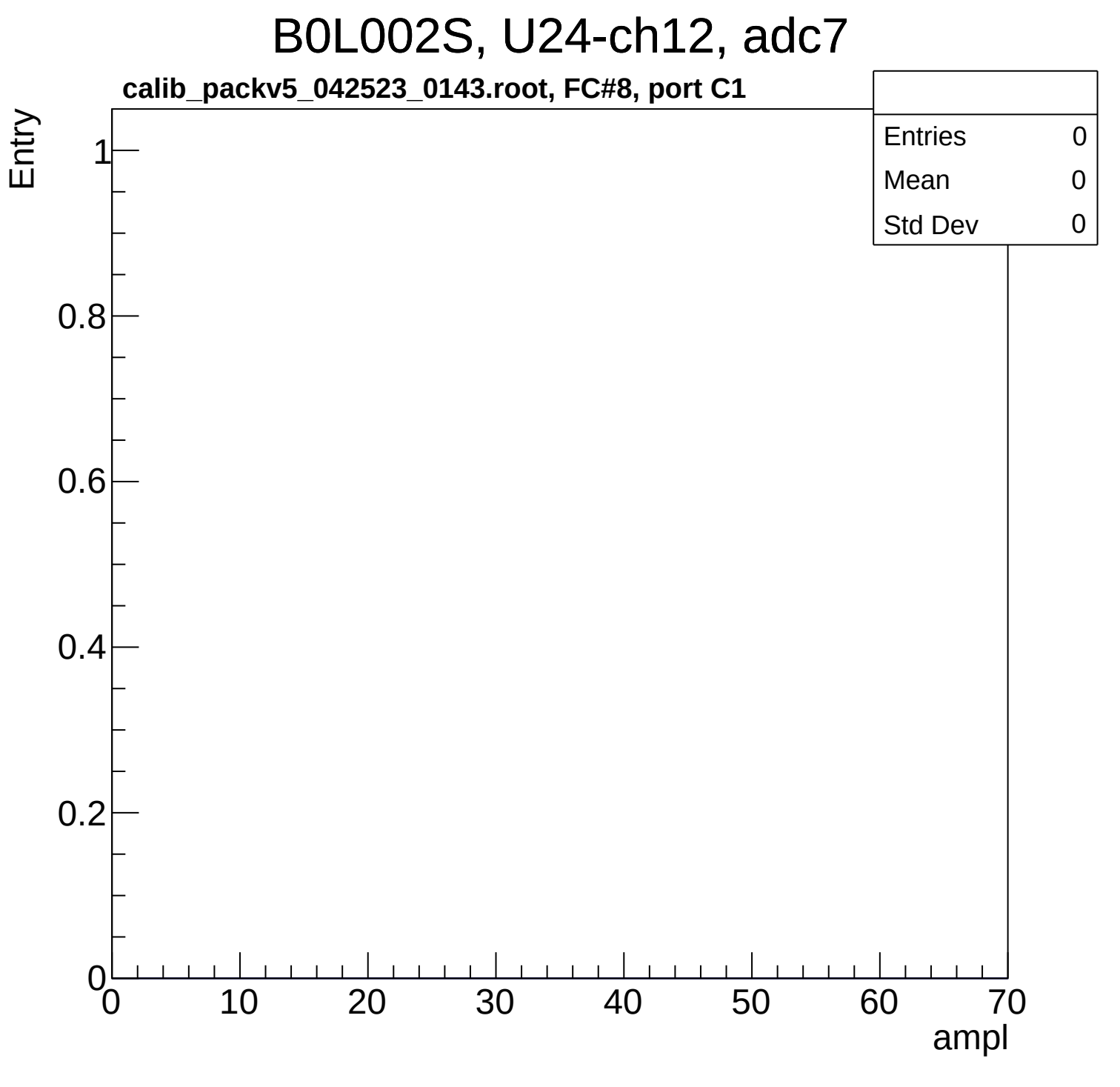
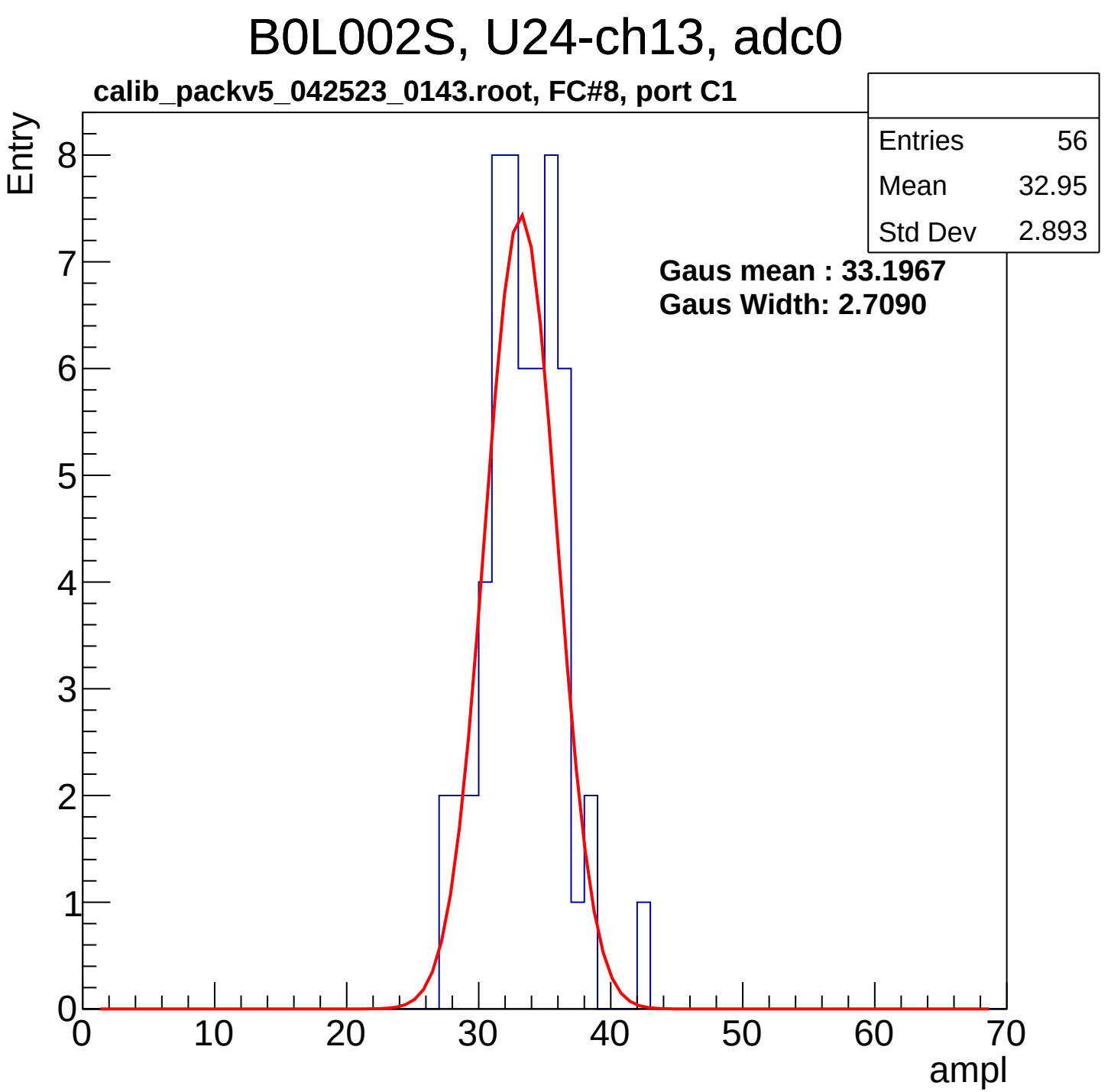
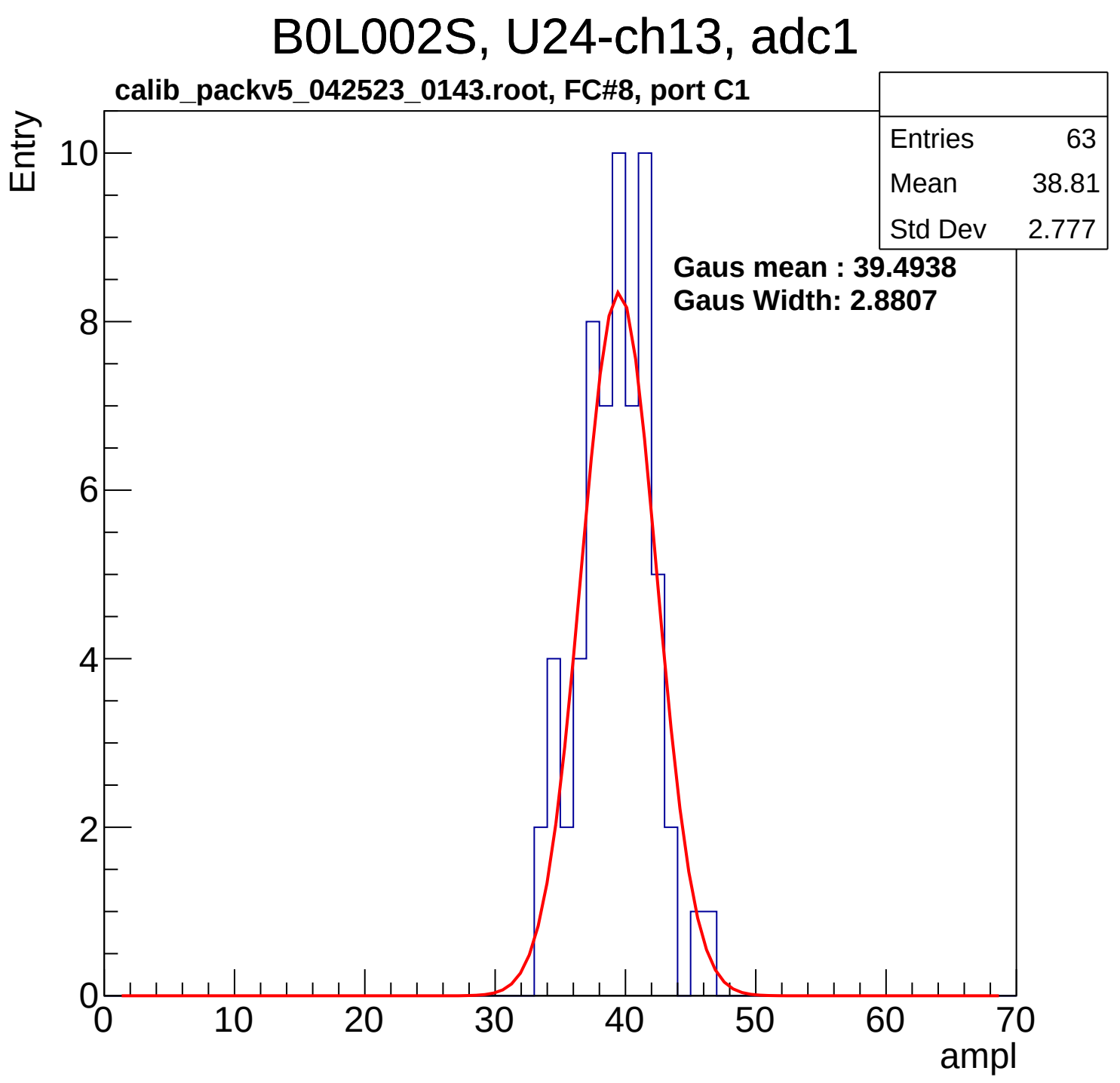
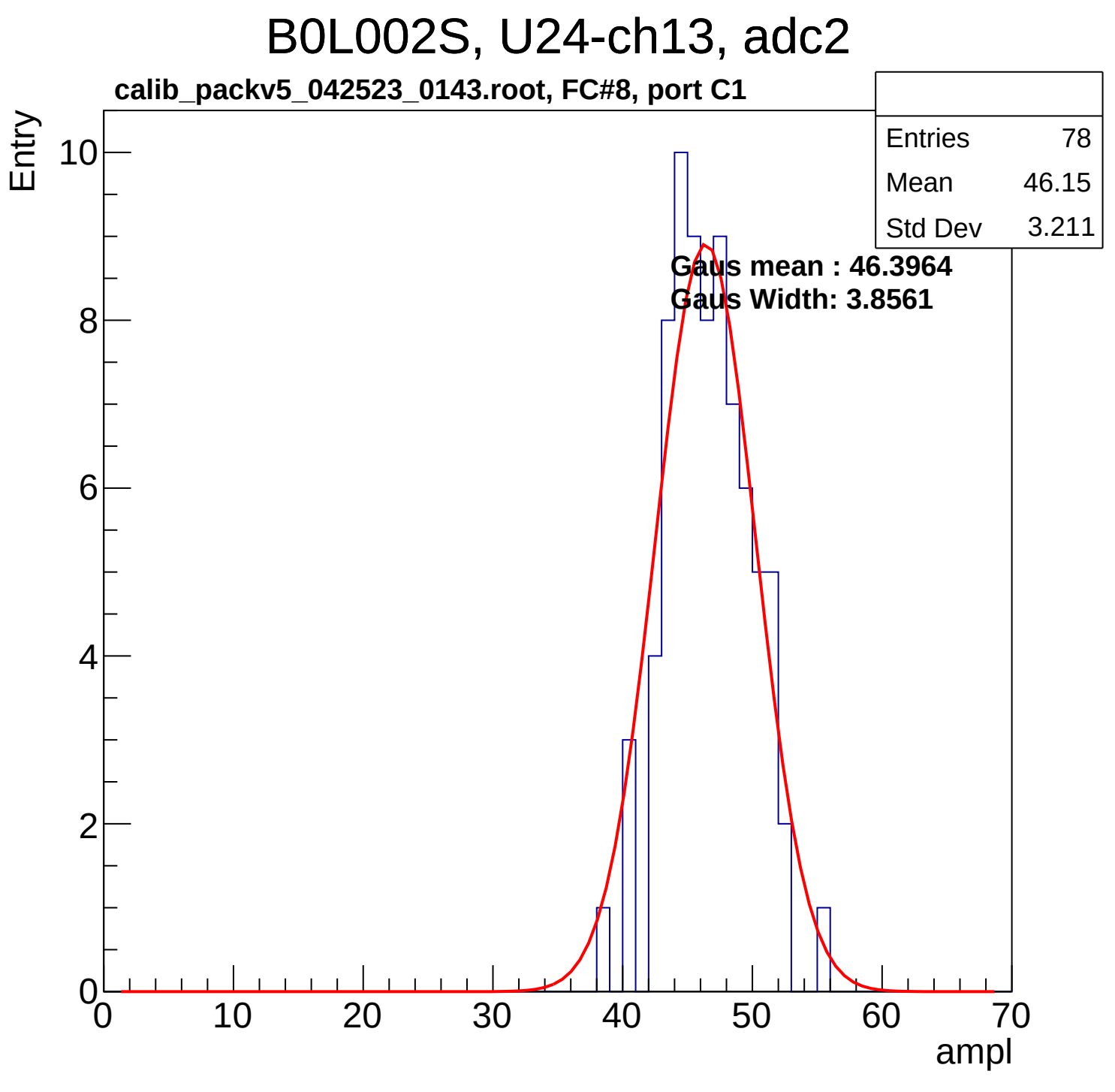

B0L002S, U24-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.94
Std Dev 1.909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

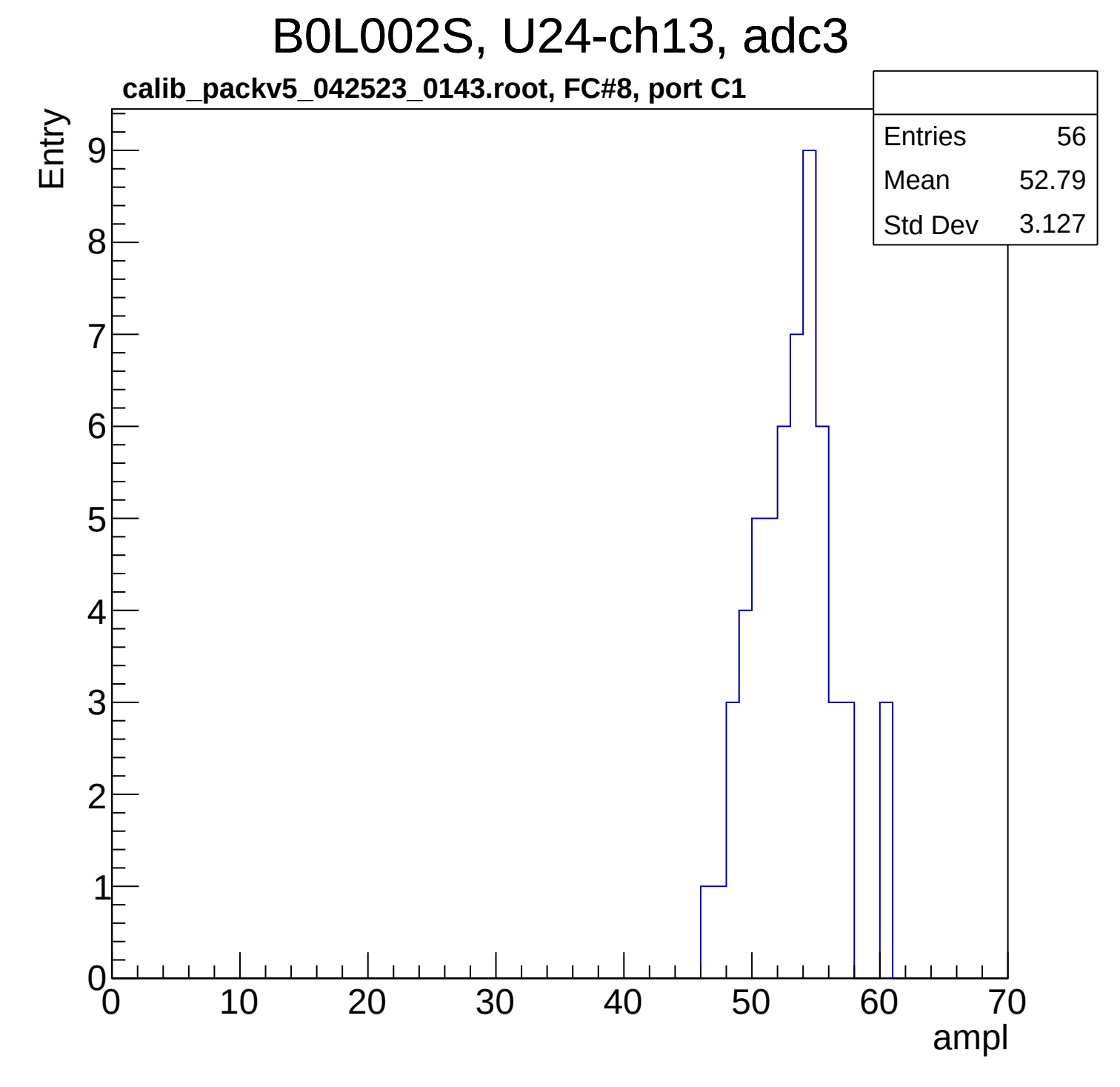

B0L002S, U24-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 32.95
Std Dev 2.893
Gaus mean : 33.1967
Gaus Width: 2.7090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.81
Std Dev 2.777
Gaus mean : 39.4938
Gaus Width: 2.8807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 46.15
Std Dev 3.211
Gaus mean : 46.3964
Gaus Width: 3.8561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 52.79
Std Dev 3.127
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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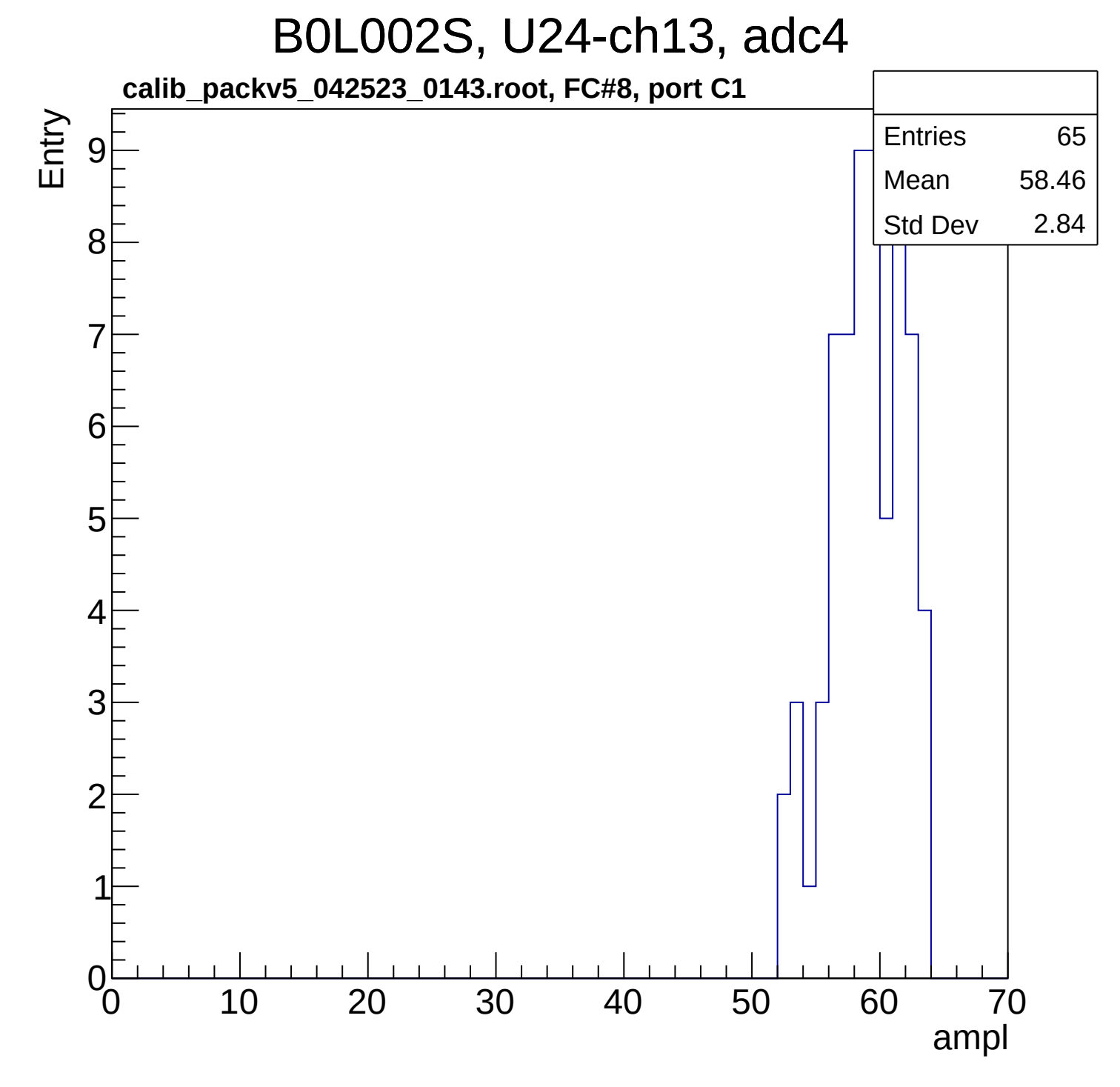
B0L002S, U24-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 58.46
Std Dev 2.84
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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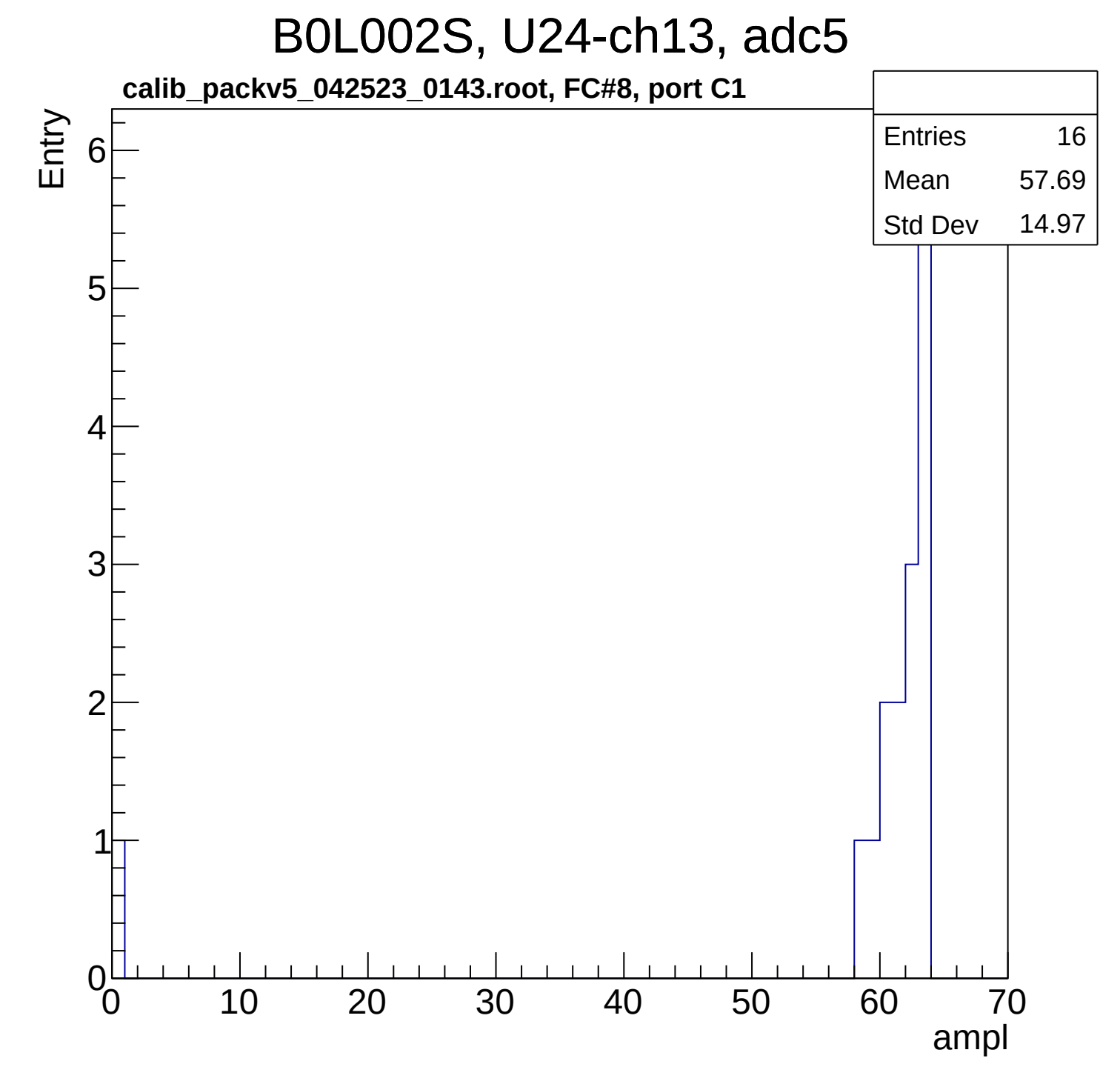
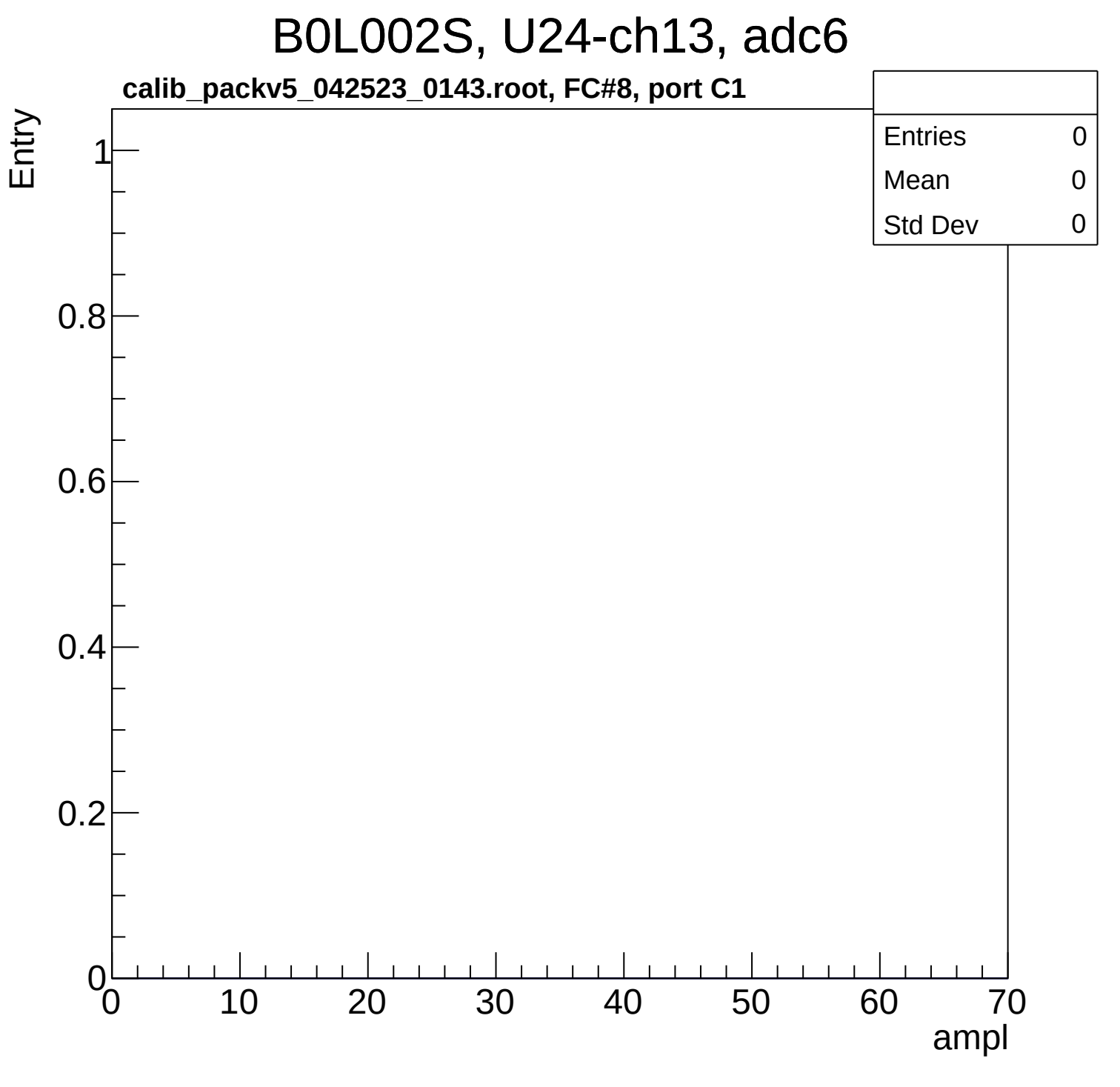
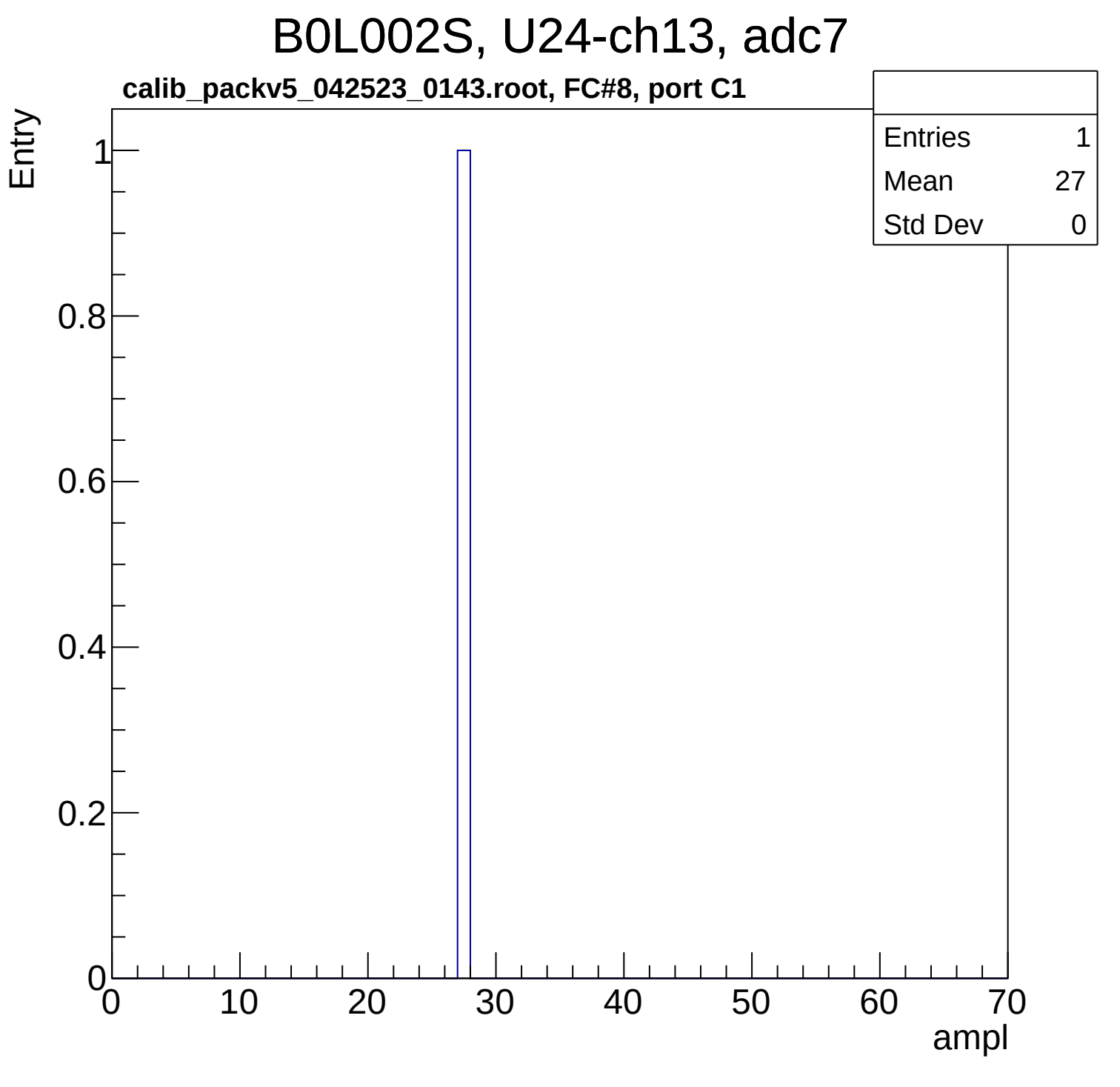

B0L002S, U24-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 57.69
Std Dev 14.97
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 27
Std Dev 0
Entry
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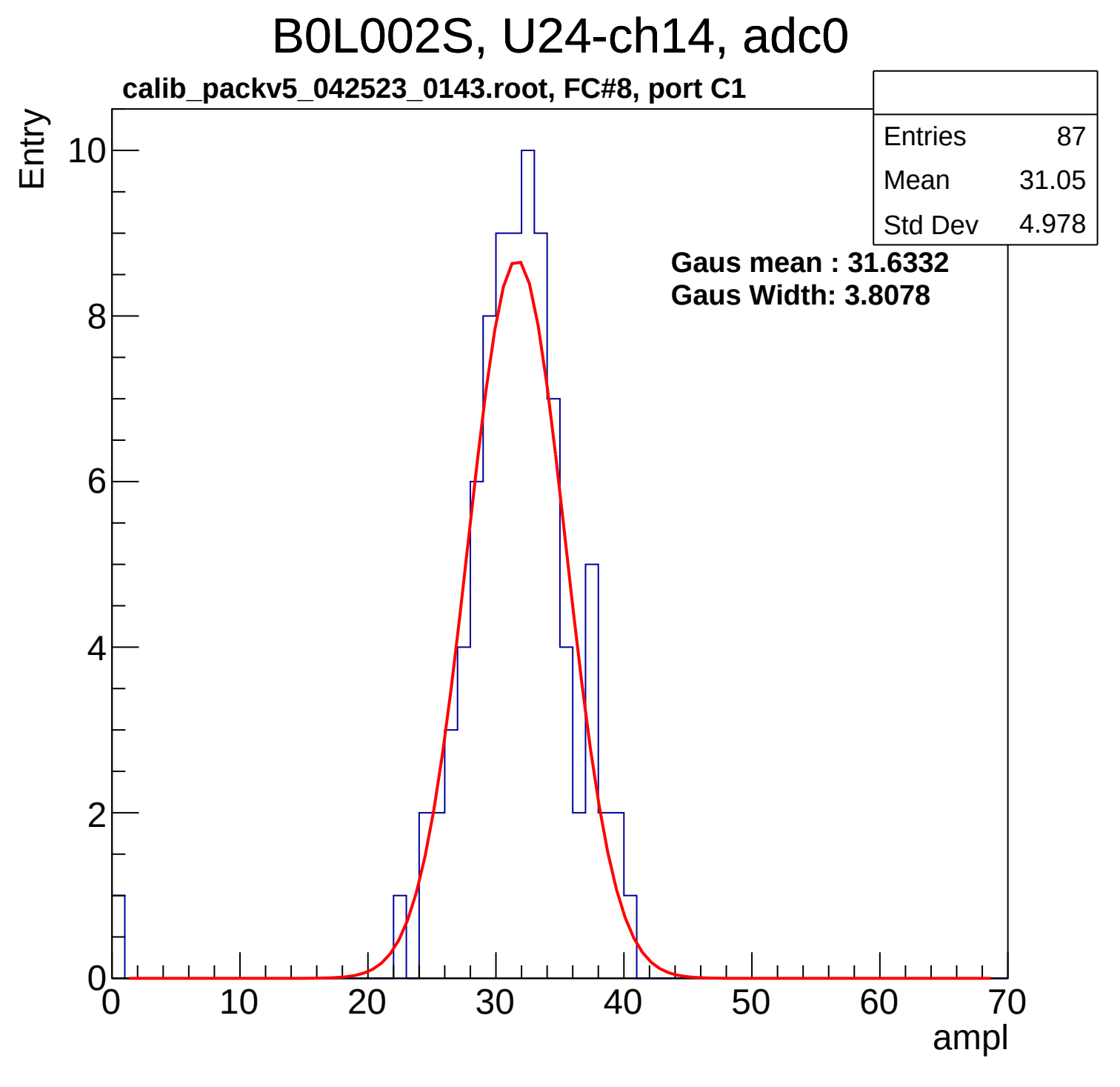
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 31.05
Std Dev 4.978
Gaus mean : 31.6332
Gaus Width: 3.8078
Entry
ampl
0
2
4
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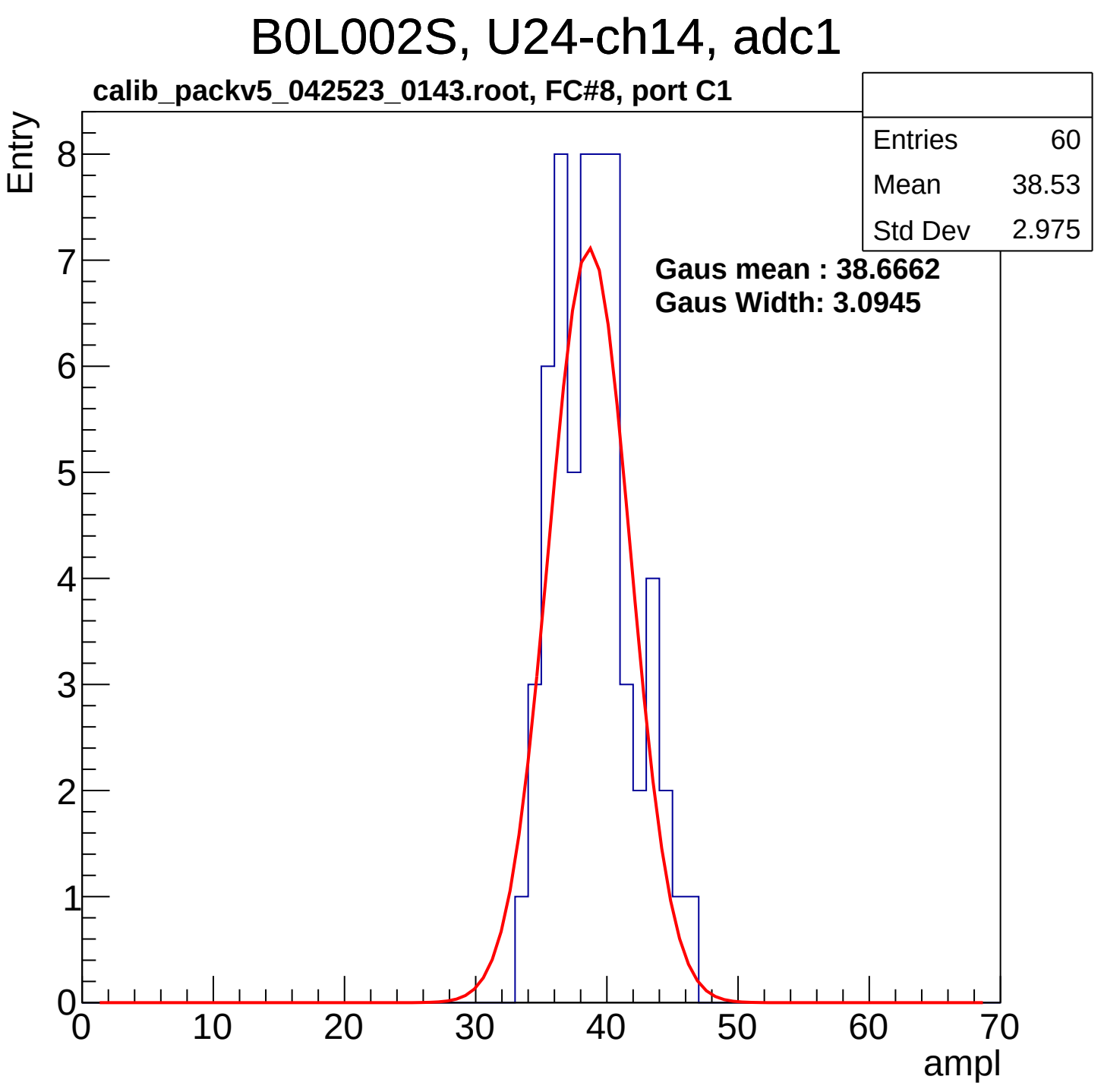
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.53
Std Dev 2.975
Gaus mean : 38.6662
Gaus Width: 3.0945
Entry
ampl
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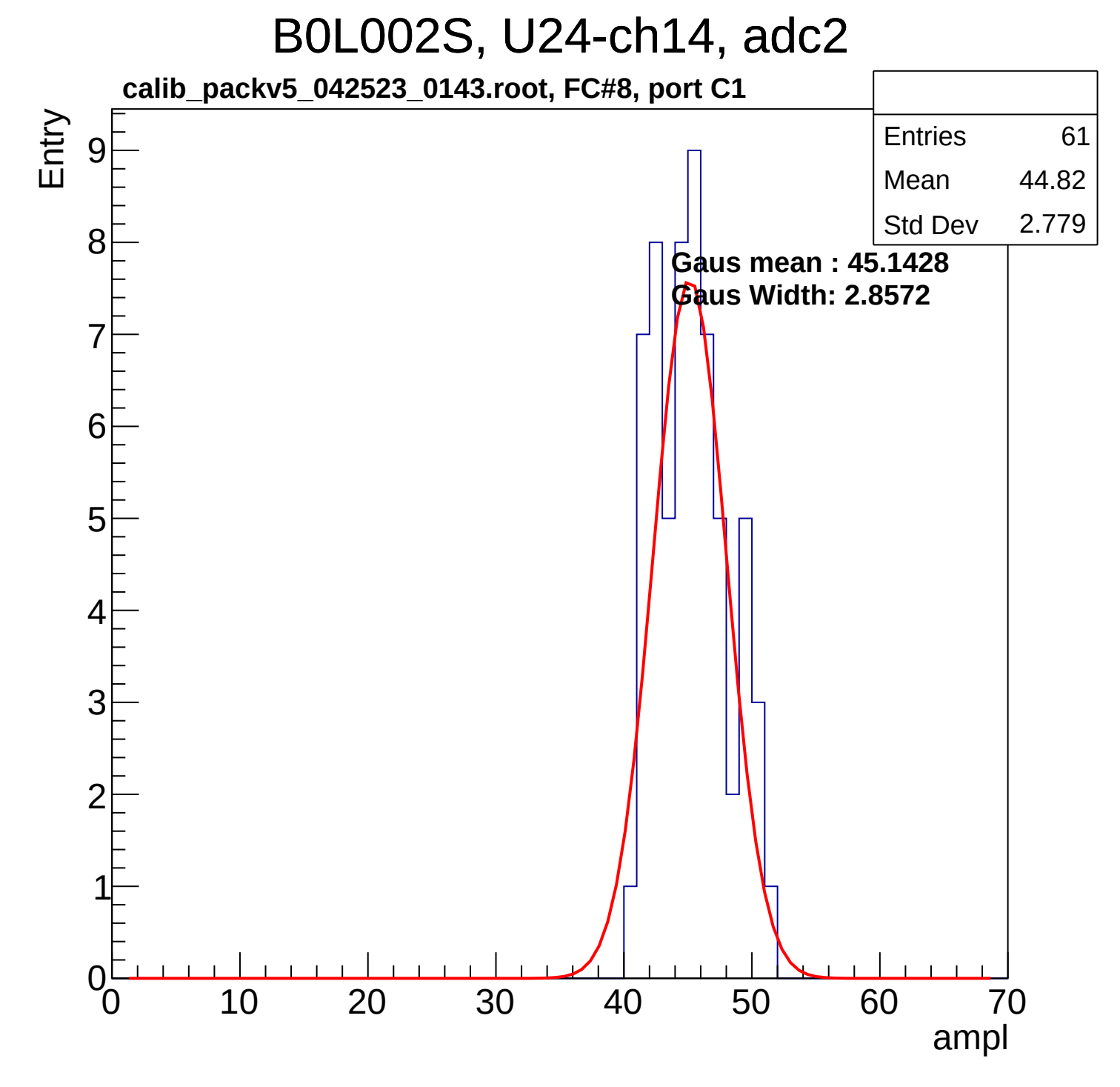
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.82
Std Dev 2.779
Gaus mean : 45.1428
Gaus Width: 2.8572
Entry
ampl
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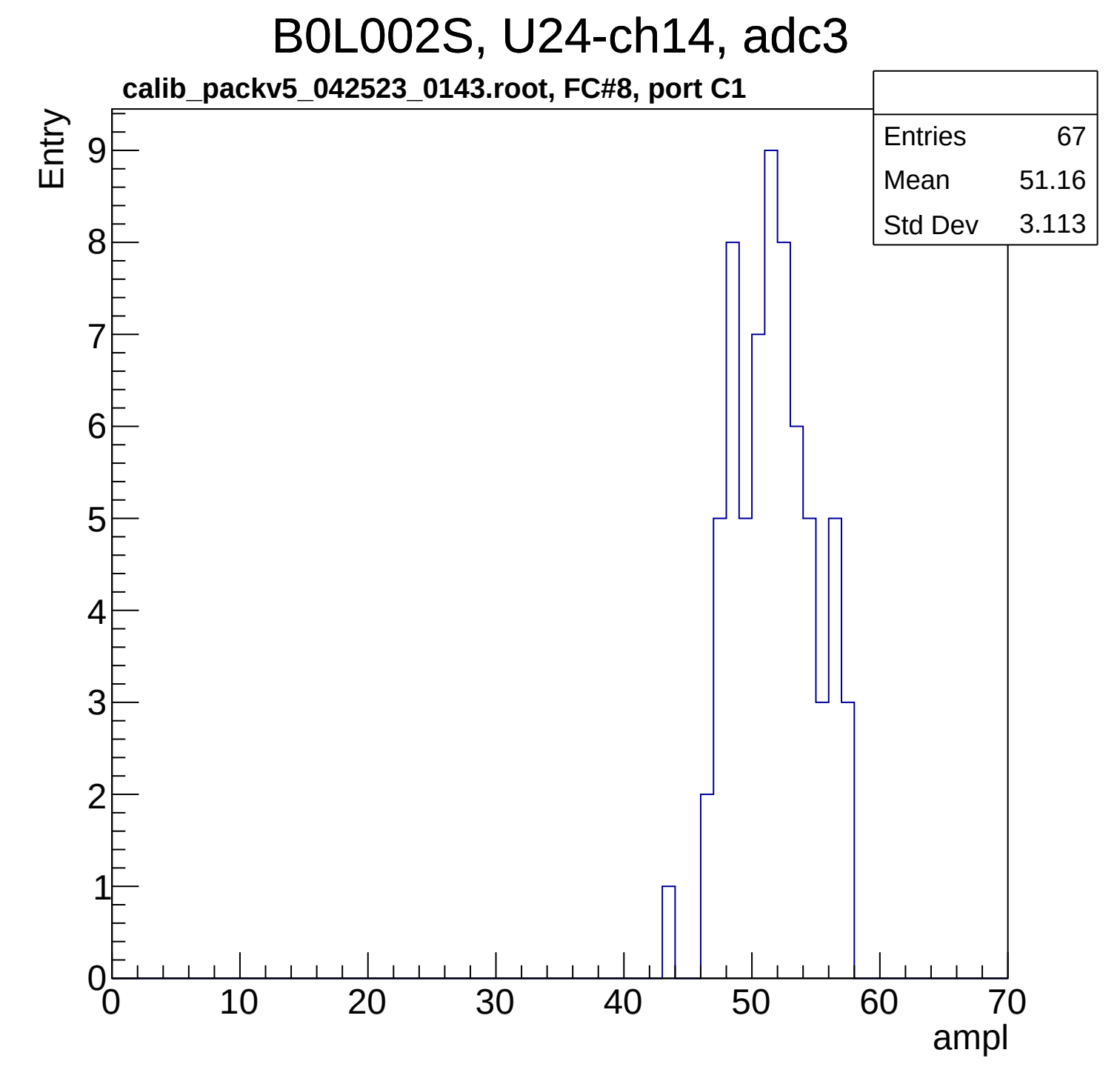
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U24-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.16
Std Dev 3.113
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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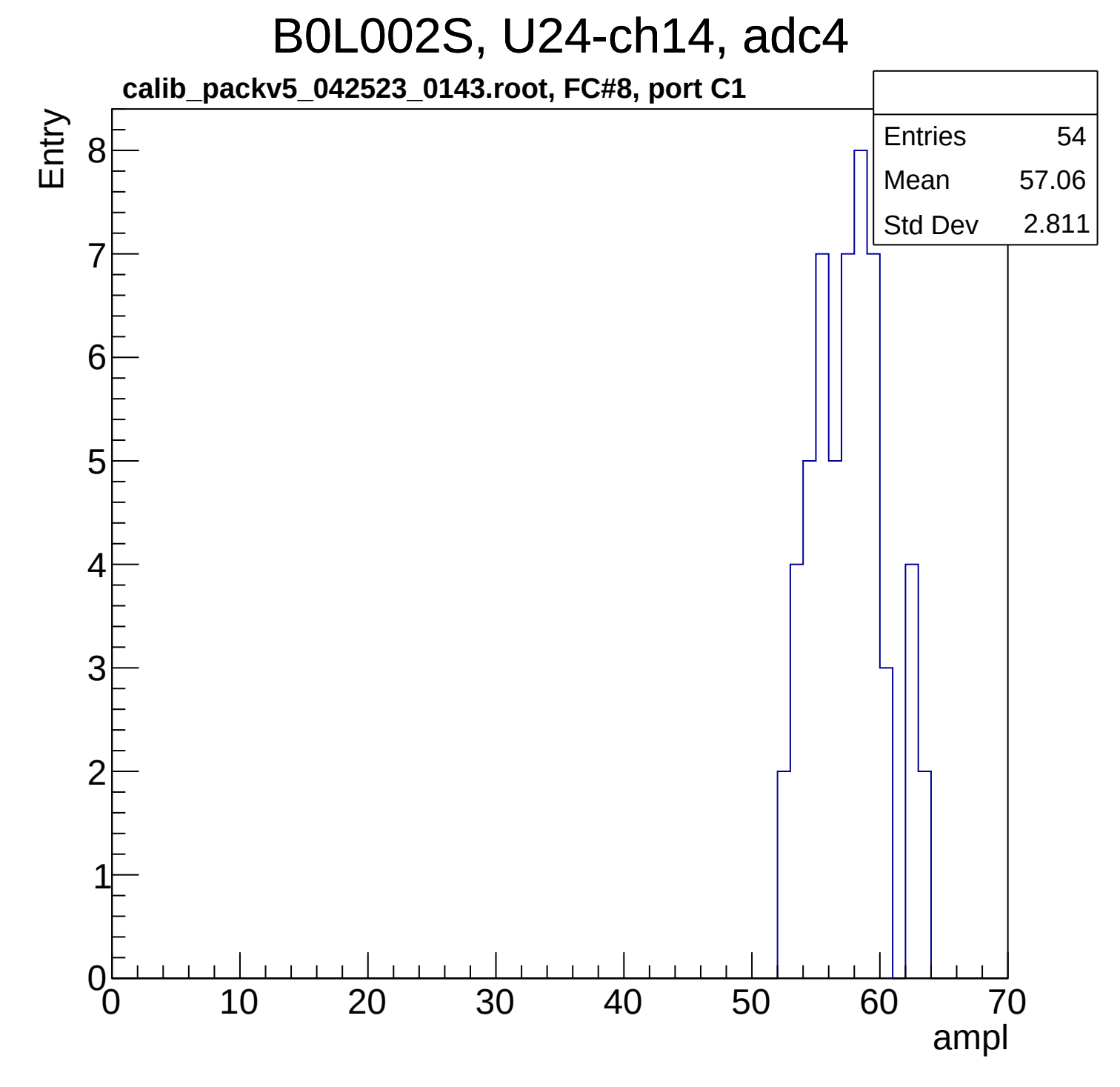
B0L002S, U24-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.06
Std Dev 2.811
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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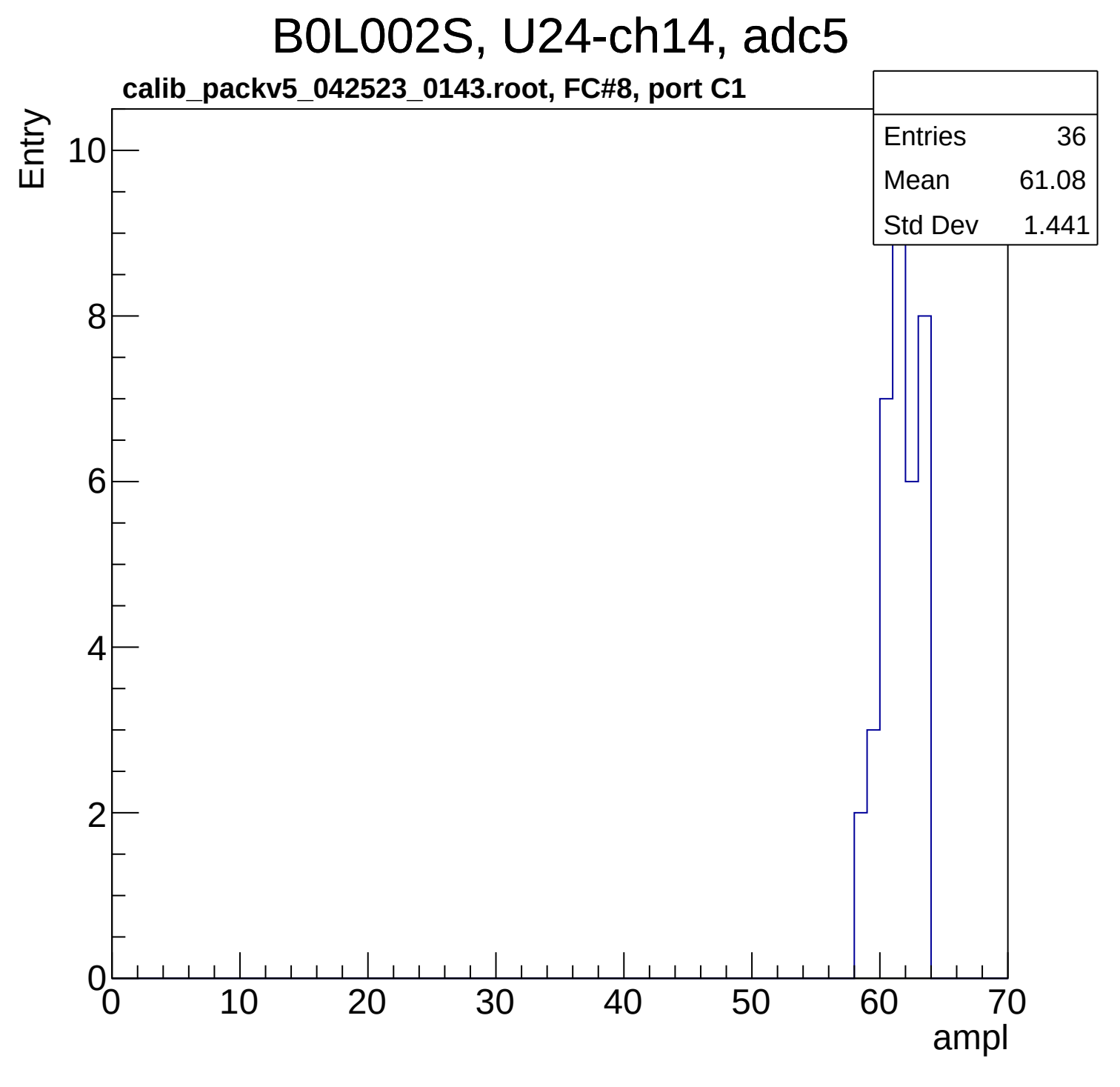
B0L002S, U24-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 61.08
Std Dev 1.441
Entry
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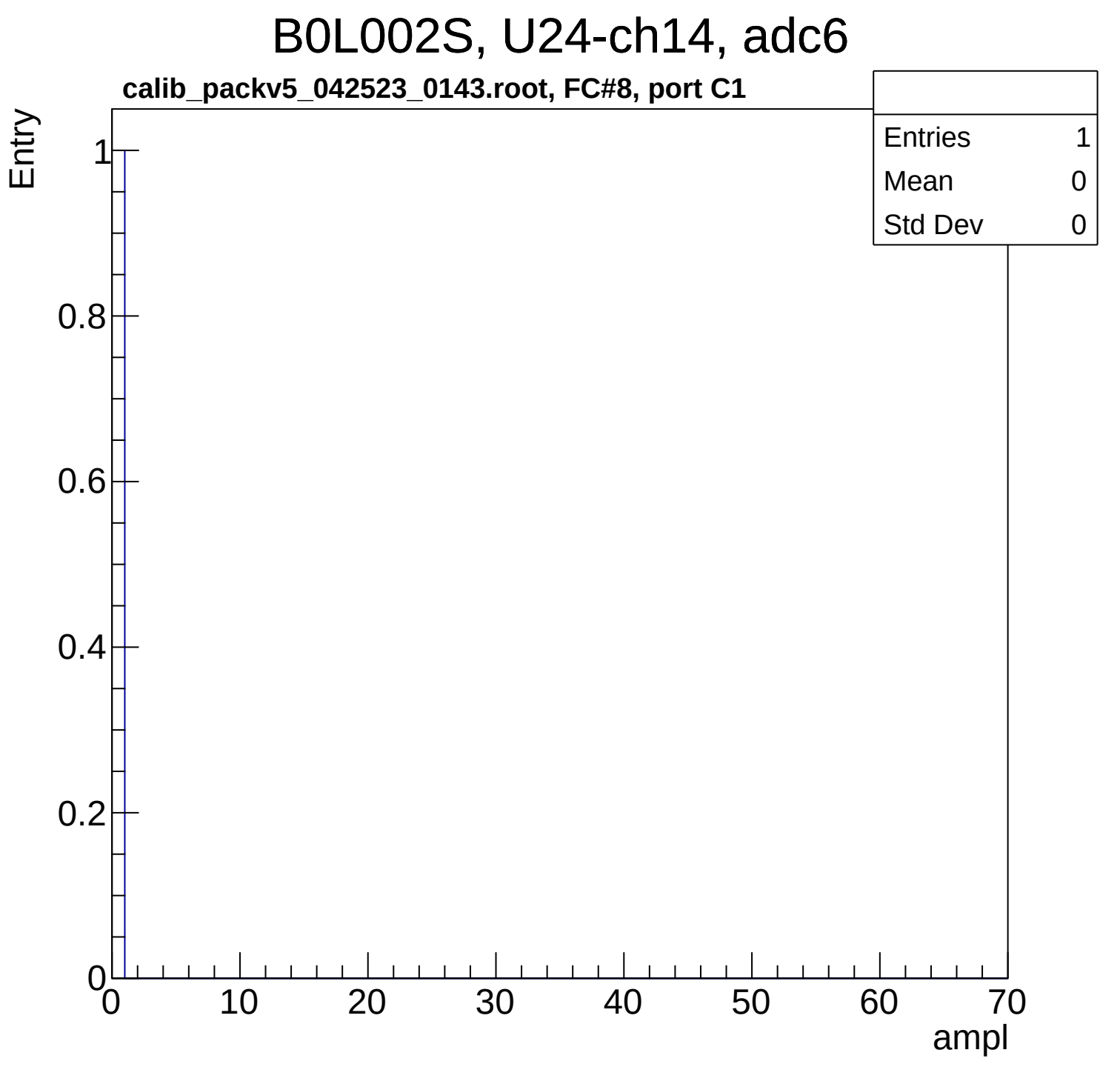
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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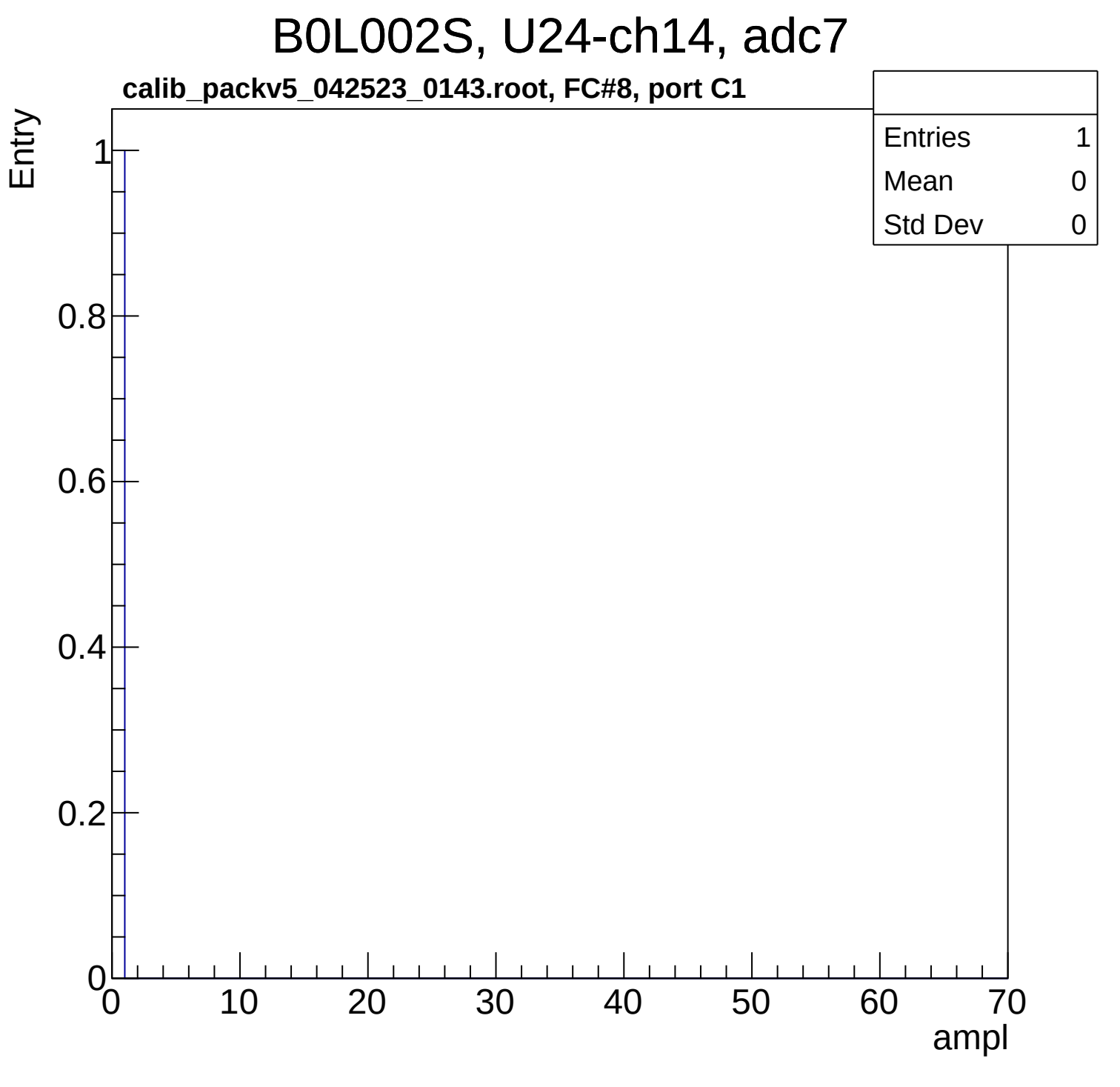
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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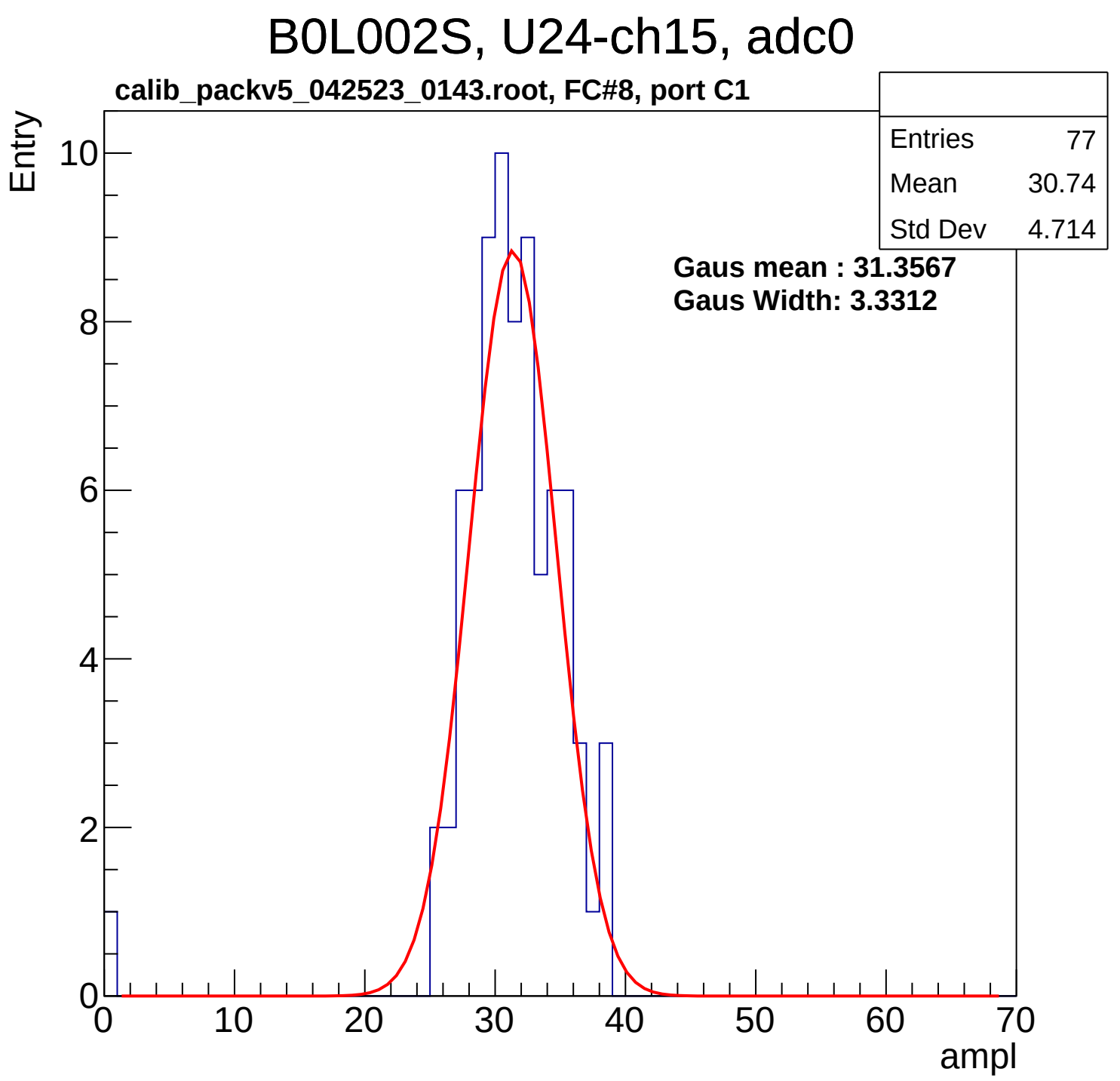
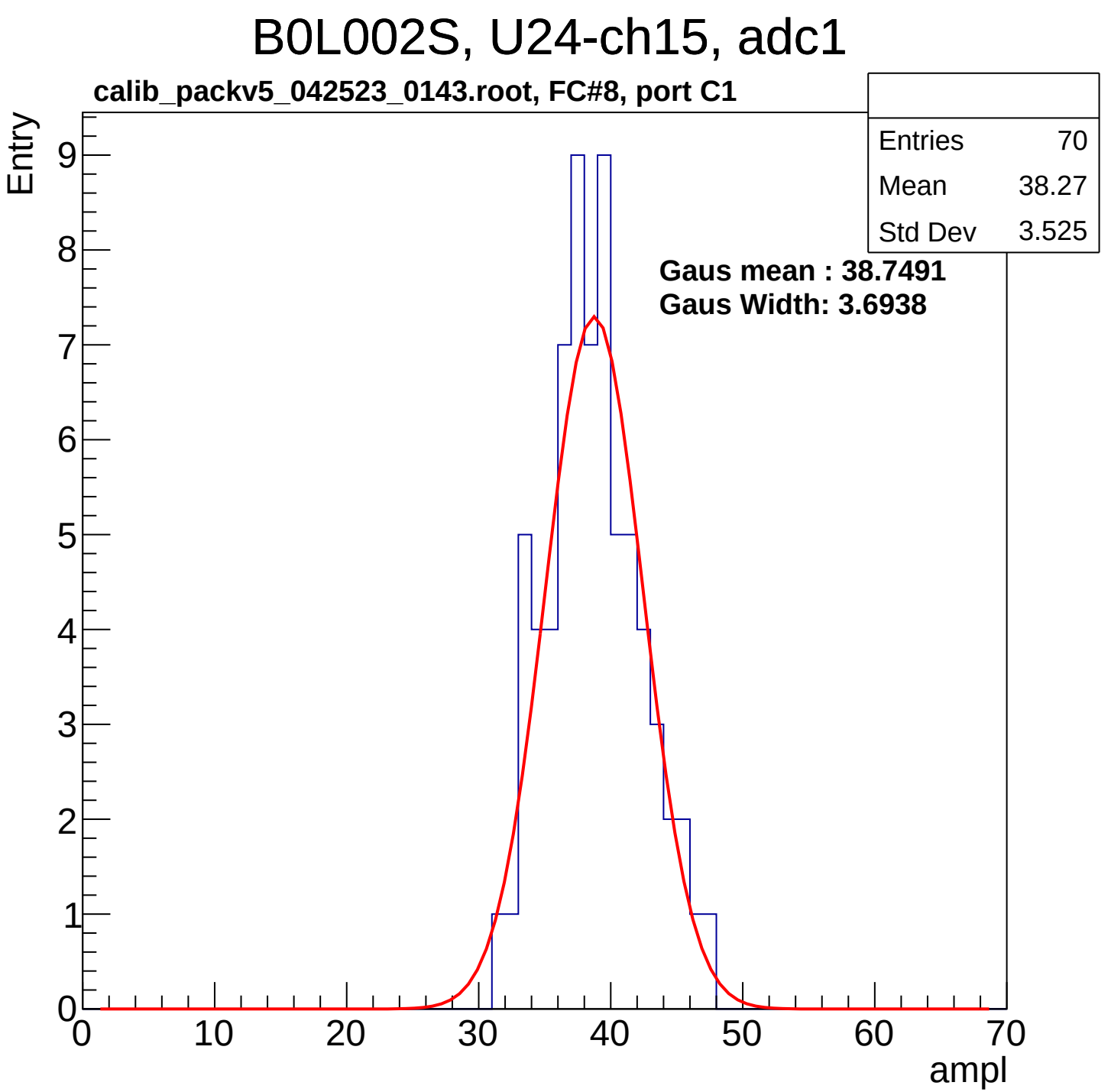
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.74
Std Dev 4.714
Gaus mean : 31.3567
Gaus Width: 3.3312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.27
Std Dev 3.525
Gaus mean : 38.7491
Gaus Width: 3.6938
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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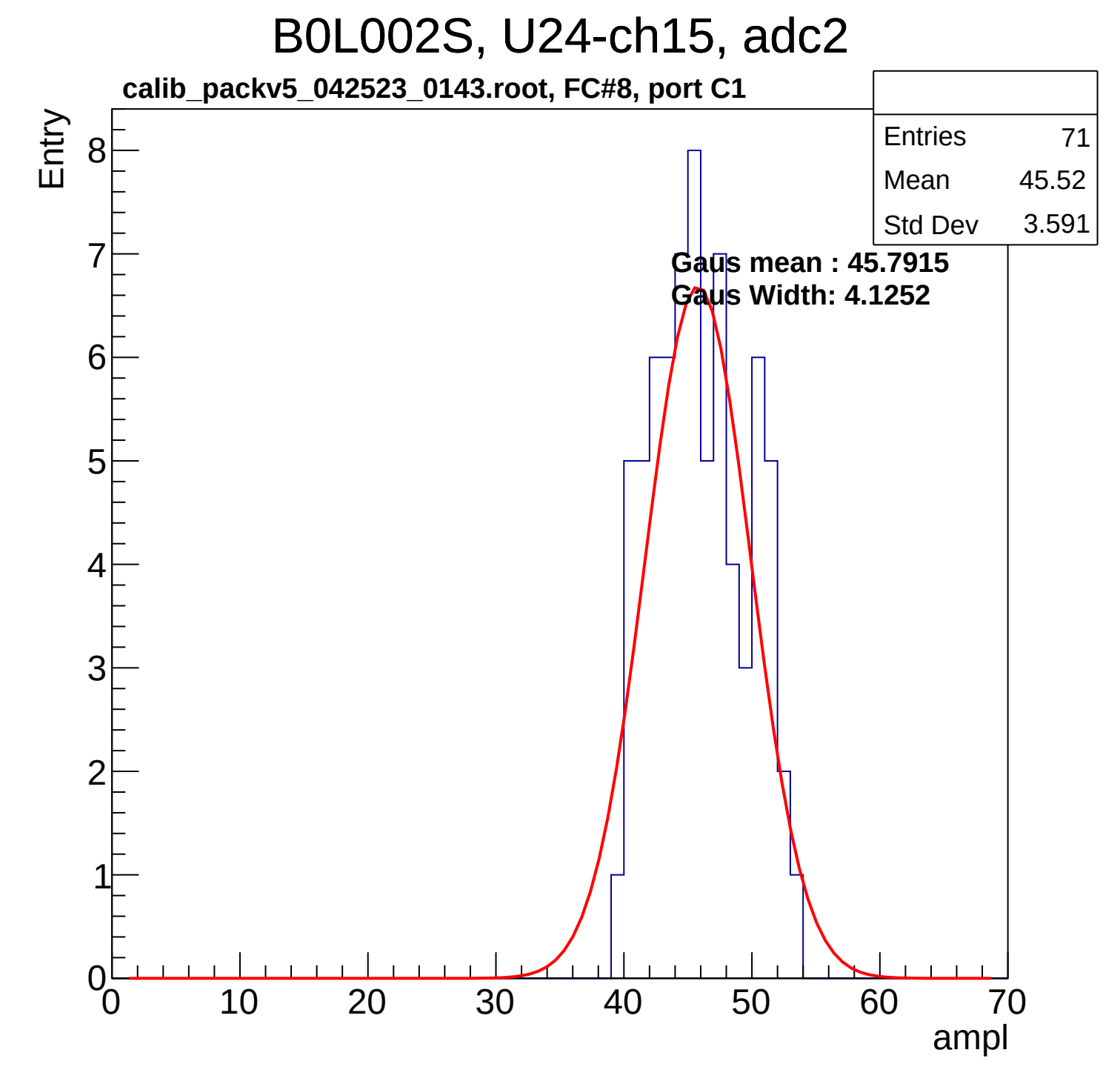
B0L002S, U24-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 45.52
Std Dev 3.591
Gaus mean : 45.7915
Gaus Width: 4.1252
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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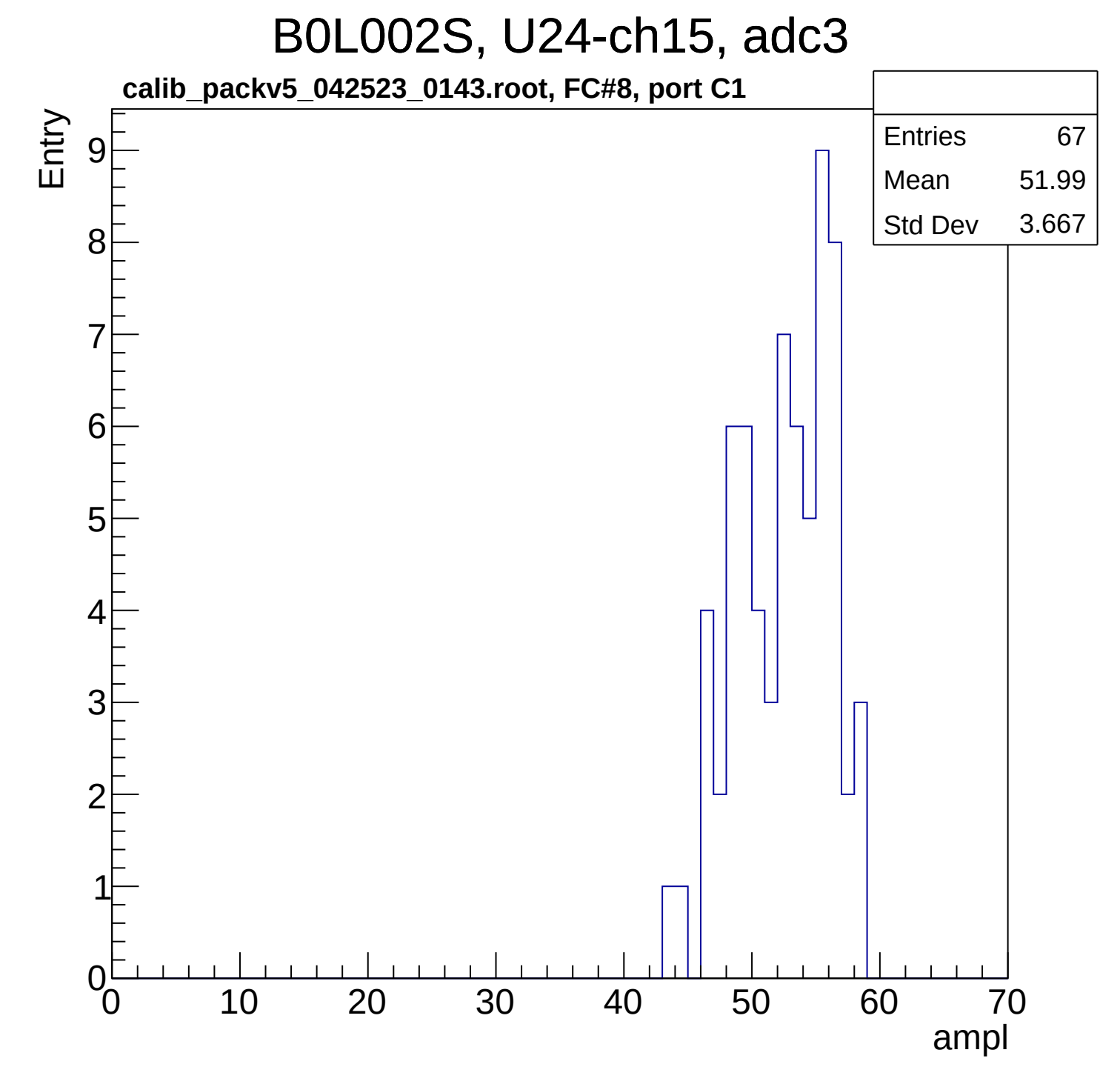
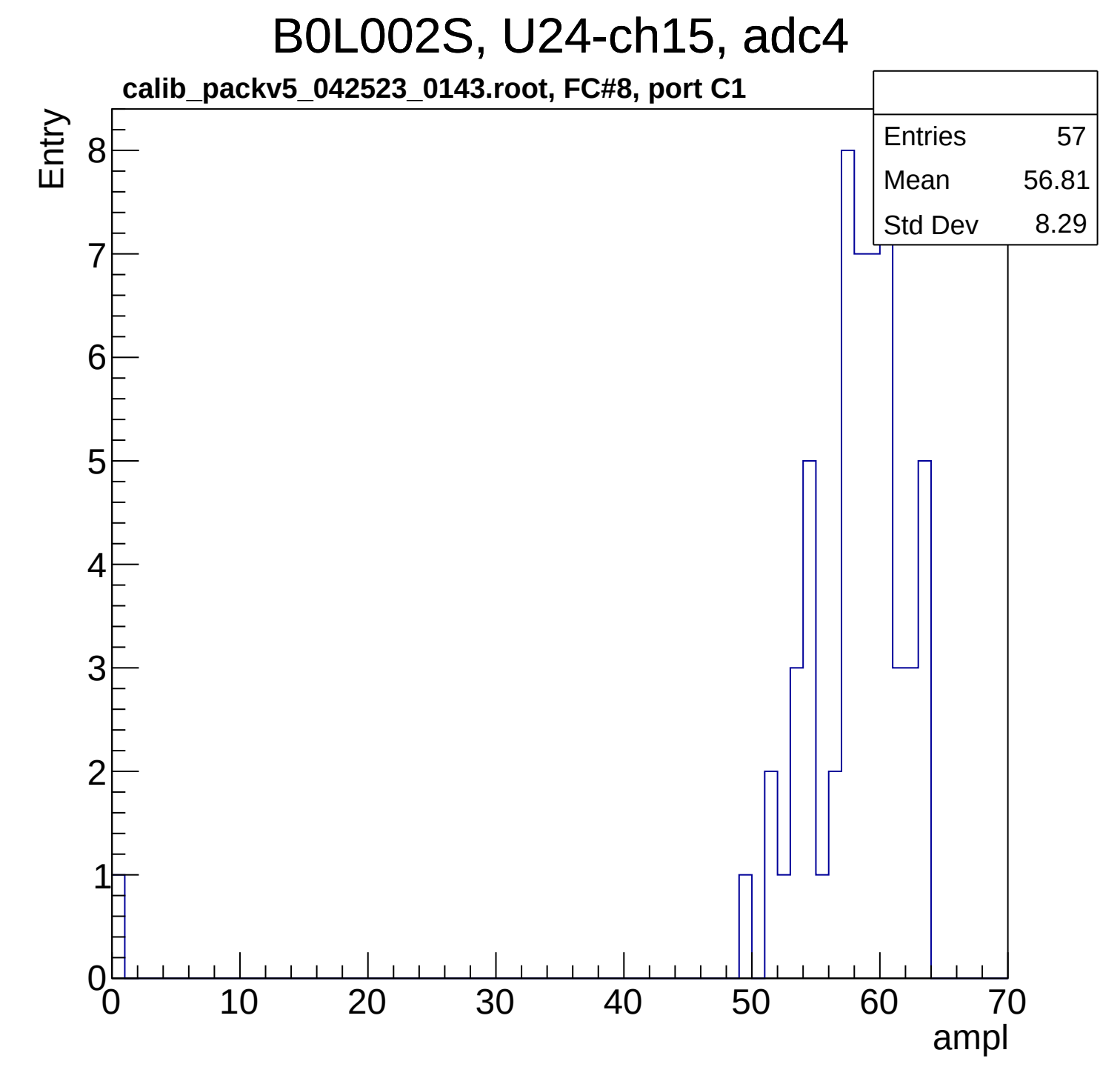

B0L002S, U24-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.99
Std Dev 3.667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.81
Std Dev 8.29
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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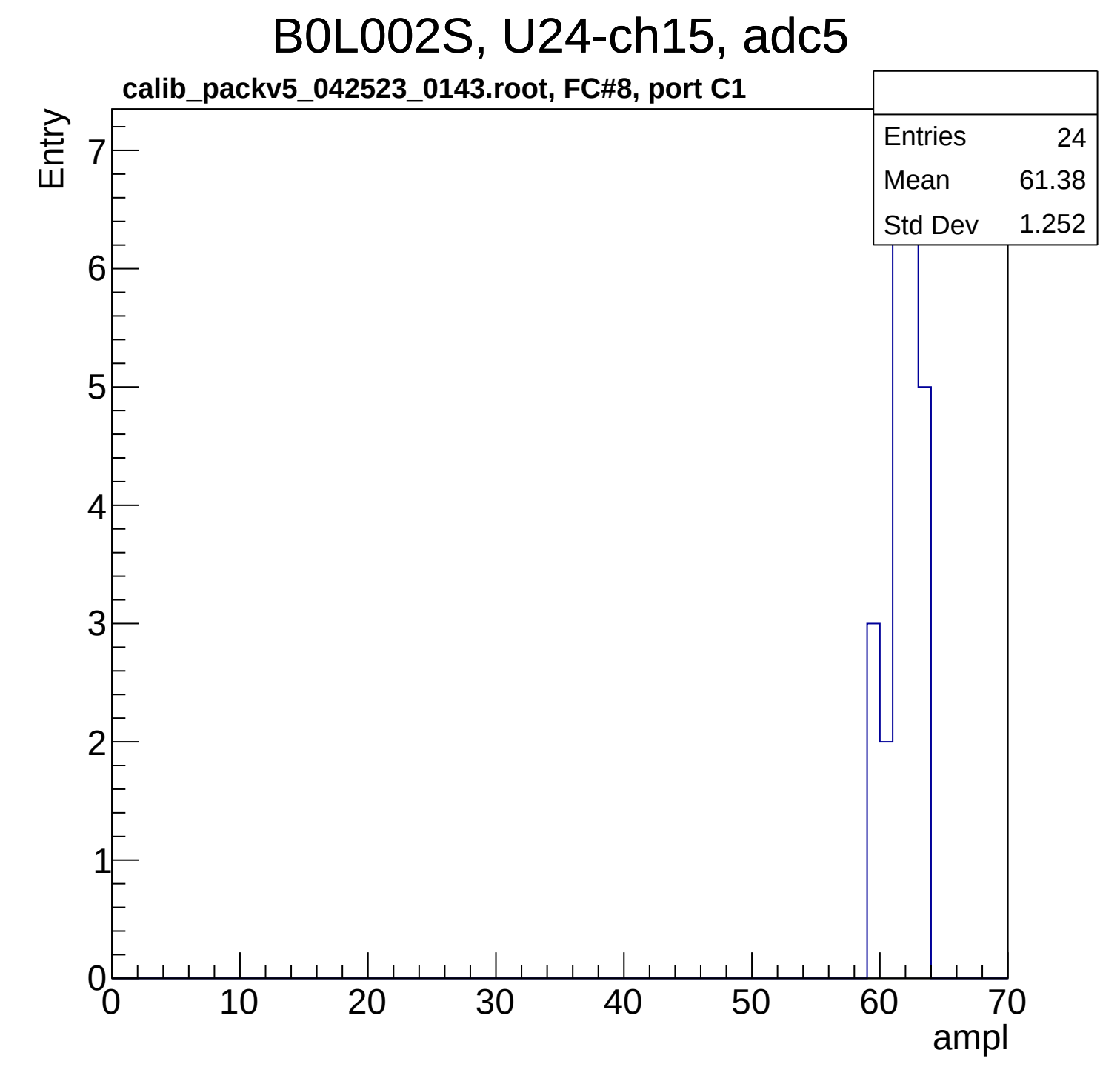
B0L002S, U24-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.38
Std Dev 1.252
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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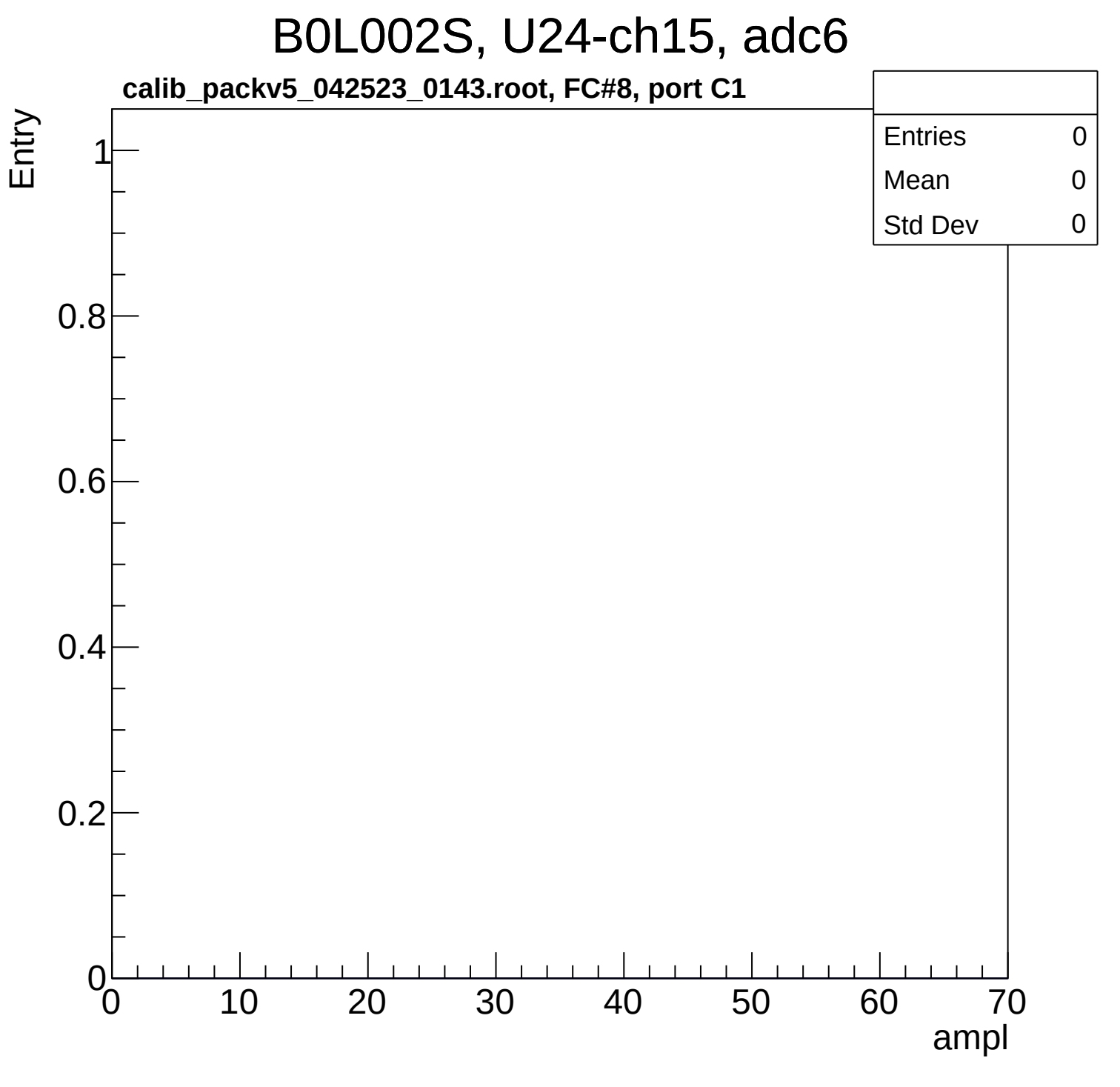
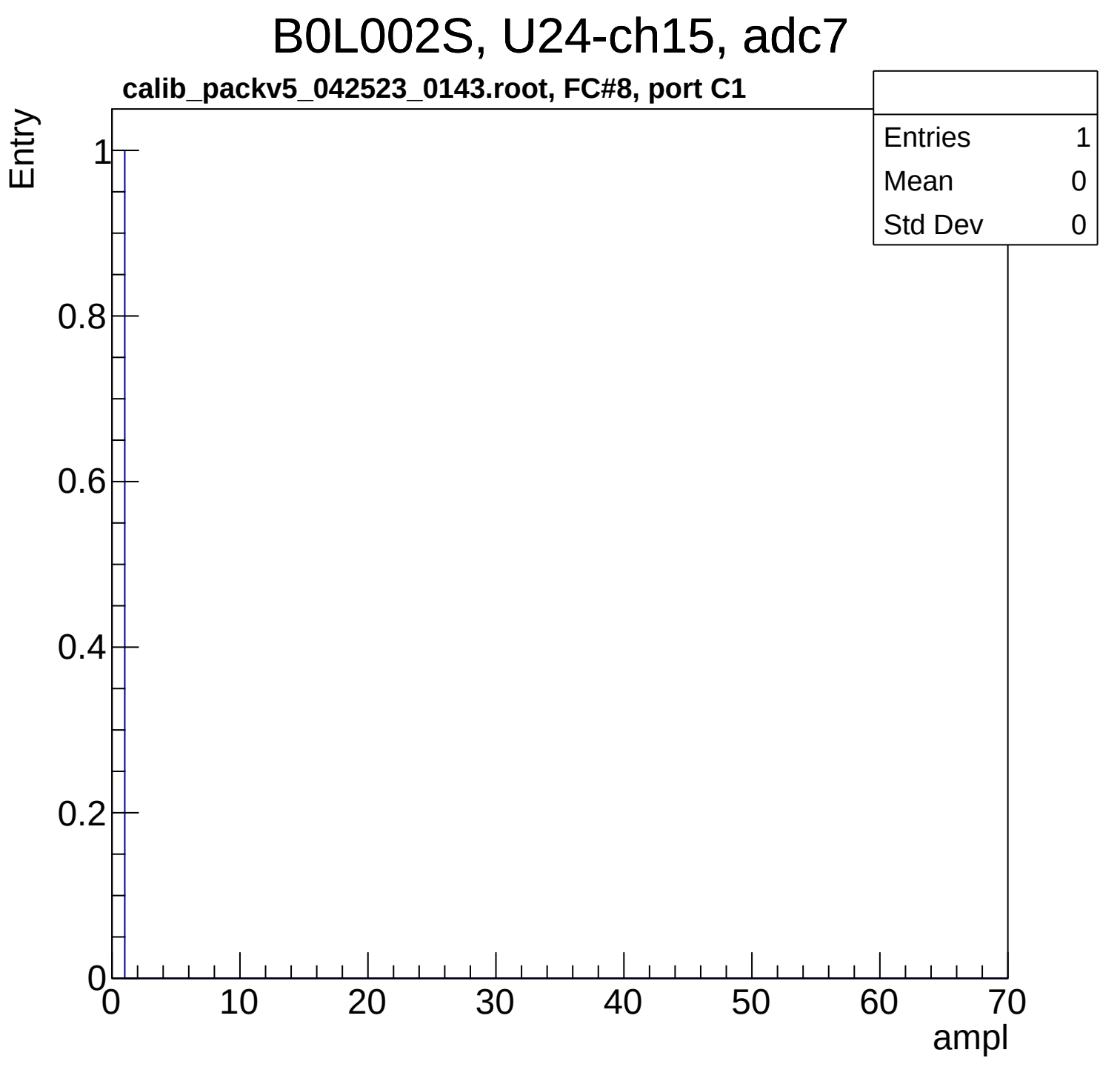

B0L002S, U24-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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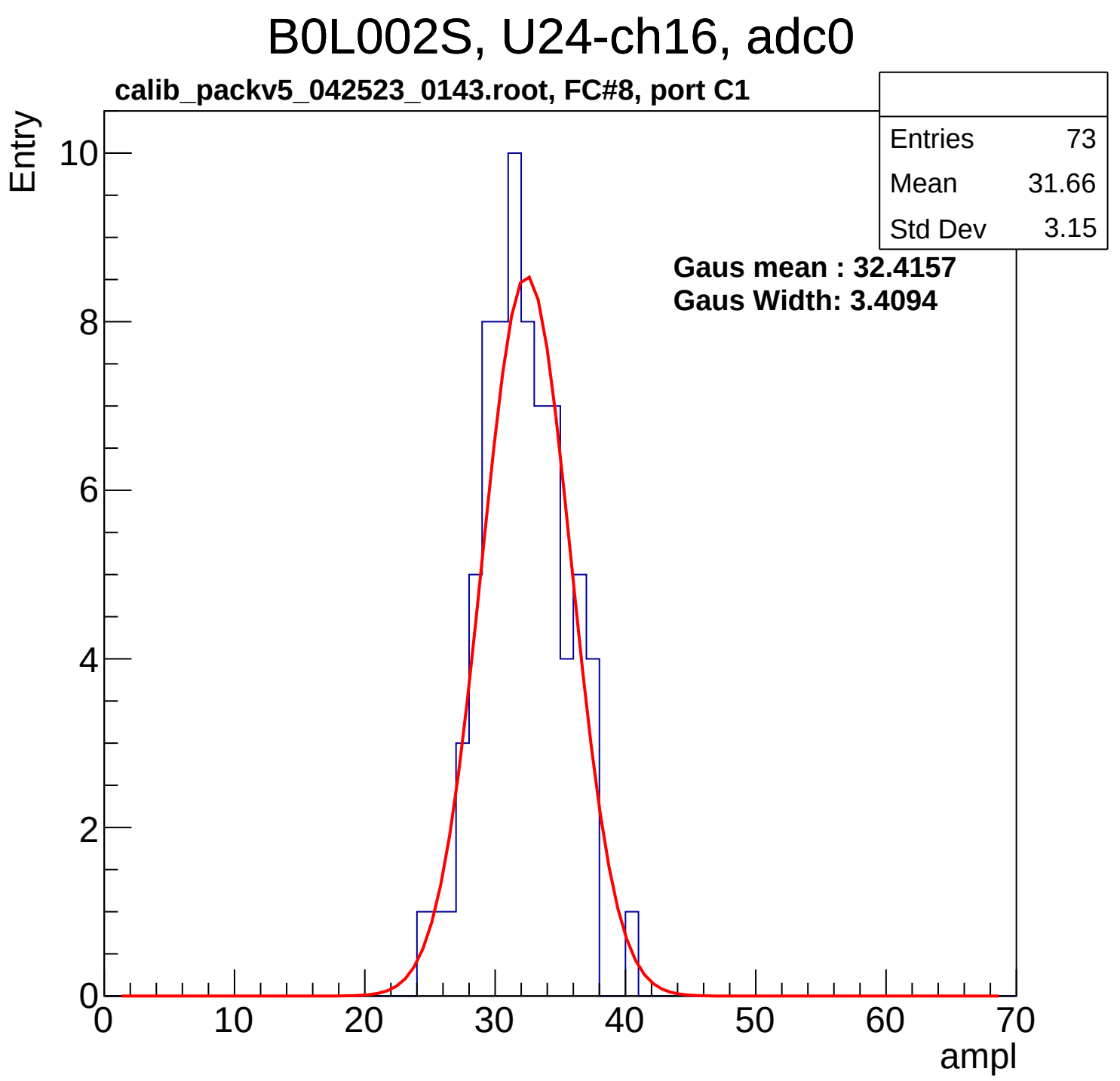
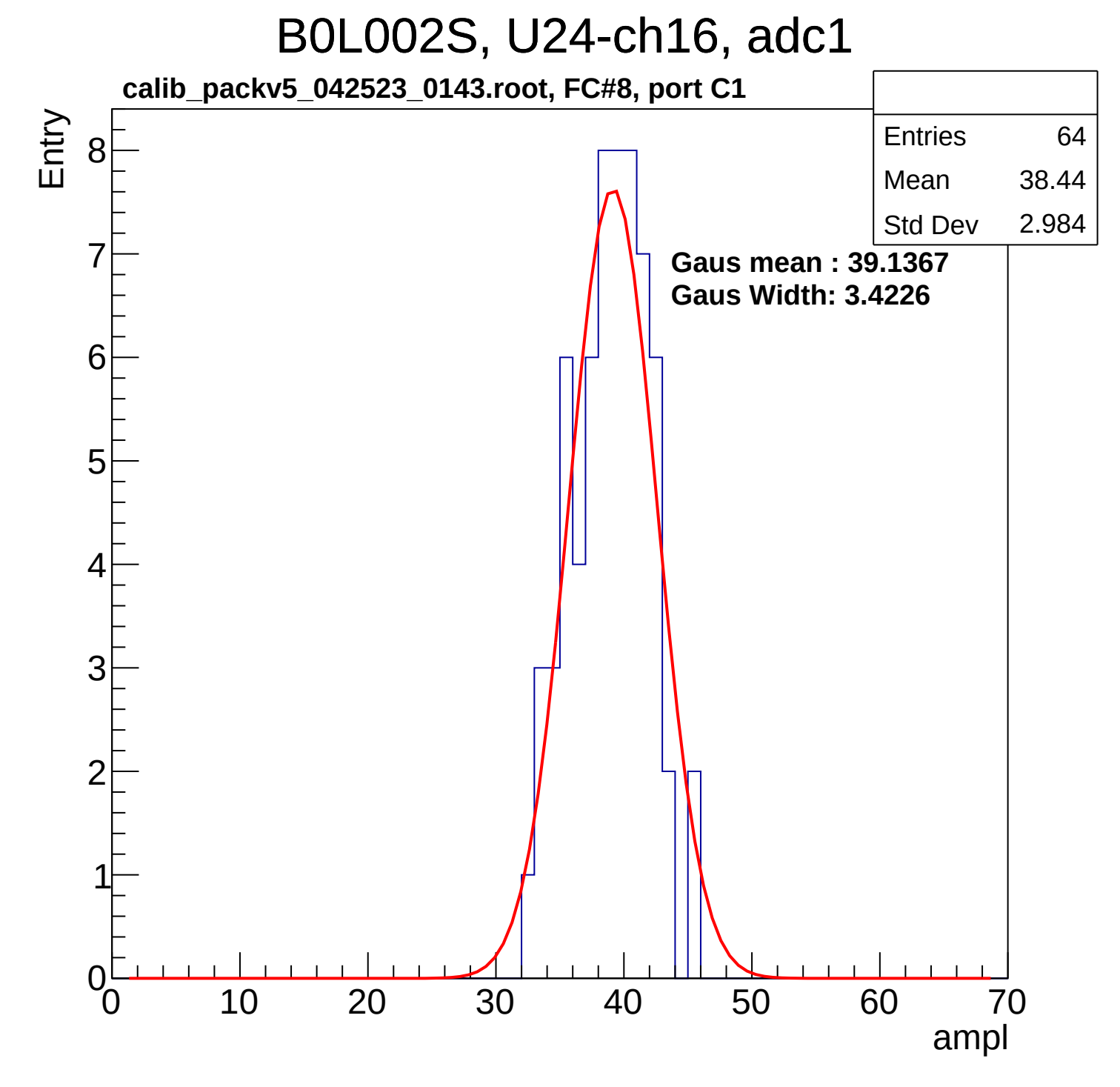
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 31.66
Std Dev 3.15
Gaus mean : 32.4157
Gaus Width: 3.4094
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.44
Std Dev 2.984
Gaus mean : 39.1367
Gaus Width: 3.4226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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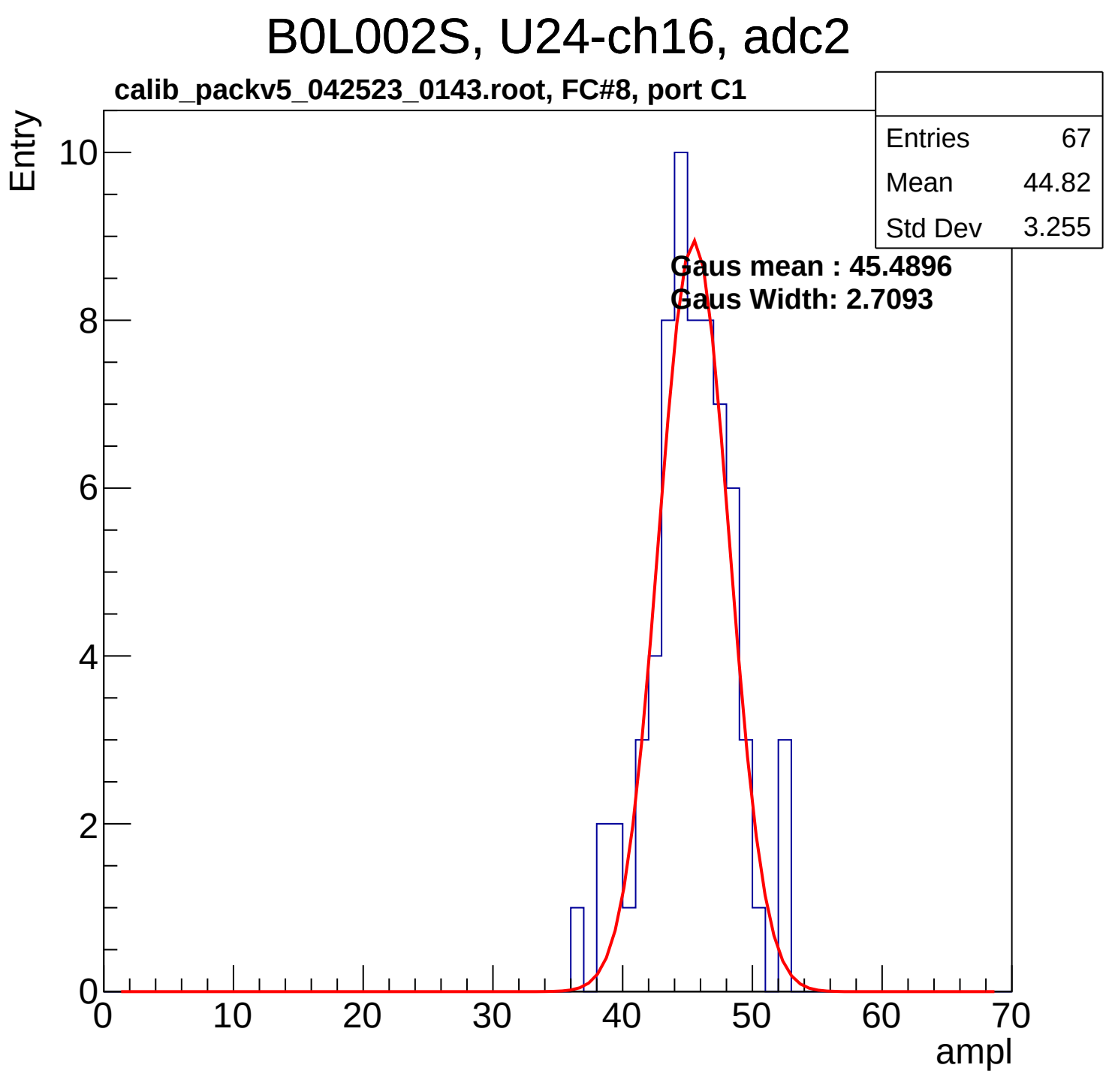
B0L002S, U24-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.82
Std Dev 3.255
Gaus mean : 45.4896
Gaus Width: 2.7093
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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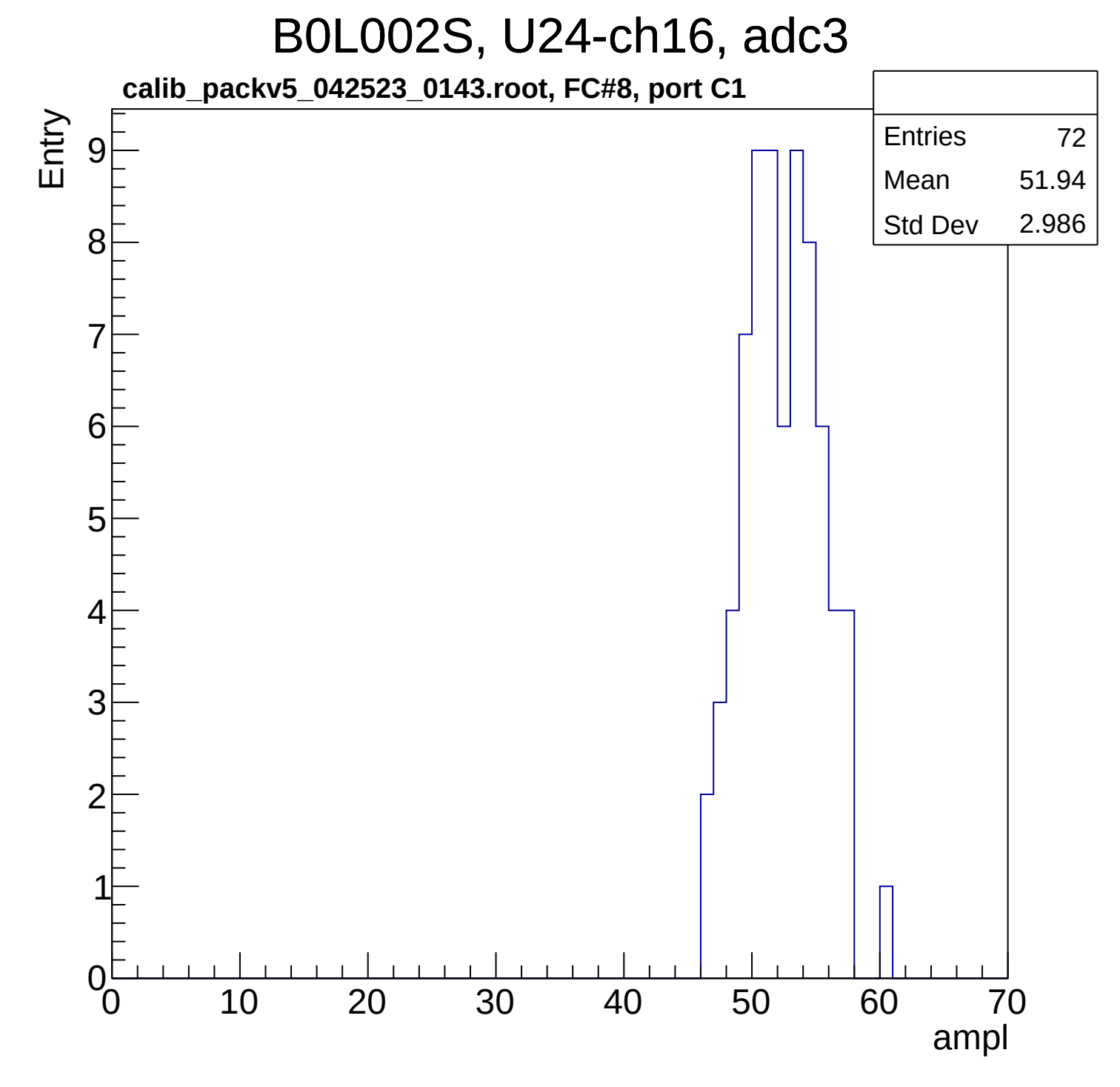
B0L002S, U24-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.94
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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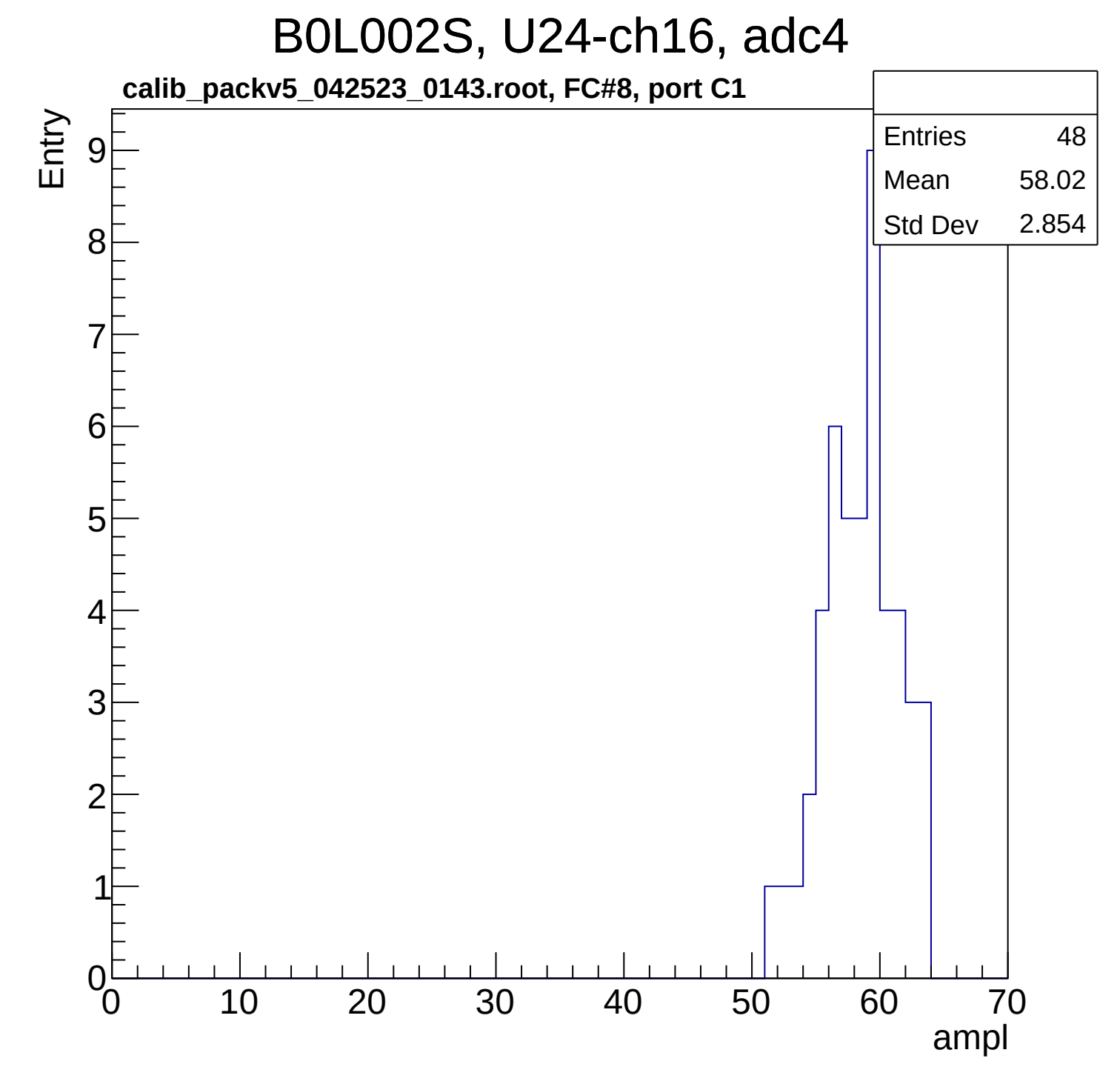
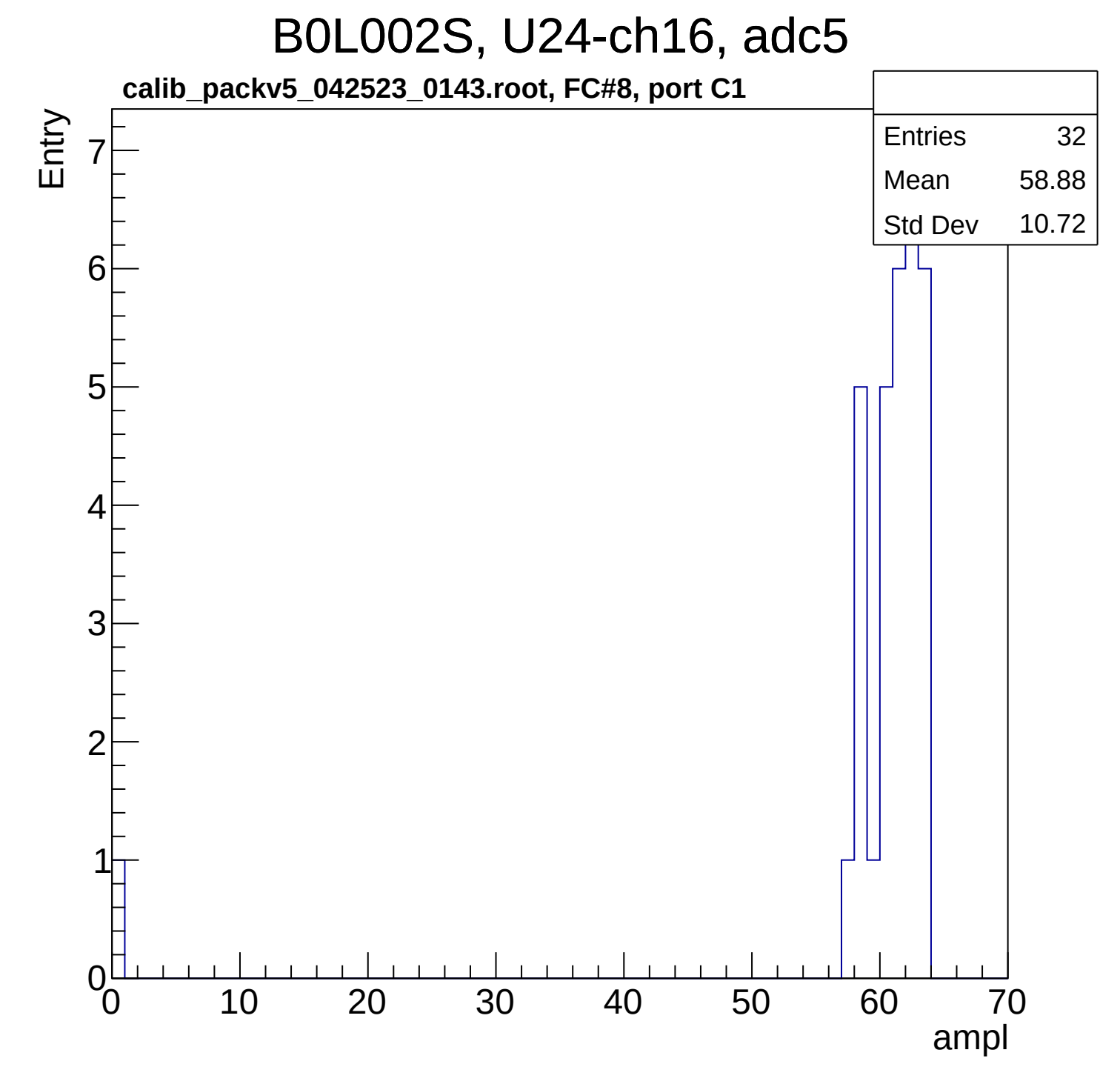
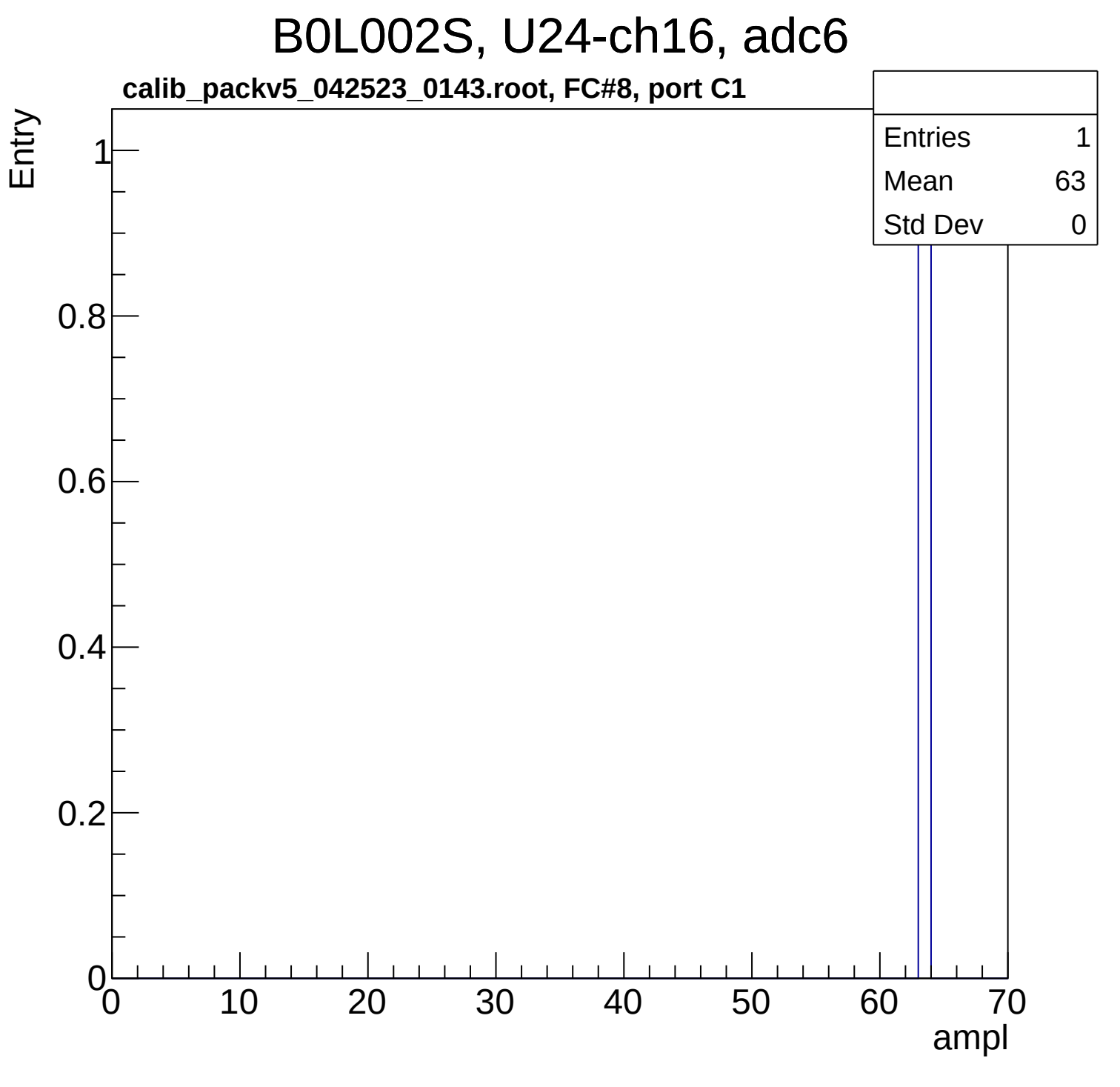
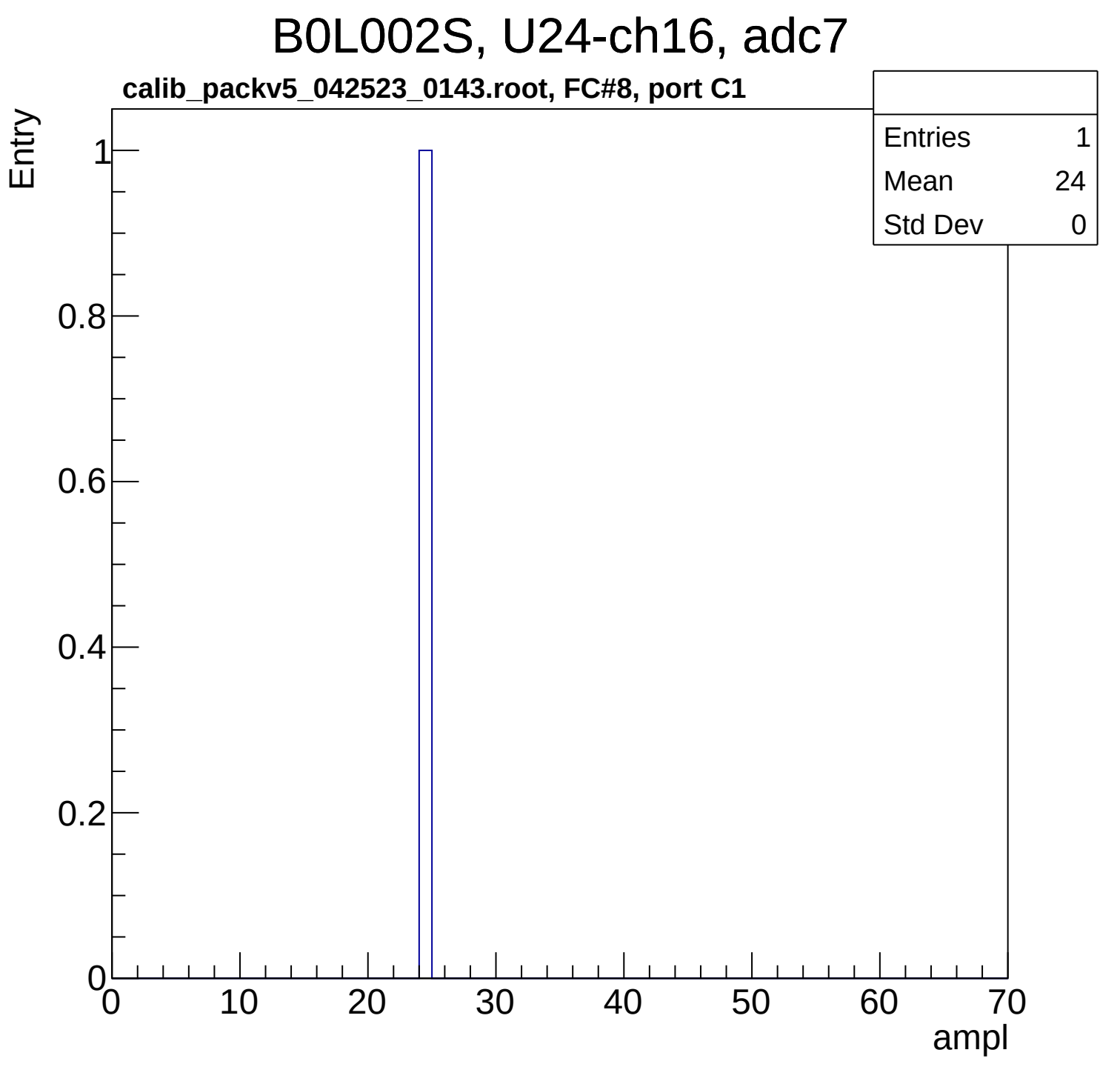

B0L002S, U24-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 58.02
Std Dev 2.854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.88
Std Dev 10.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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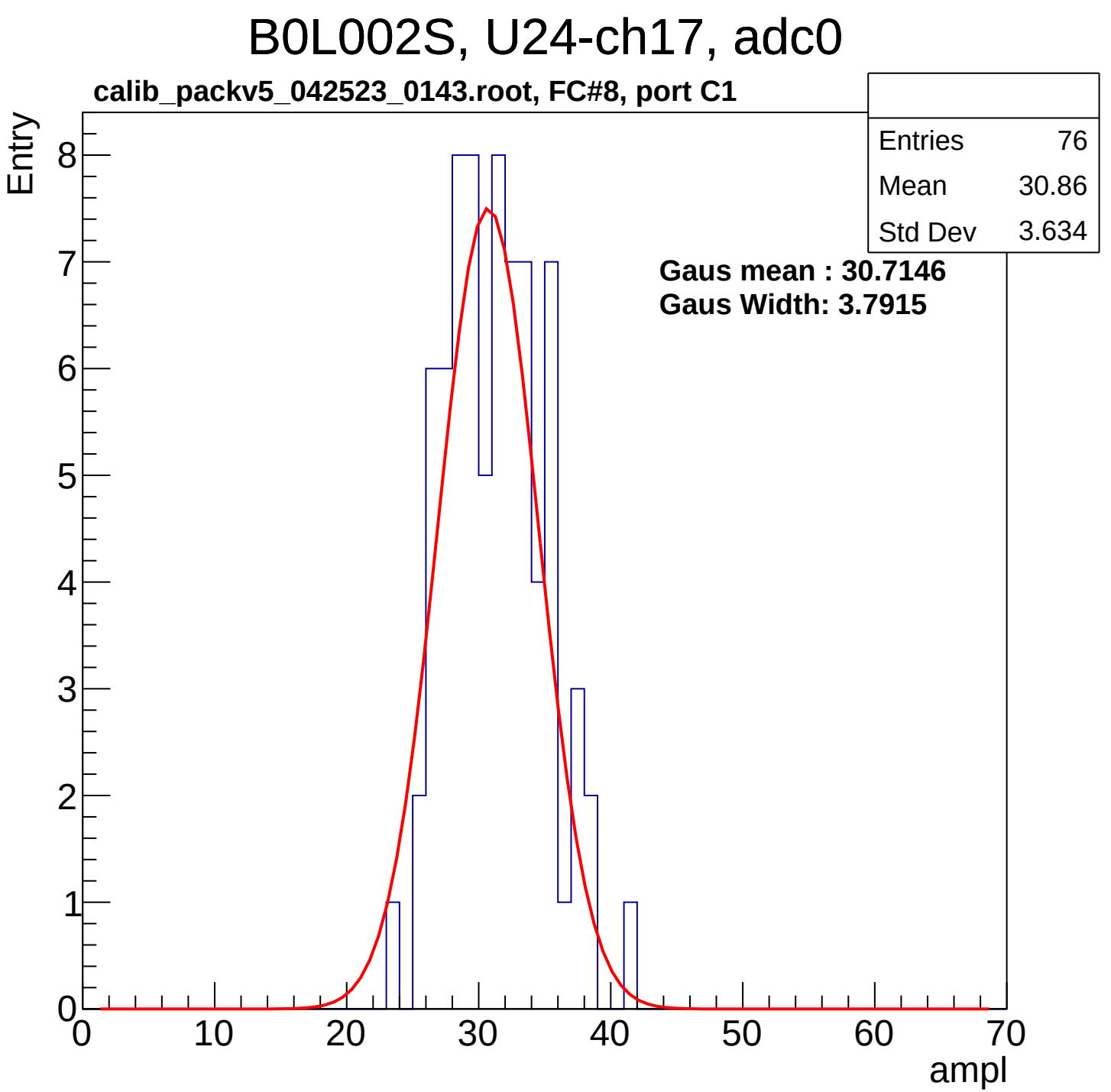
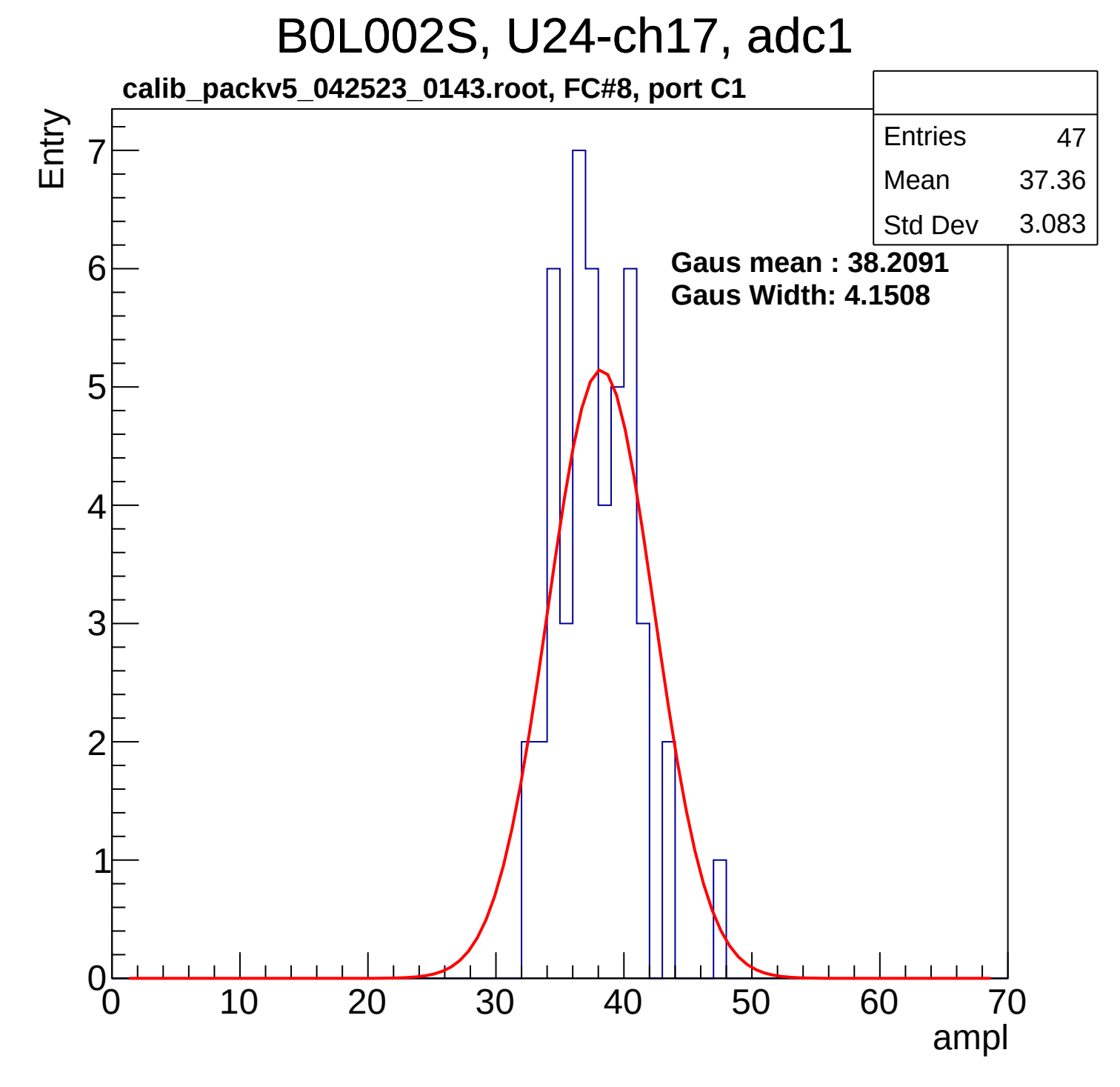
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.86
Std Dev 3.634
Gaus mean : 30.7146
Gaus Width: 3.7915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 37.36
Std Dev 3.083
Gaus mean : 38.2091
Gaus Width: 4.1508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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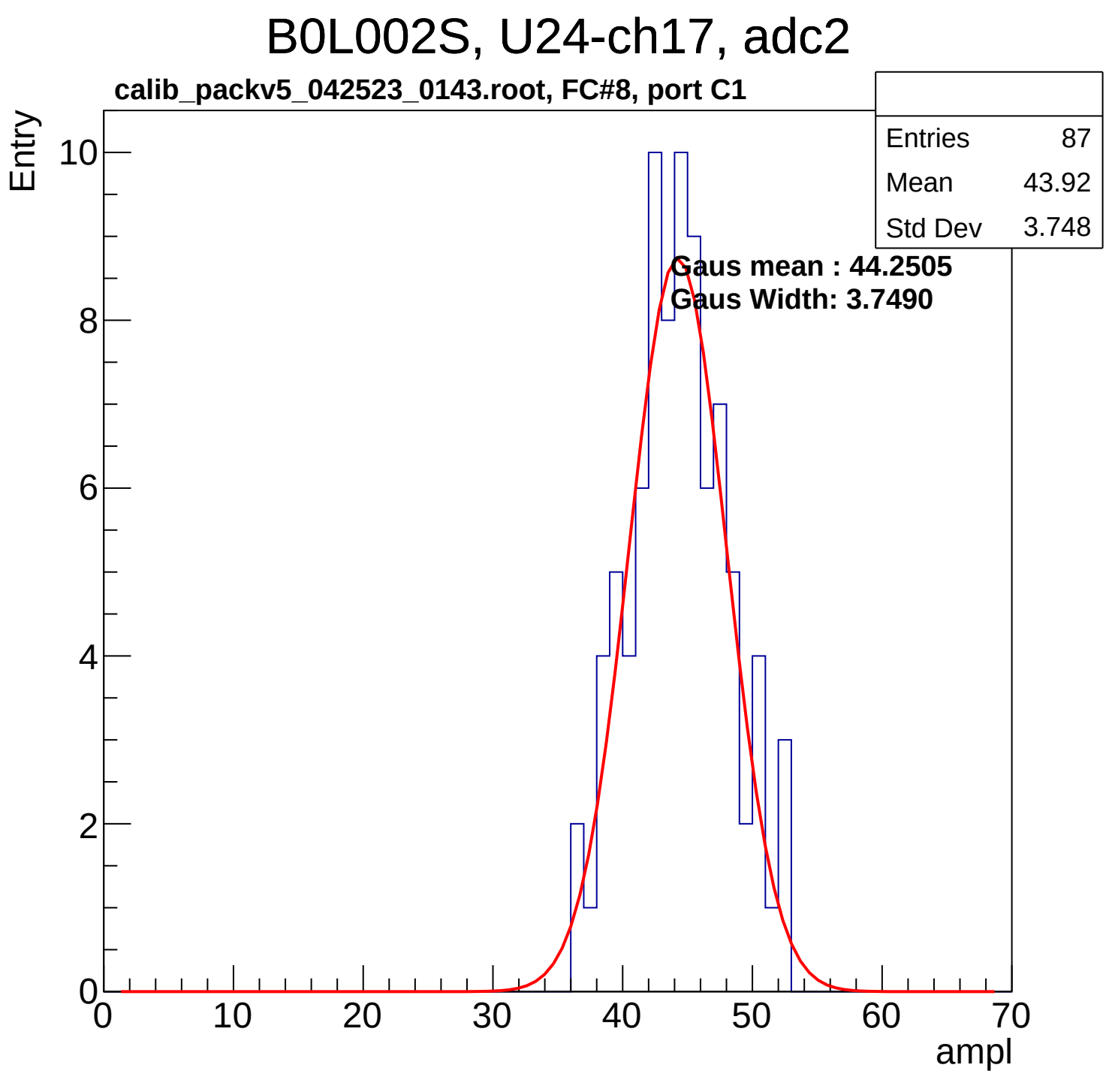
B0L002S, U24-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 43.92
Std Dev 3.748
Gaus mean : 44.2505
Gaus Width: 3.7490
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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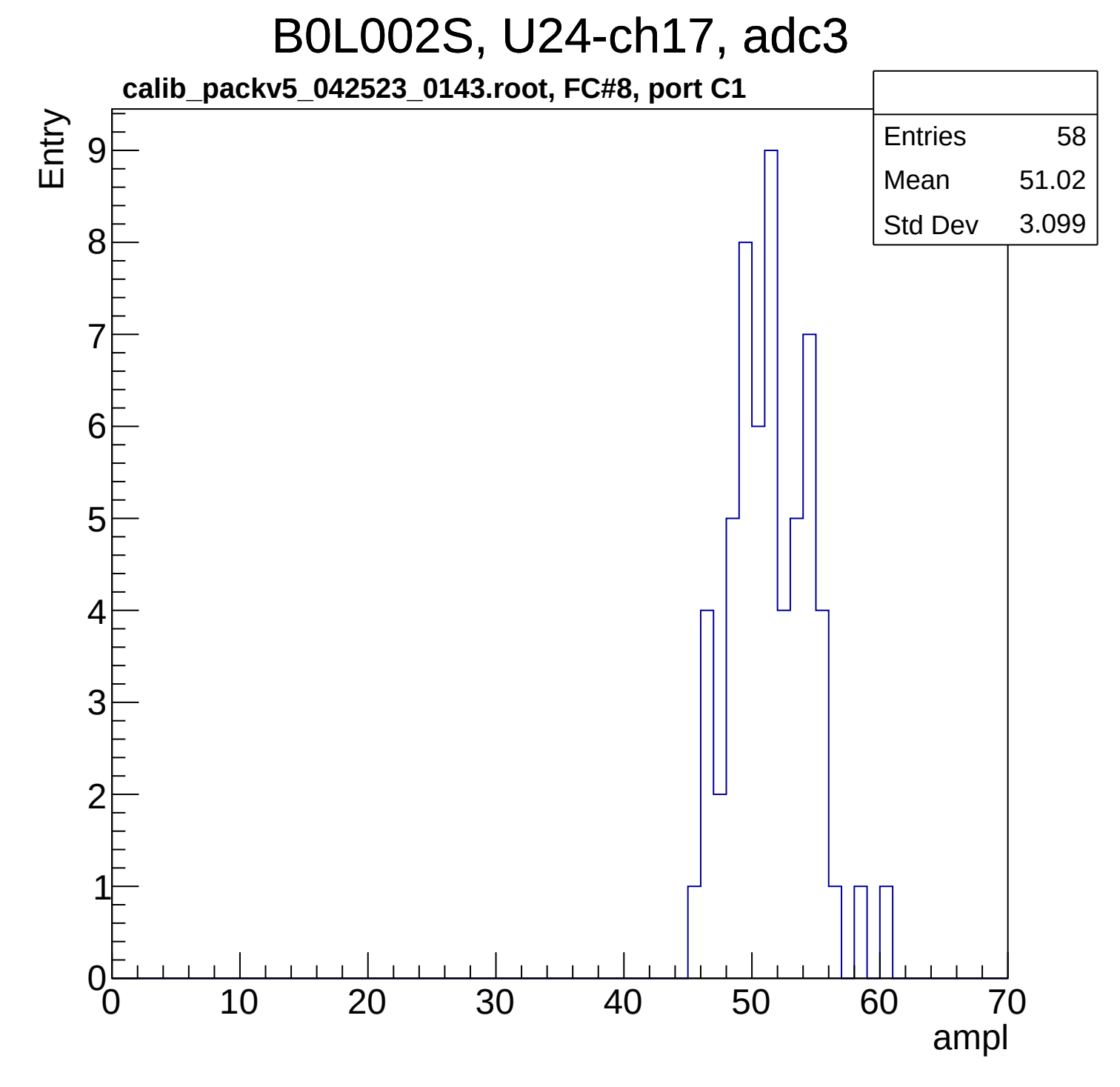
B0L002S, U24-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.02
Std Dev 3.099
Entry
ampl
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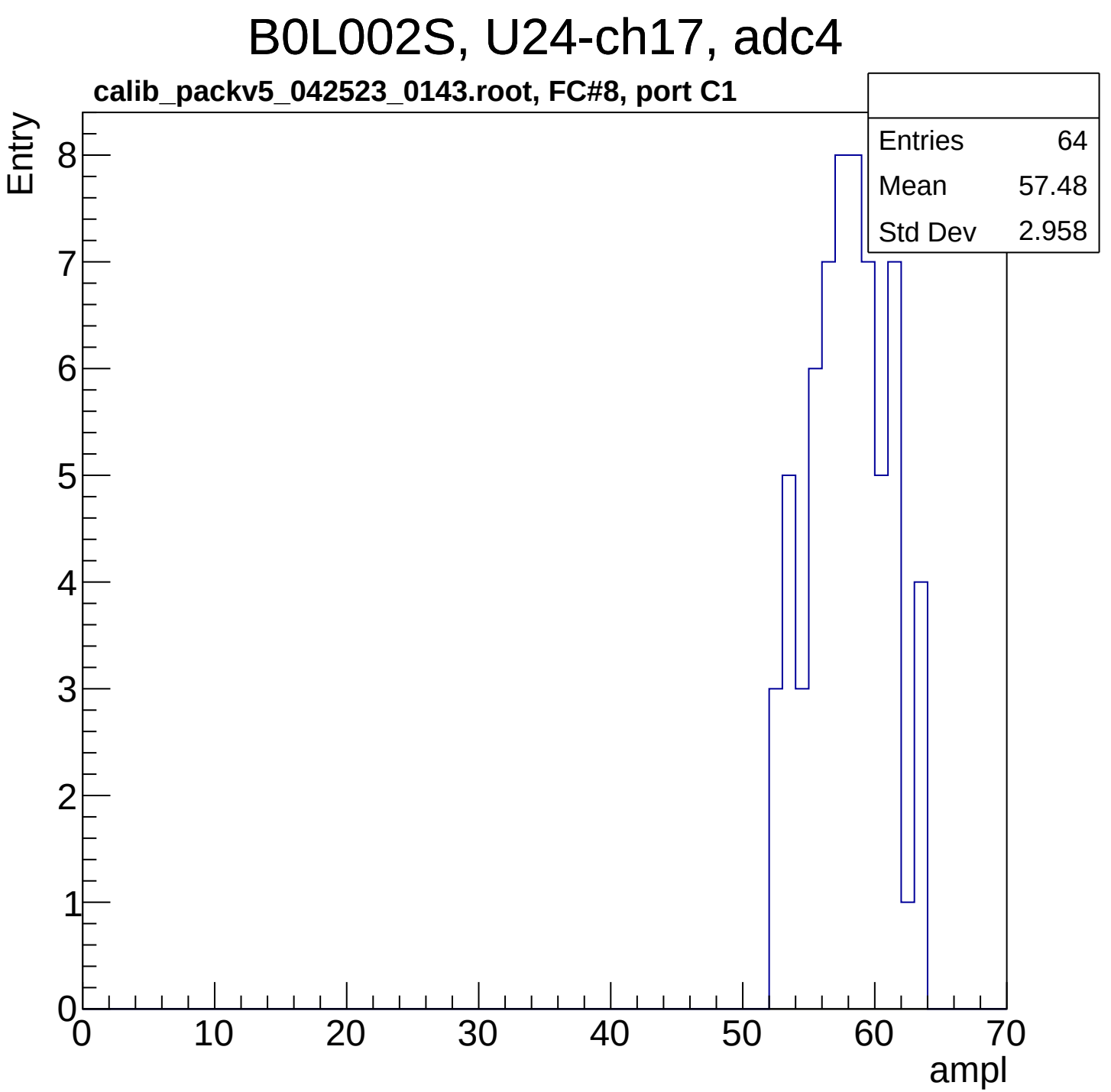
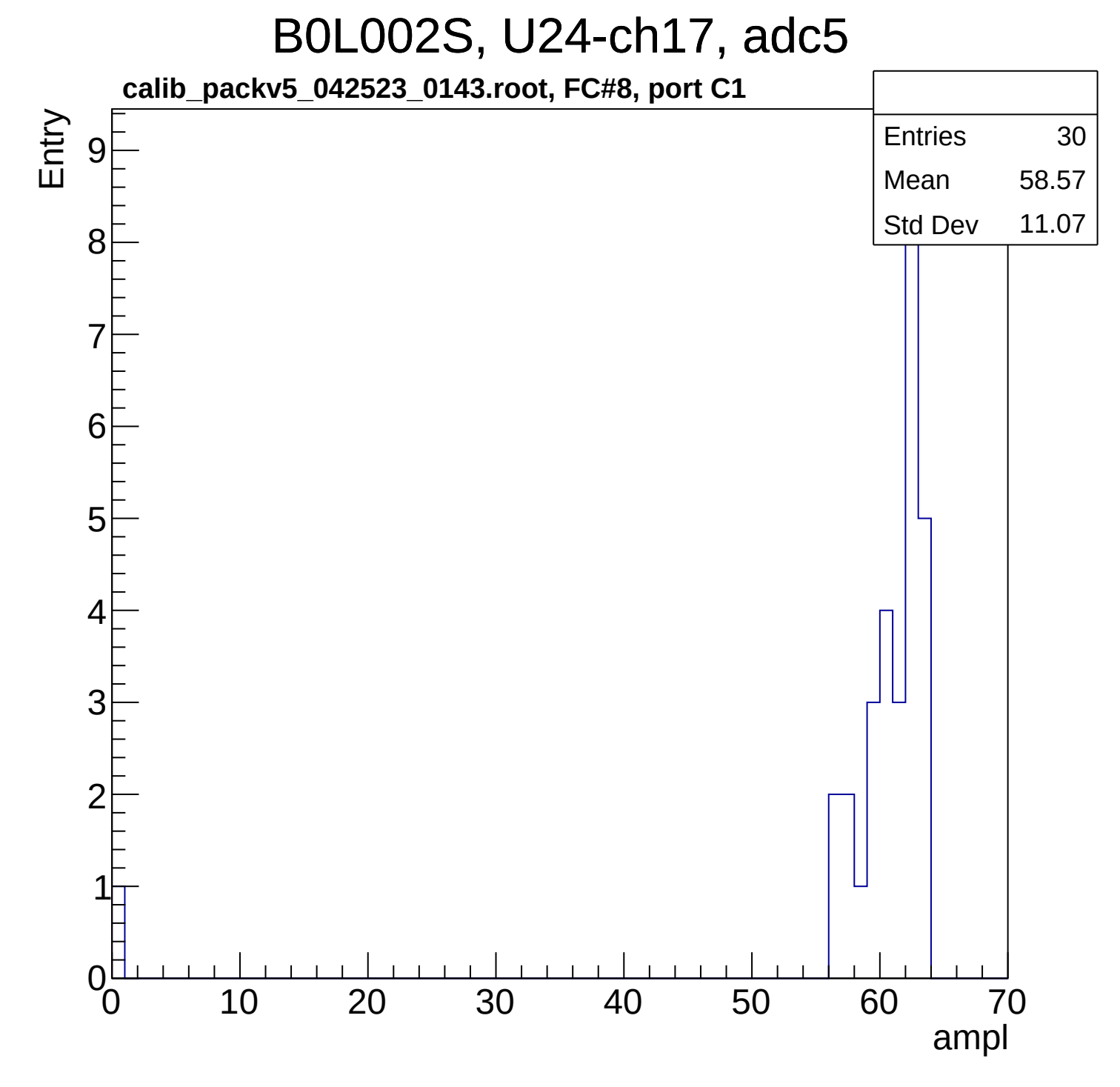
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 57.48
Std Dev 2.958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.57
Std Dev 11.07
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
50
60
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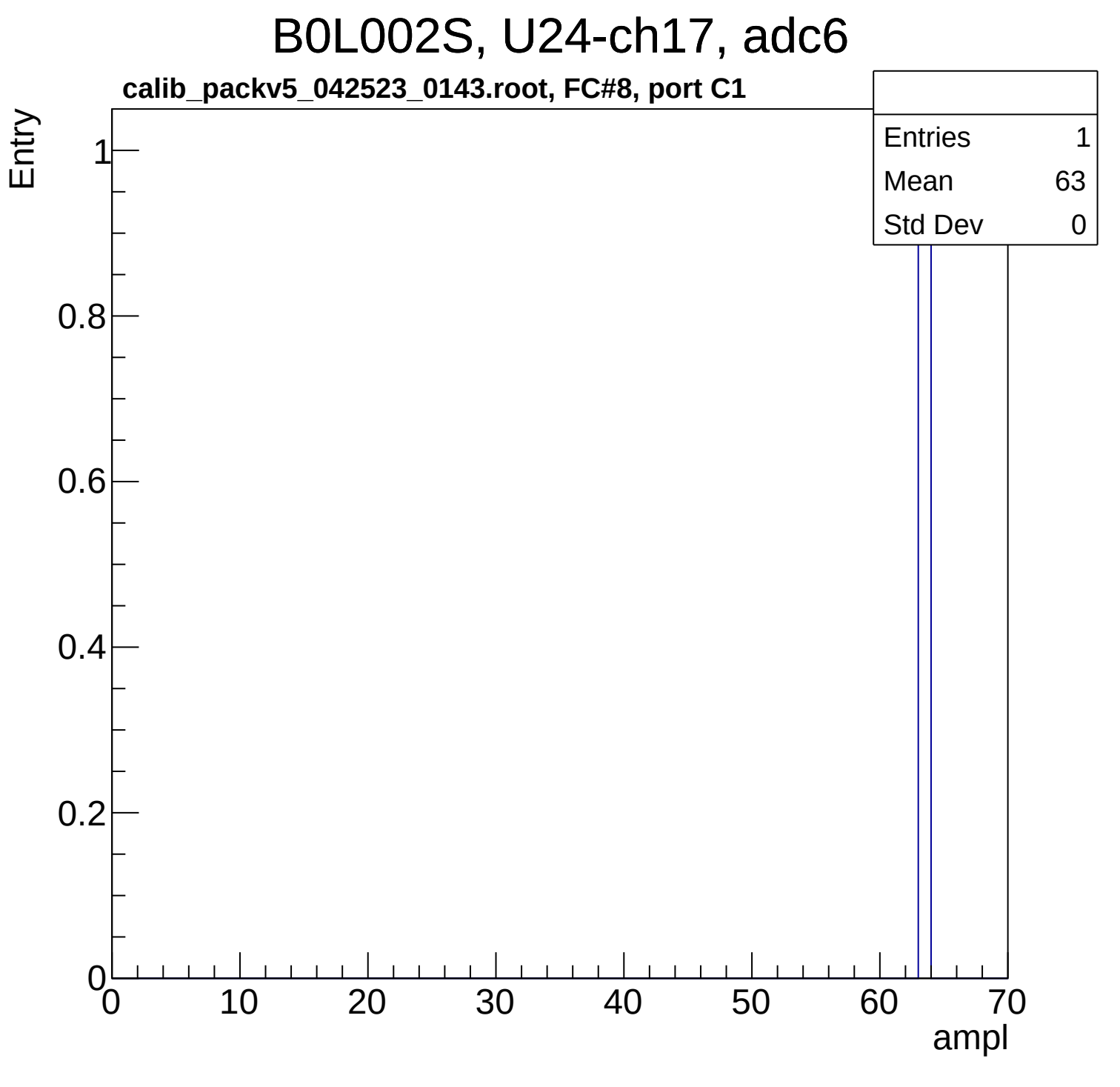
B0L002S, U24-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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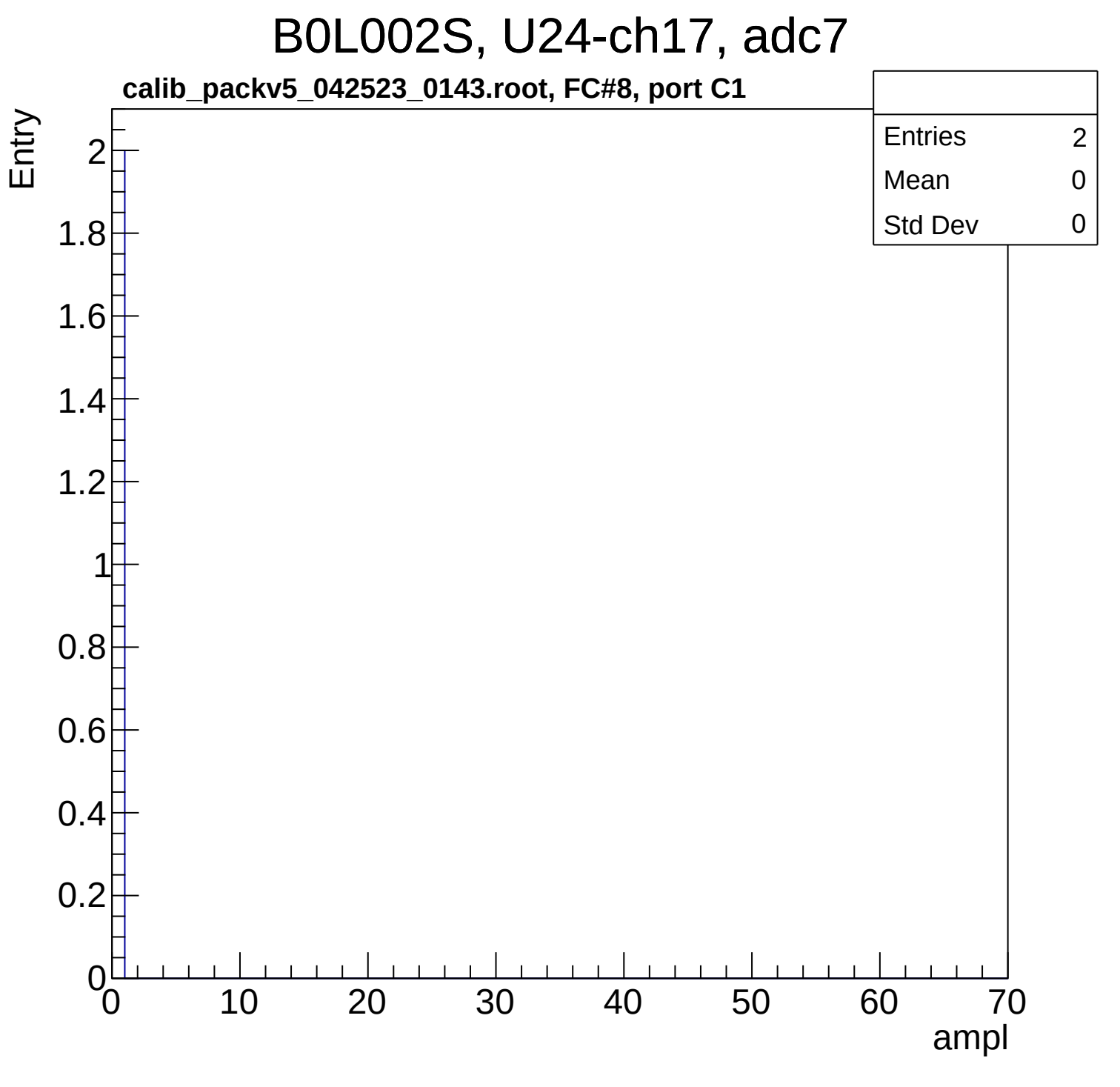
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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10
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30
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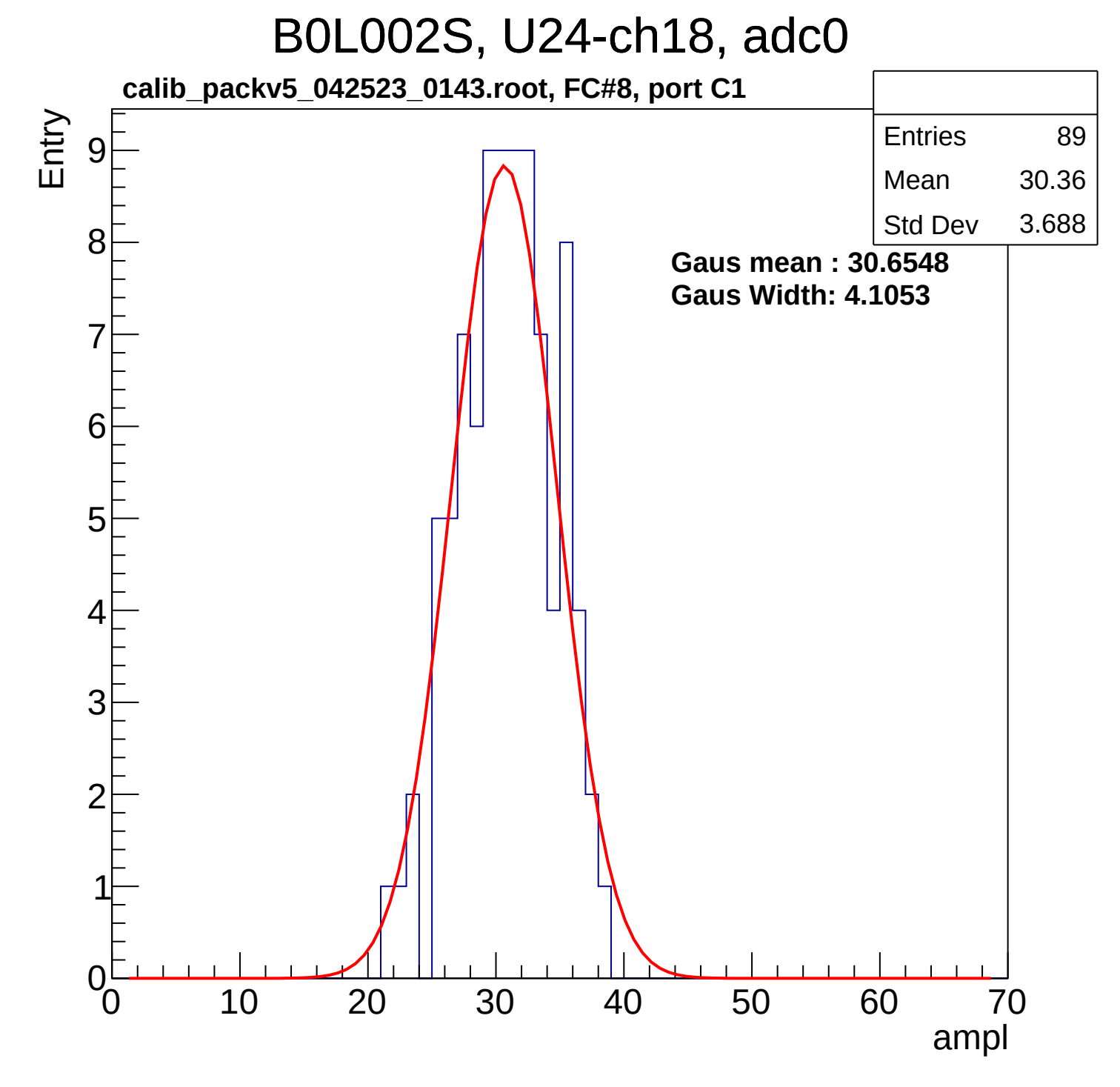
50
60
70
ampl


B0L002S, U24-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 30.36
Std Dev 3.688
Gaus mean : 30.6548
Gaus Width: 4.1053
Entry
ampl
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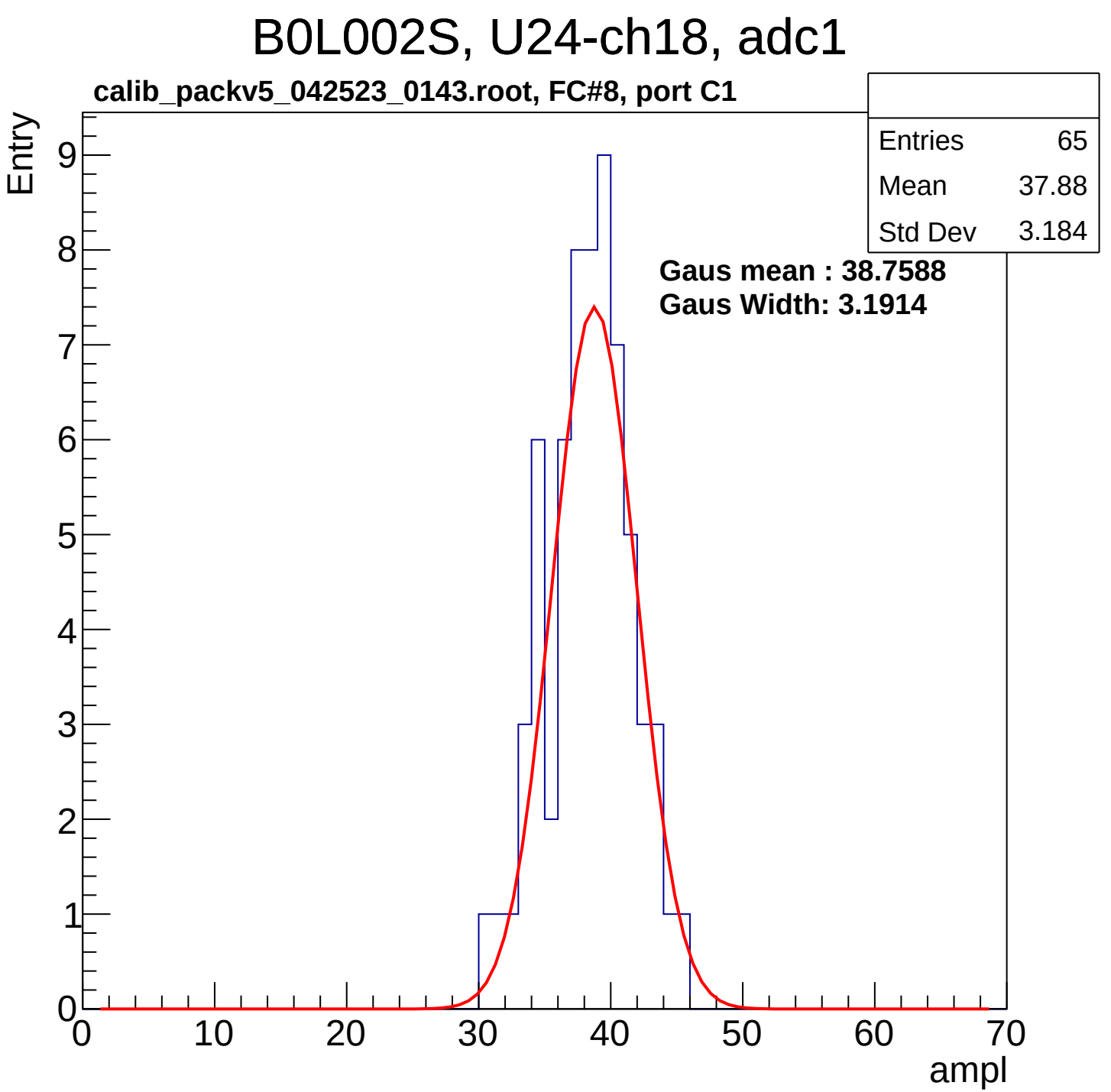
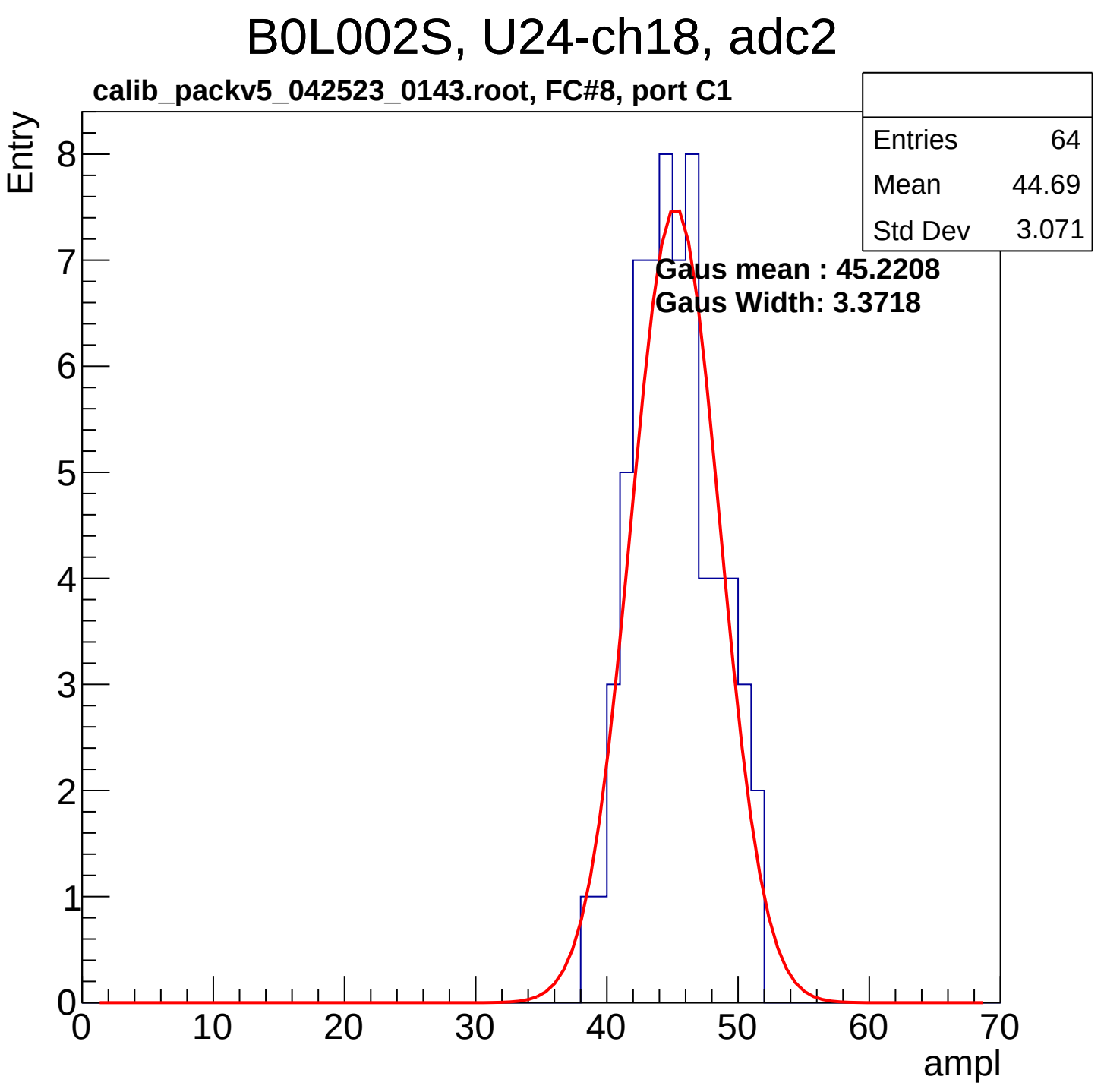
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.88
Std Dev 3.184
Gaus mean : 38.7588
Gaus Width: 3.1914
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U24-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.69
Std Dev 3.071
Gaus mean : 45.2208
Gaus Width: 3.3718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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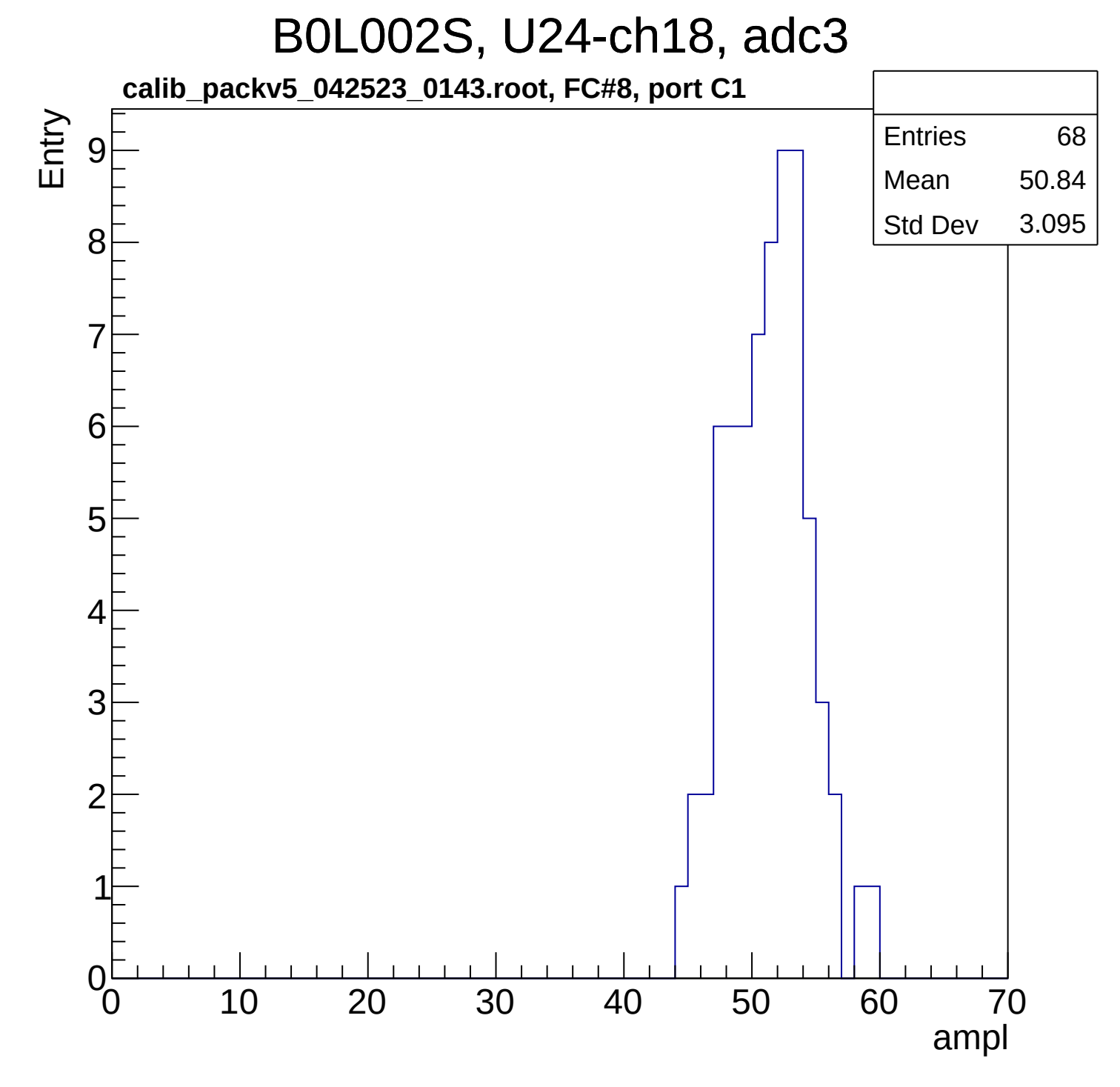
40
50
60
70


B0L002S, U24-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.84
Std Dev 3.095
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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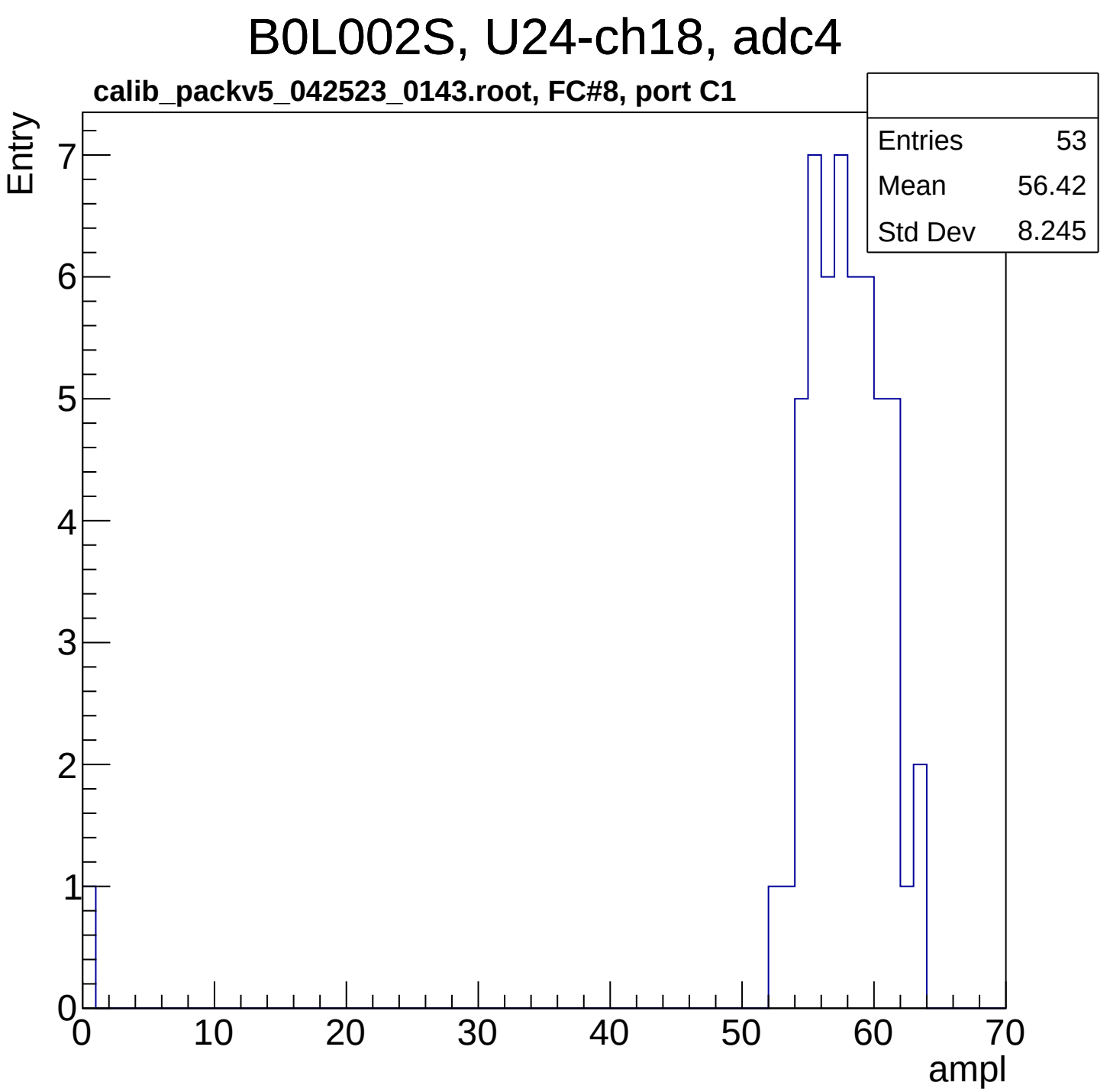
40
50
60
70


B0L002S, U24-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.42
Std Dev 8.245
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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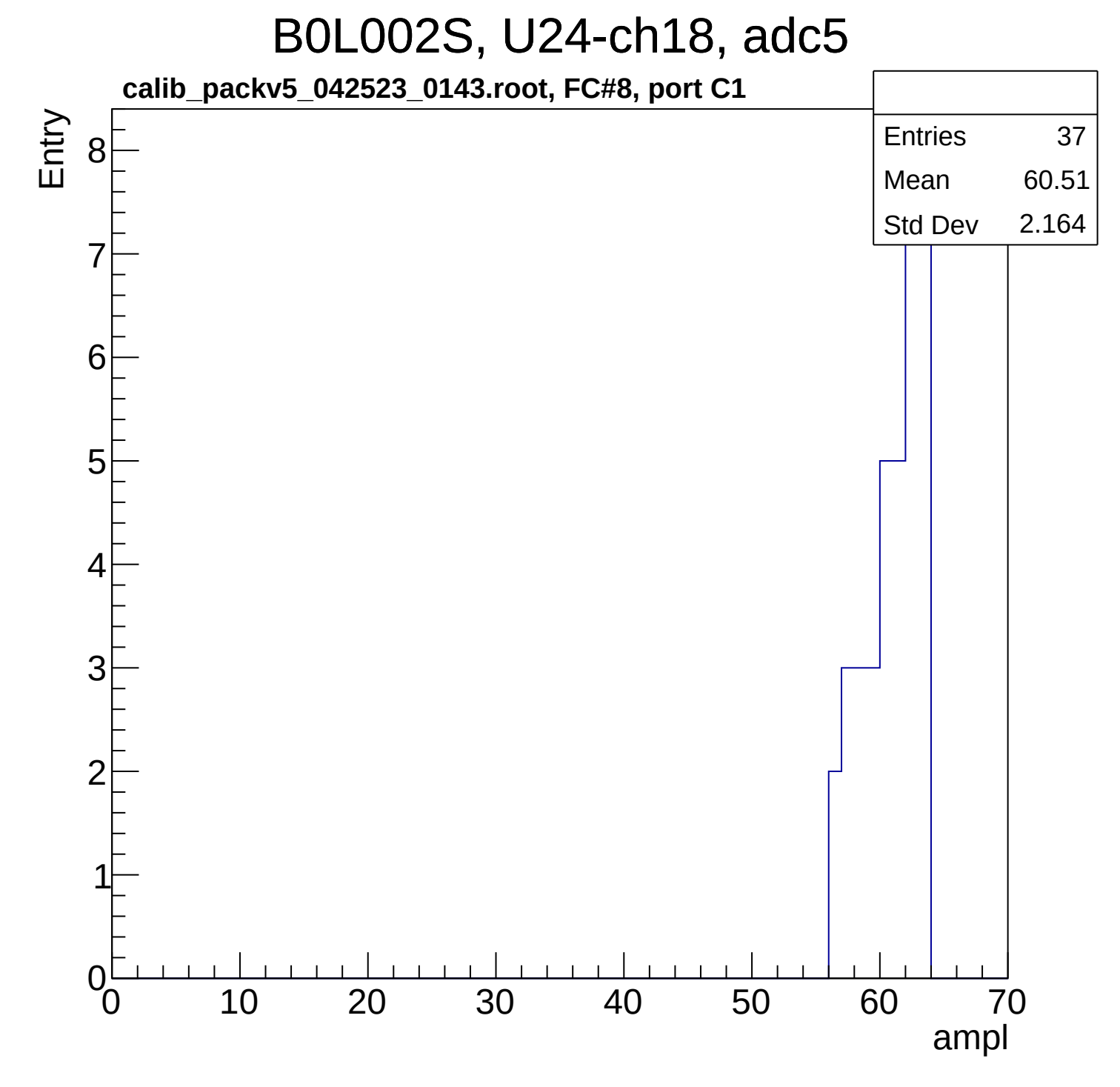
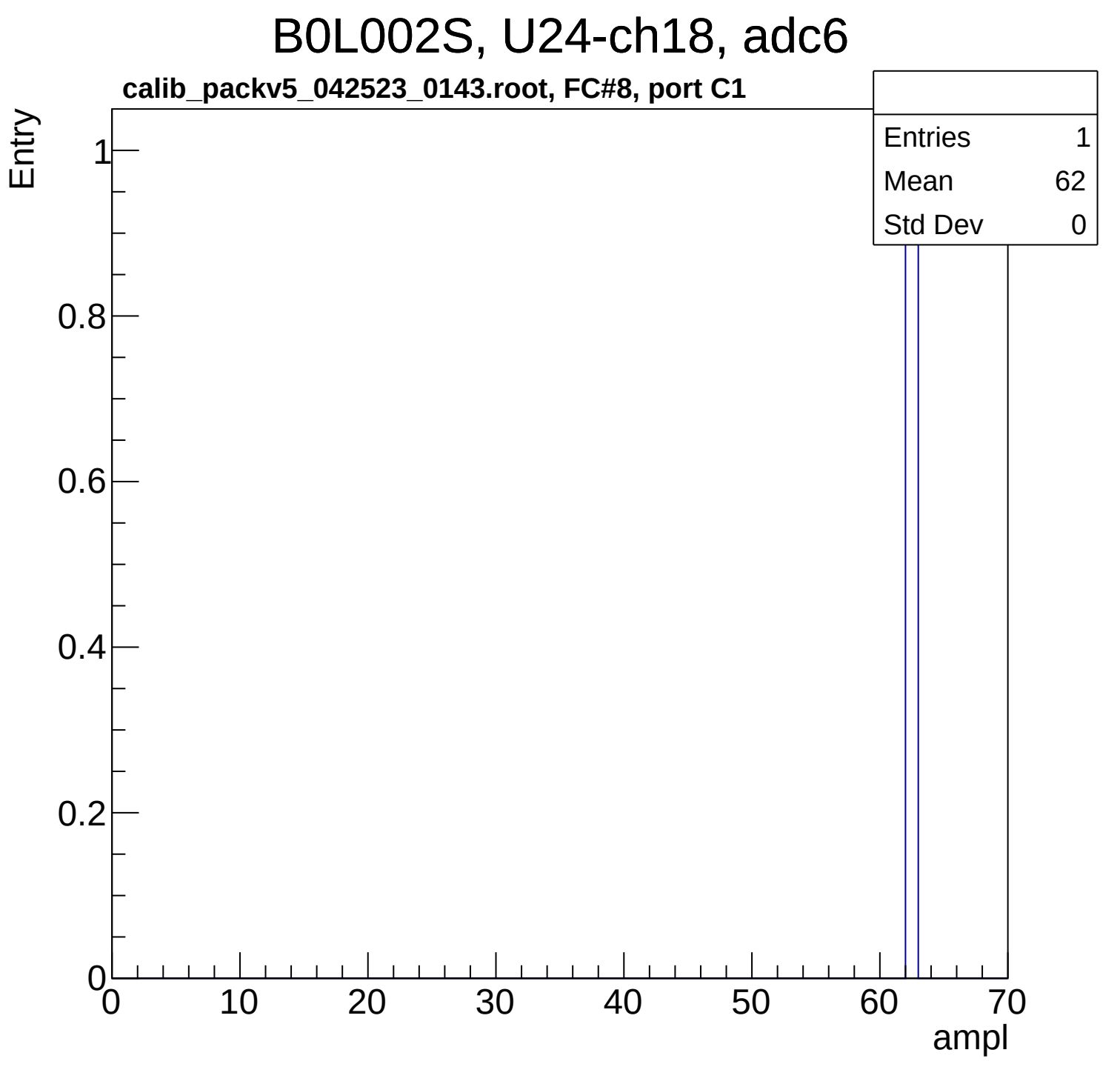
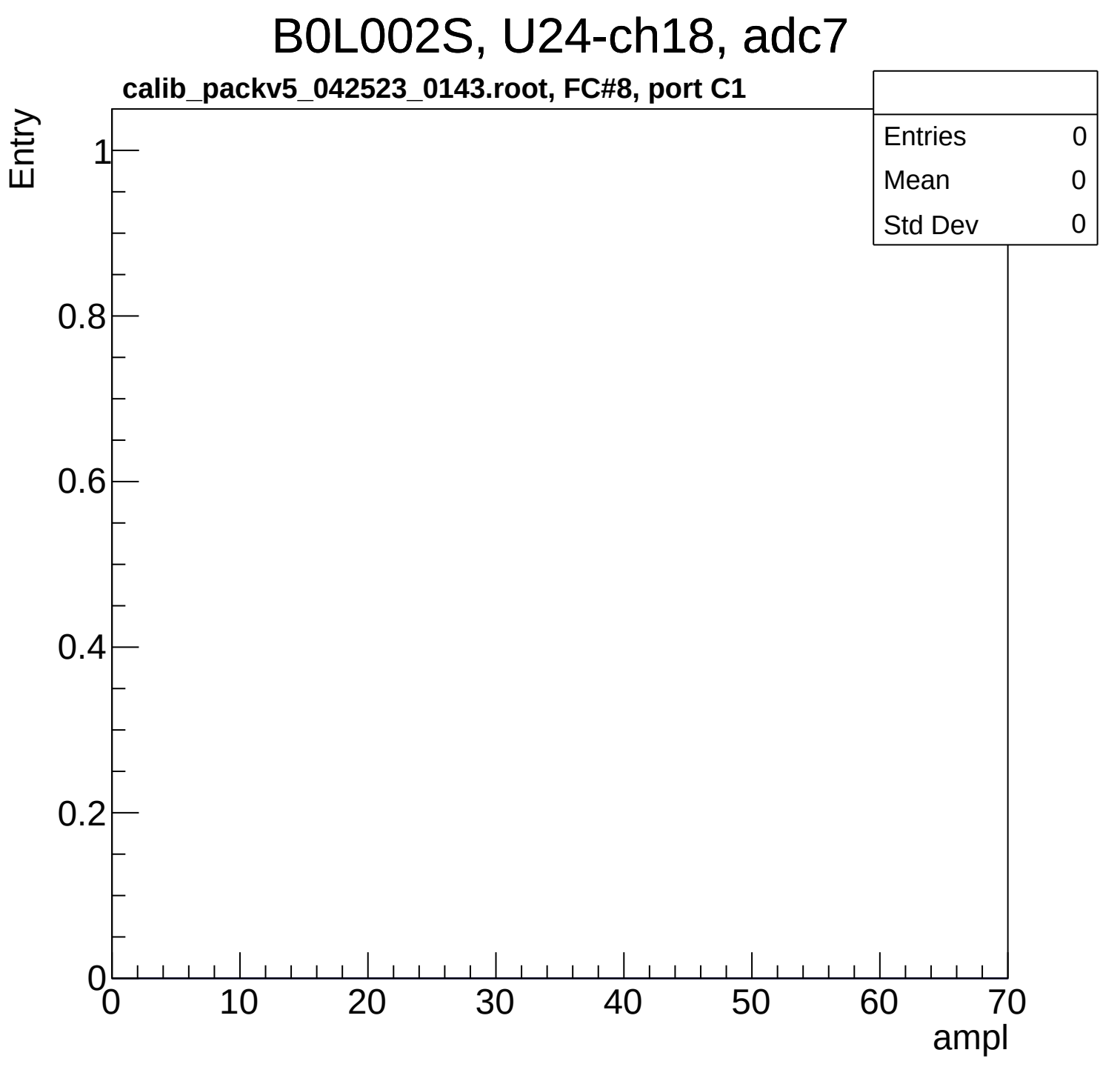

B0L002S, U24-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 60.51
Std Dev 2.164
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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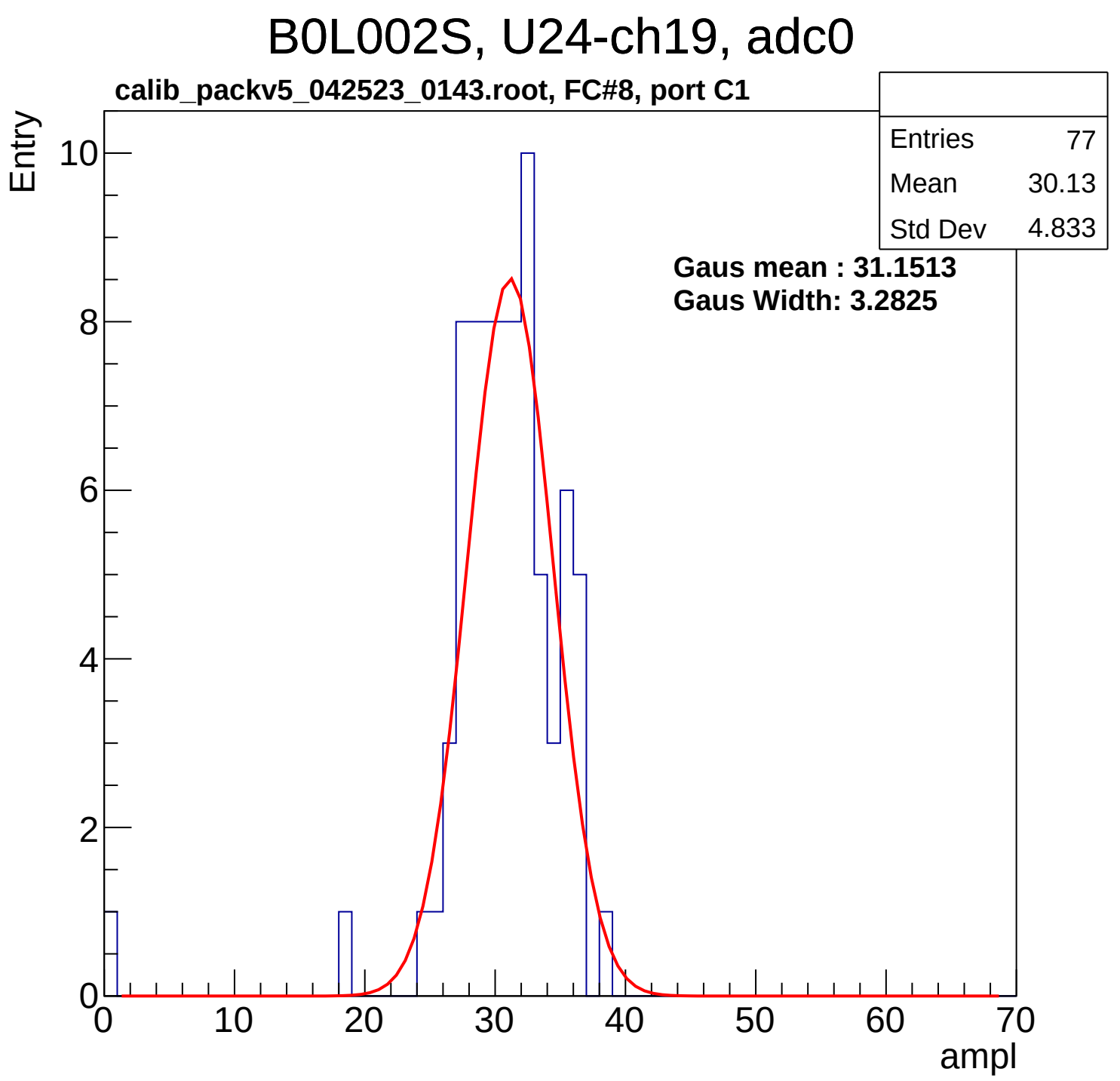
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.13
Std Dev 4.833
Gaus mean : 31.1513
Gaus Width: 3.2825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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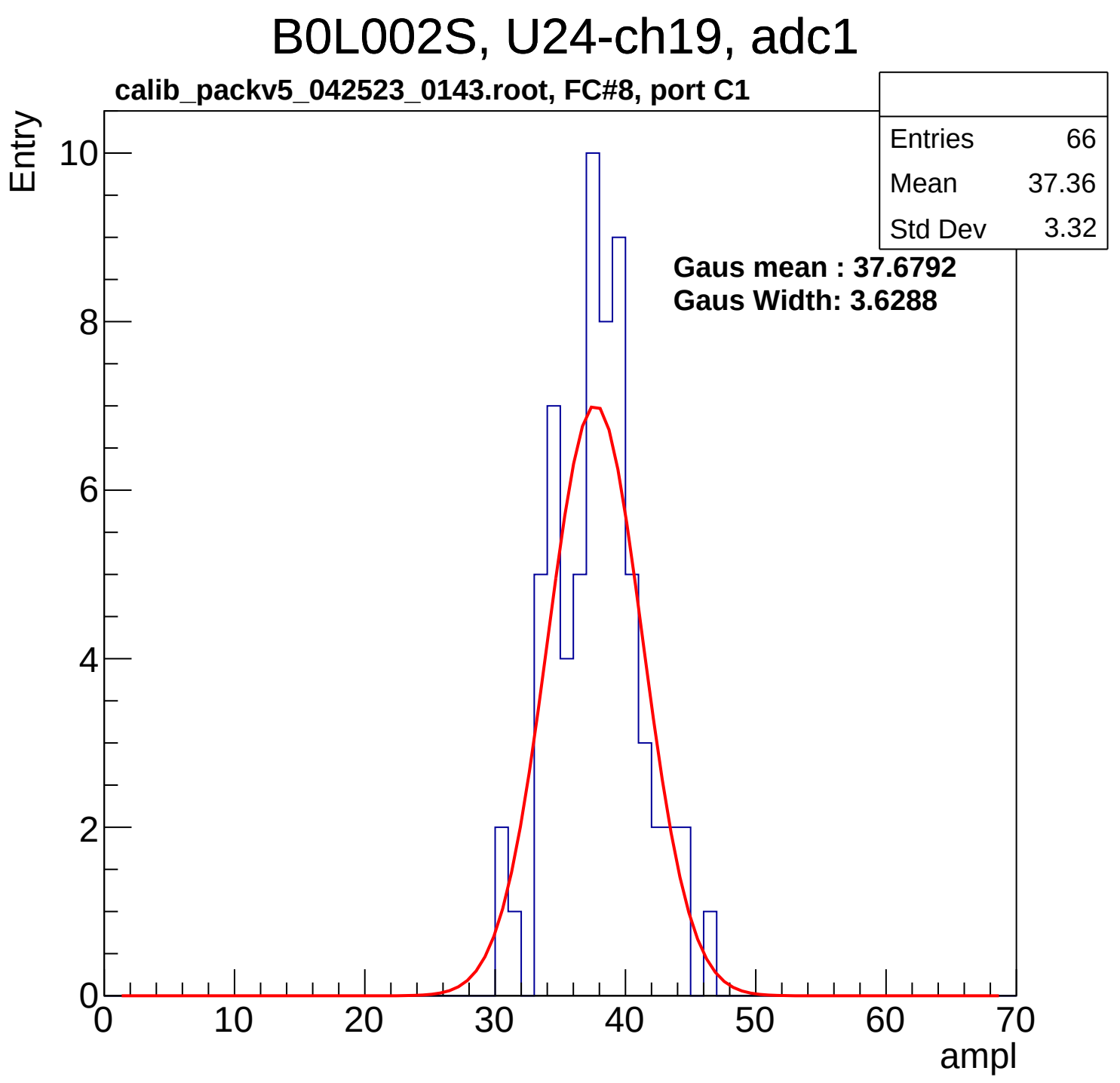
B0L002S, U24-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.36
Std Dev 3.32
Gaus mean : 37.6792
Gaus Width: 3.6288
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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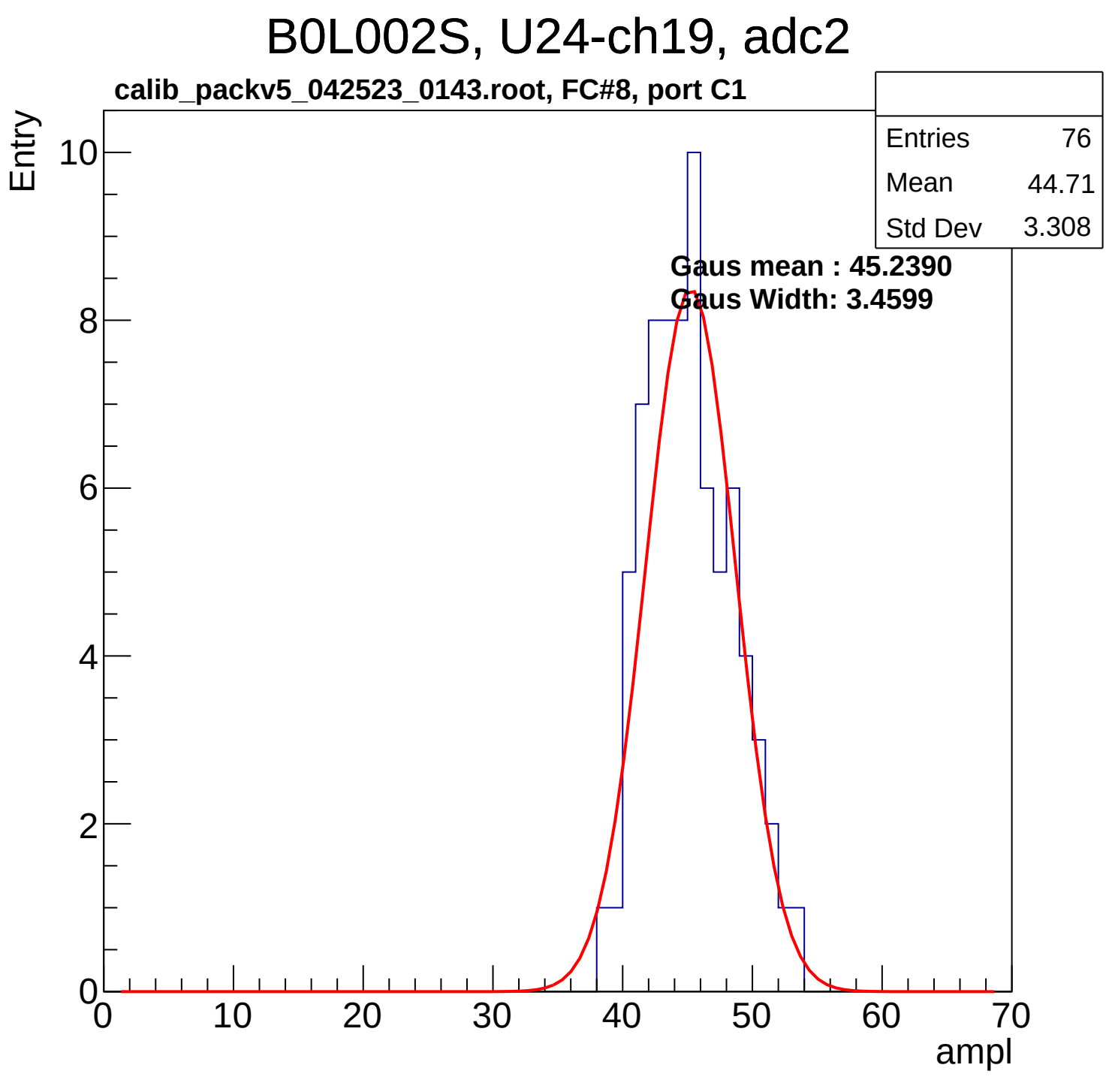
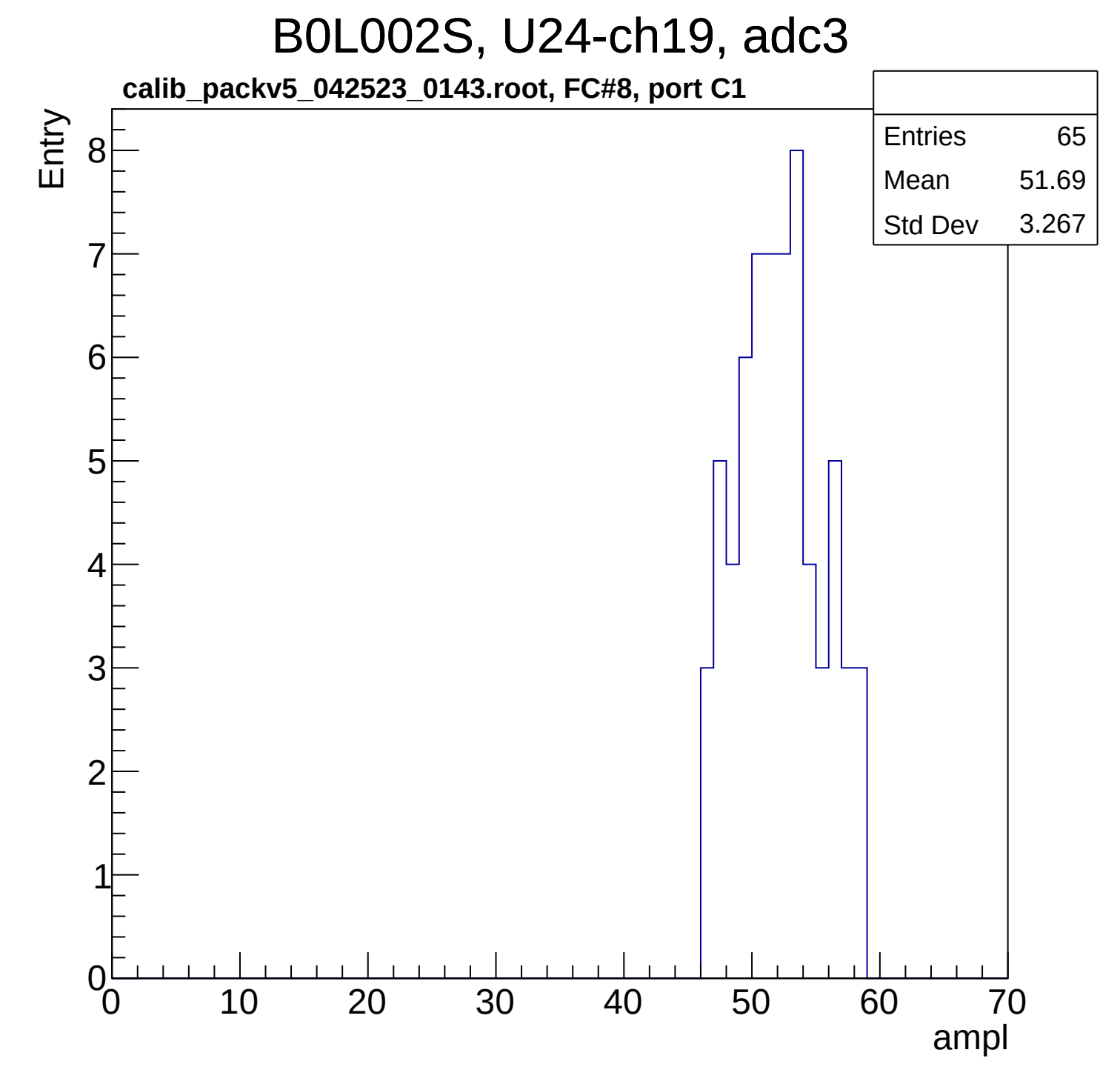
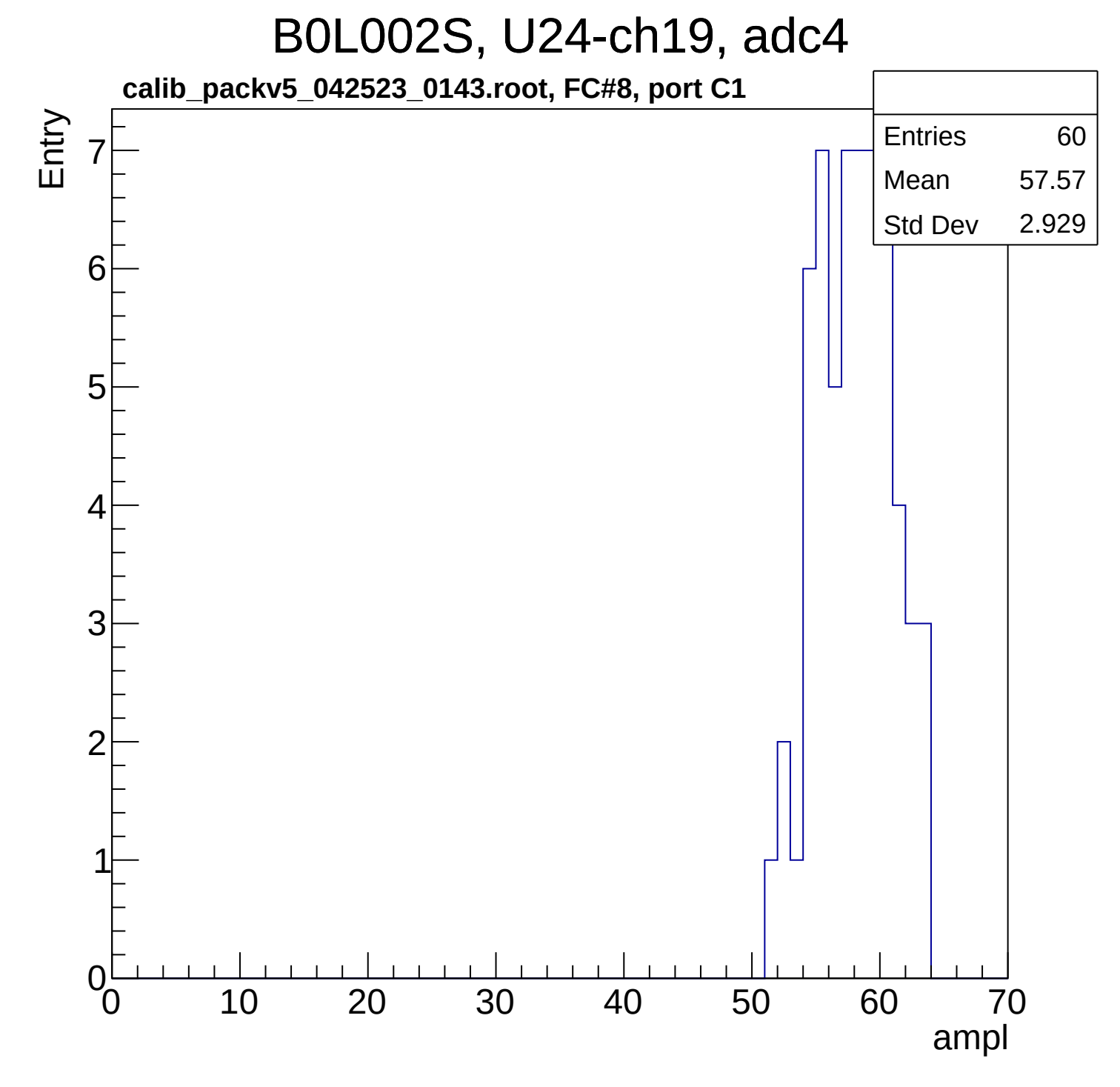
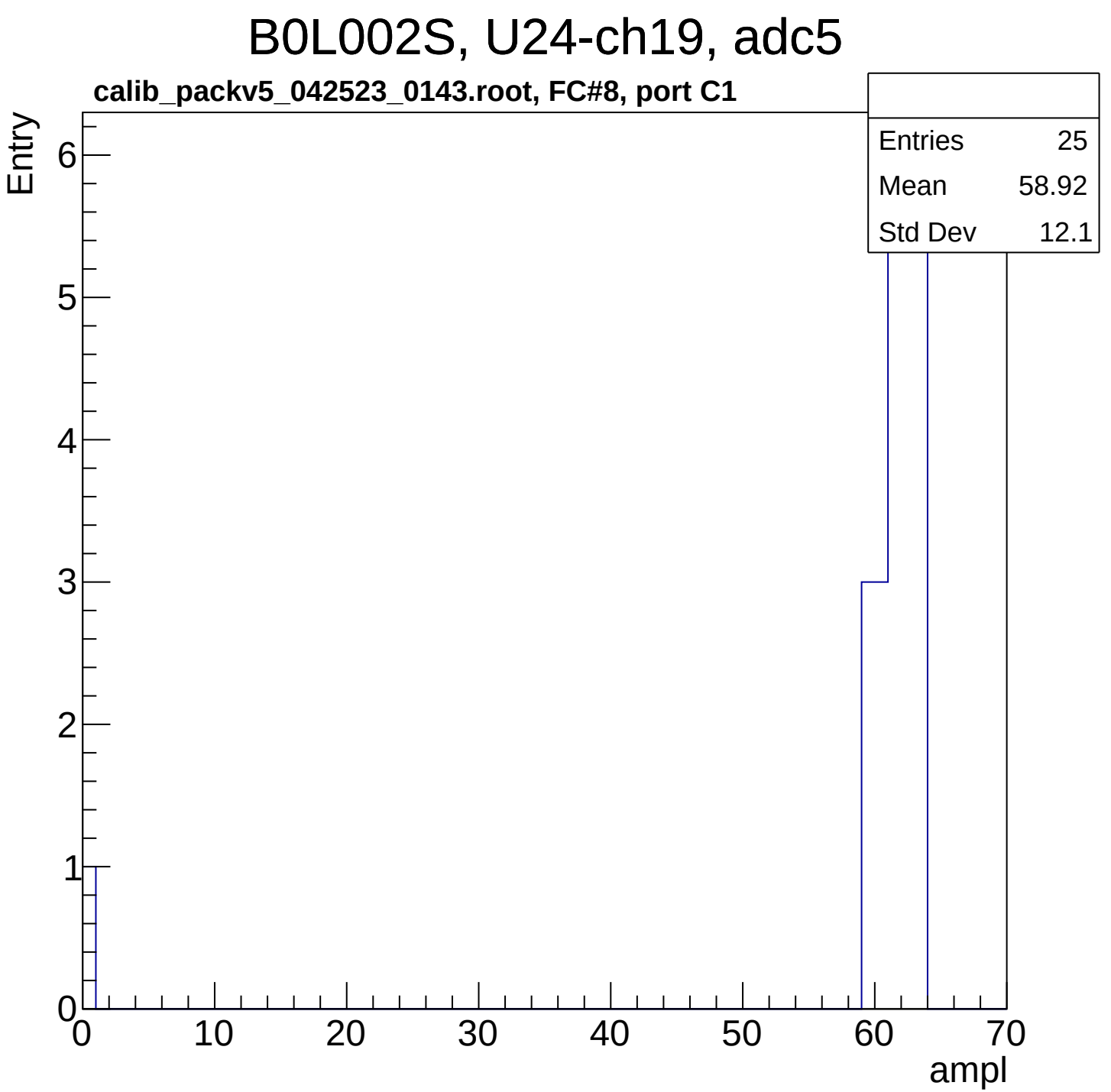
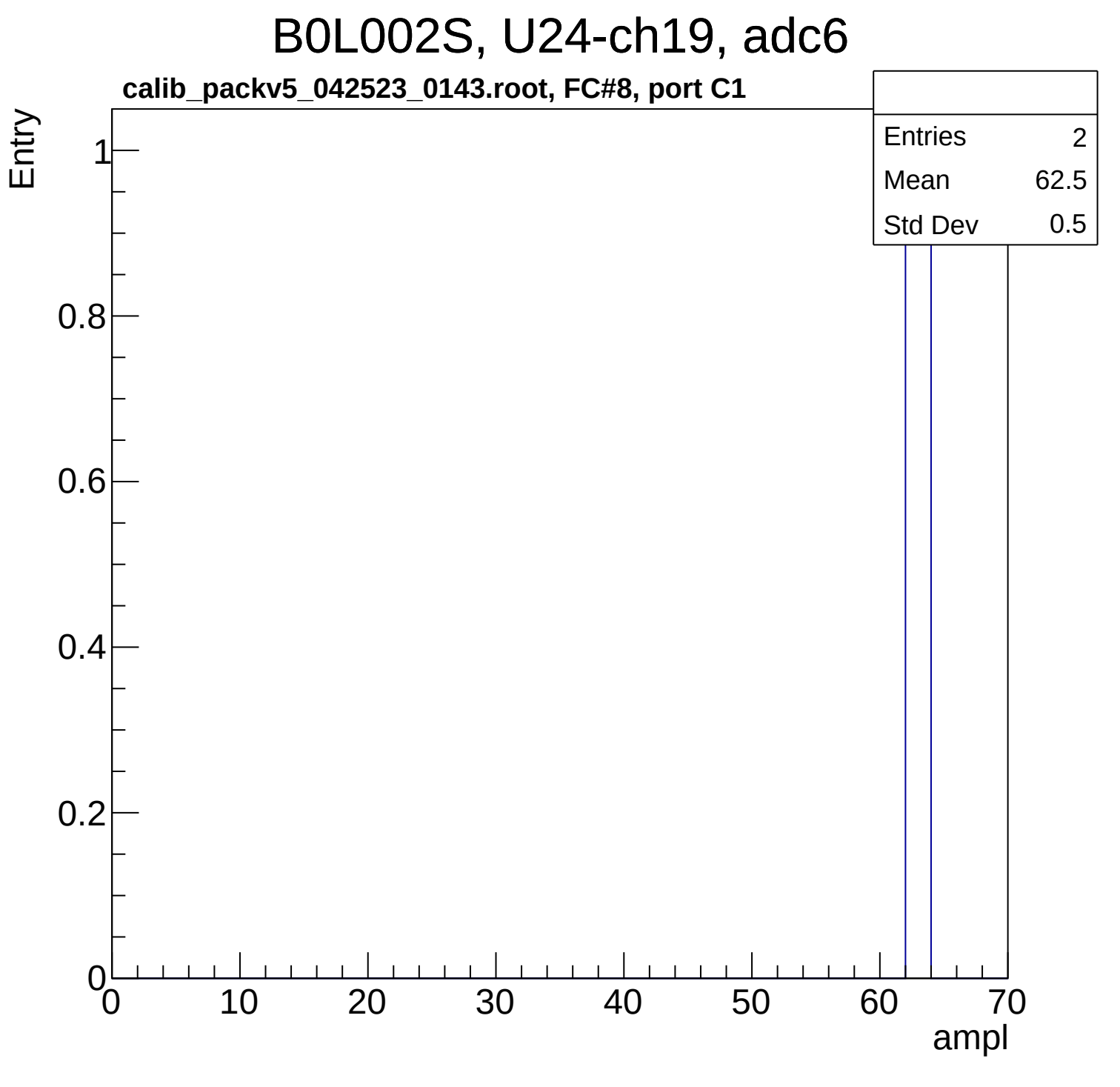
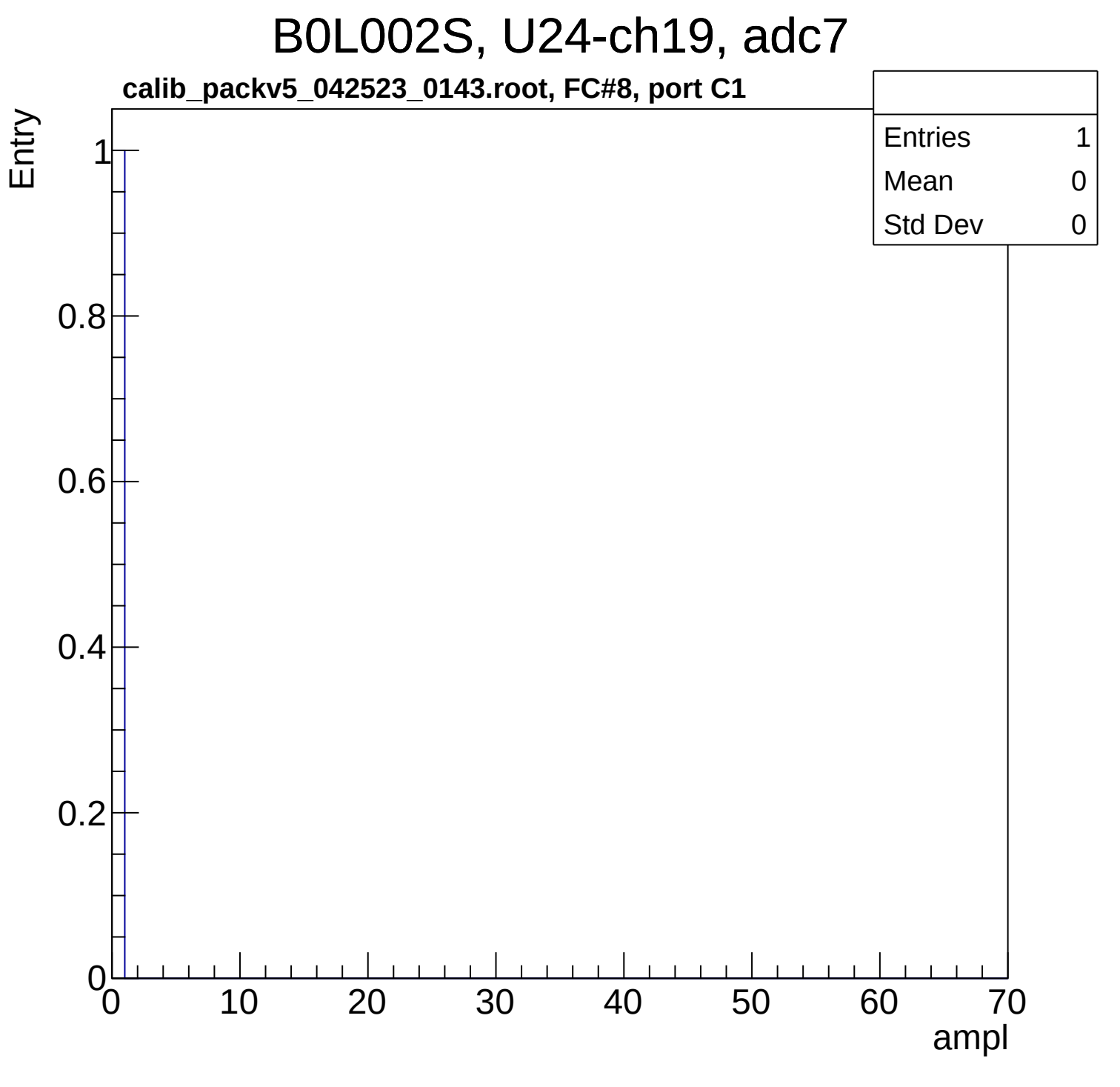

B0L002S, U24-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 44.71
Std Dev 3.308
Gaus mean : 45.2390
Gaus Width: 3.4599
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

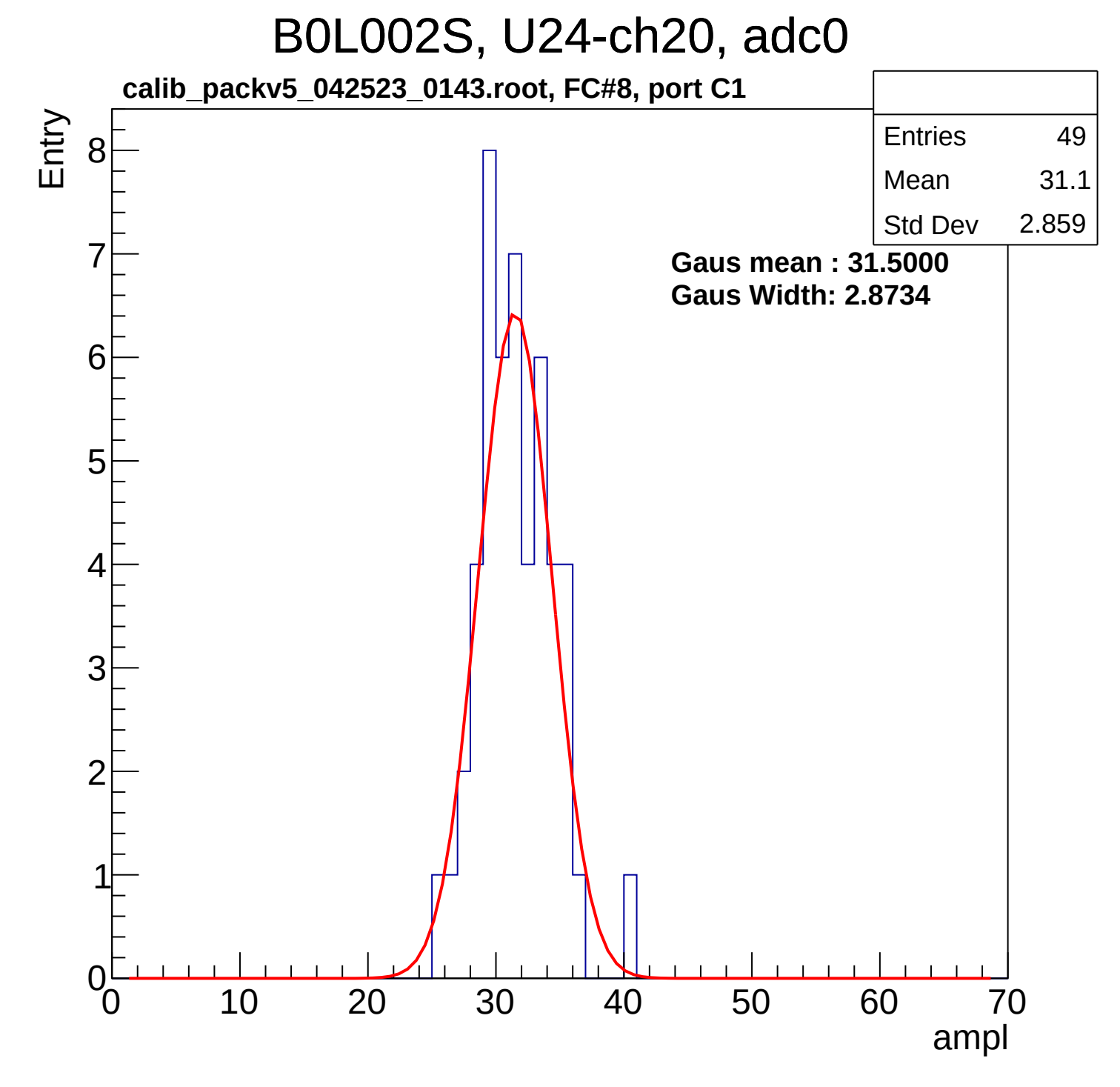

B0L002S, U24-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.69
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

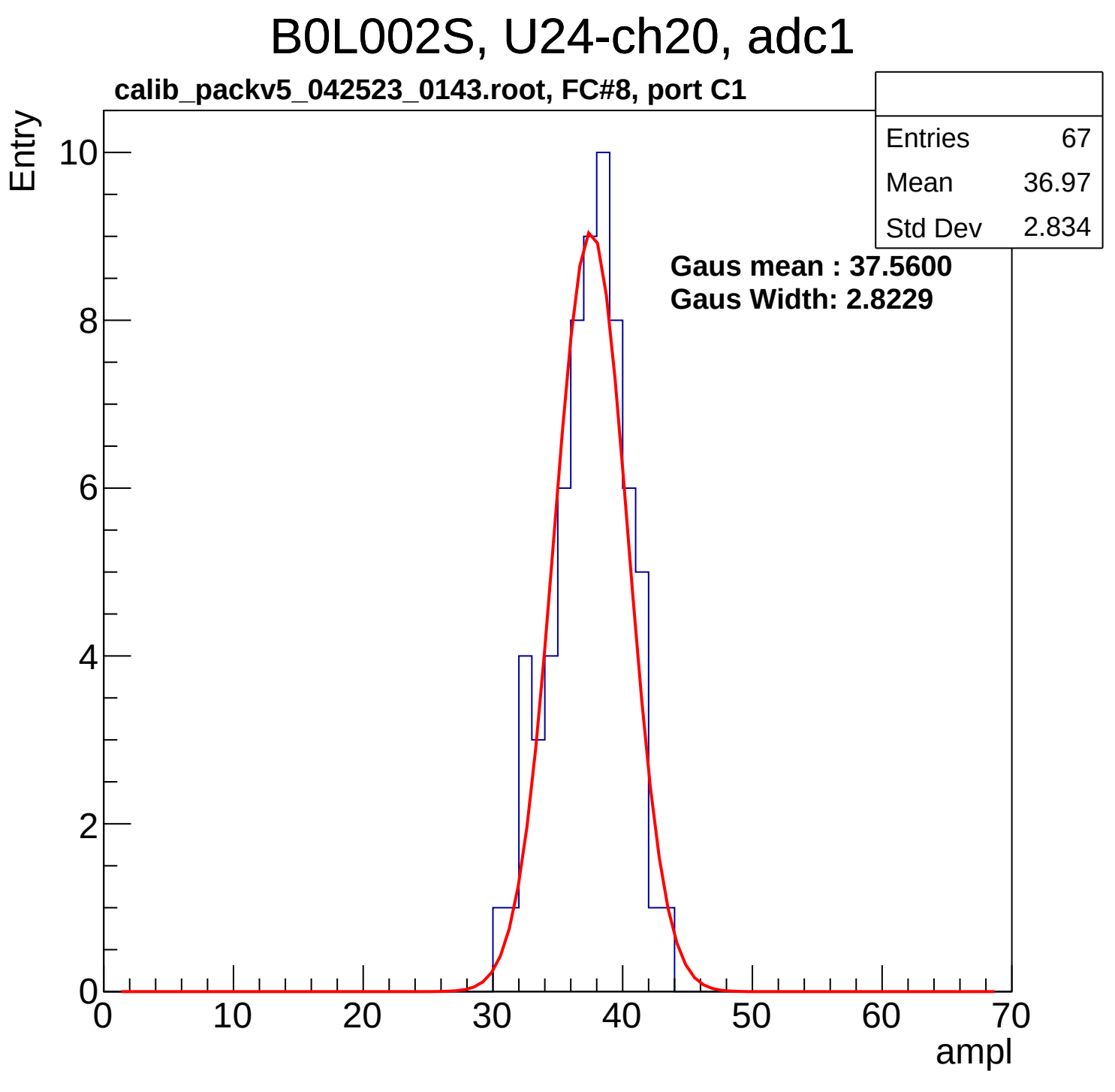

B0L002S, U24-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.57
Std Dev 2.929
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

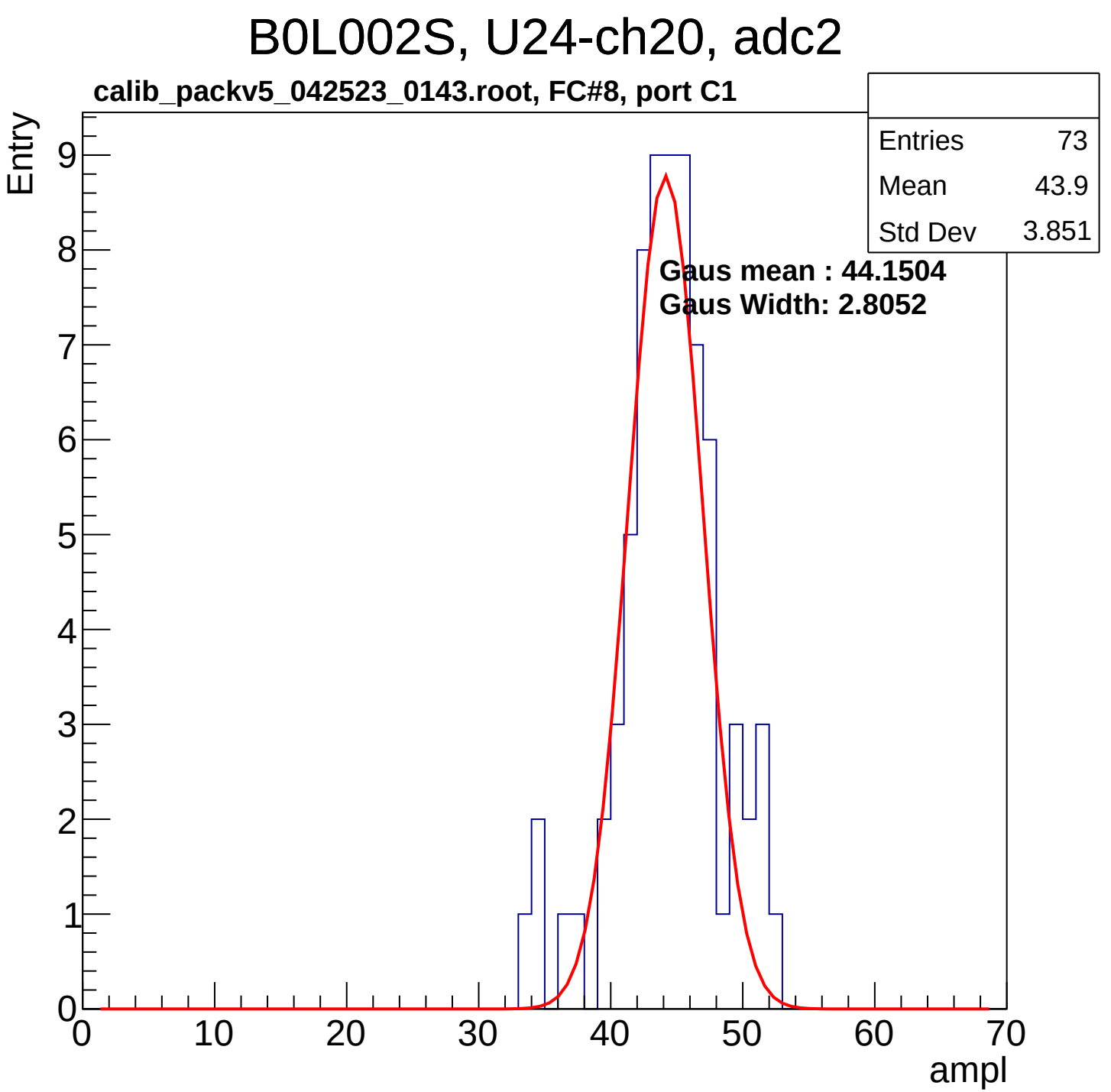

B0L002S, U24-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.92
Std Dev 12.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 31.1
Std Dev 2.859
Gaus mean : 31.5000
Gaus Width: 2.8734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.97
Std Dev 2.834
Gaus mean : 37.5600
Gaus Width: 2.8229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.9
Std Dev 3.851
Gaus mean : 44.1504
Gaus Width: 2.8052
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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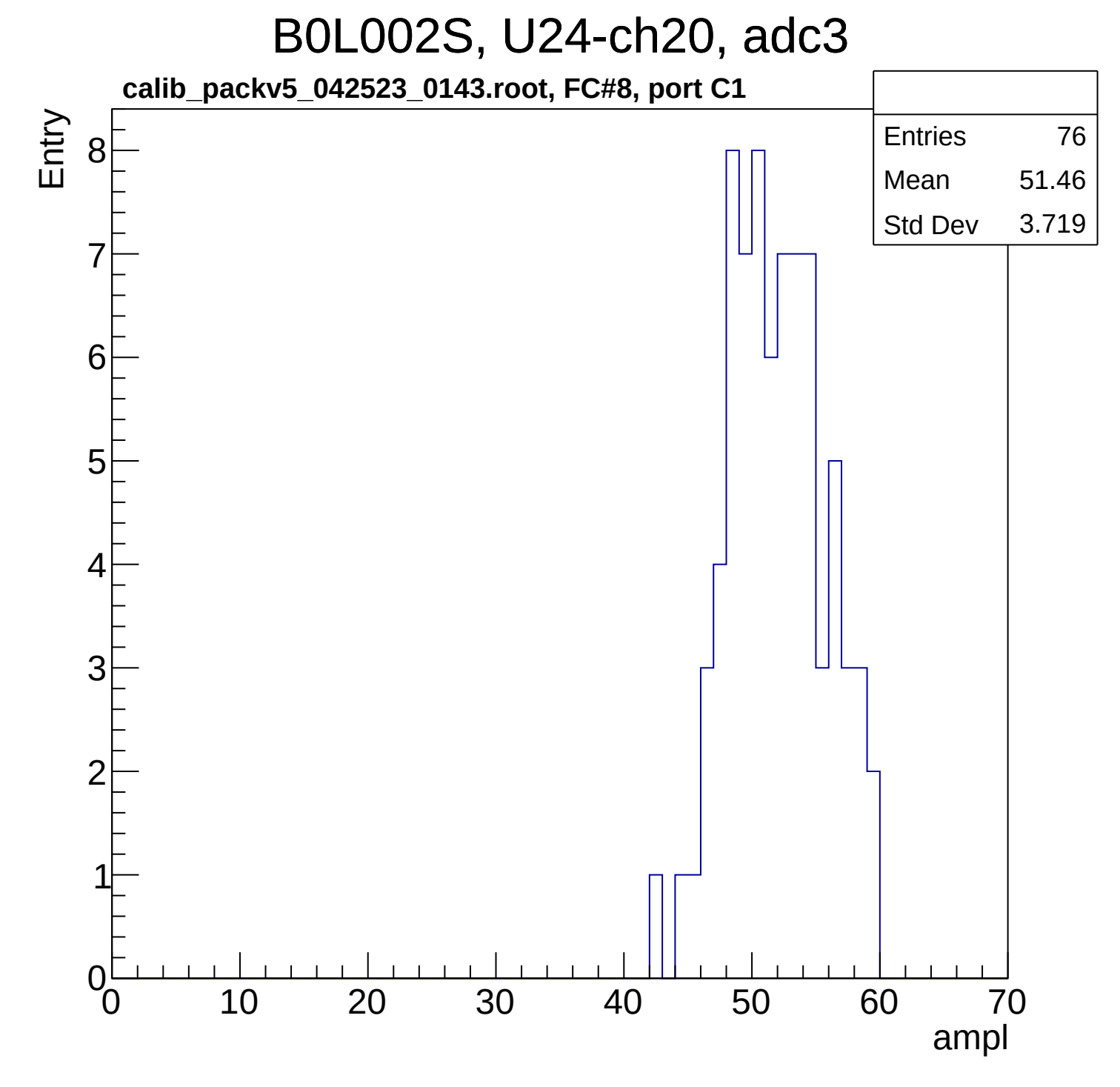
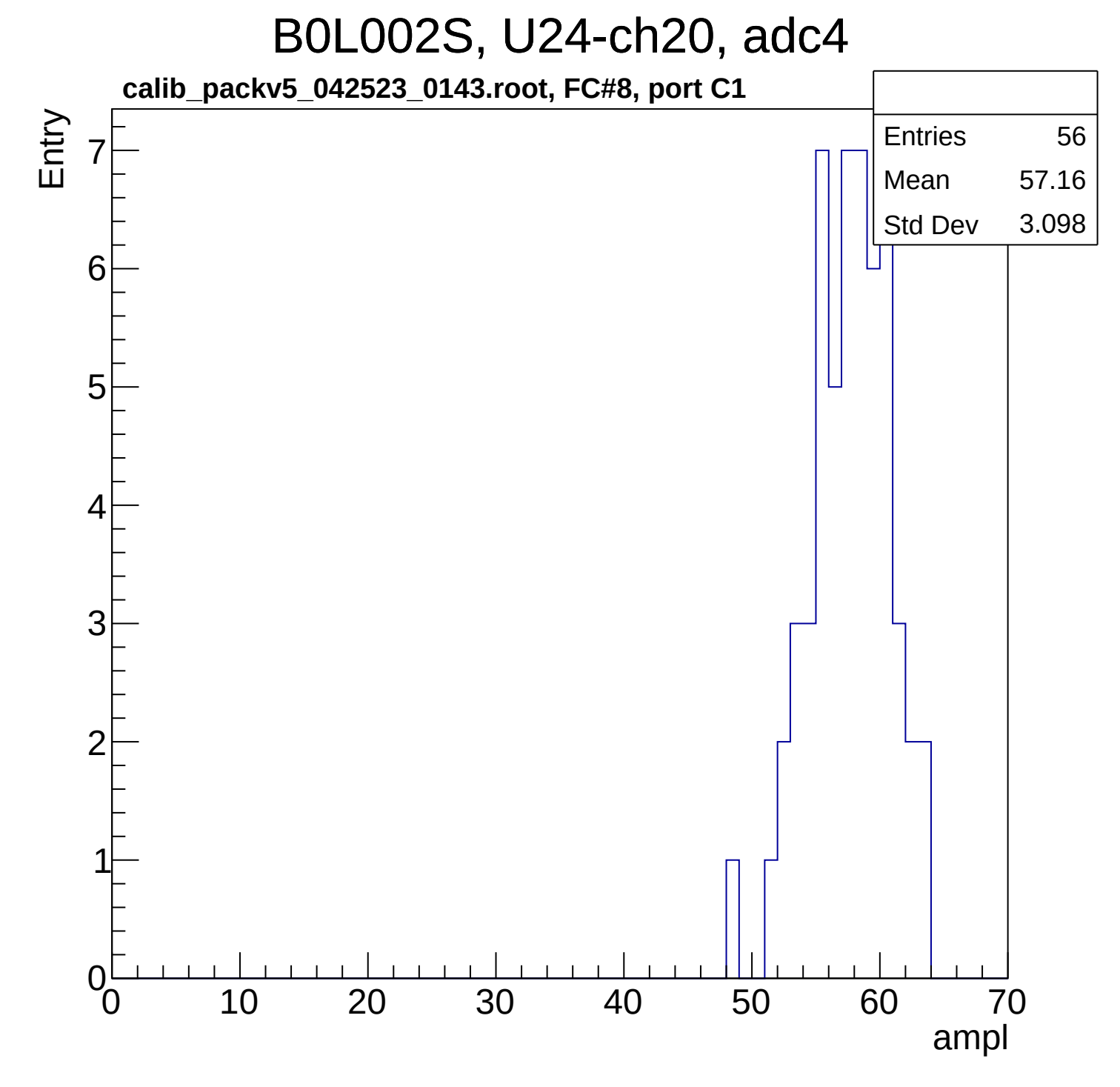
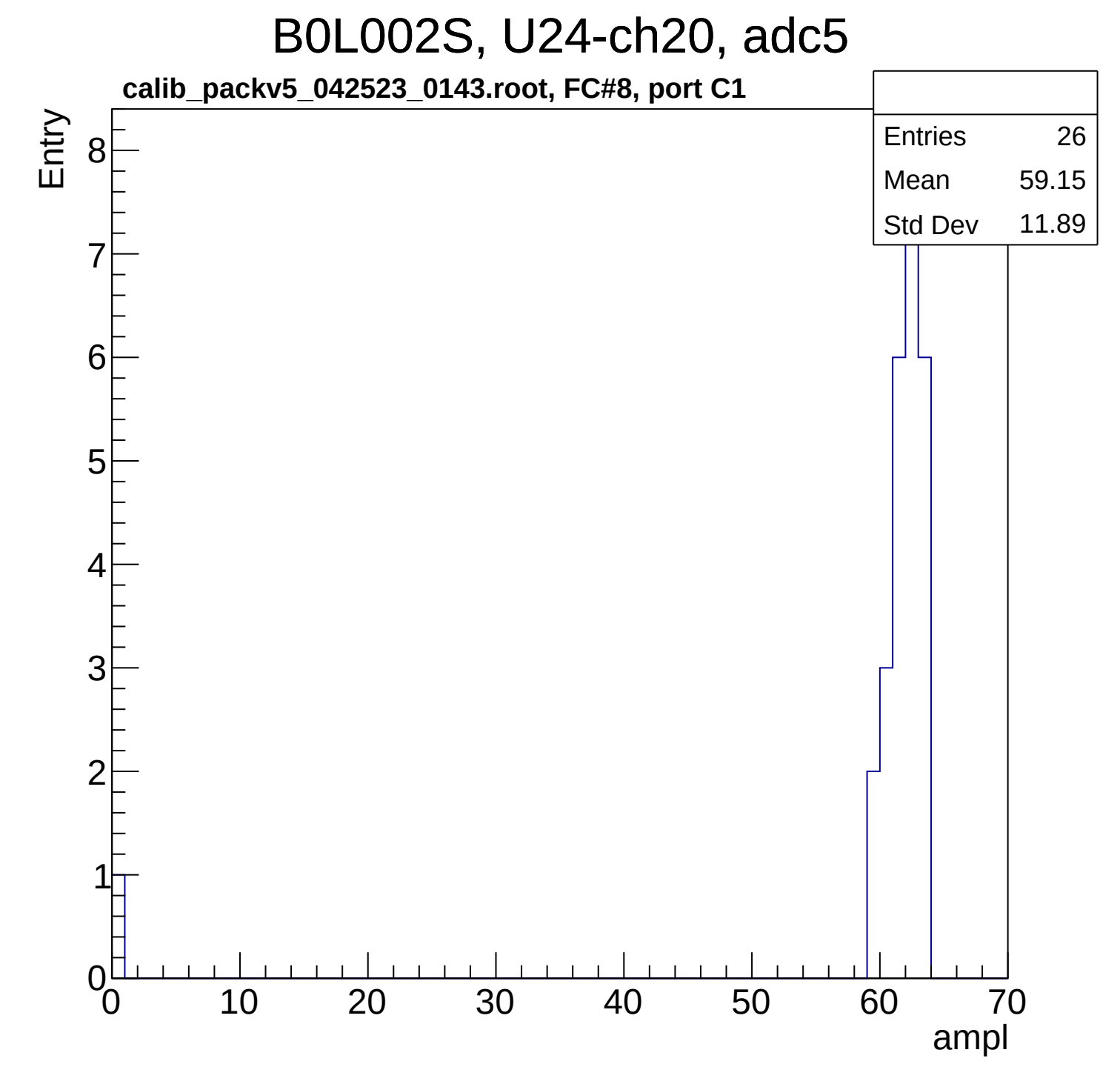

B0L002S, U24-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.46
Std Dev 3.719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.16
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59.15
Std Dev 11.89
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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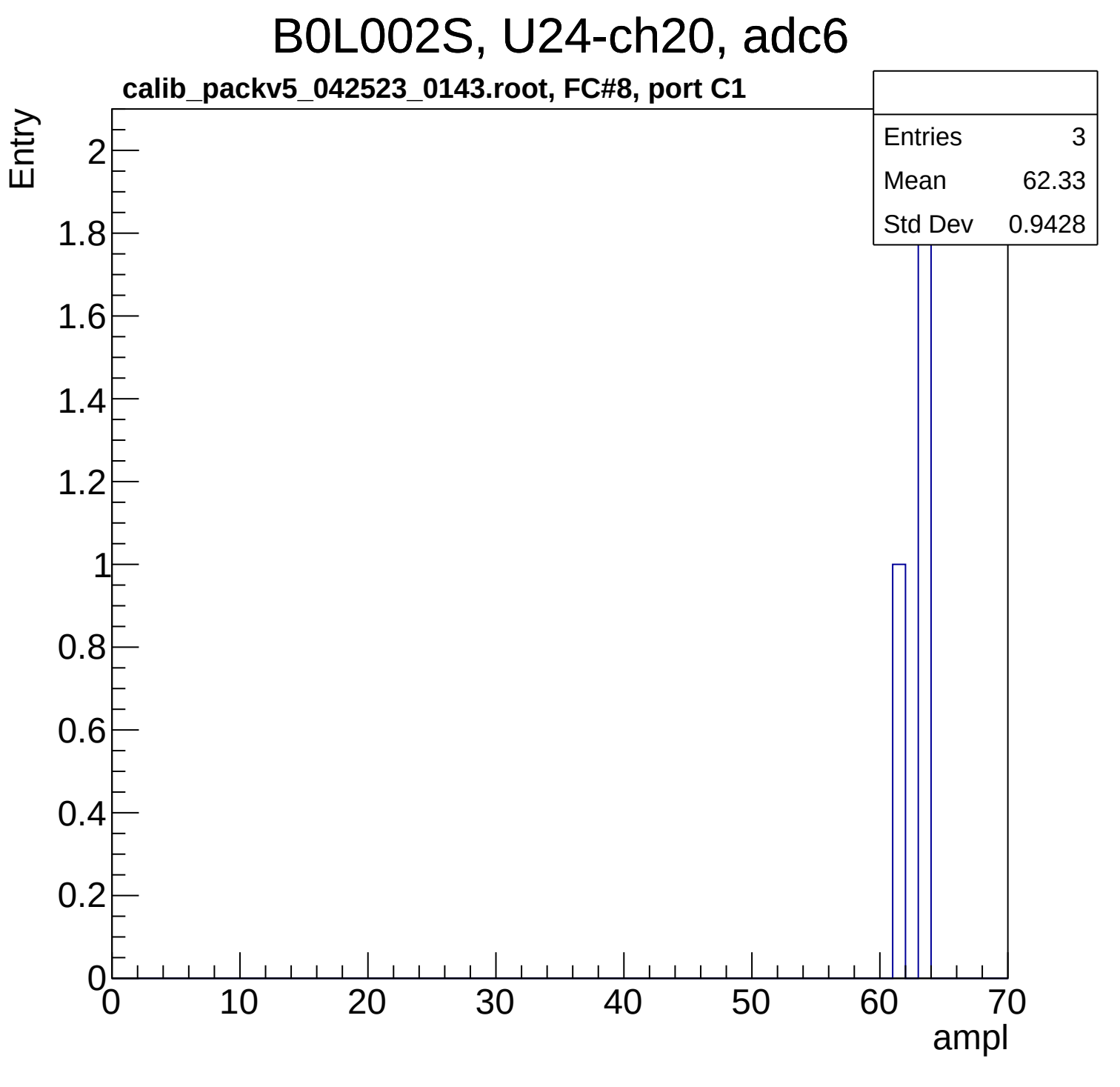
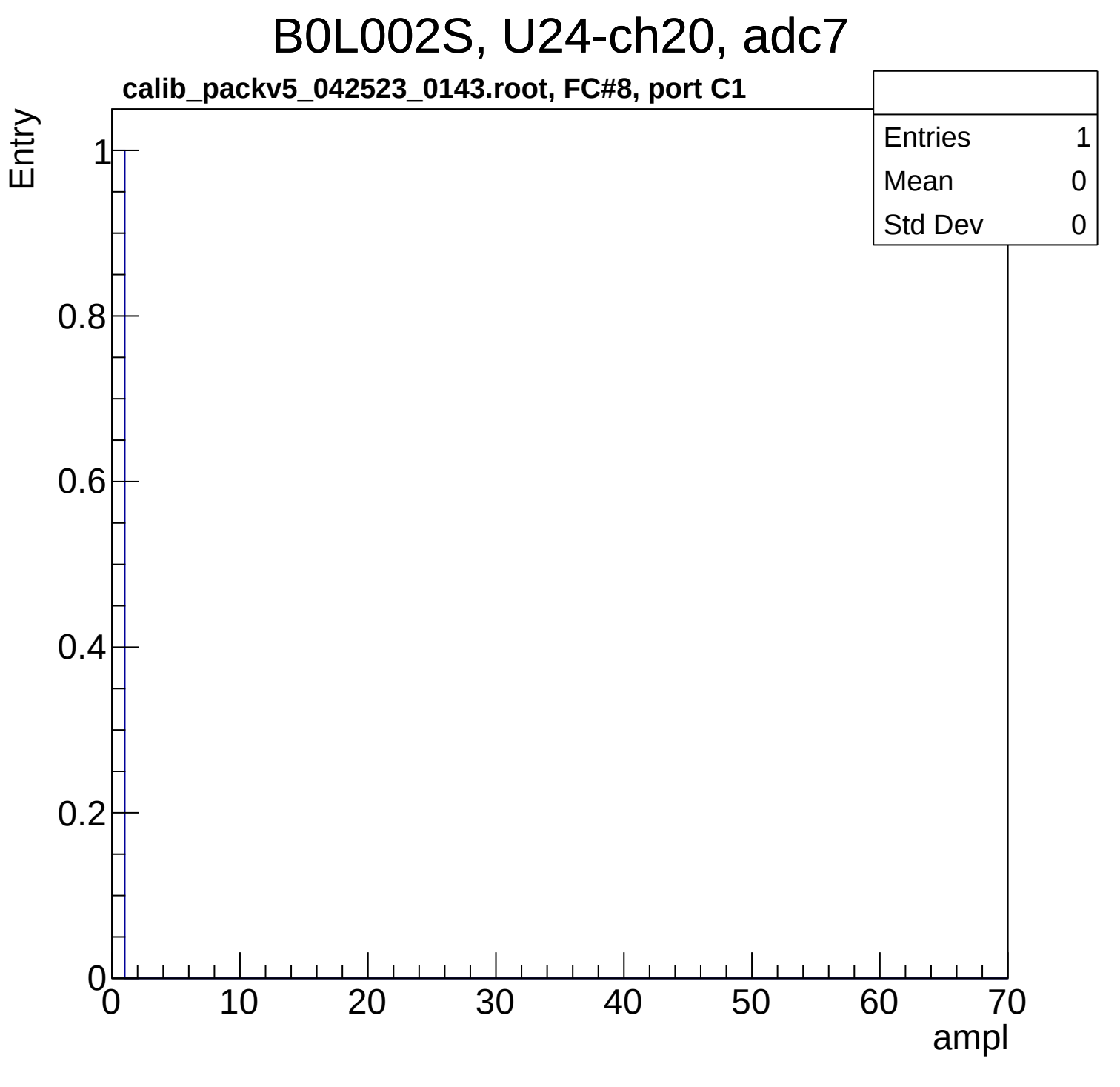
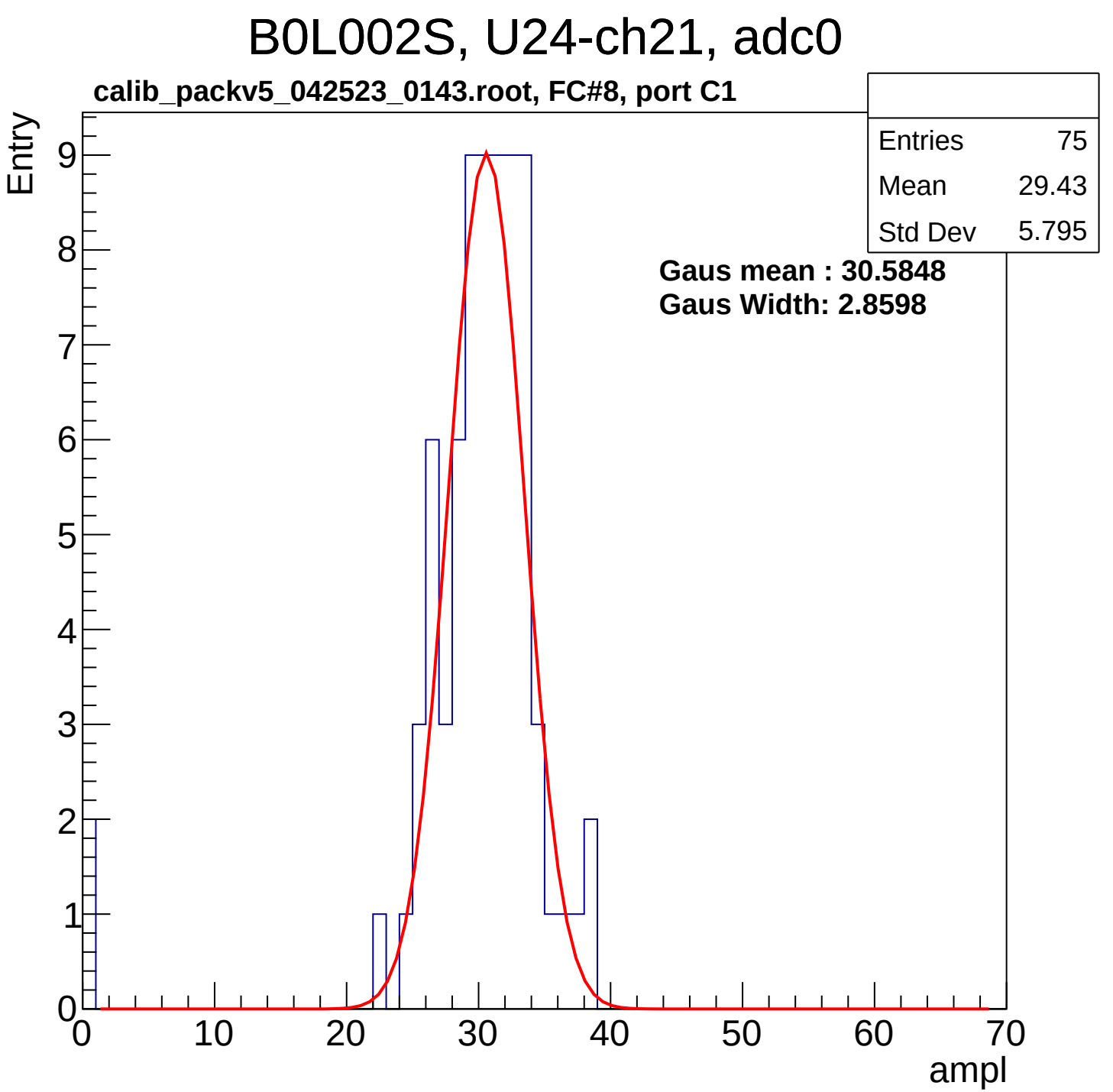
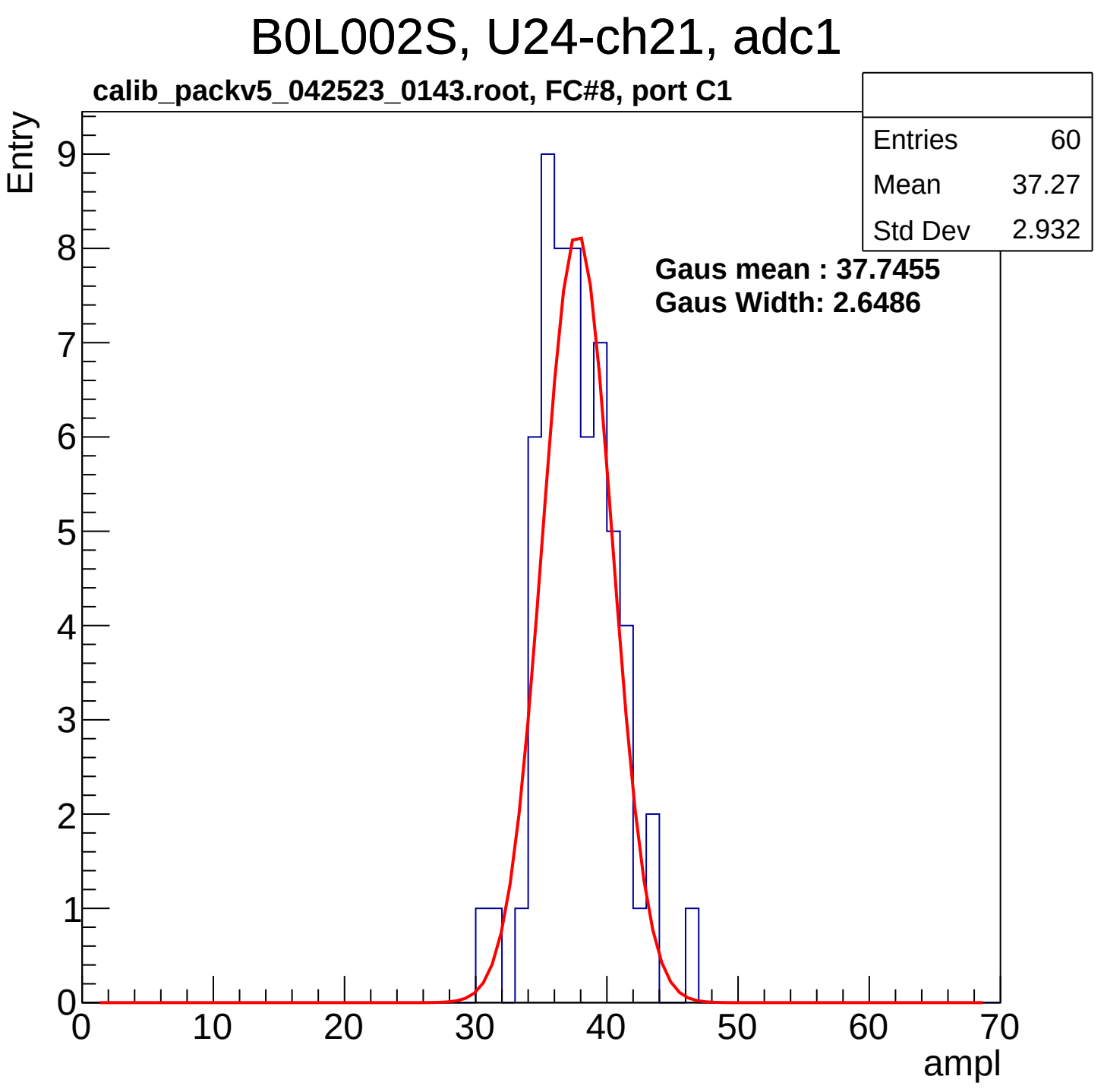
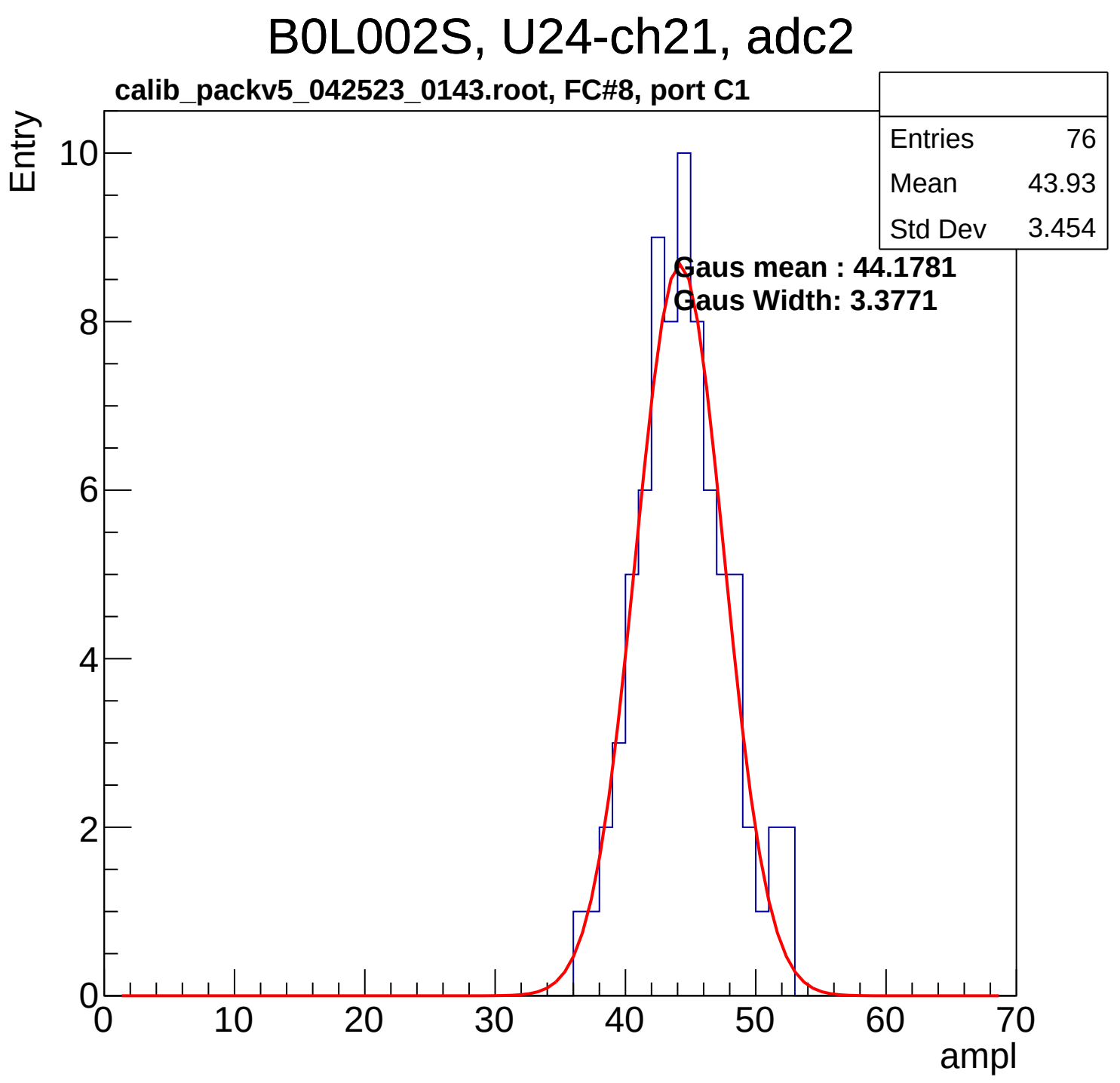
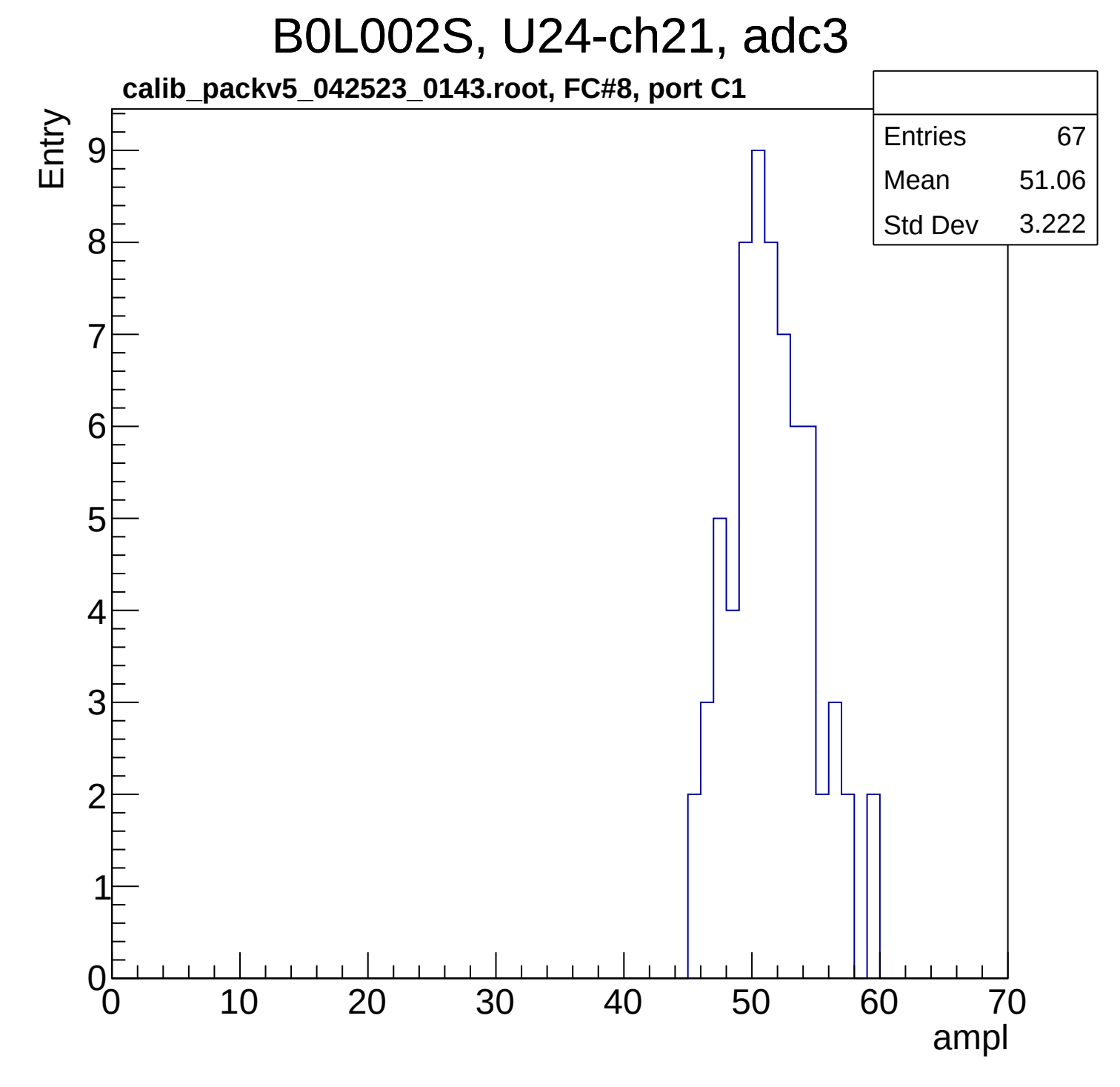

B0L002S, U24-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

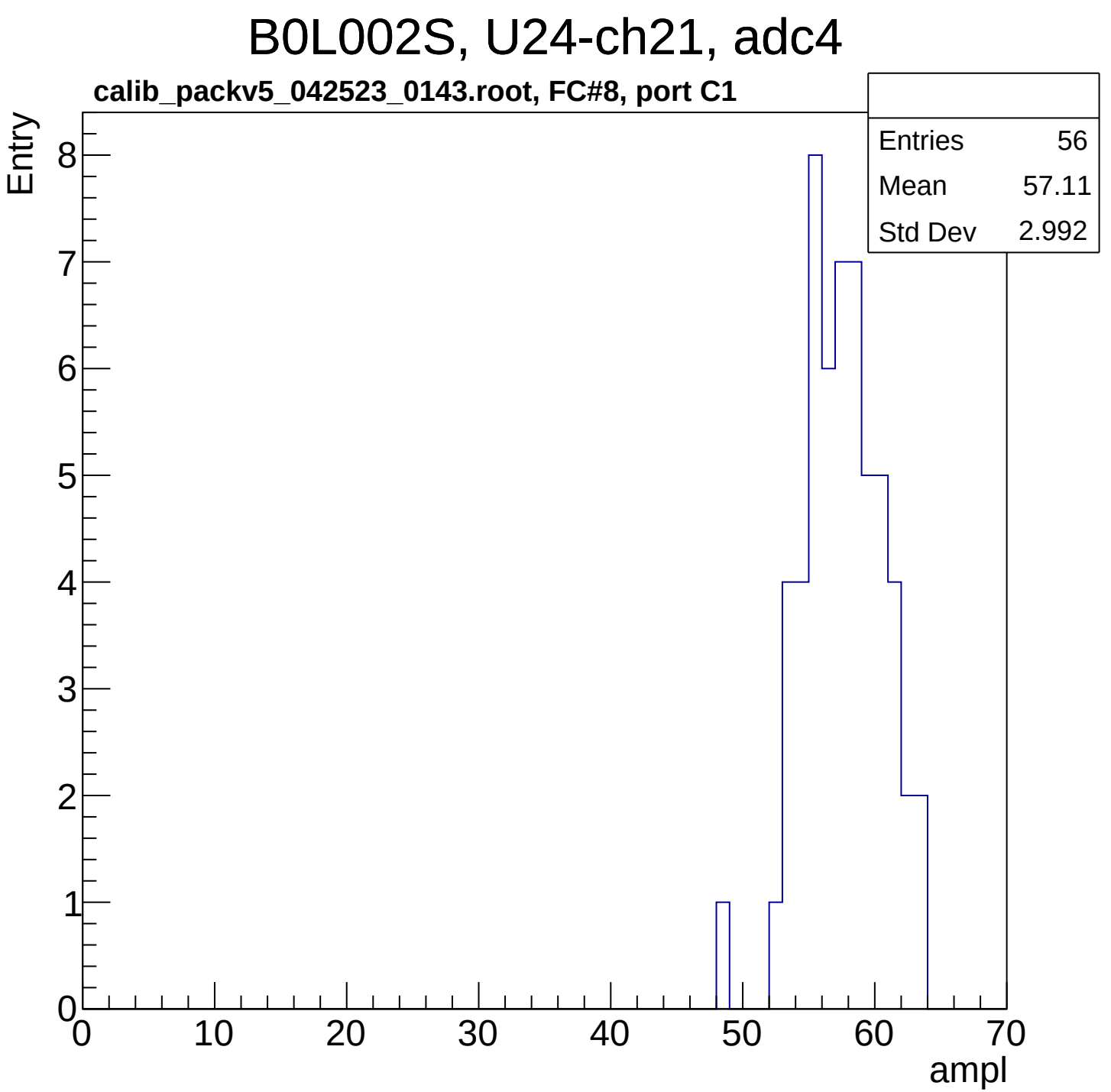

B0L002S, U24-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

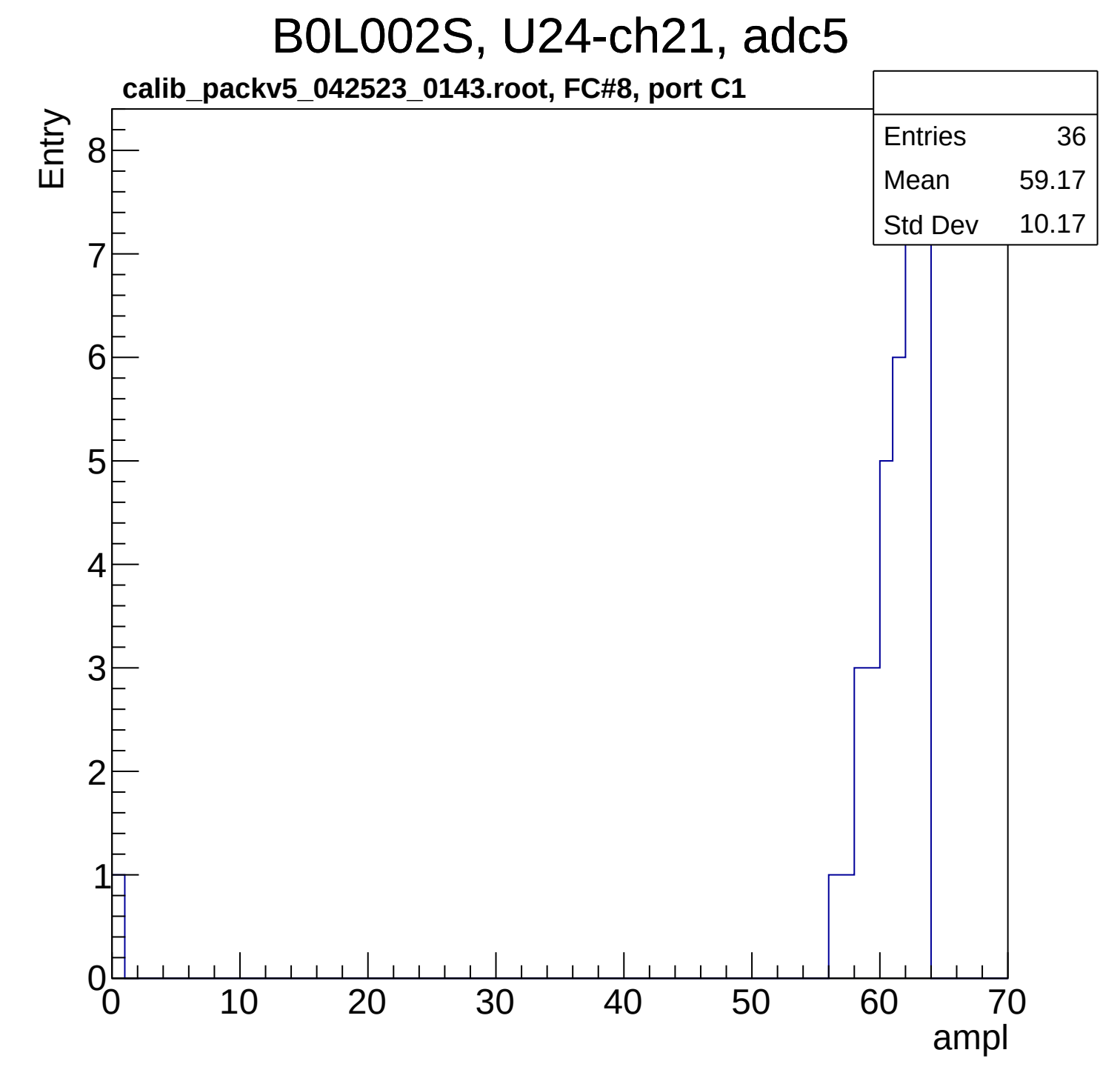

B0L002S, U24-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.43
Std Dev 5.795
Gaus mean : 30.5848
Gaus Width: 2.8598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

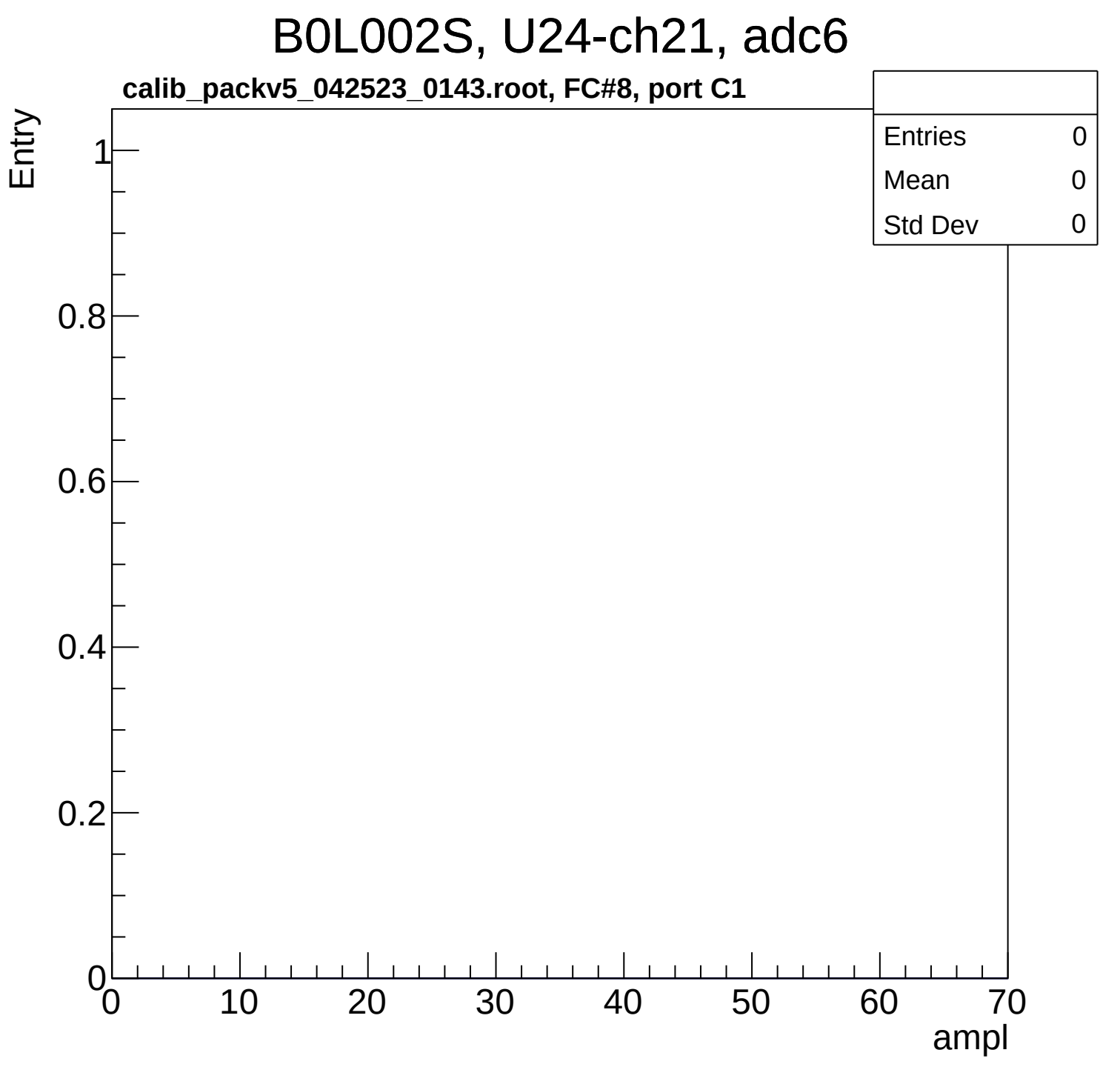

B0L002S, U24-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.27
Std Dev 2.932
Gaus mean : 37.7455
Gaus Width: 2.6486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

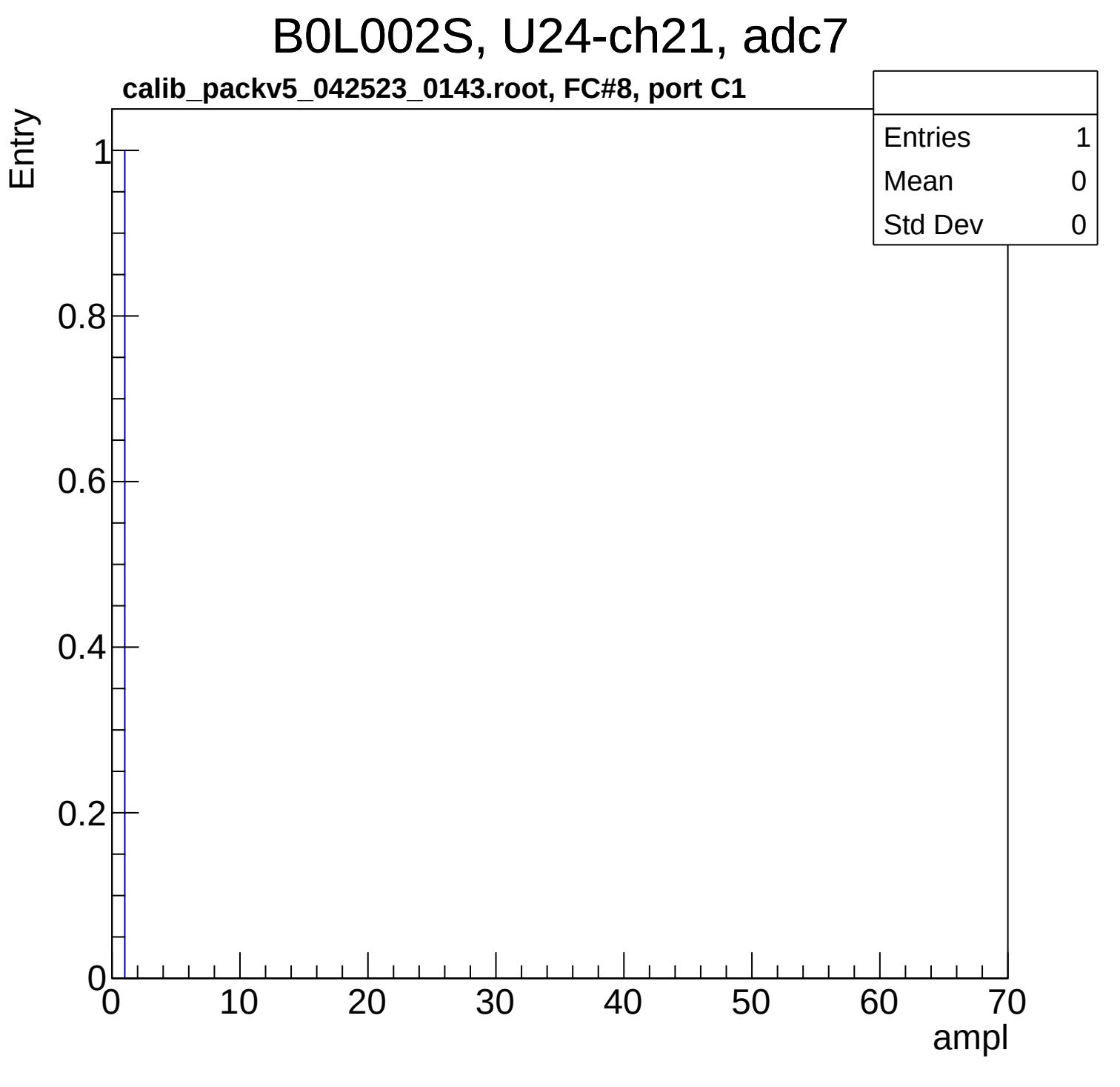

B0L002S, U24-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 43.93
Std Dev 3.454
Gaus mean : 44.1781
Gaus Width: 3.3771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

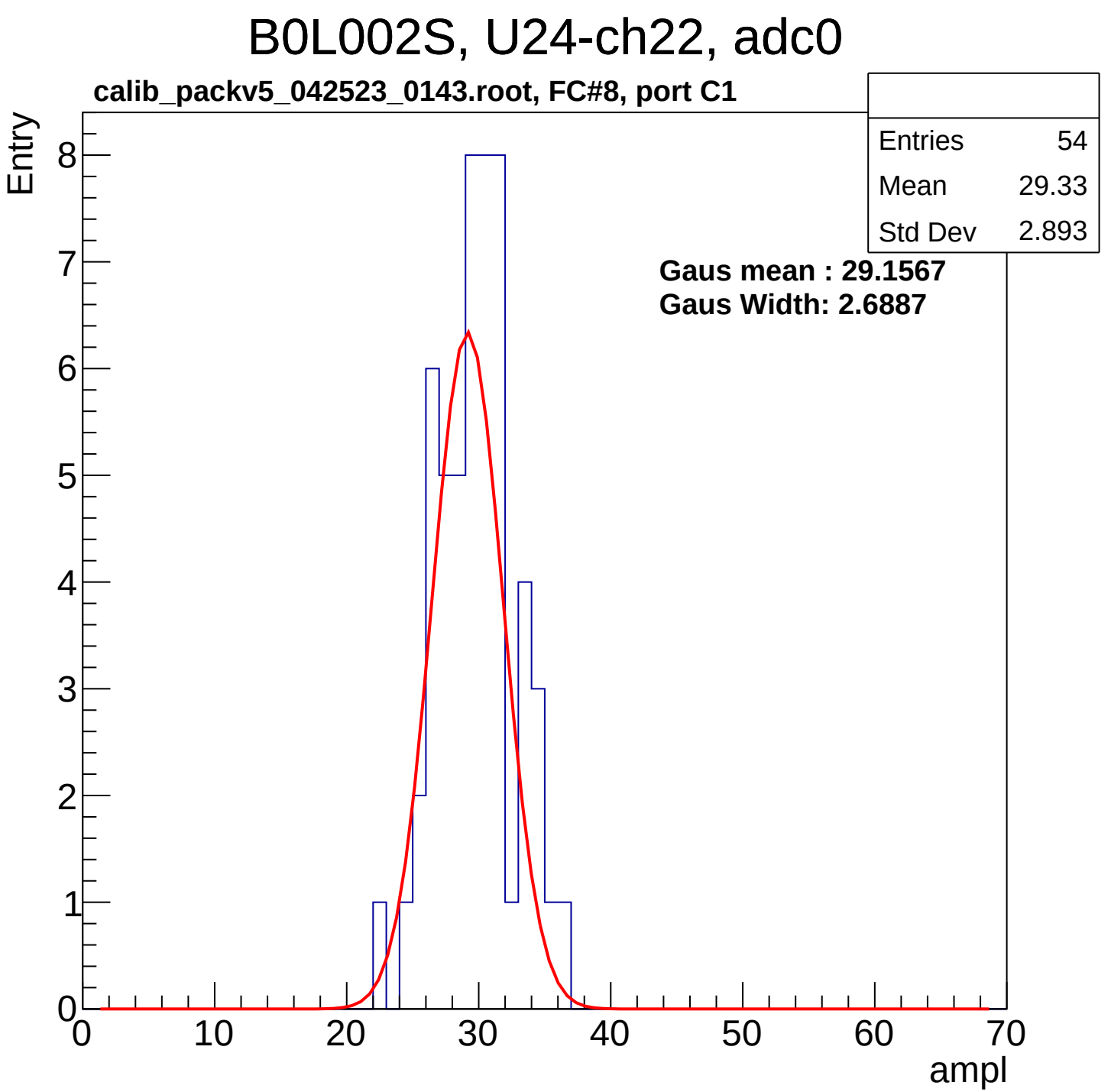

B0L002S, U24-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.06
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

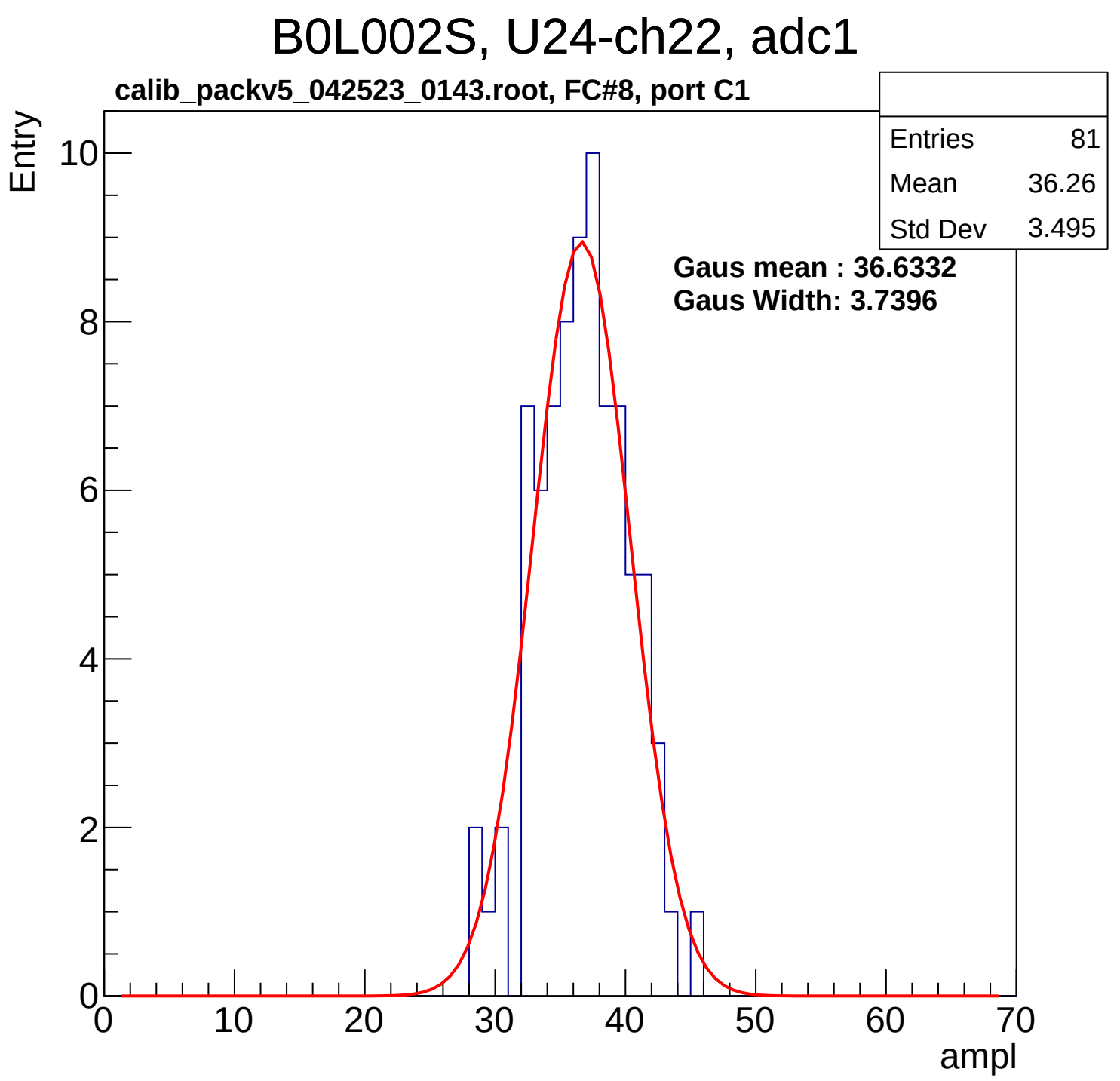

B0L002S, U24-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.11
Std Dev 2.992
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

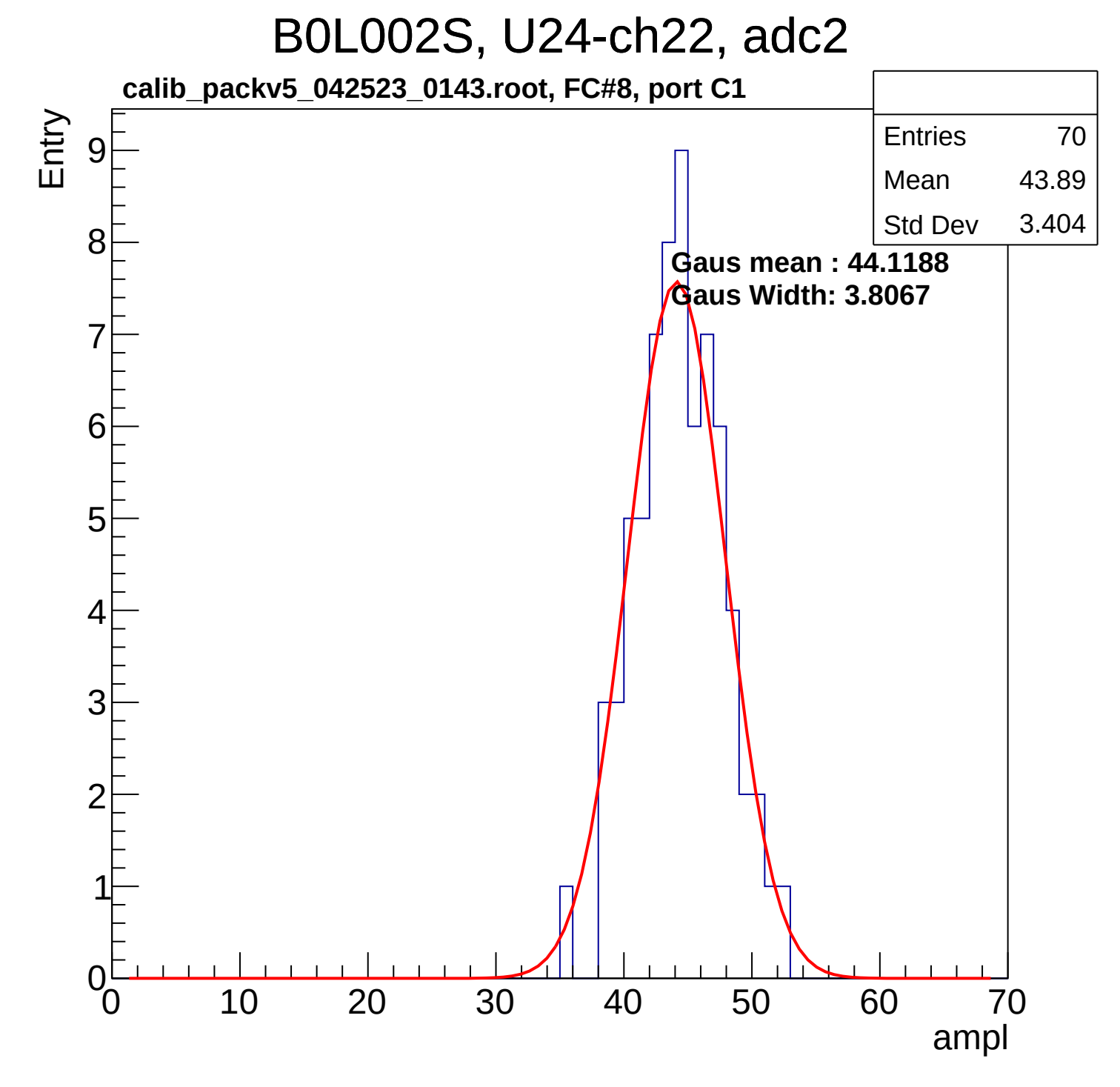

B0L002S, U24-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.17
Std Dev 10.17
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

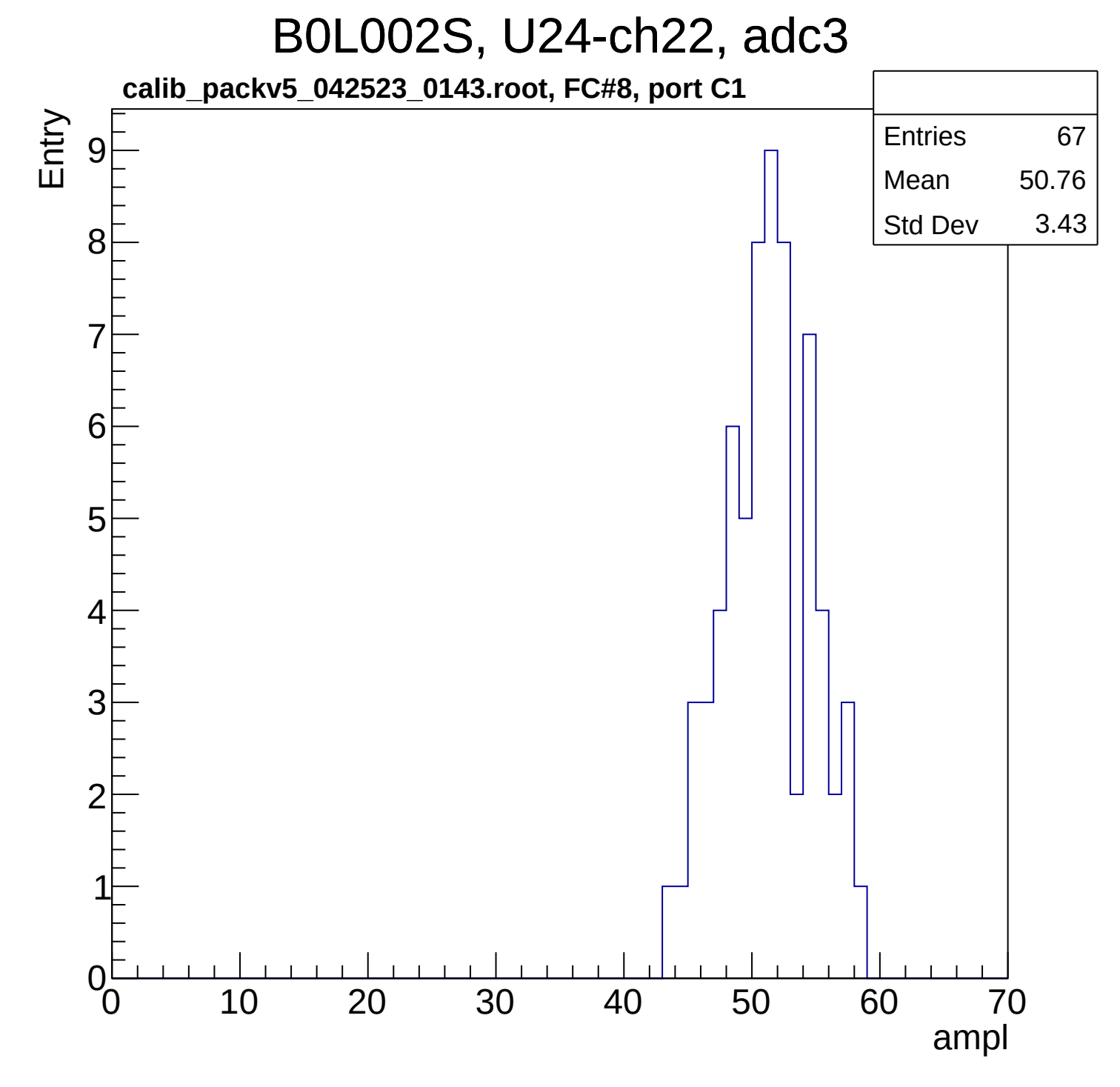

B0L002S, U24-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 29.33
Std Dev 2.893
Gaus mean : 29.1567
Gaus Width: 2.6887
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 36.26
Std Dev 3.495
Gaus mean : 36.6332
Gaus Width: 3.7396
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.89
Std Dev 3.404
Gaus mean : 44.1188
Gaus Width: 3.8067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.76
Std Dev 3.43
Entry
ampl
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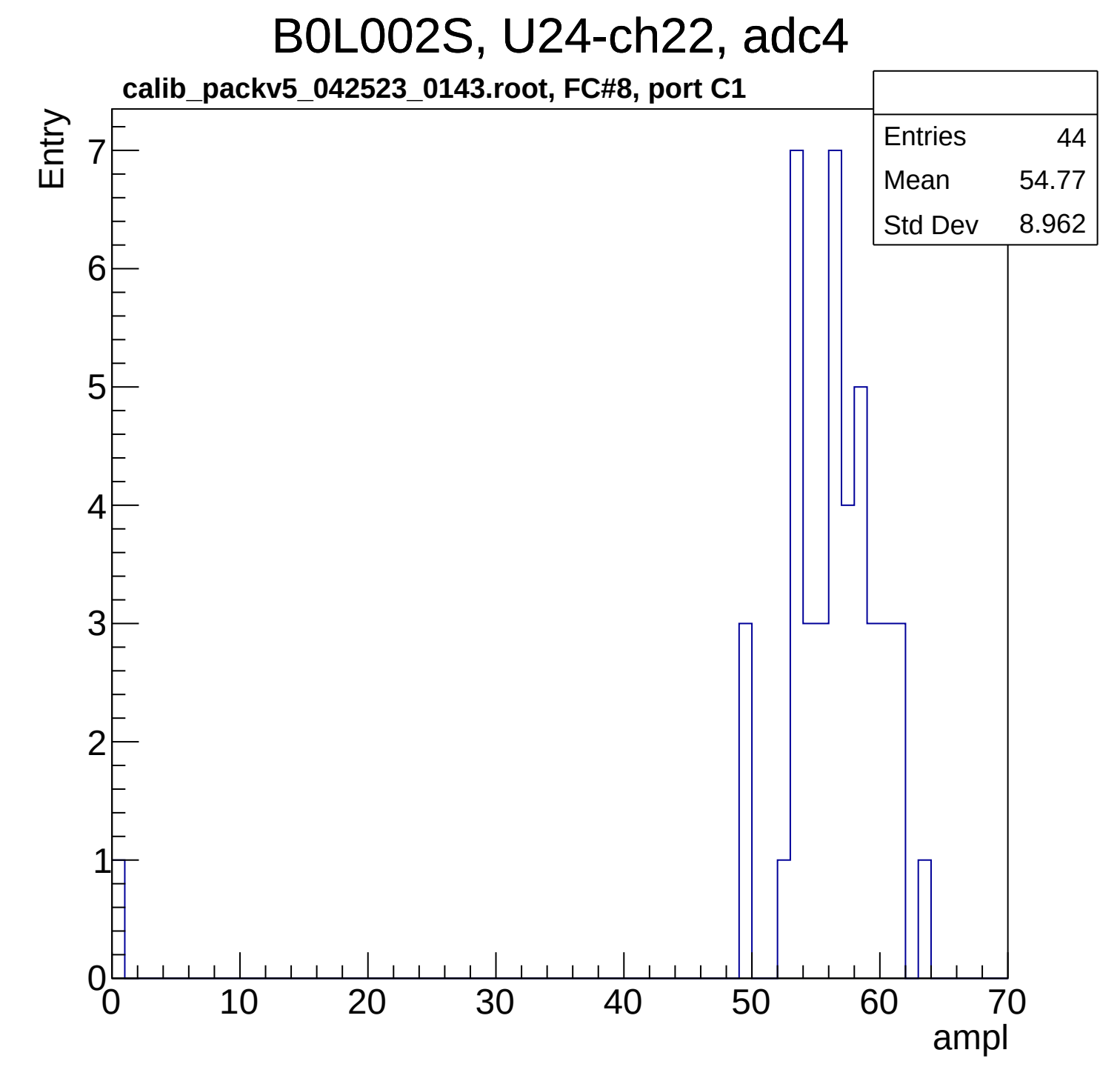
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 54.77
Std Dev 8.962
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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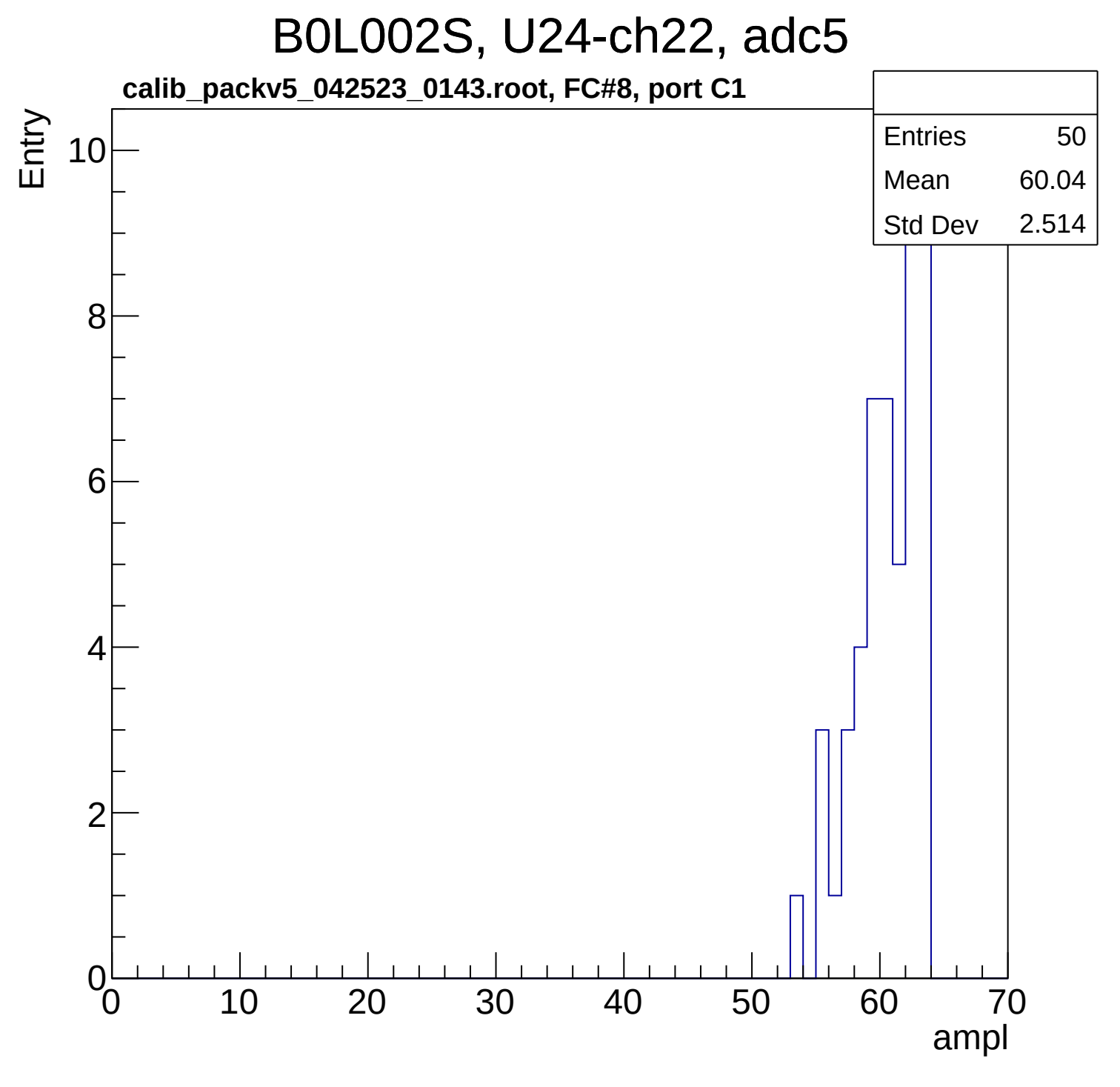
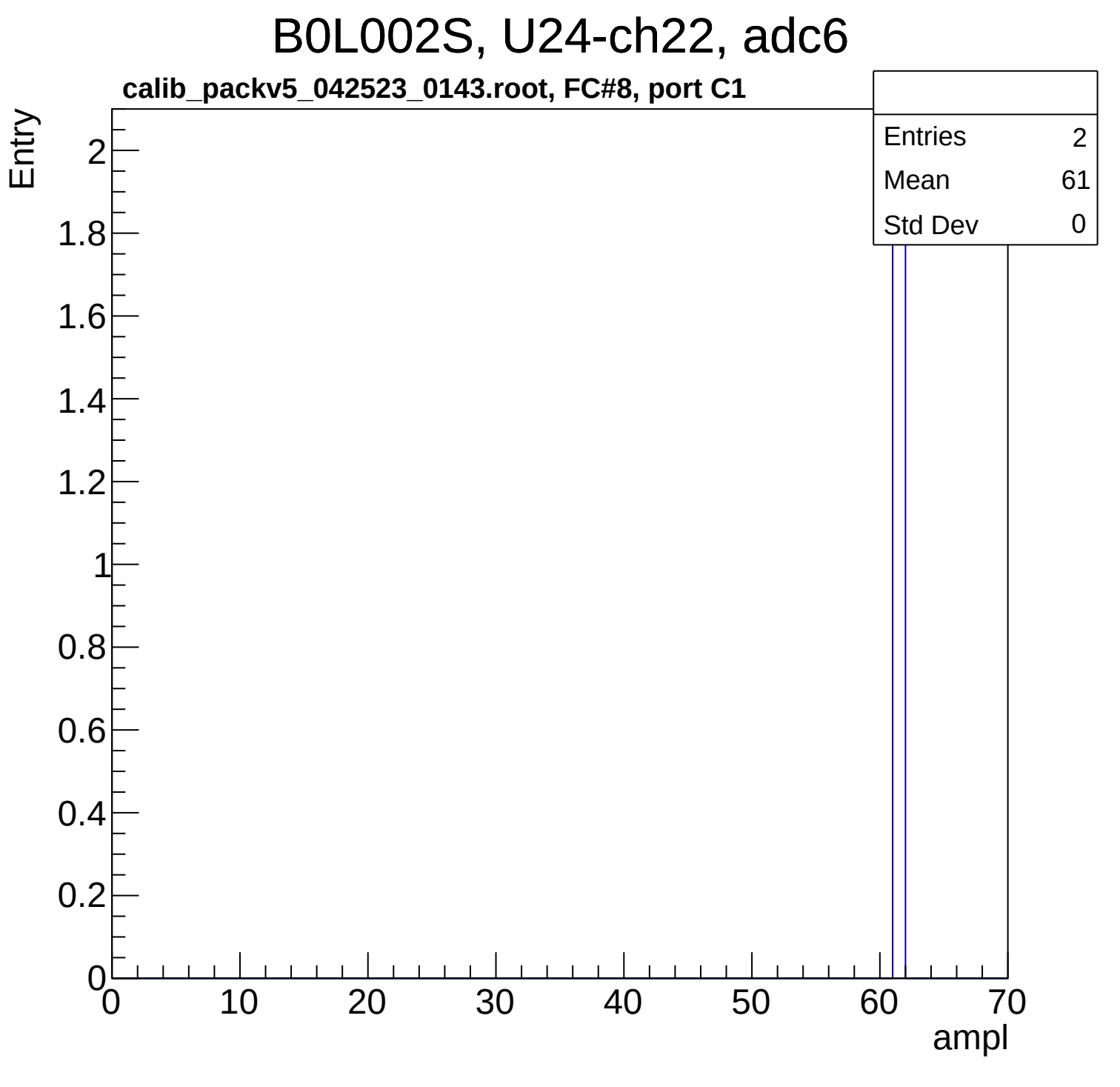
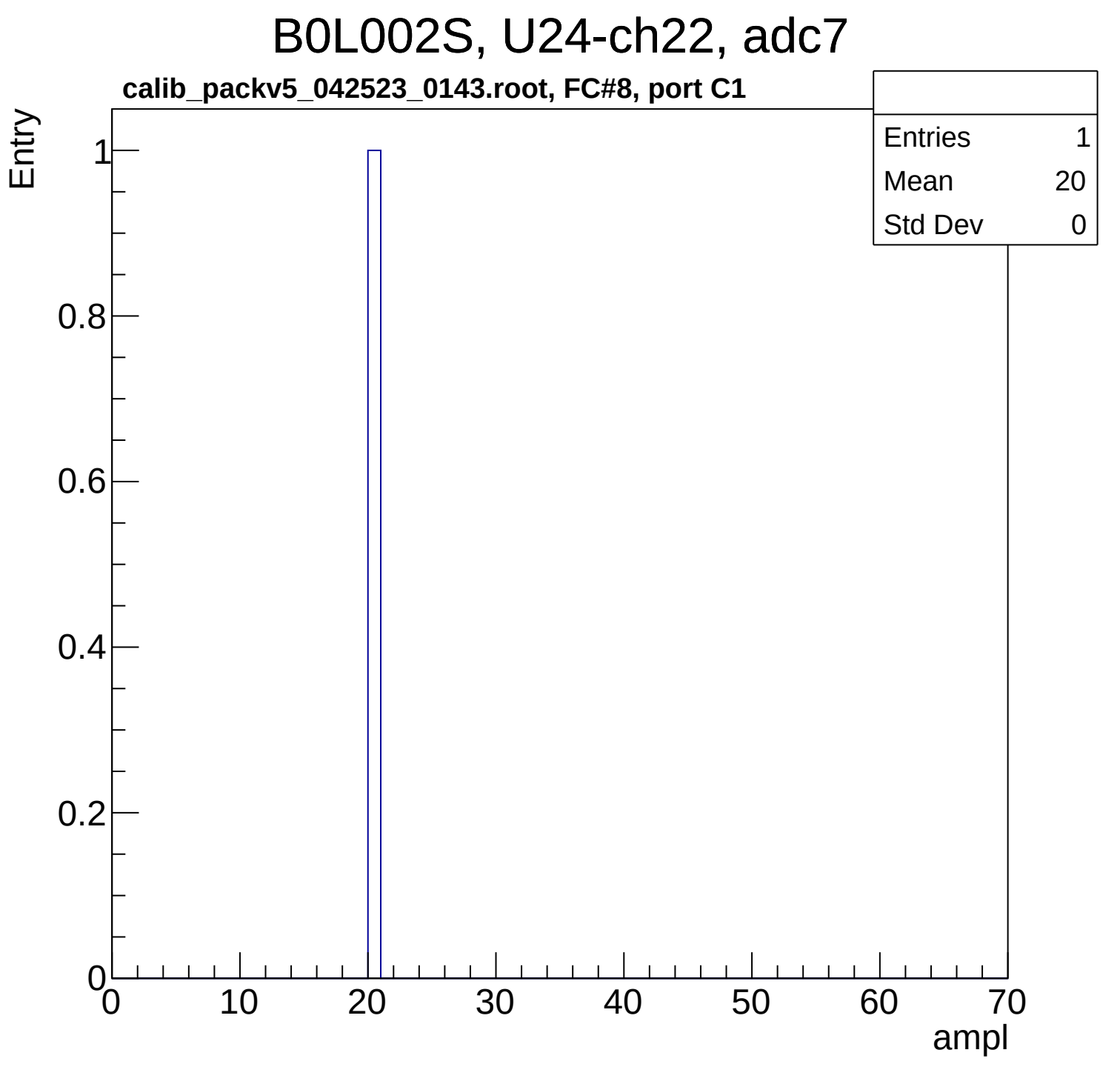
40
50
60
70


B0L002S, U24-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 60.04
Std Dev 2.514
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 20
Std Dev 0
Entry
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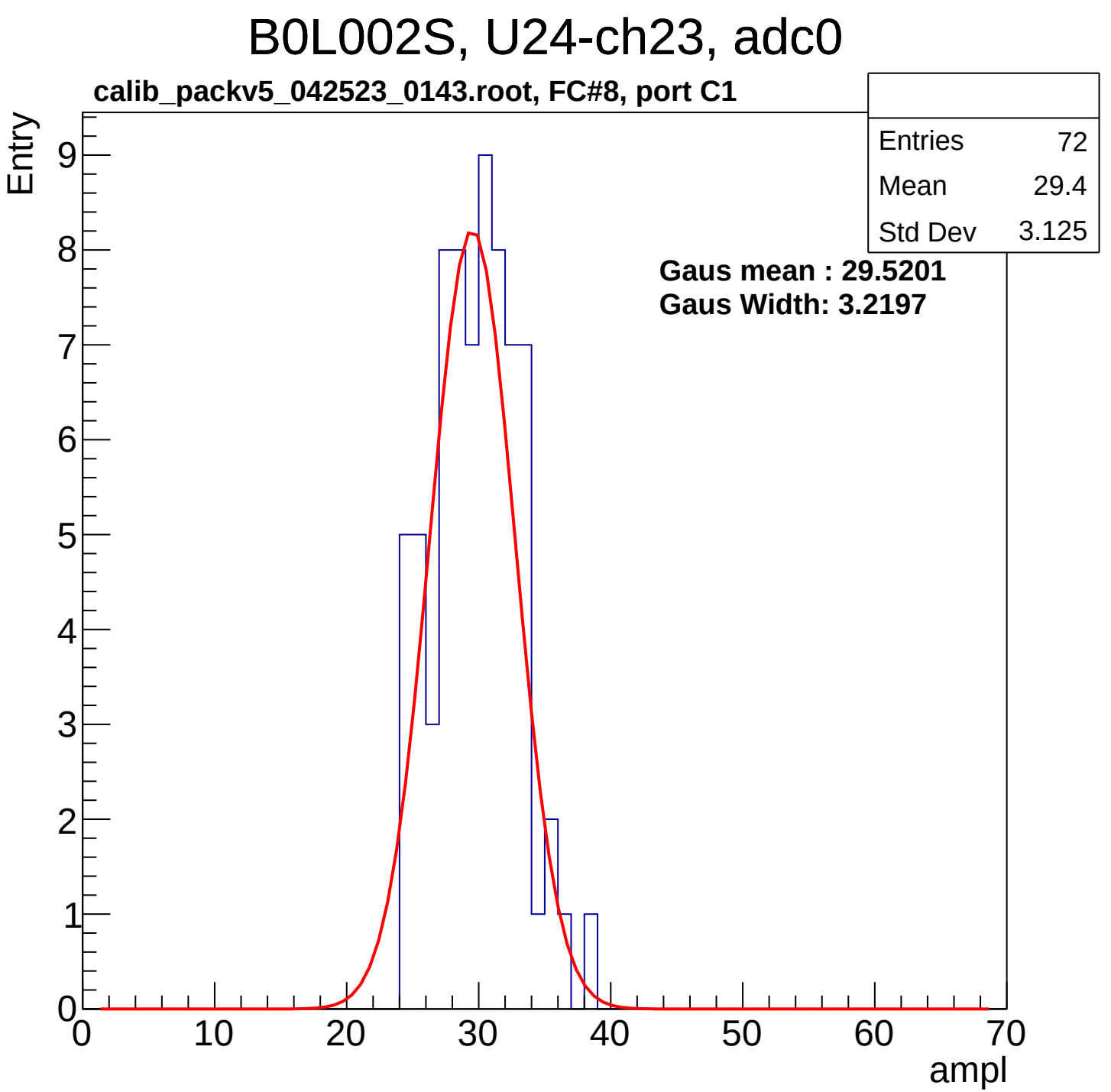
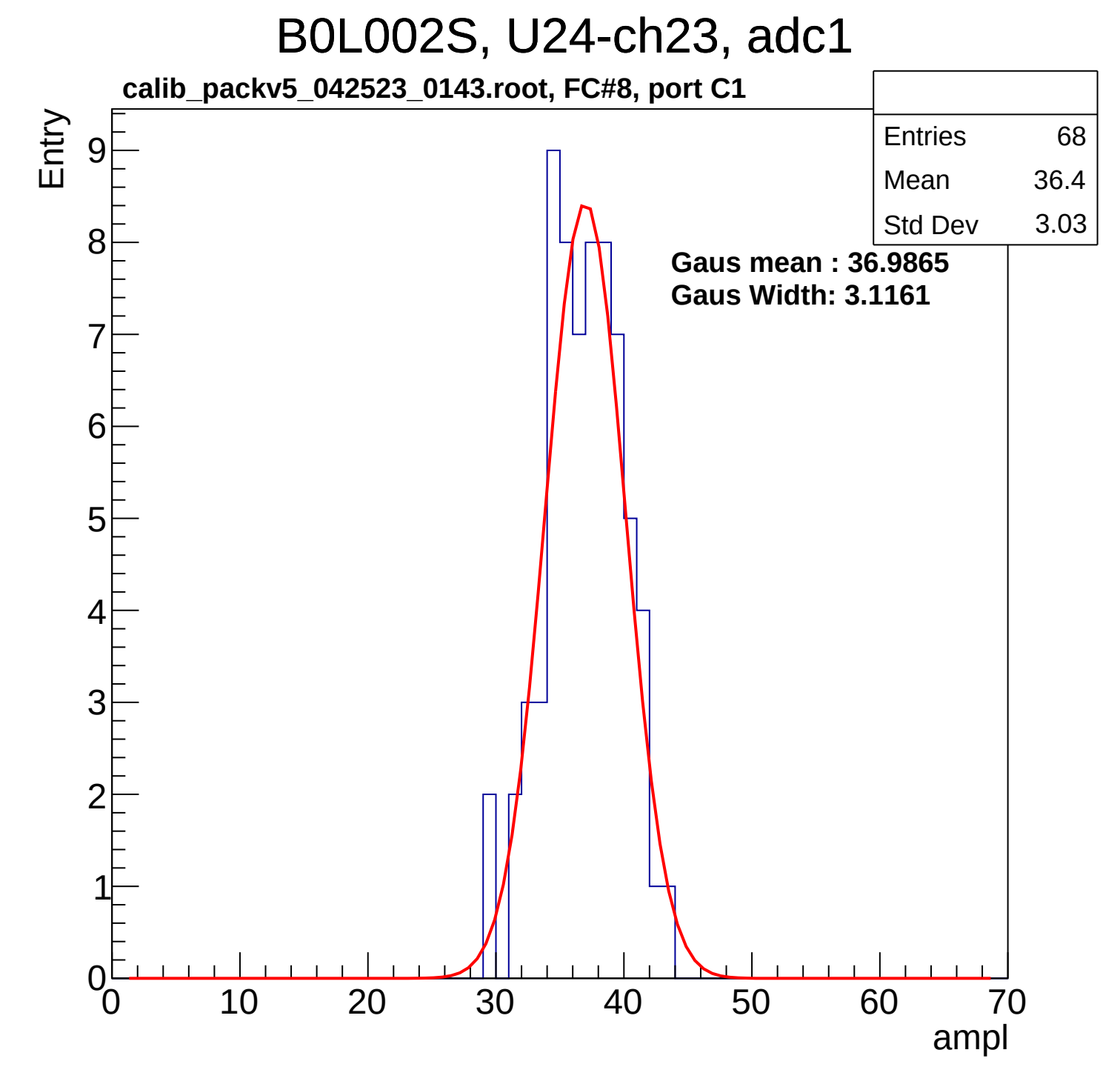
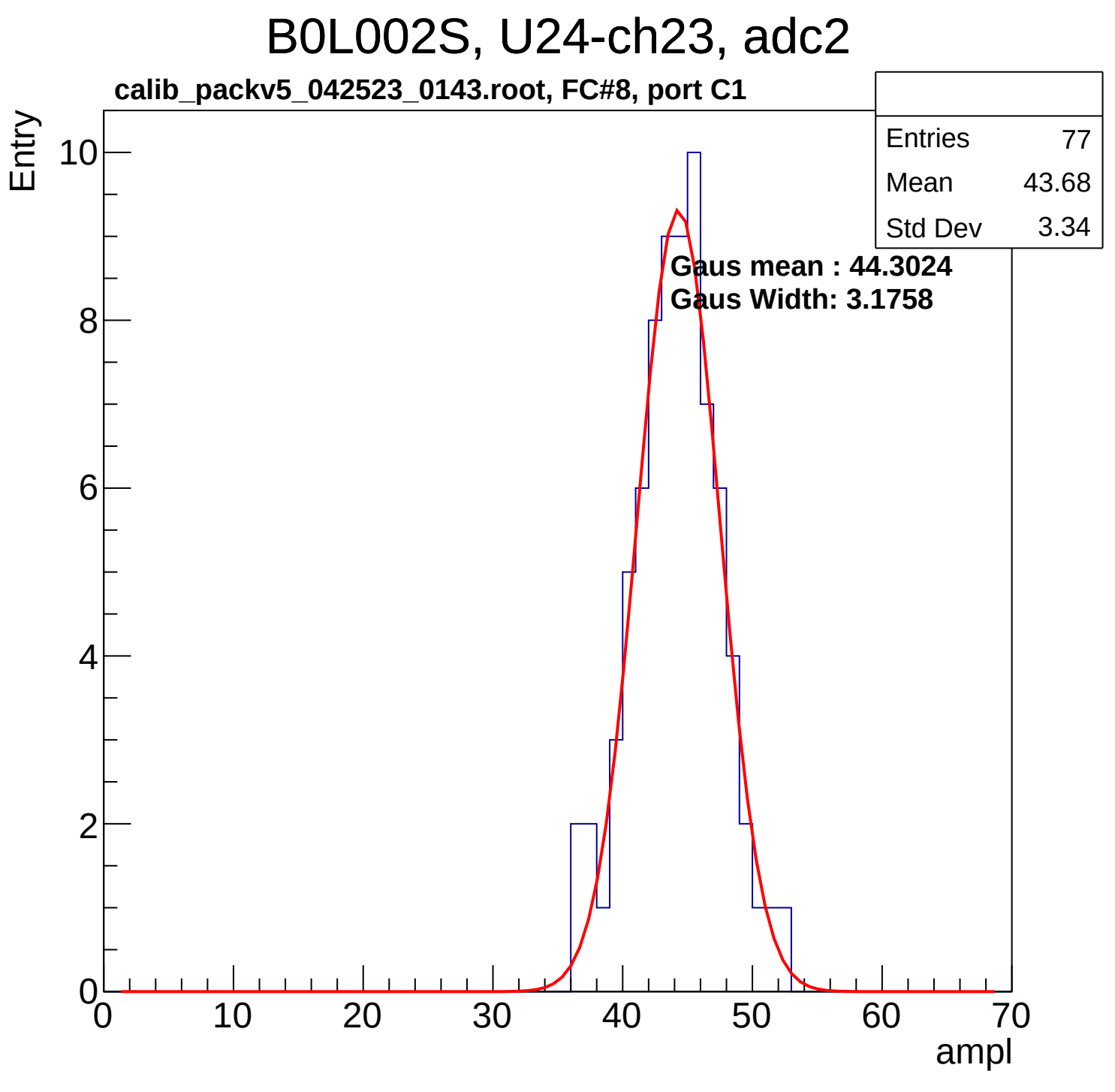
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.4
Std Dev 3.125
Gaus mean : 29.5201
Gaus Width: 3.2197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.4
Std Dev 3.03
Gaus mean : 36.9865
Gaus Width: 3.1161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 43.68
Std Dev 3.34
Gaus mean : 44.3024
Gaus Width: 3.1758
Entry
ampl
0
2
4
6
8
10
0
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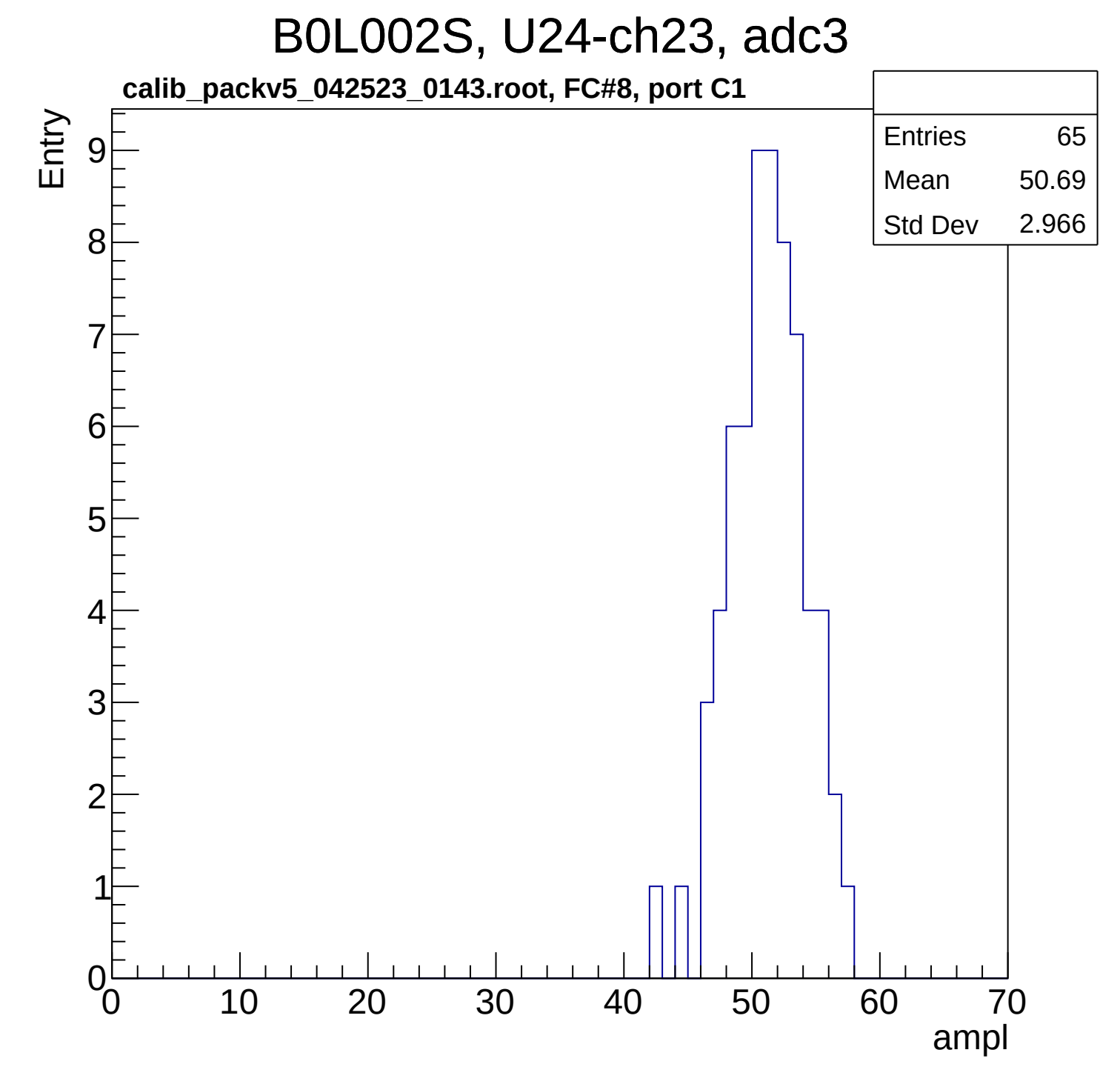
B0L002S, U24-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.69
Std Dev 2.966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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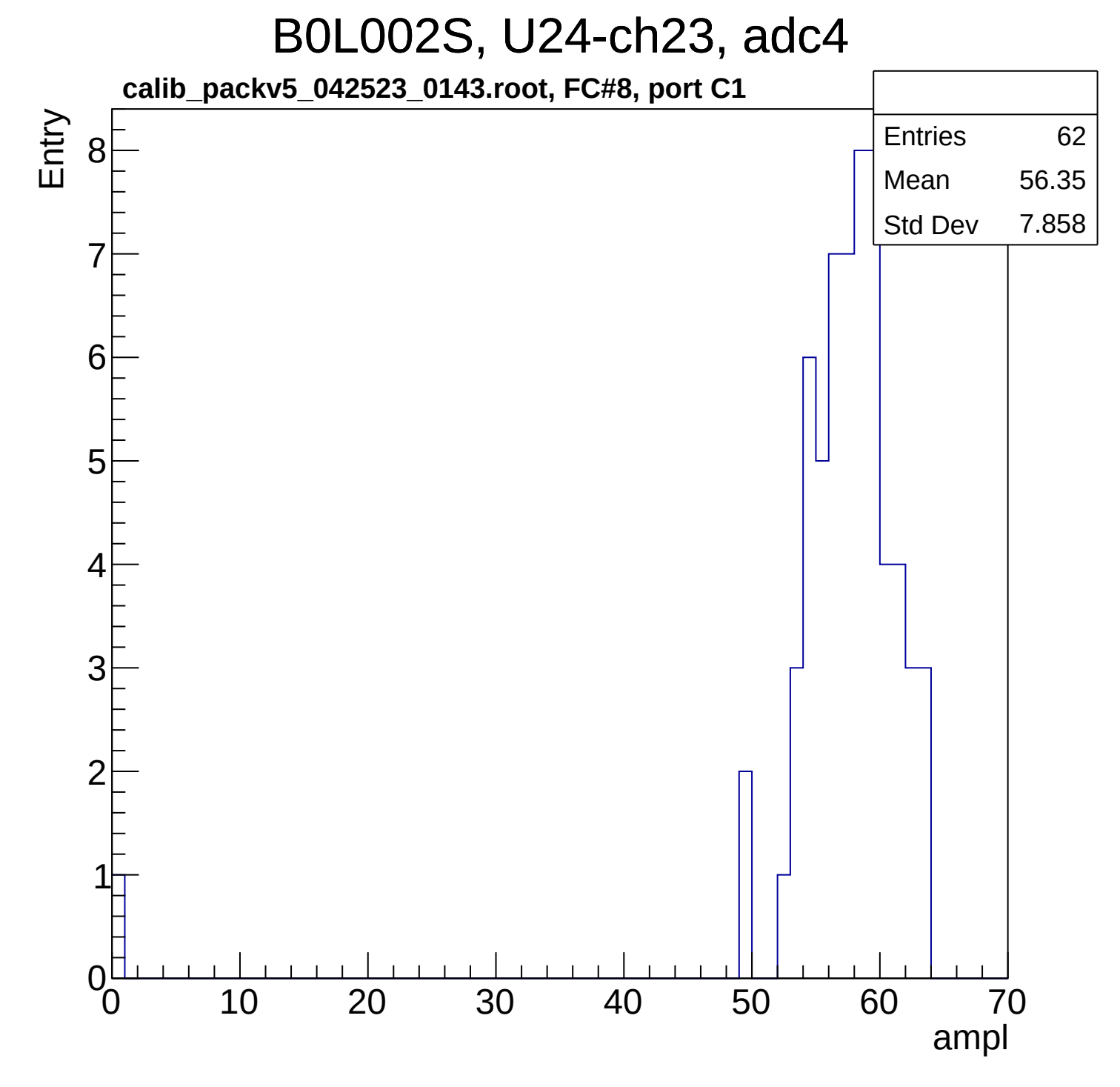
B0L002S, U24-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.35
Std Dev 7.858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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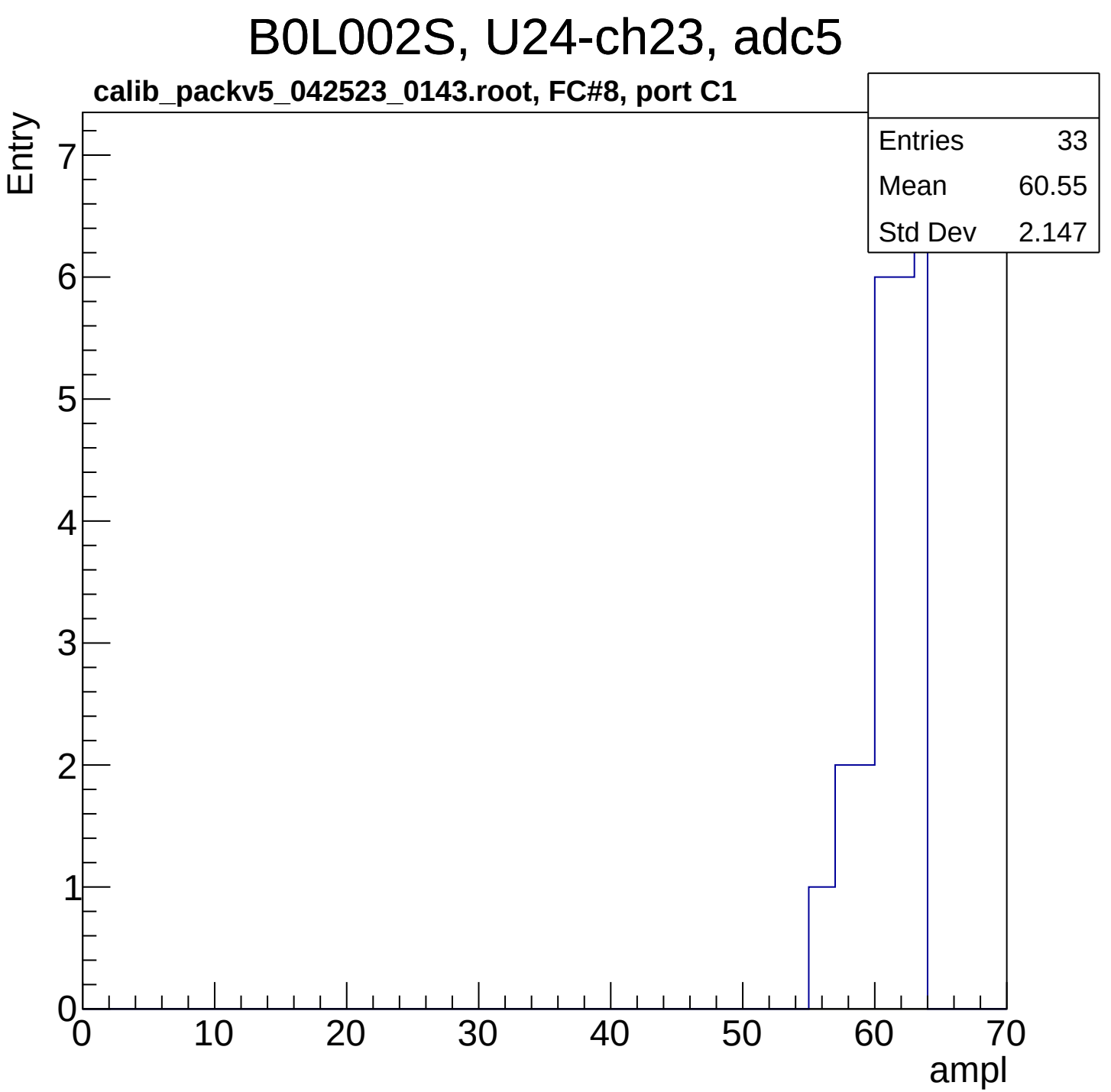
B0L002S, U24-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 60.55
Std Dev 2.147
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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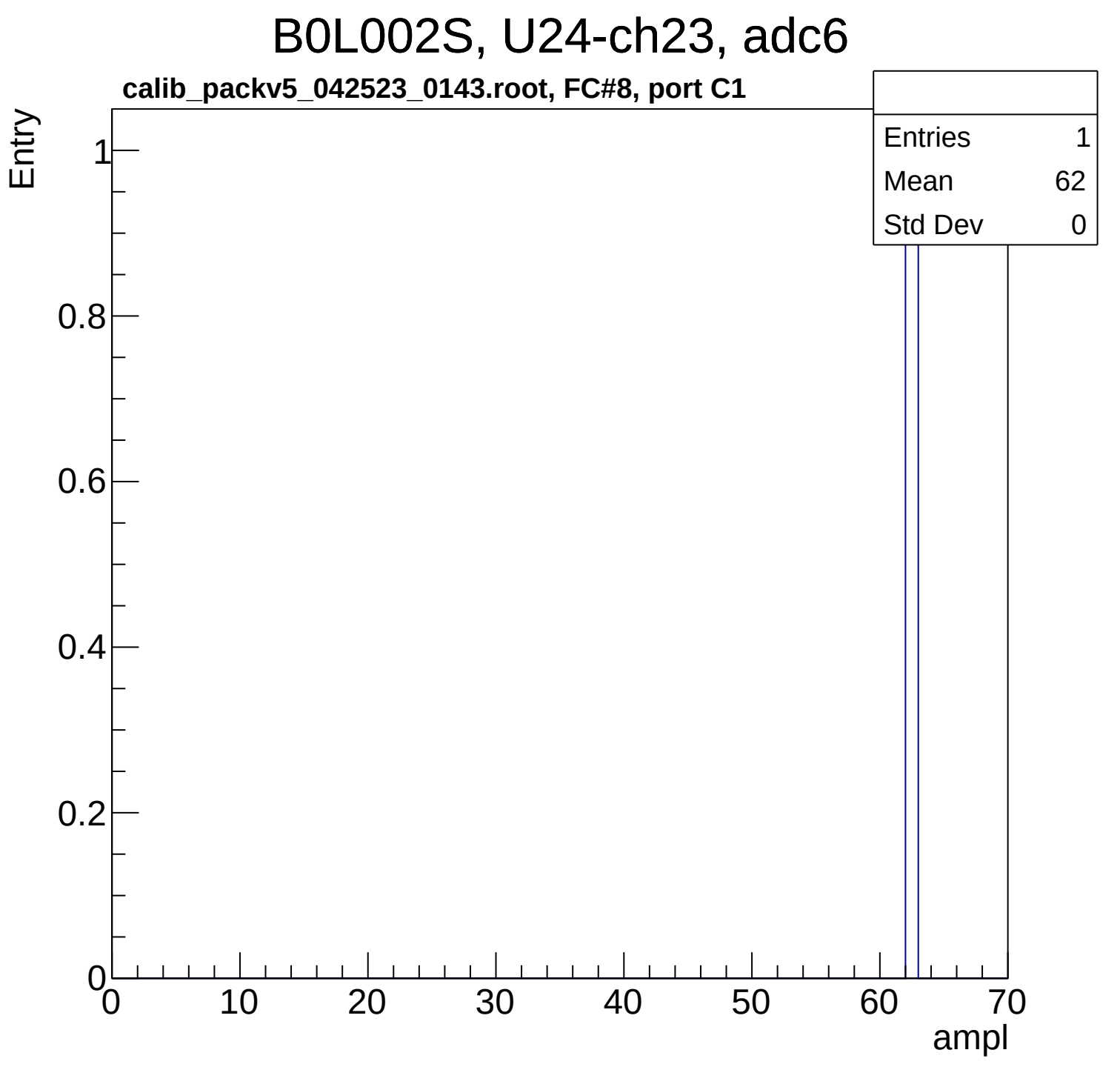
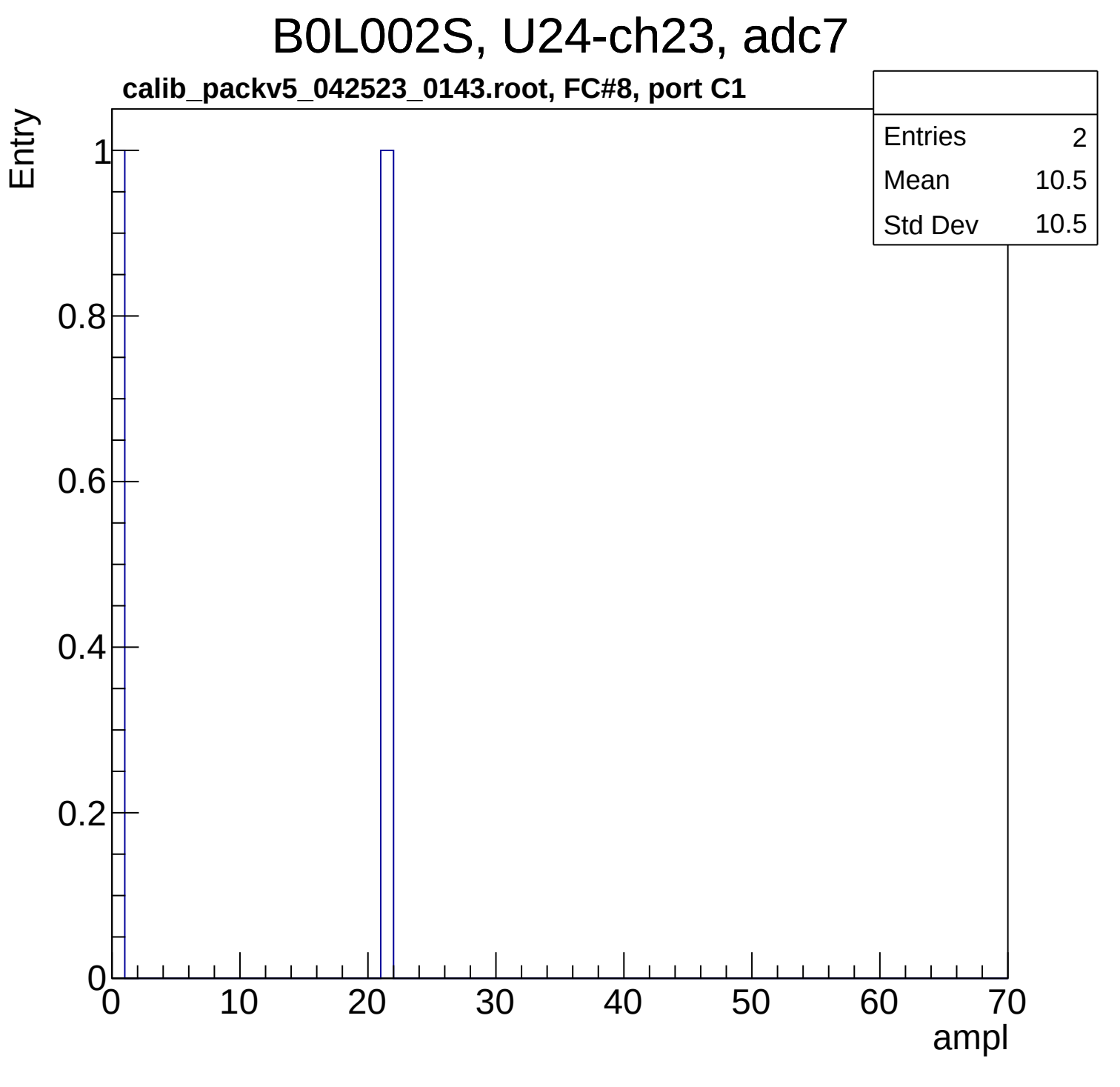
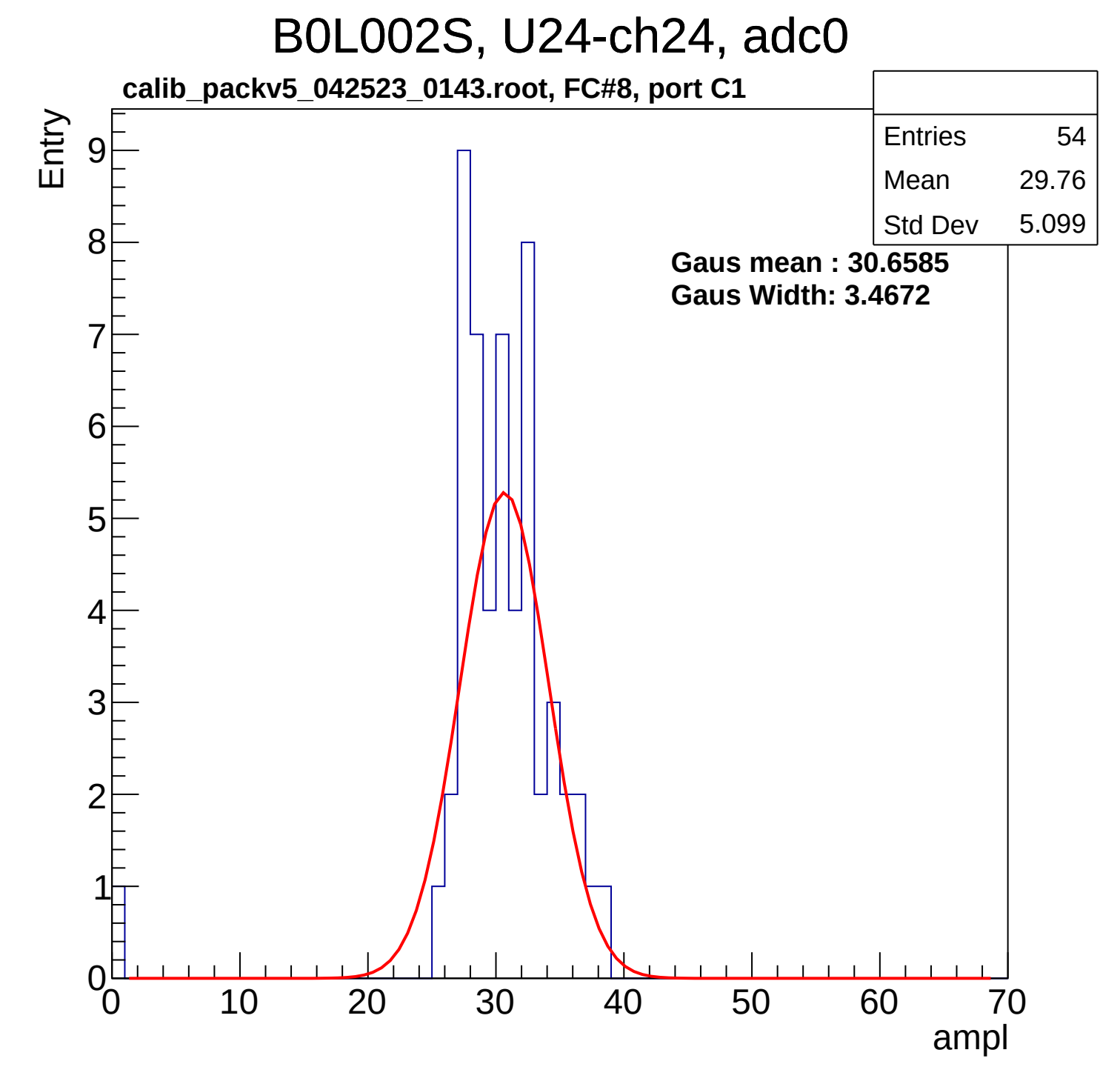
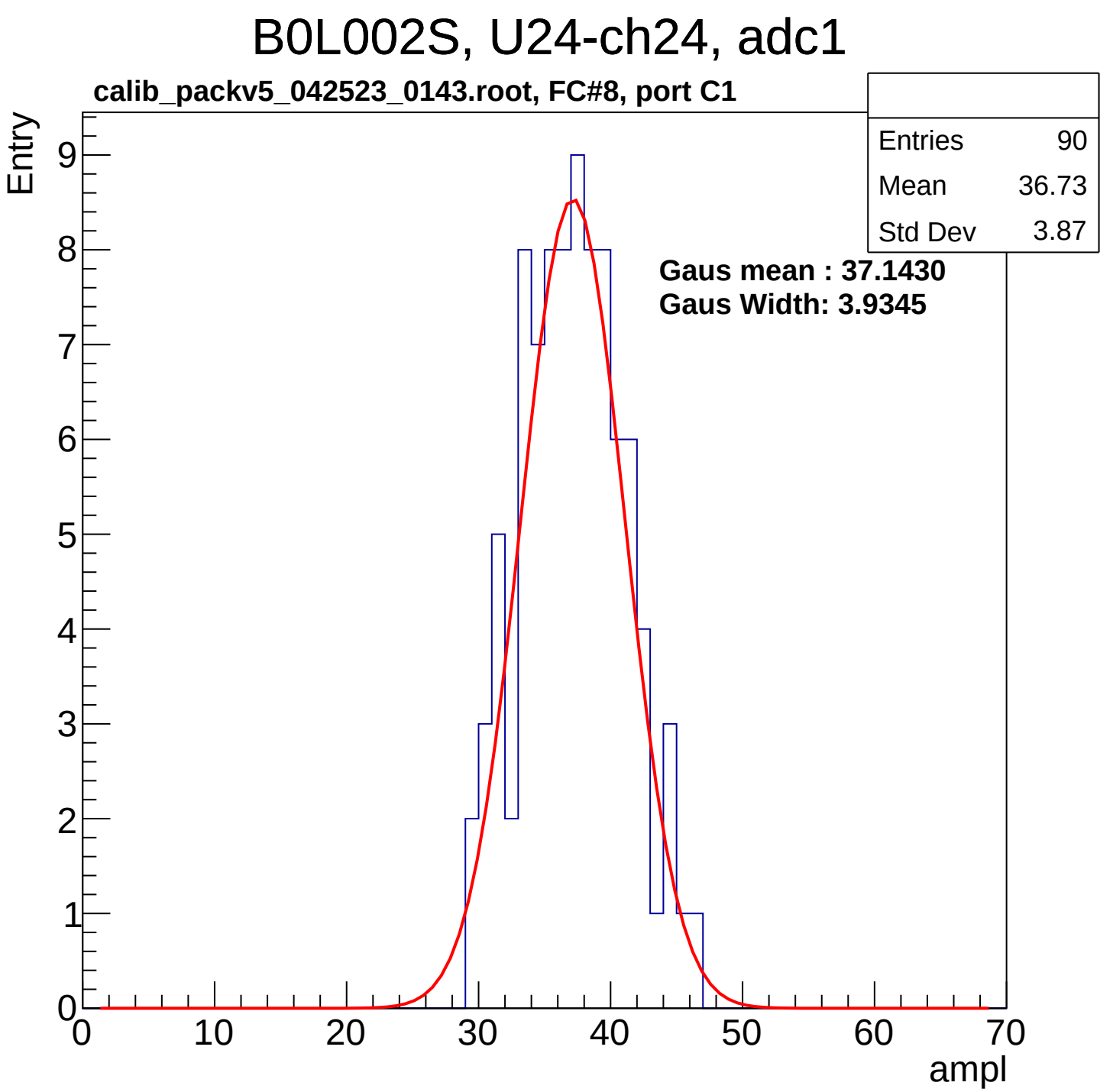

B0L002S, U24-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 29.76
Std Dev 5.099
Gaus mean : 30.6585
Gaus Width: 3.4672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 36.73
Std Dev 3.87
Gaus mean : 37.1430
Gaus Width: 3.9345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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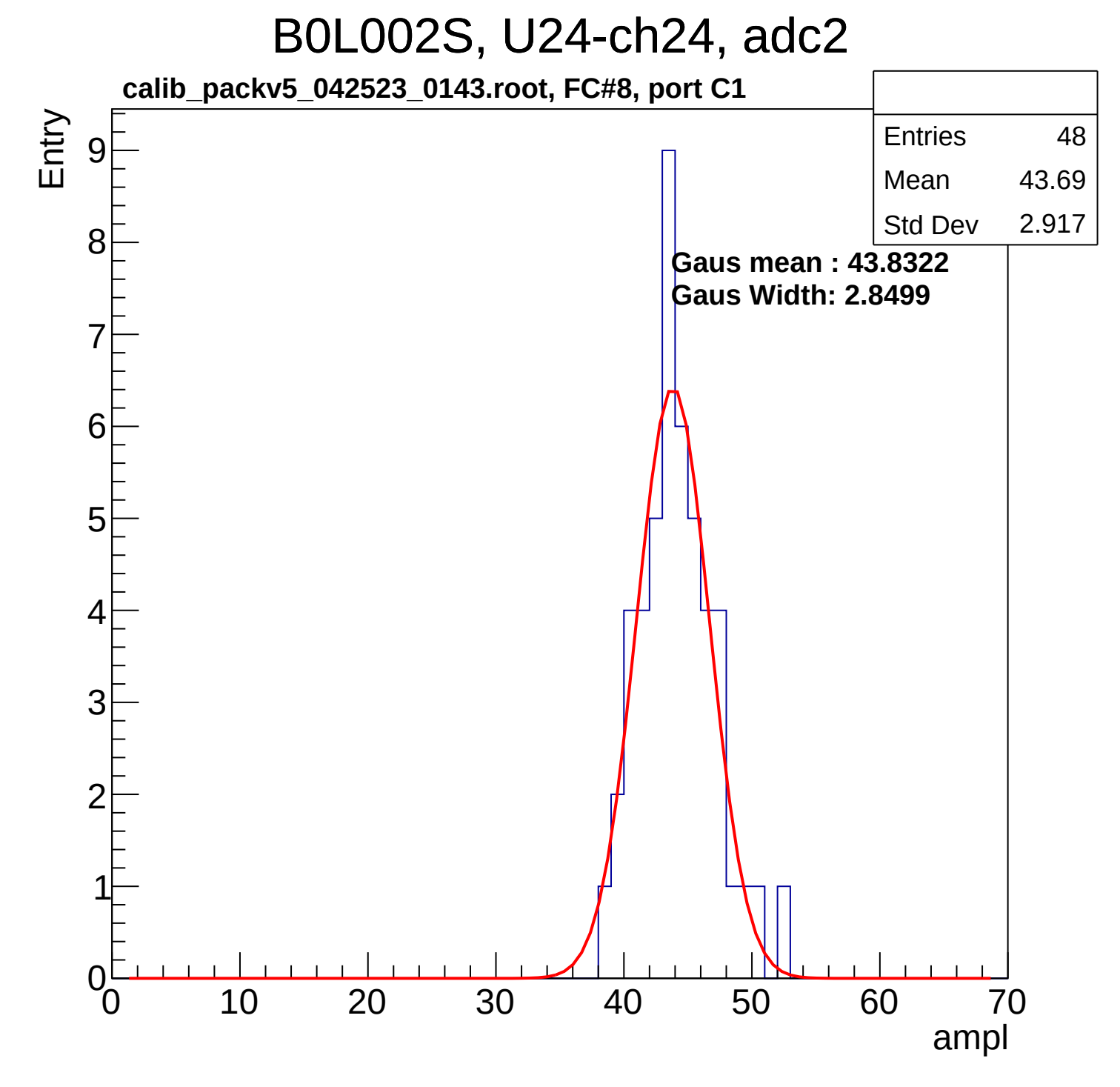
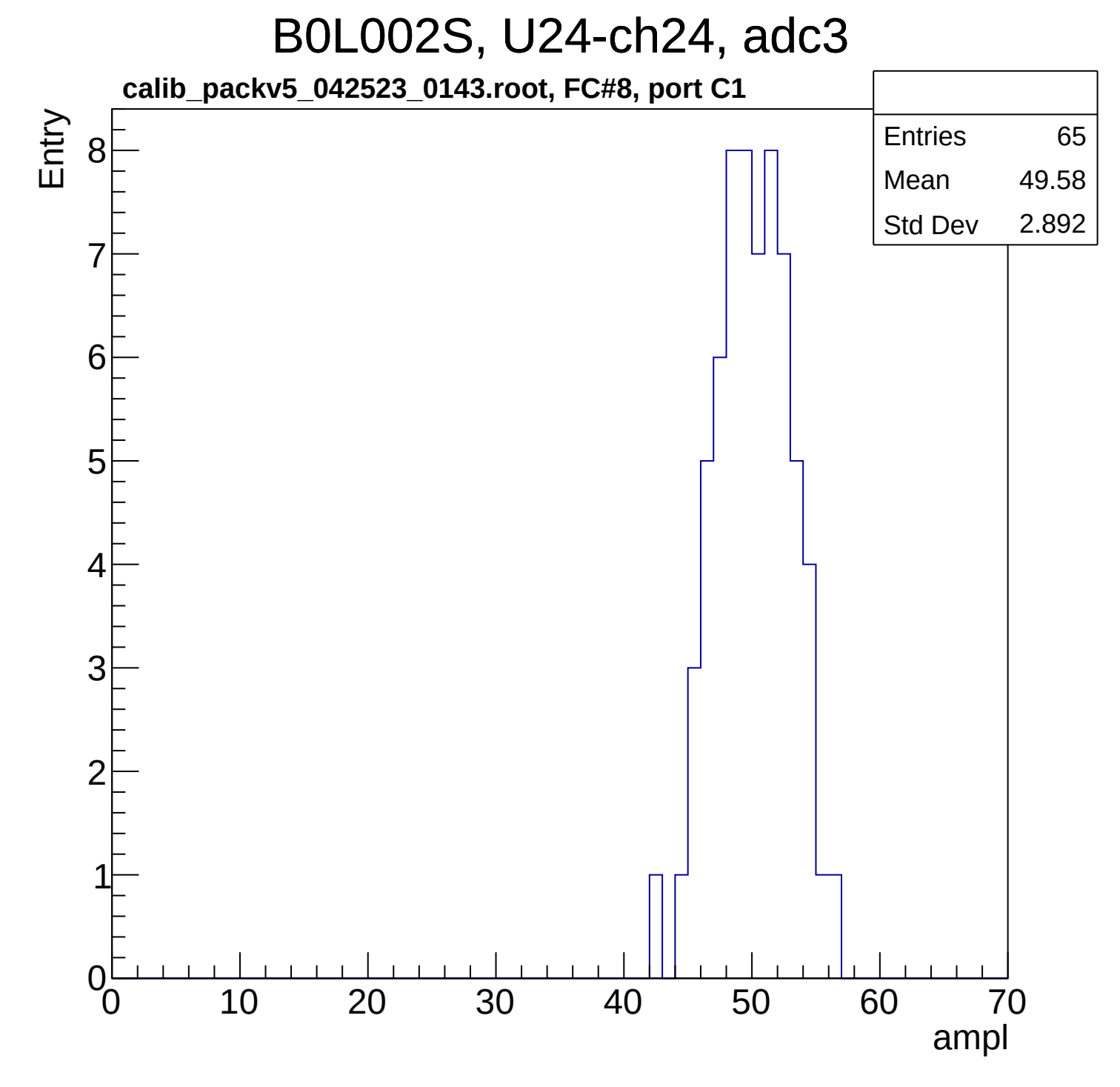
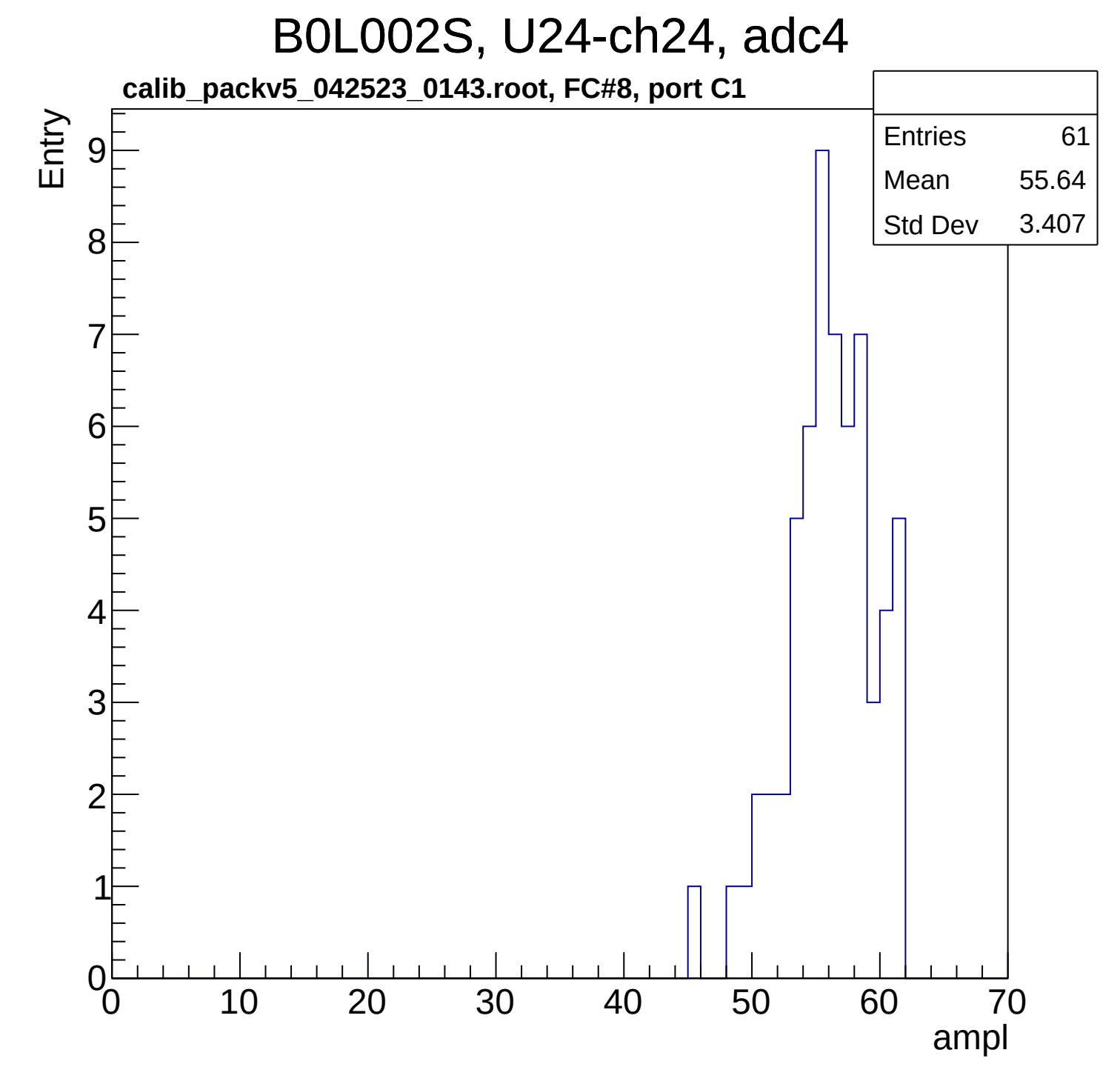
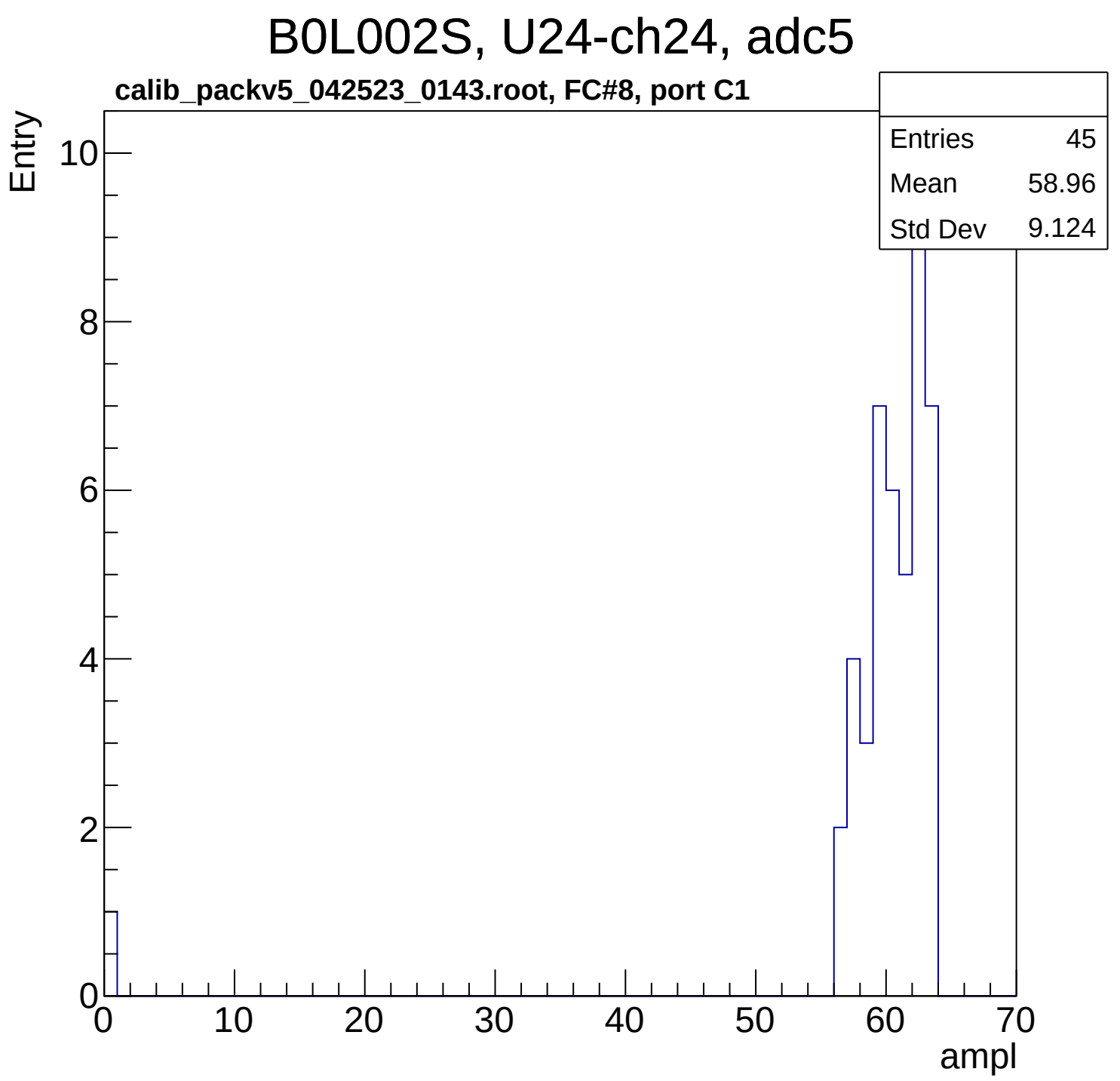
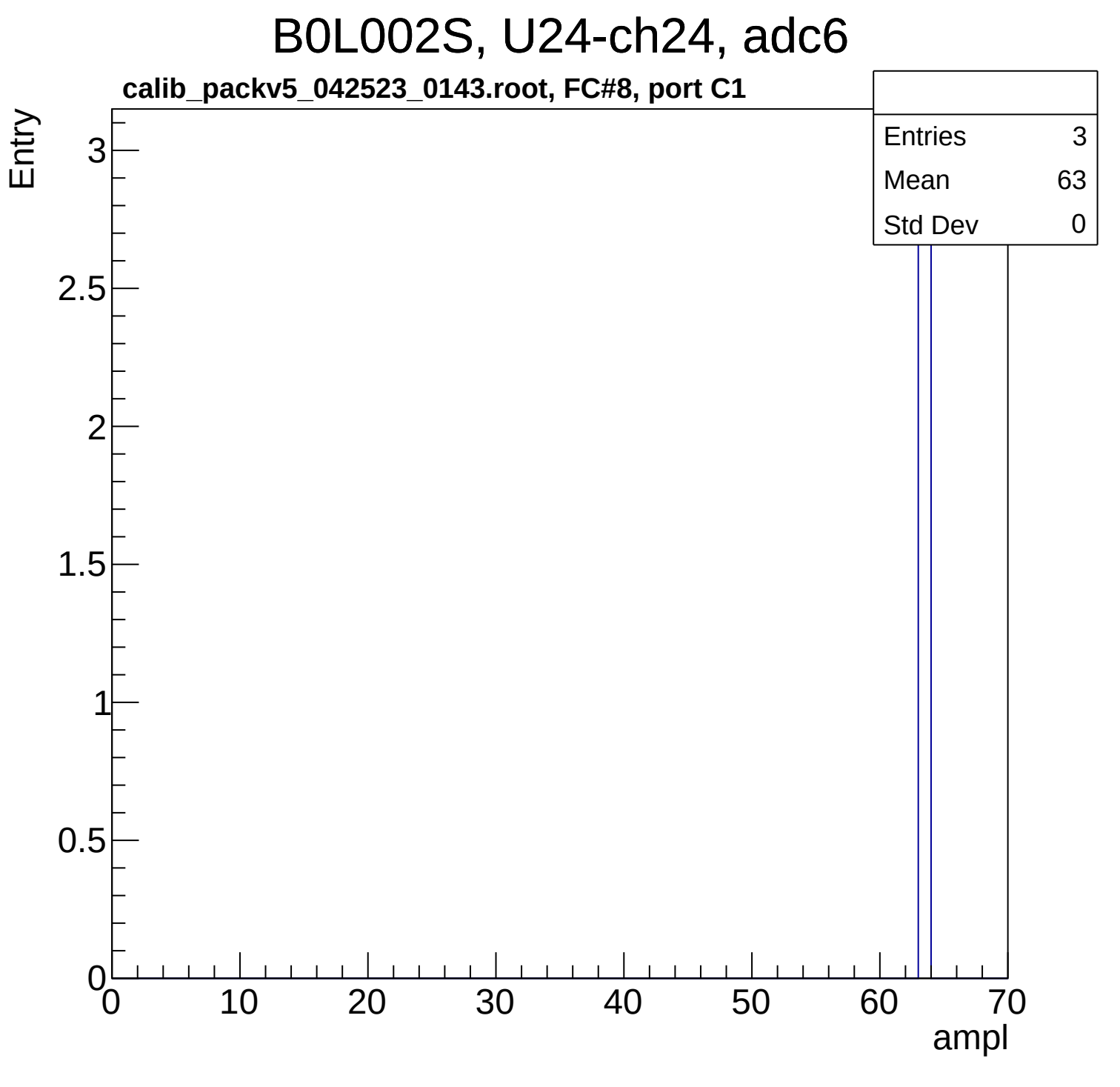
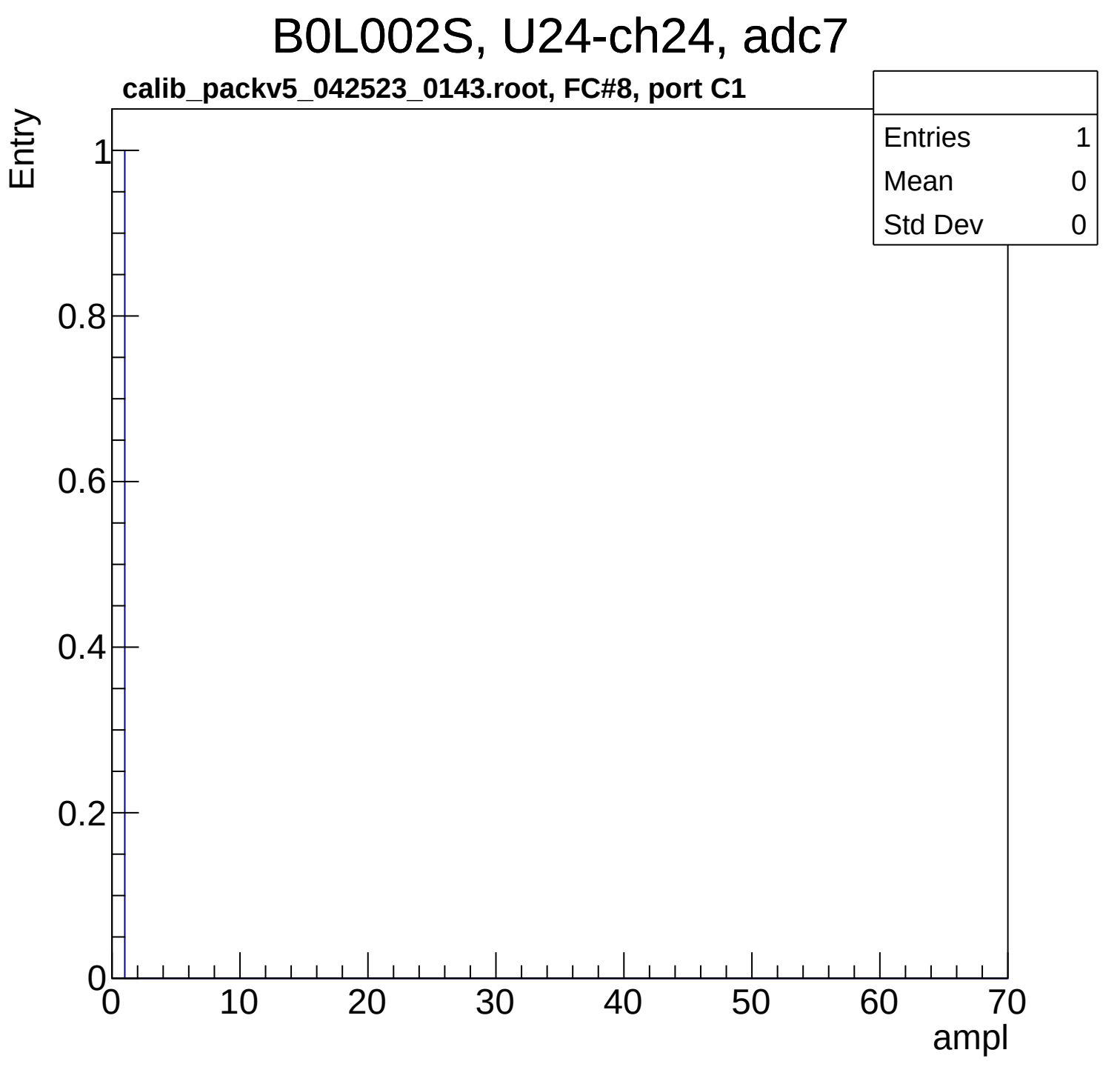

B0L002S, U24-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 43.69
Std Dev 2.917
Gaus mean : 43.8322
Gaus Width: 2.8499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

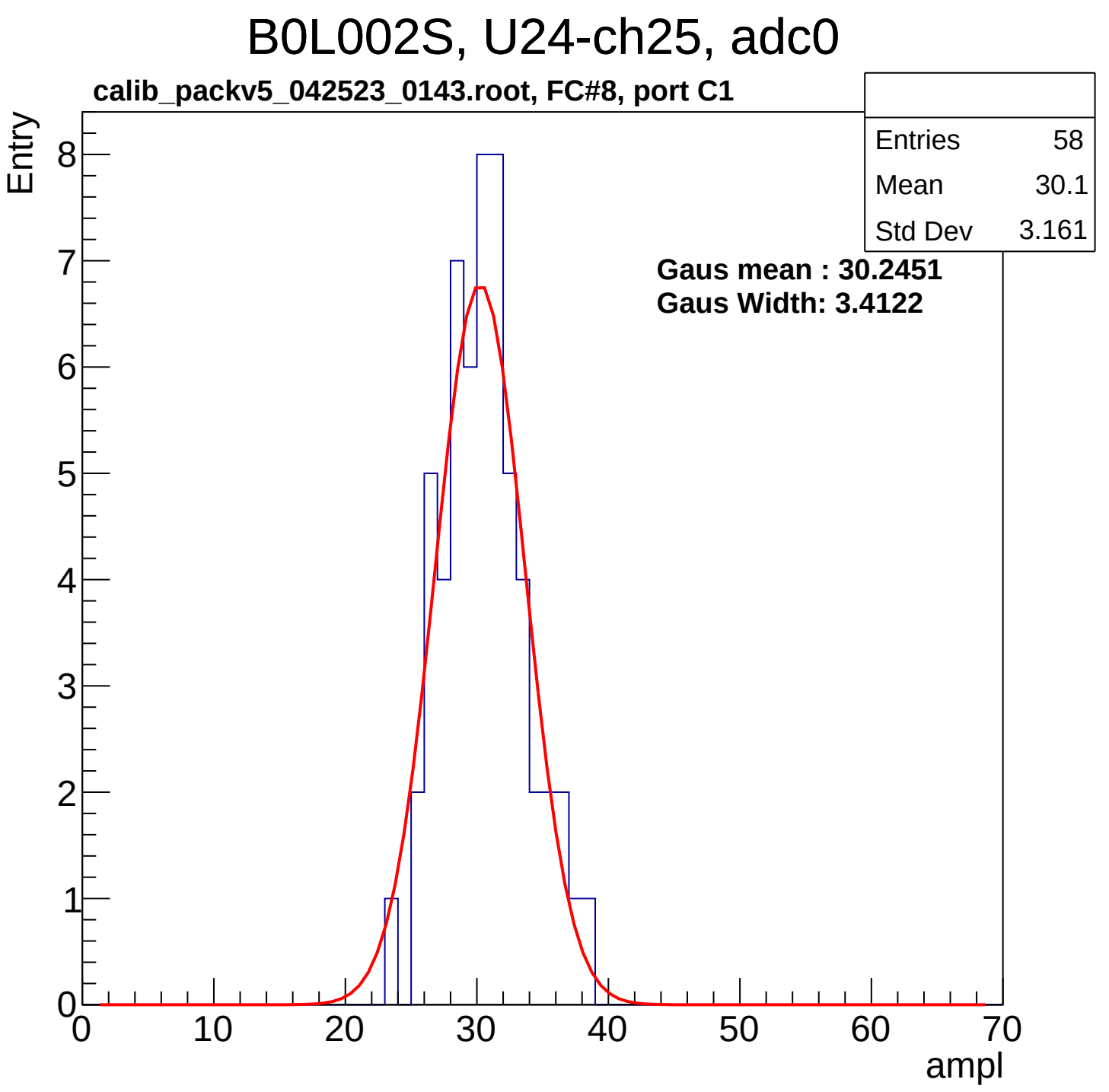

B0L002S, U24-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.58
Std Dev 2.892
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

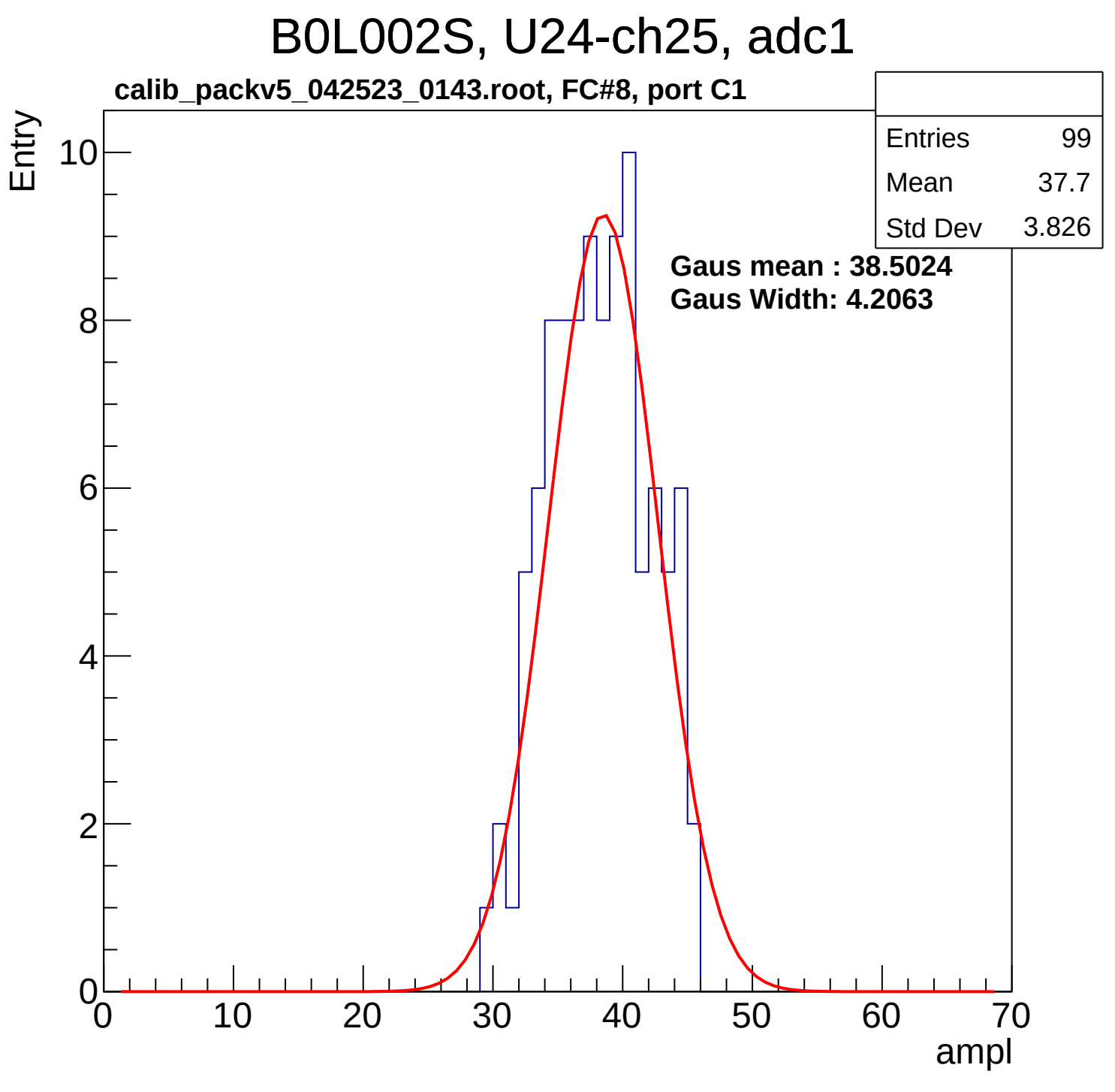

B0L002S, U24-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.64
Std Dev 3.407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

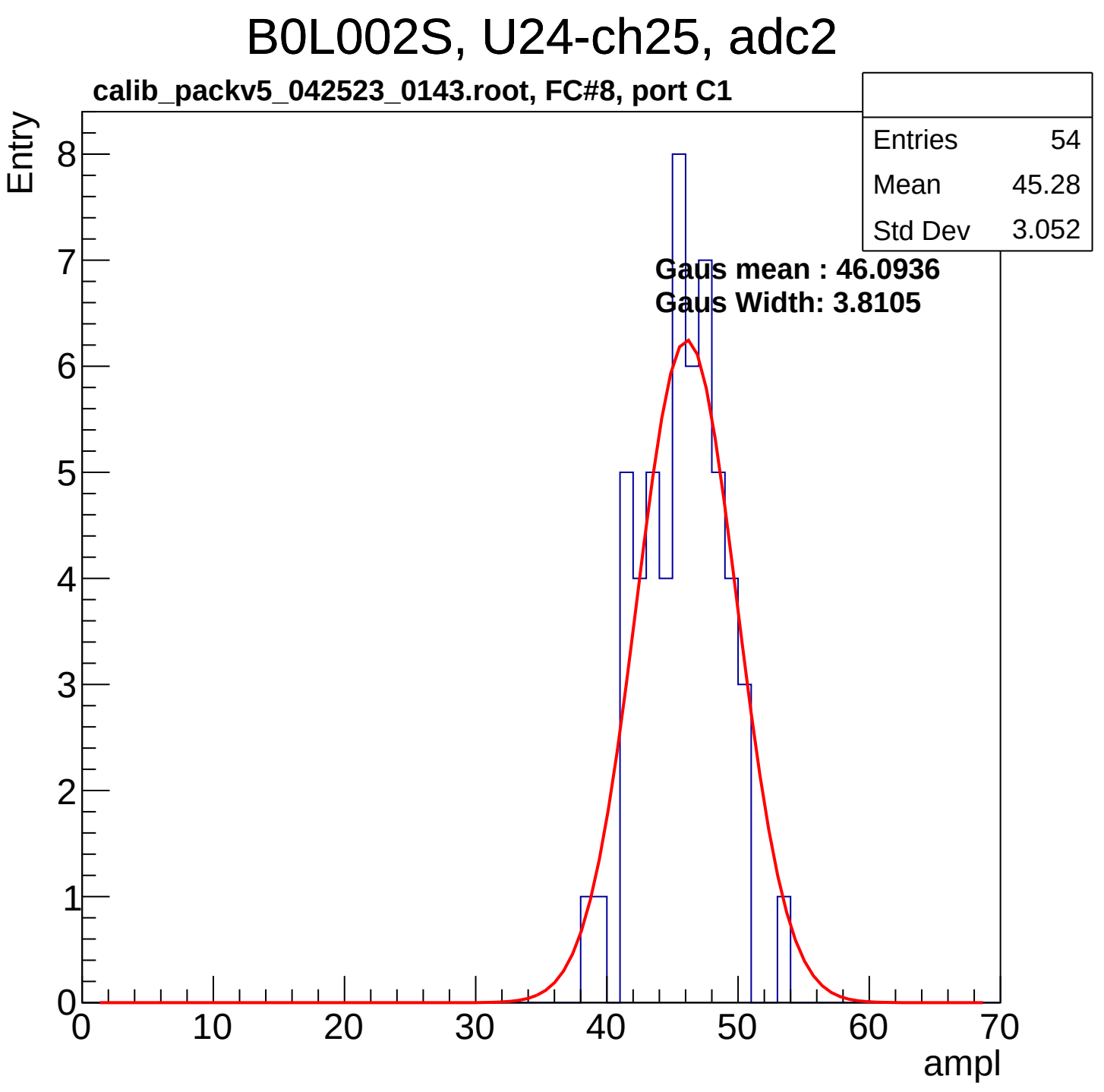

B0L002S, U24-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.96
Std Dev 9.124
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

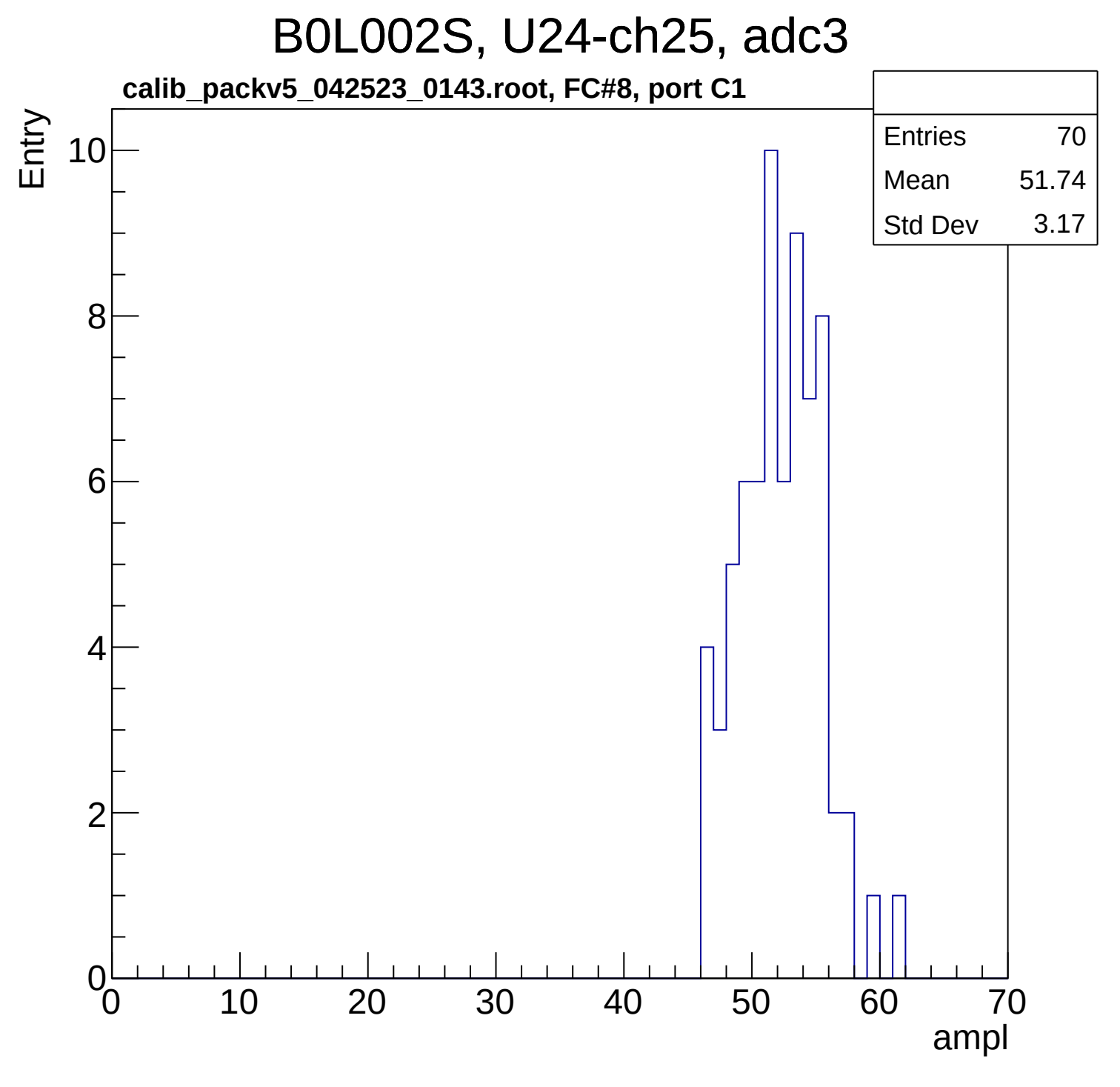

B0L002S, U24-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

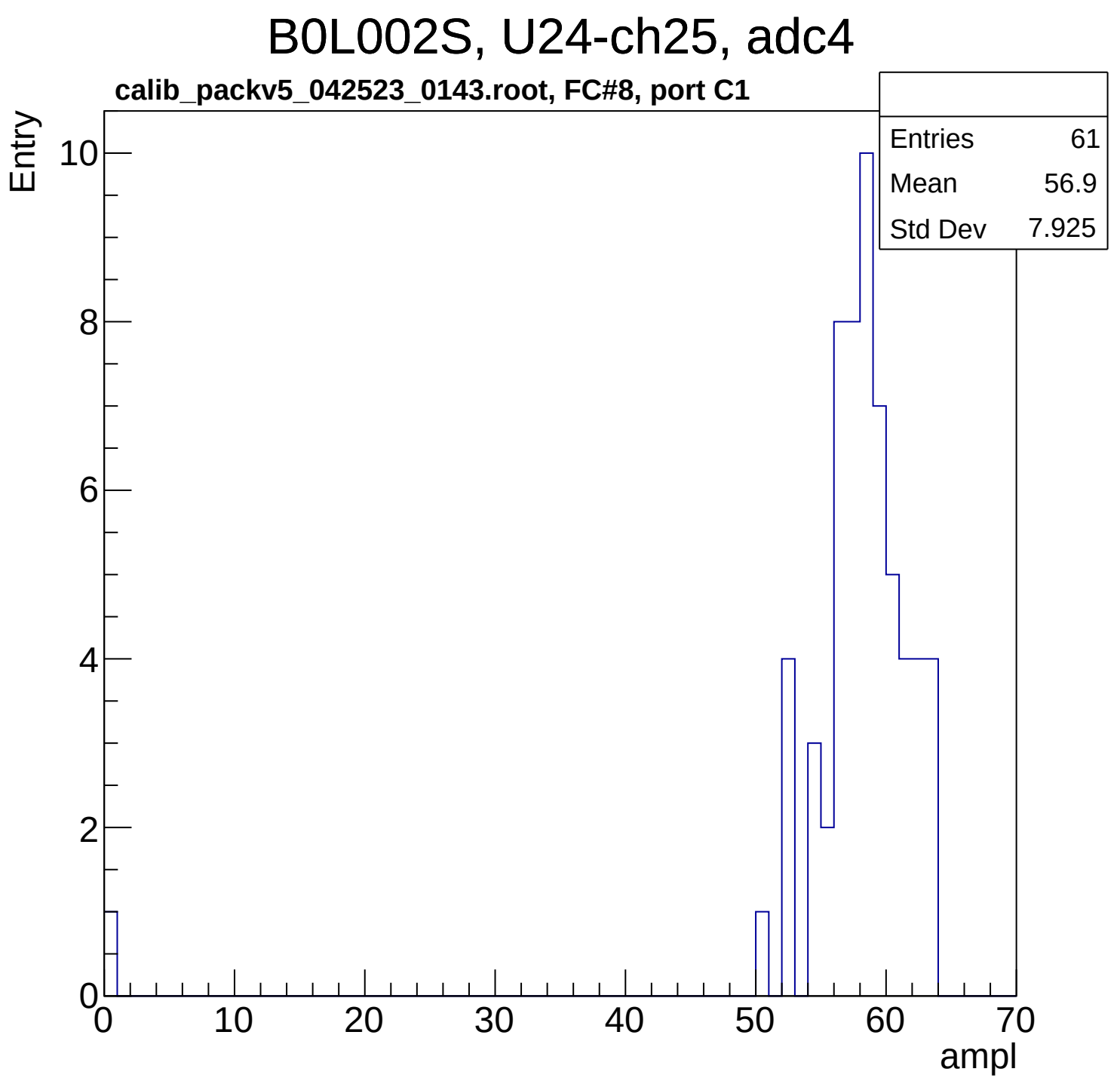

B0L002S, U24-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

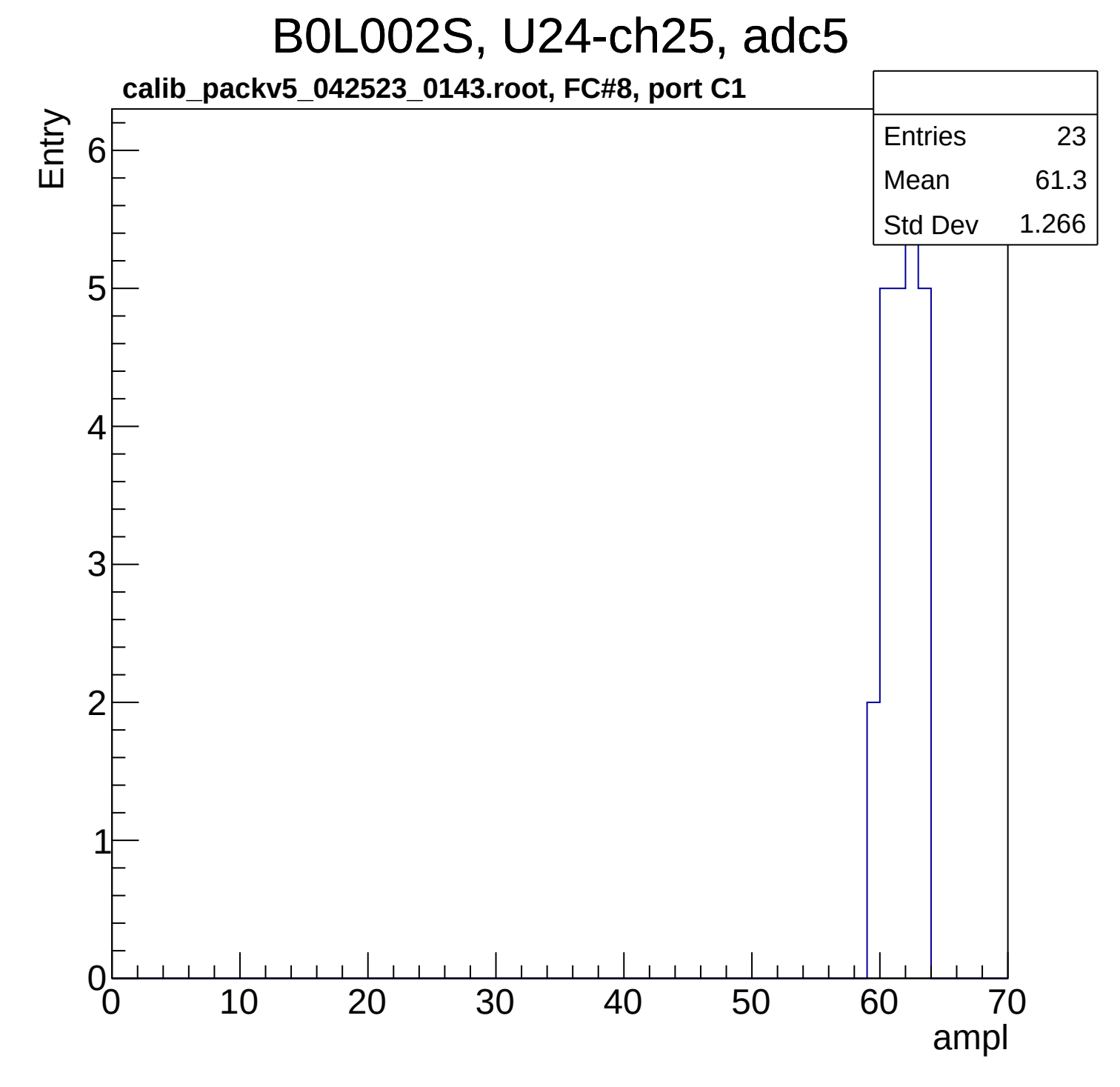

B0L002S, U24-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.1
Std Dev 3.161
Gaus mean : 30.2451
Gaus Width: 3.4122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

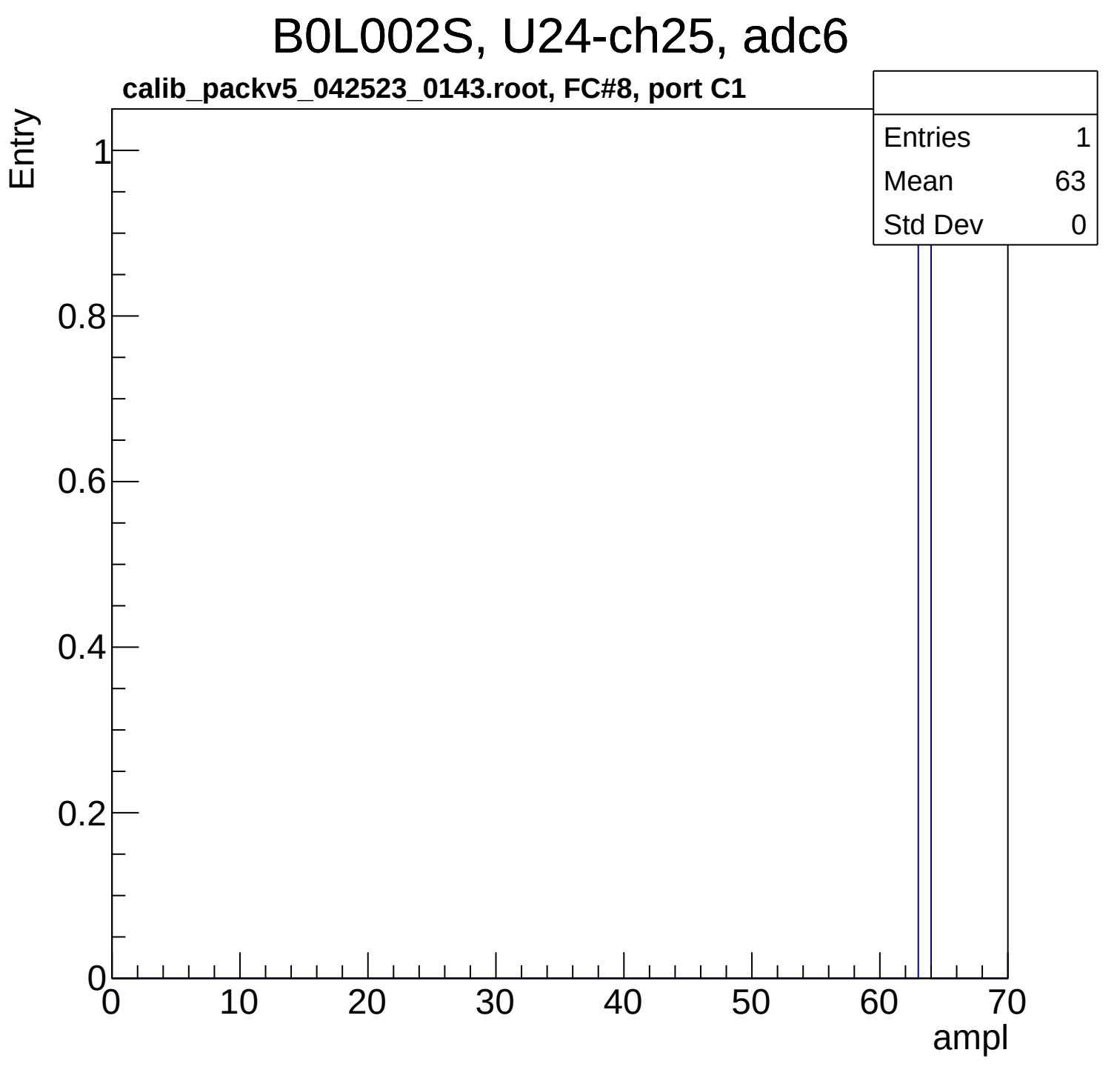

B0L002S, U24-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 99
Mean 37.7
Std Dev 3.826
Gaus mean : 38.5024
Gaus Width: 4.2063
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 45.28
Std Dev 3.052
Gaus mean : 46.0936
Gaus Width: 3.8105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.74
Std Dev 3.17
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.9
Std Dev 7.925
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 61.3
Std Dev 1.266
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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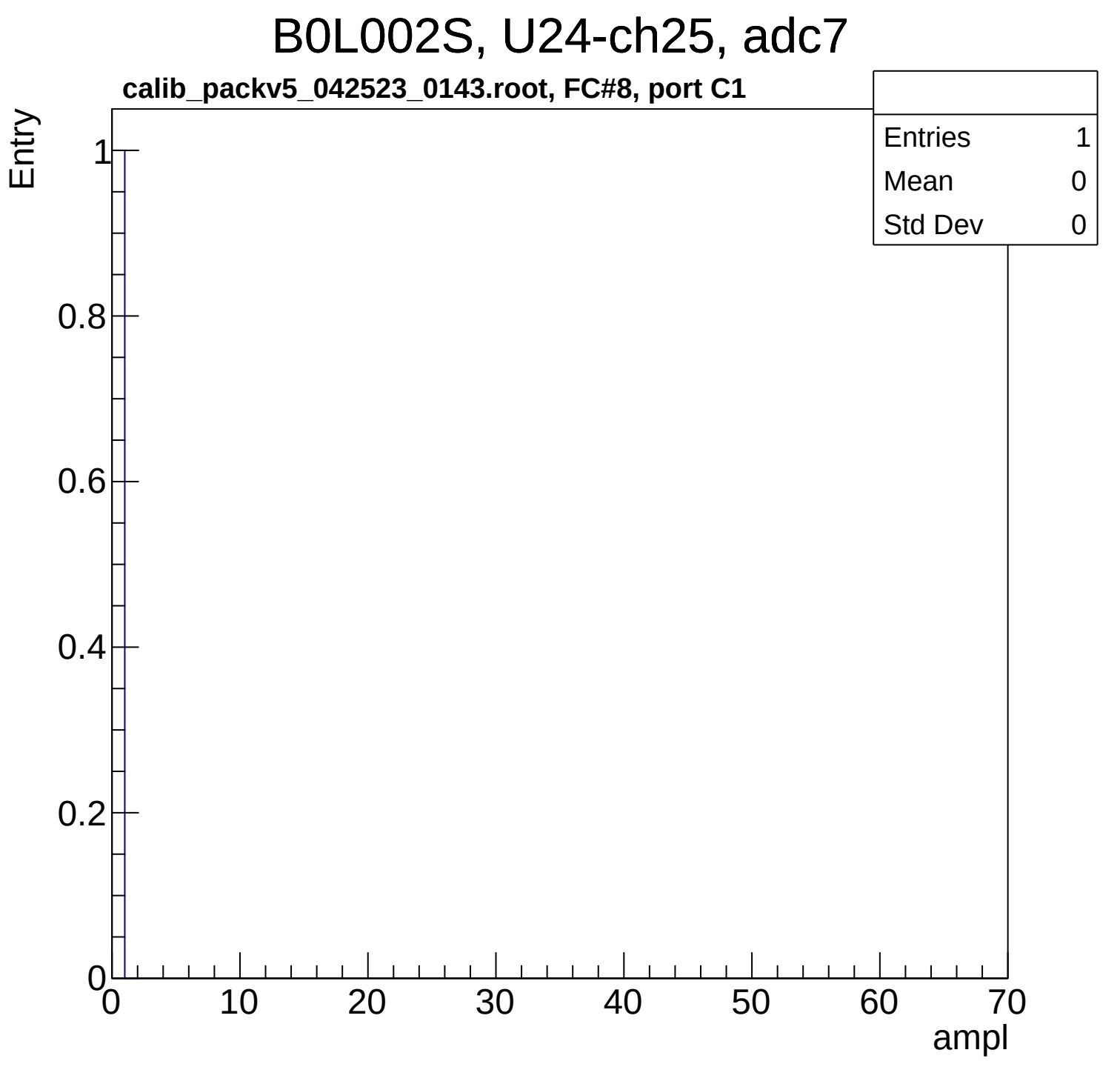
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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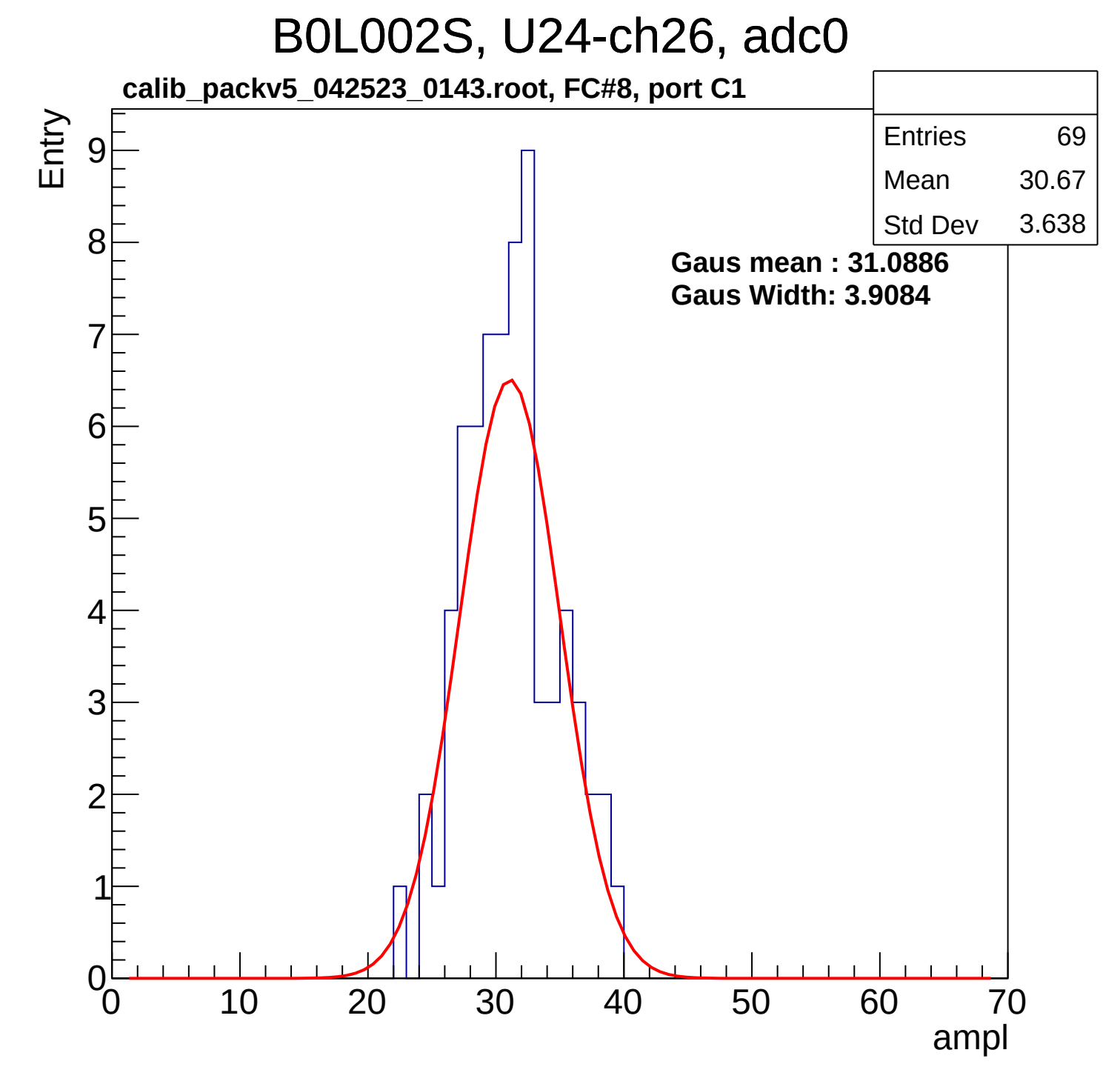
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.67
Std Dev 3.638
Gaus mean : 31.0886
Gaus Width: 3.9084
Entry
ampl
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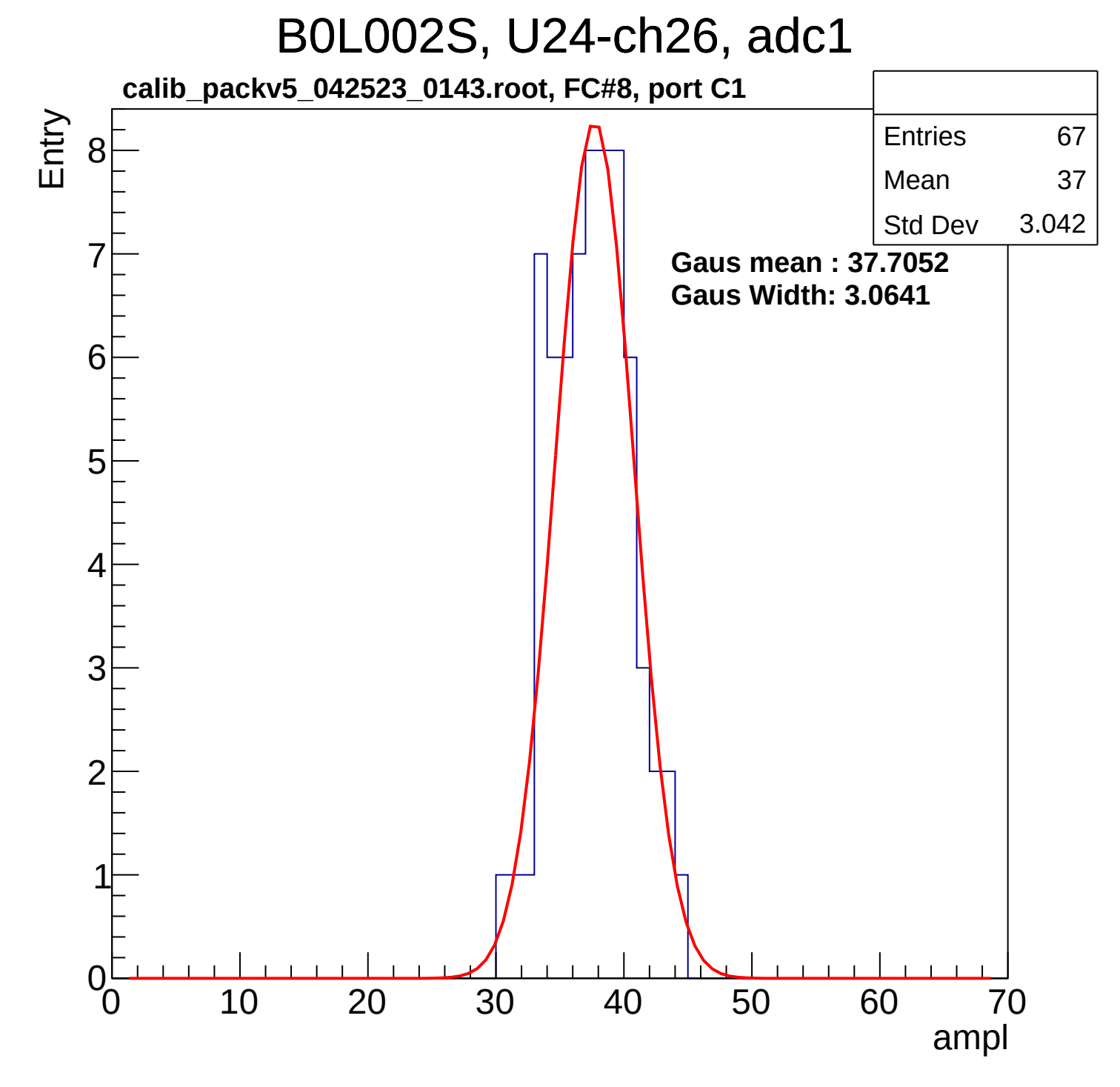
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37
Std Dev 3.042
Gaus mean : 37.7052
Gaus Width: 3.0641
Entry
ampl
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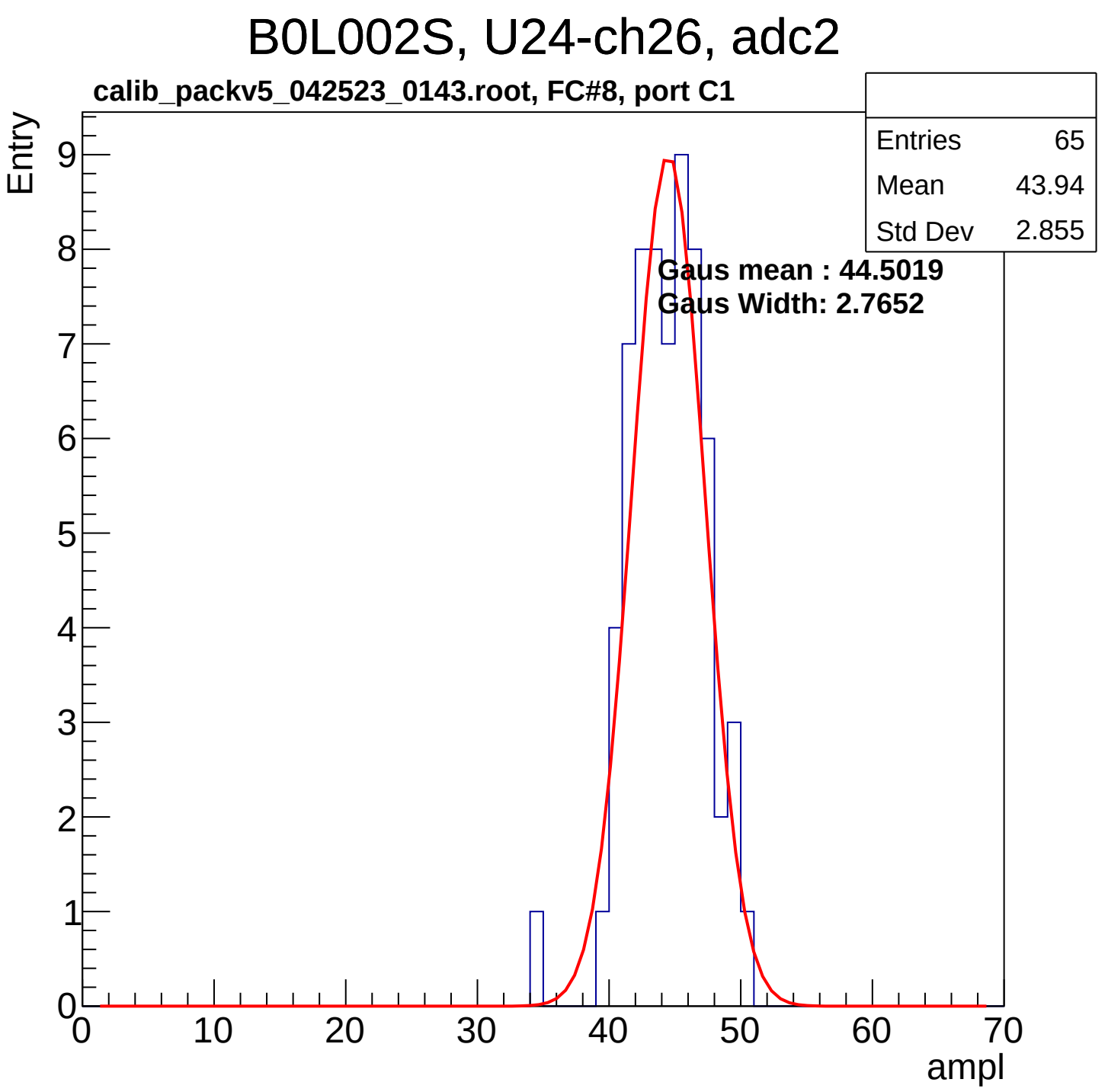
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.94
Std Dev 2.855
Gaus mean : 44.5019
Gaus Width: 2.7652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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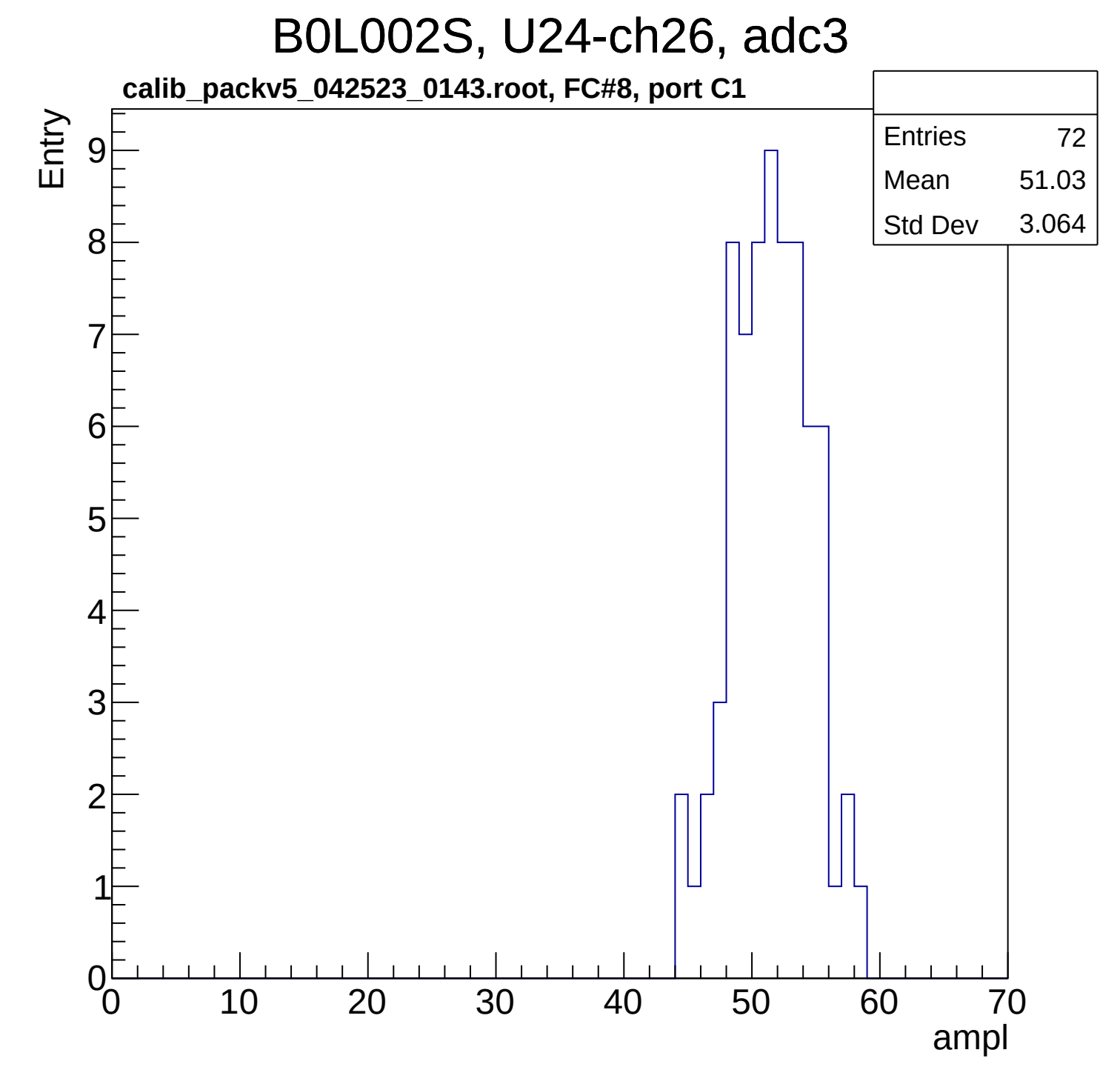
B0L002S, U24-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.03
Std Dev 3.064
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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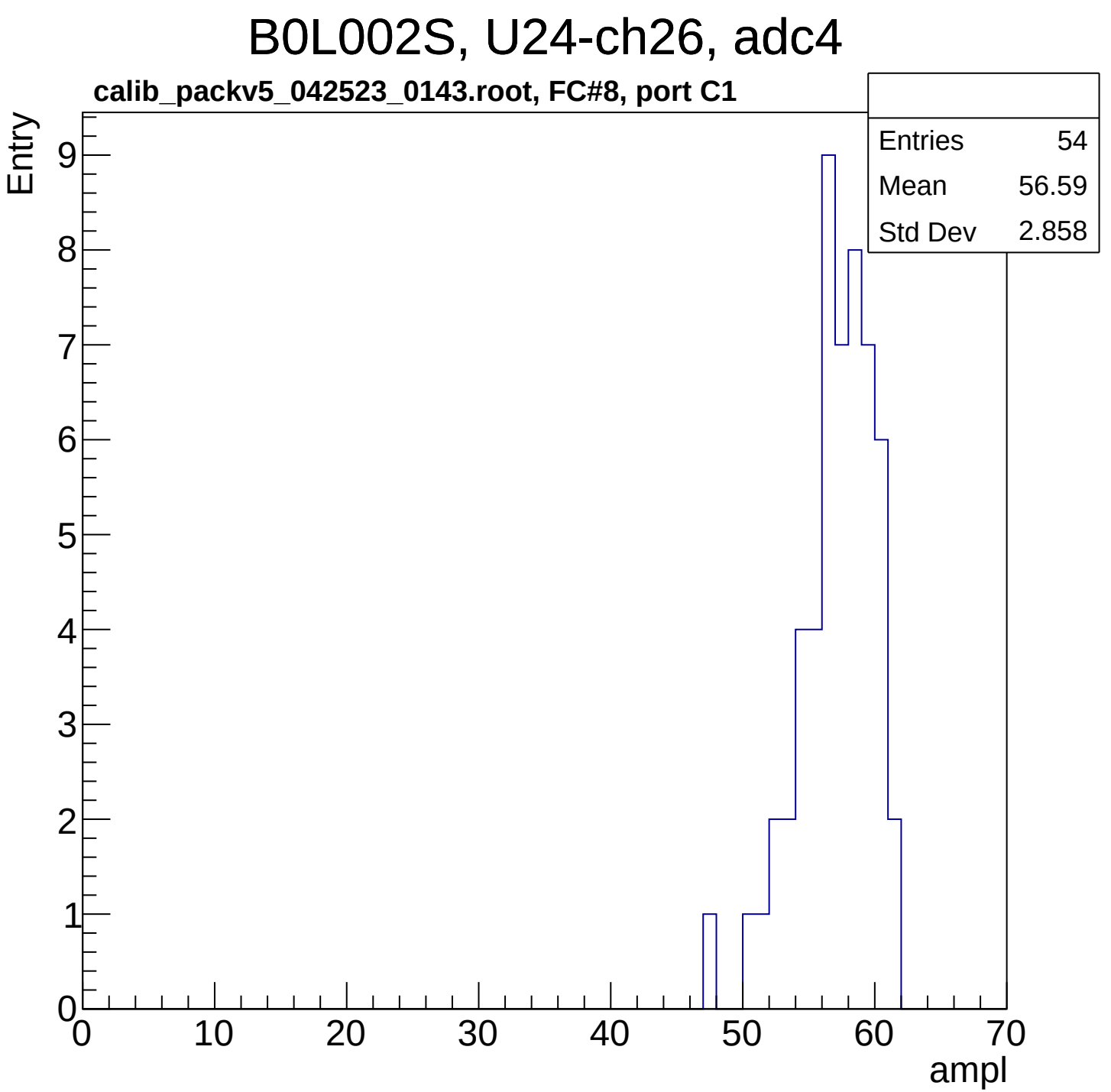
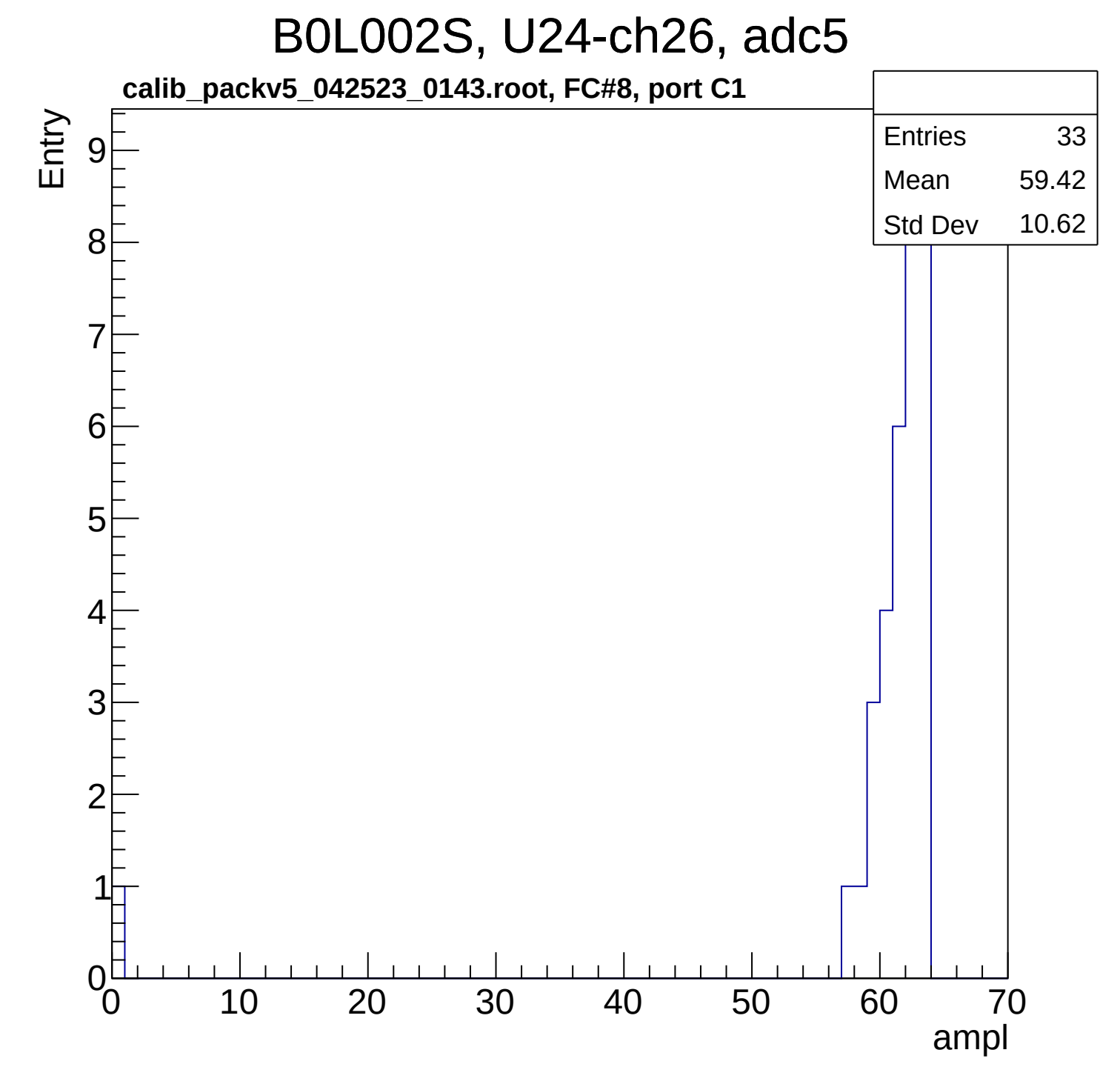
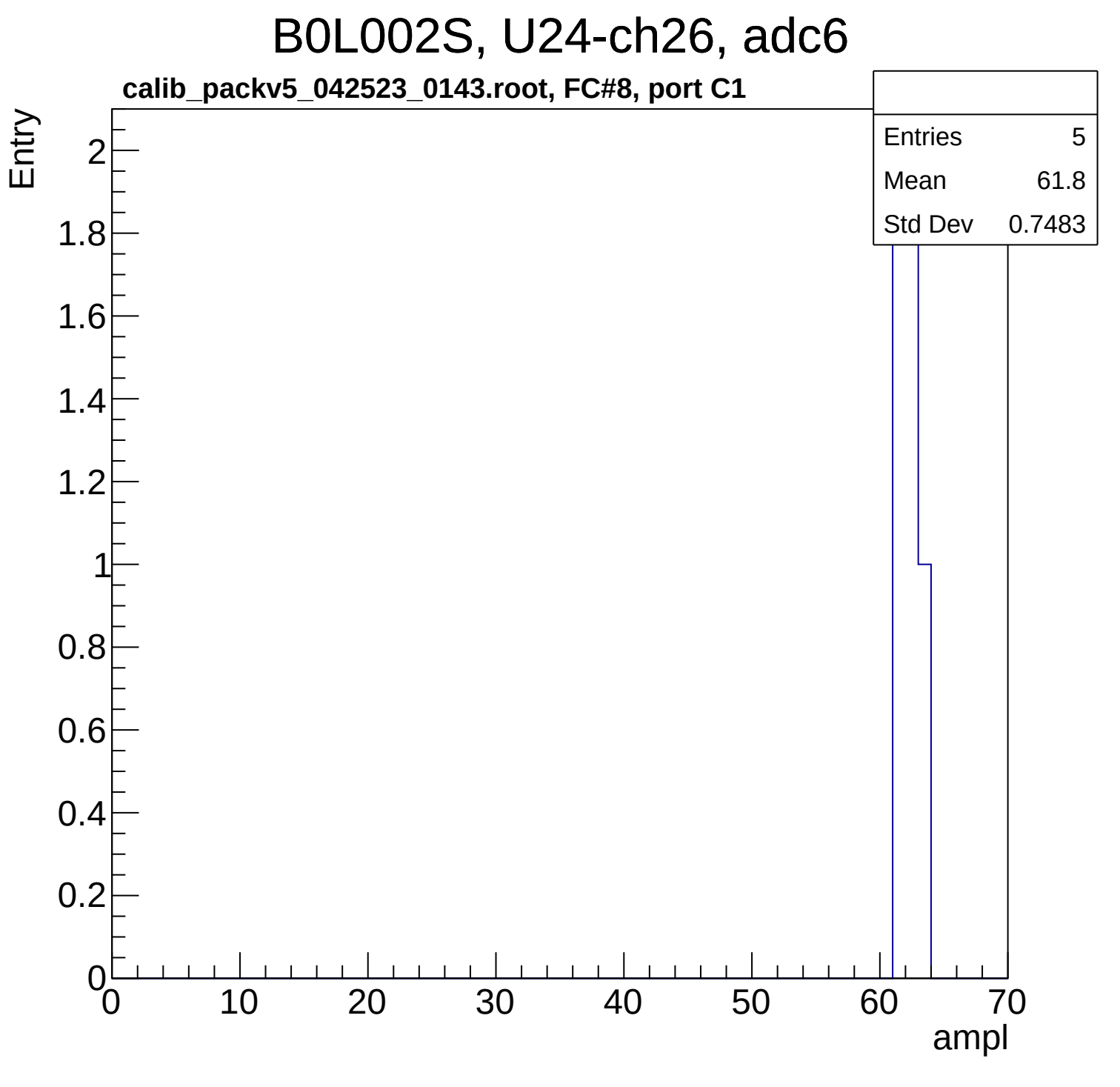
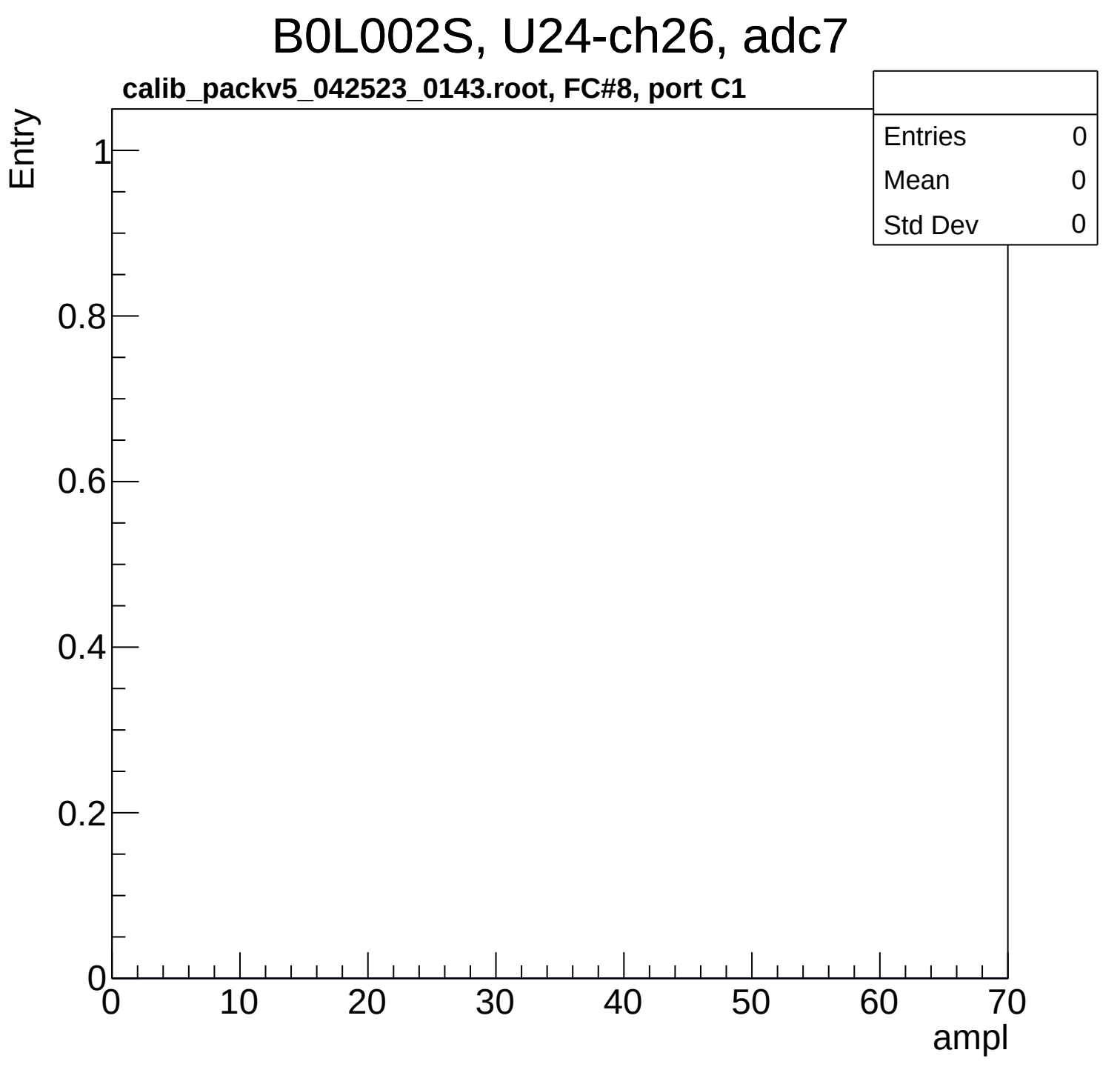
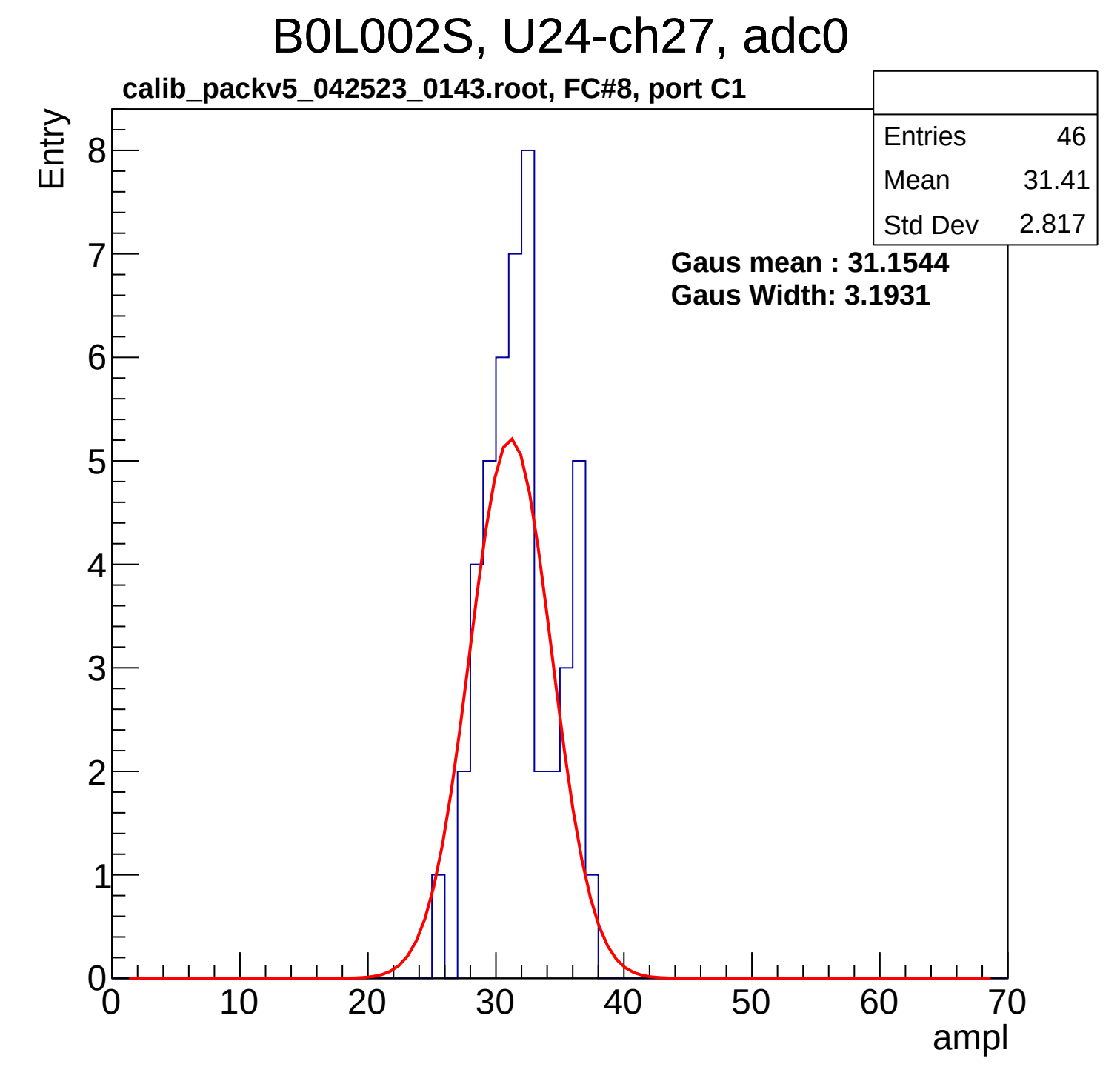
40
50
60
70

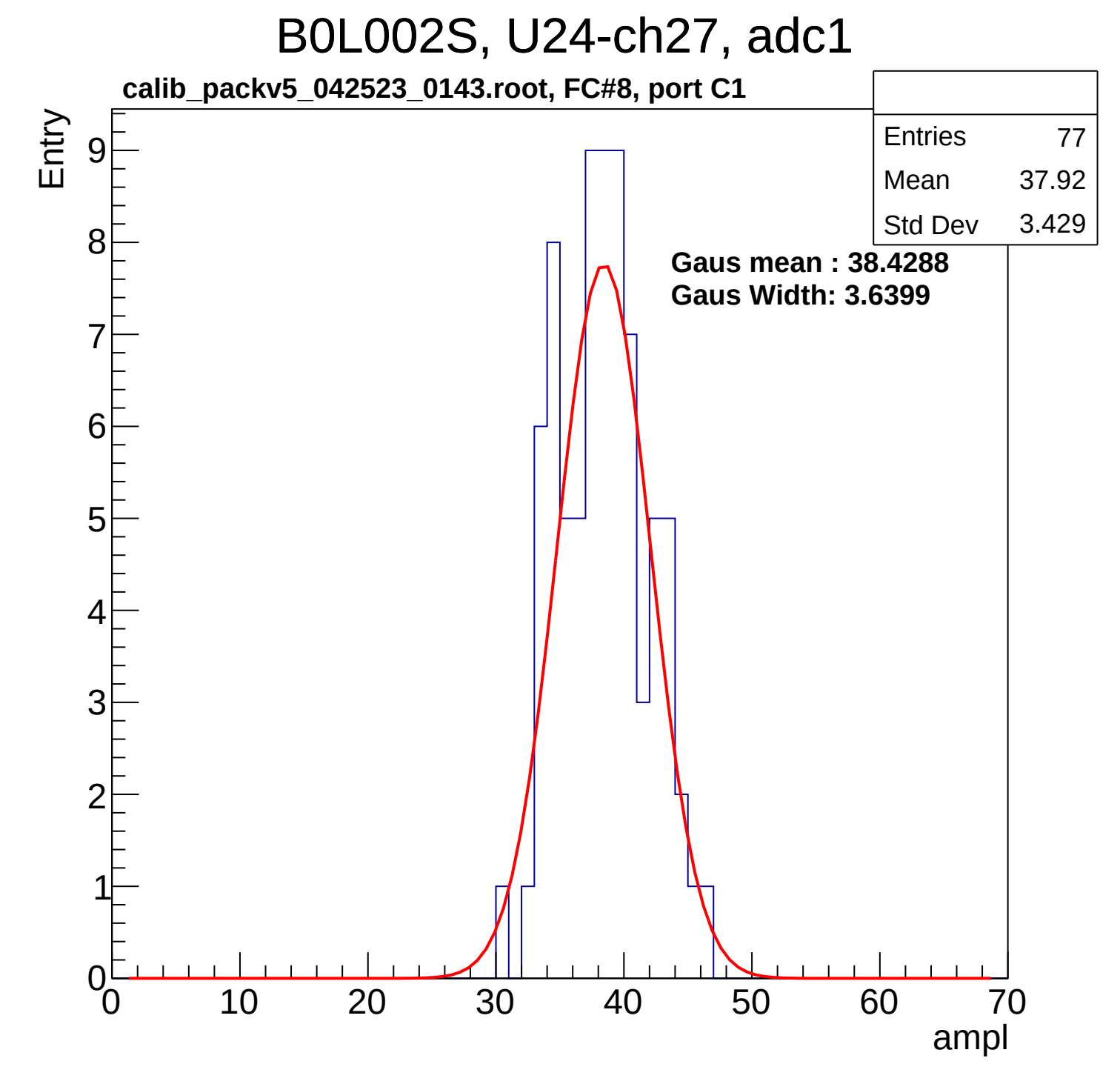

B0L002S, U24-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.59
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.42
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 31.41
Std Dev 2.817
Gaus mean : 31.1544
Gaus Width: 3.1931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.92
Std Dev 3.429
Gaus mean : 38.4288
Gaus Width: 3.6399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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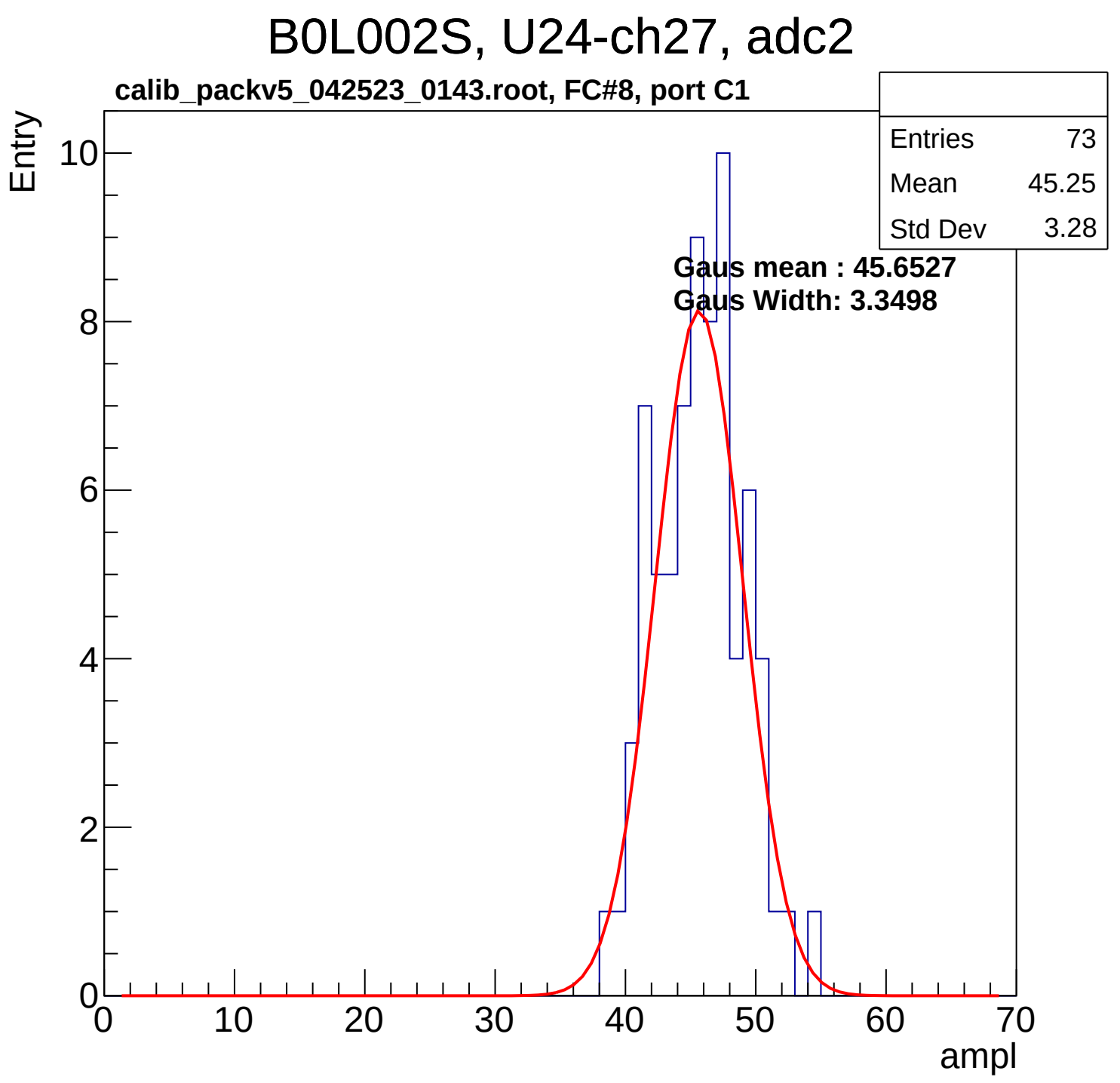
B0L002S, U24-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 45.25
Std Dev 3.28
Gaus mean : 45.6527
Gaus Width: 3.3498
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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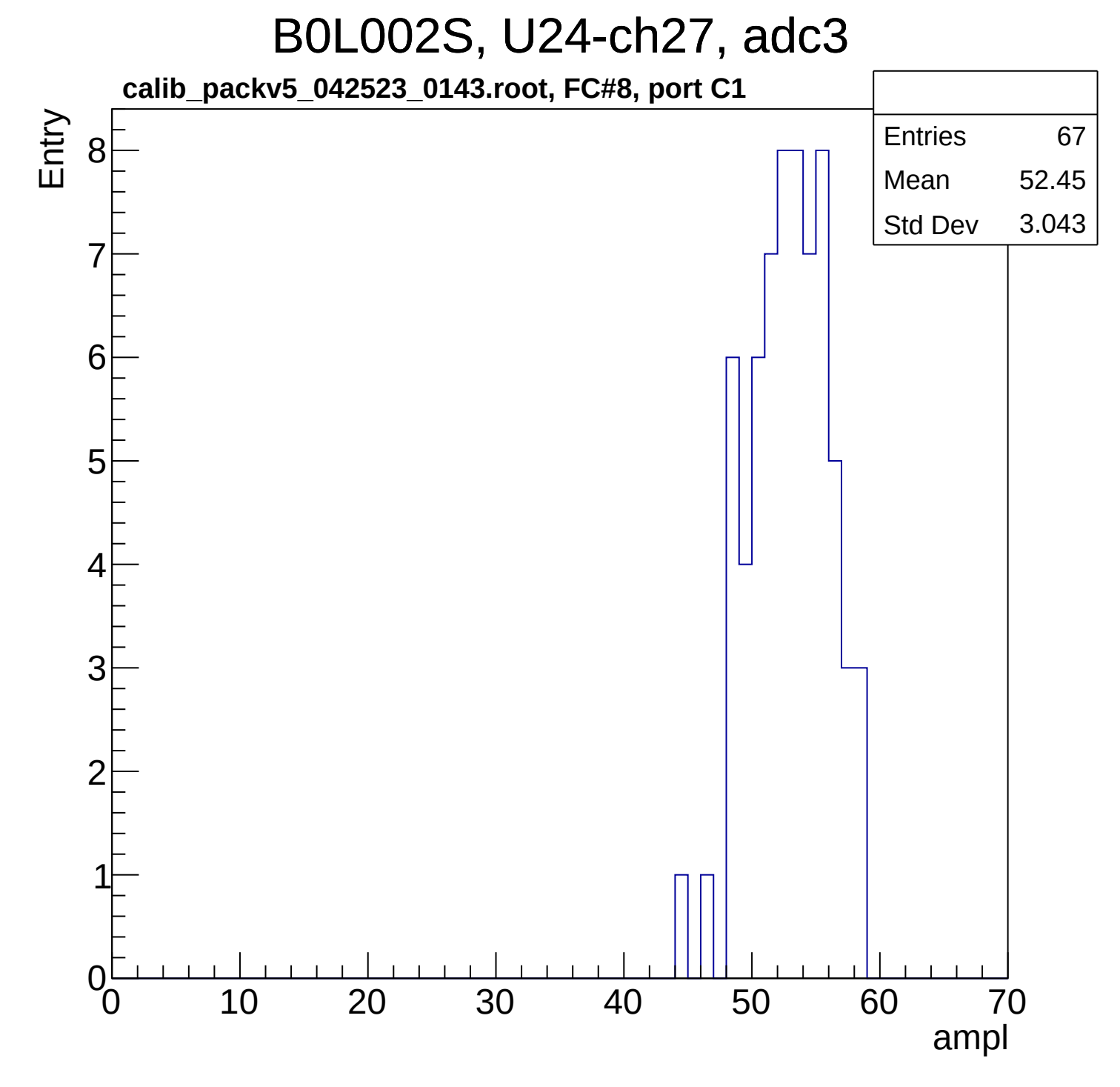
B0L002S, U24-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 52.45
Std Dev 3.043
Entry
ampl
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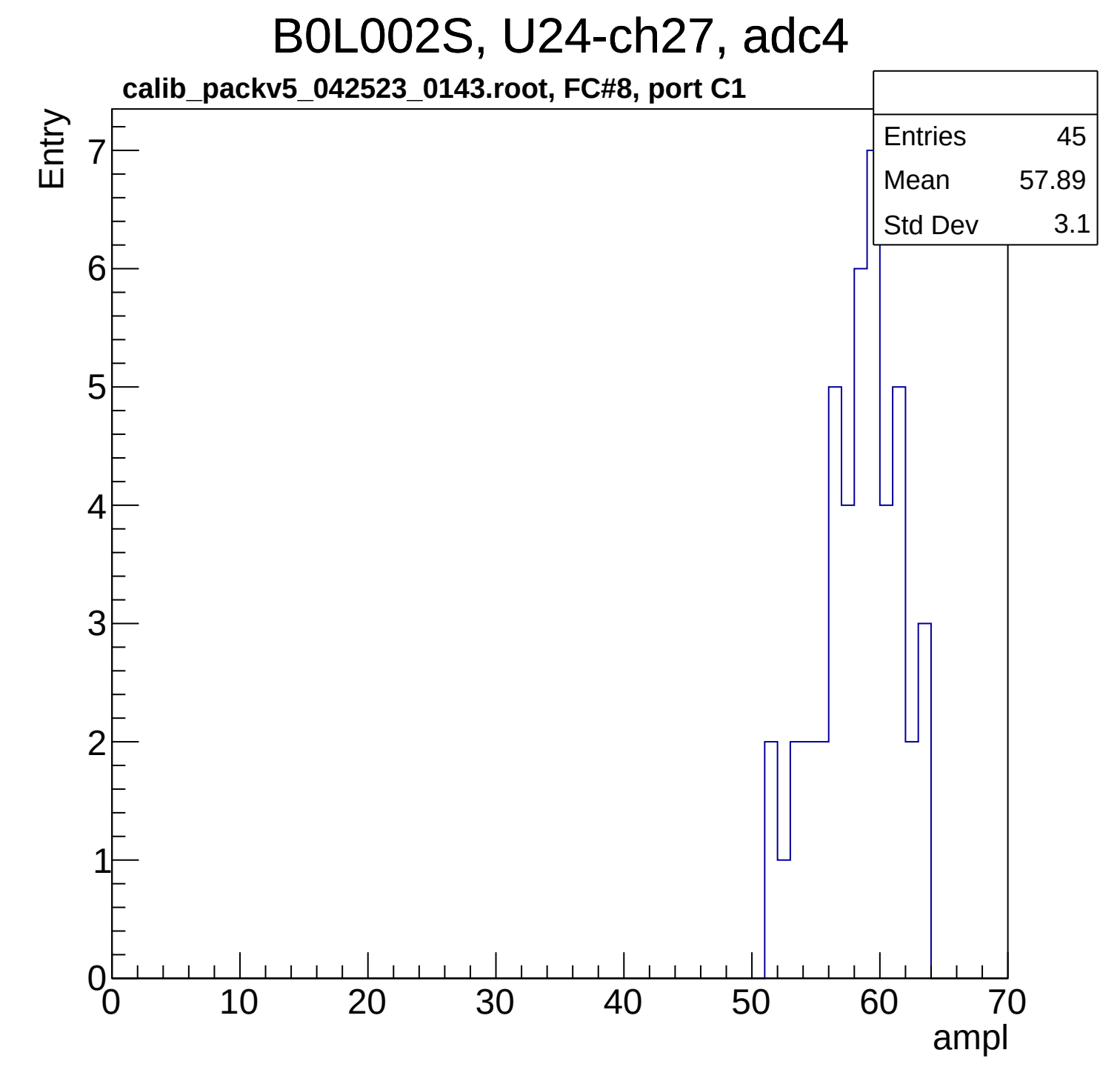
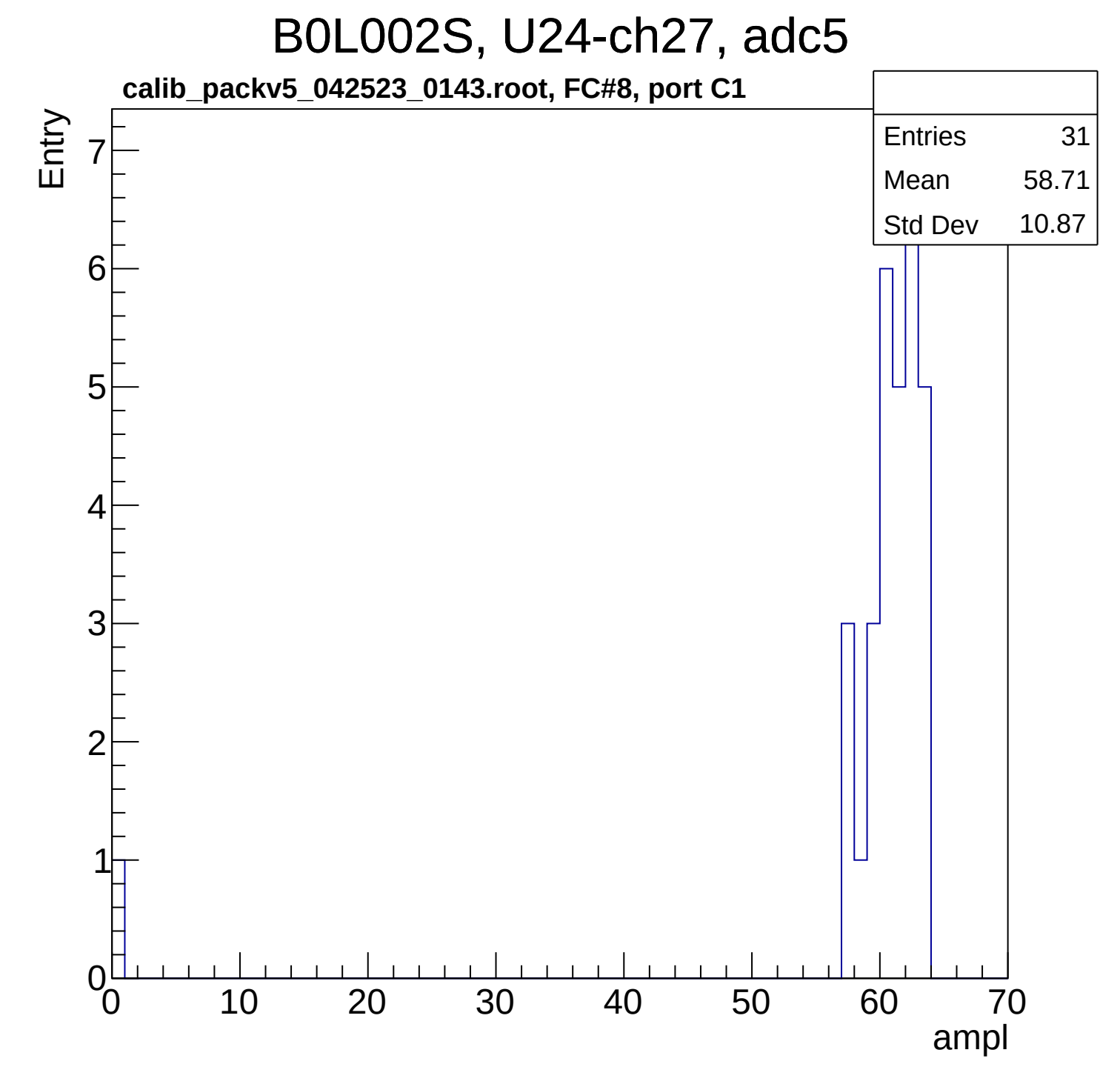
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57.89
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 58.71
Std Dev 10.87
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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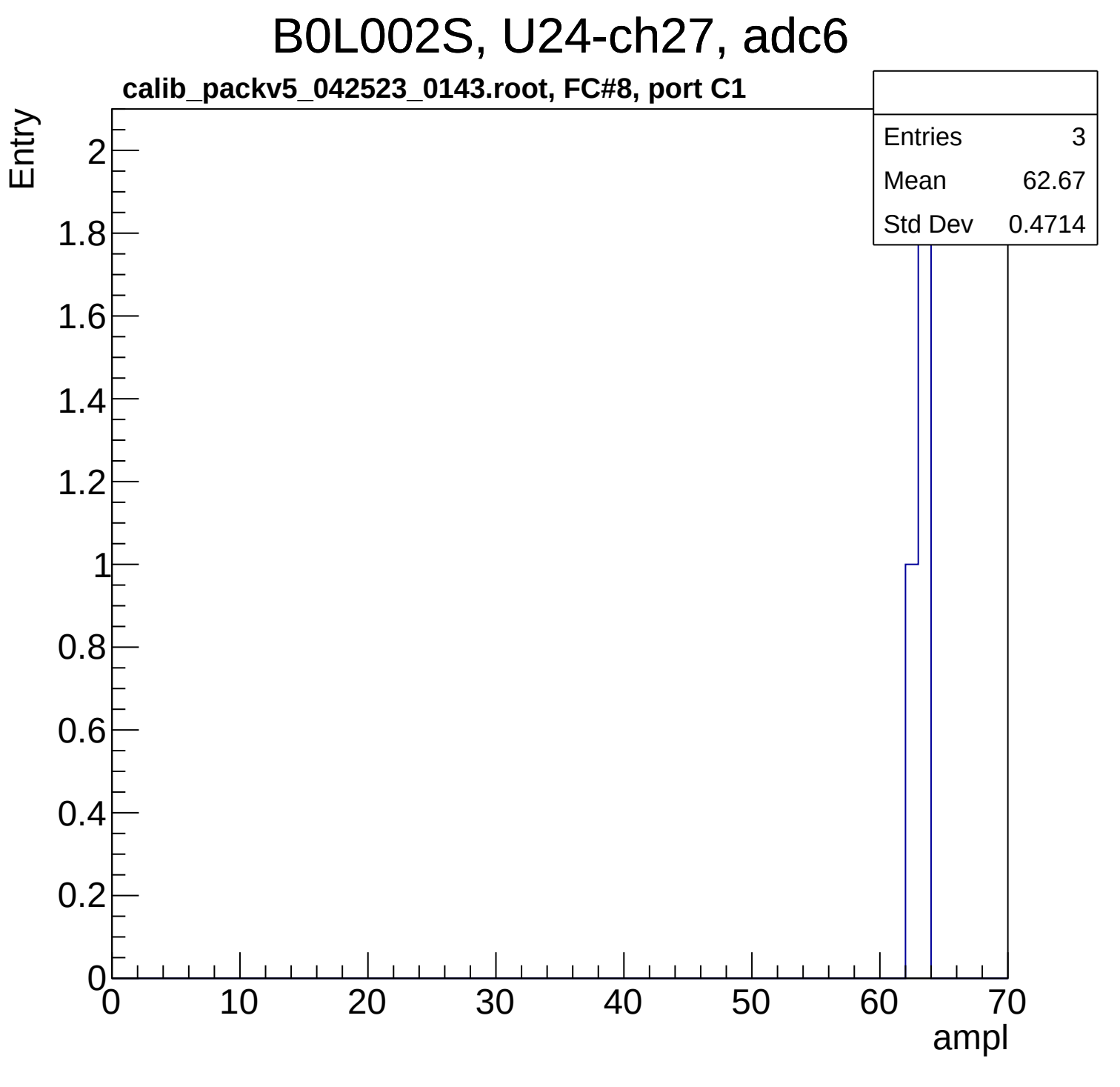
B0L002S, U24-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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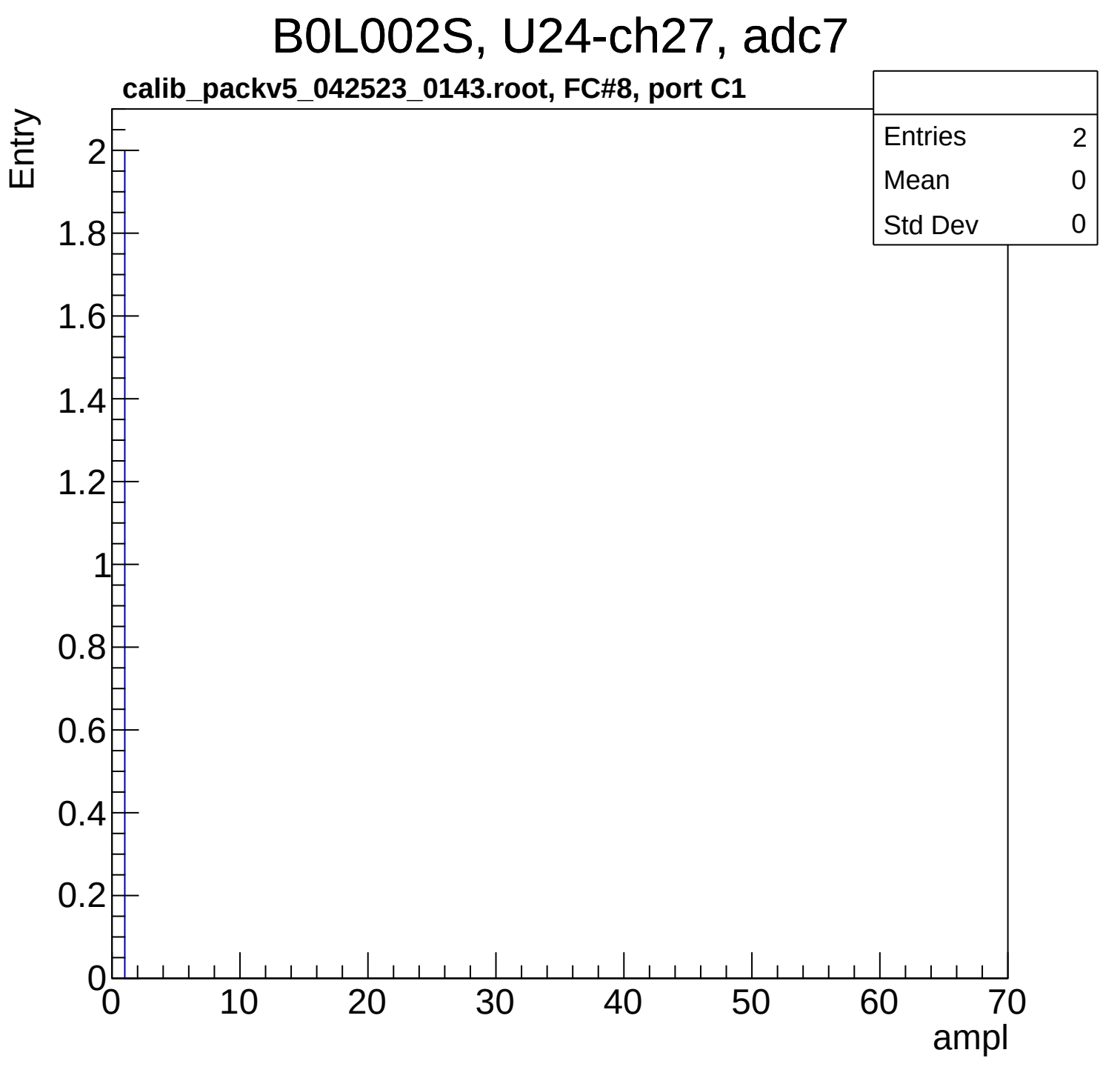
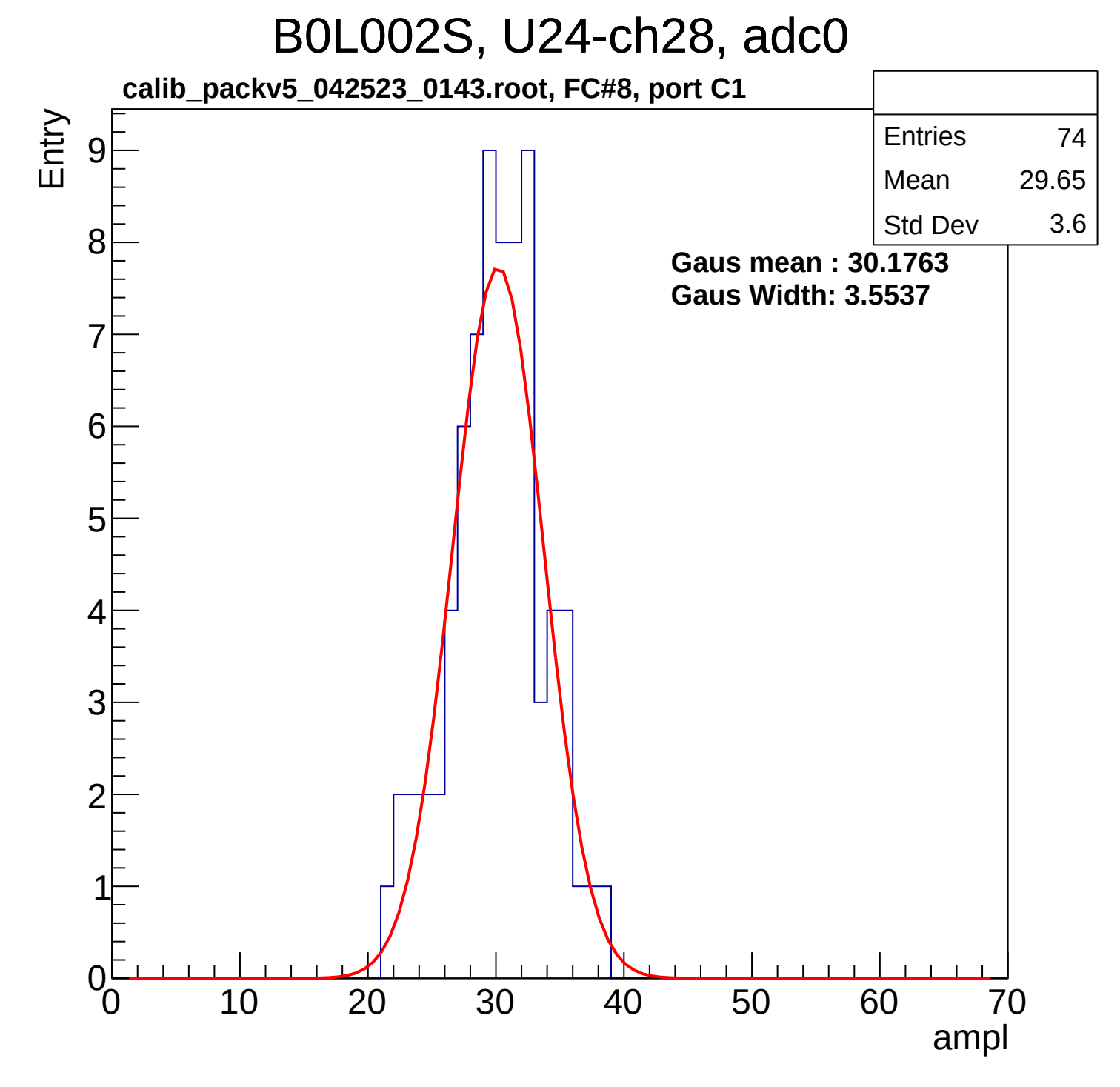

B0L002S, U24-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.65
Std Dev 3.6
Gaus mean : 30.1763
Gaus Width: 3.5537
Entry
ampl
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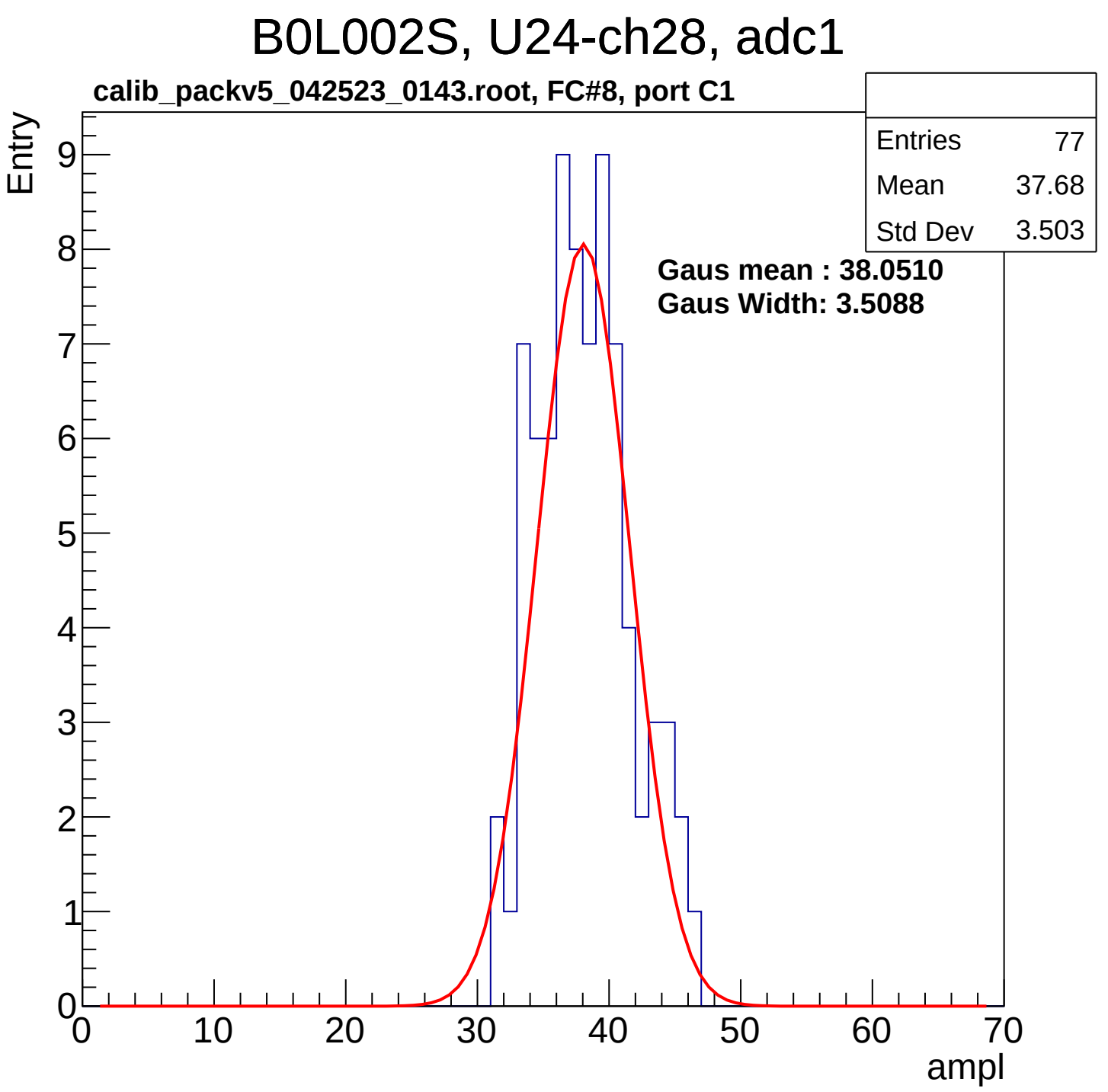
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.68
Std Dev 3.503
Gaus mean : 38.0510
Gaus Width: 3.5088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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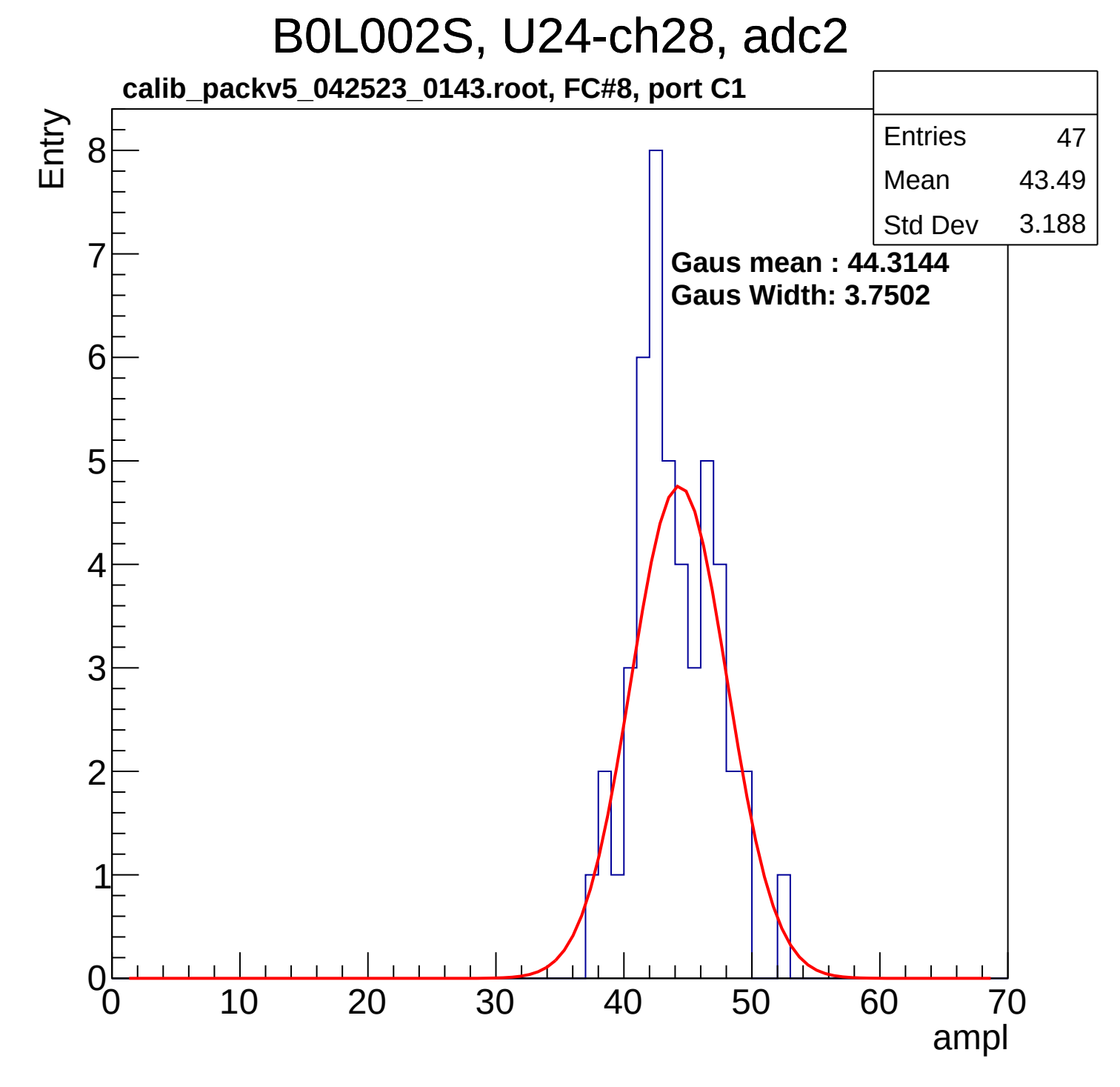
B0L002S, U24-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 43.49
Std Dev 3.188
Gaus mean : 44.3144
Gaus Width: 3.7502
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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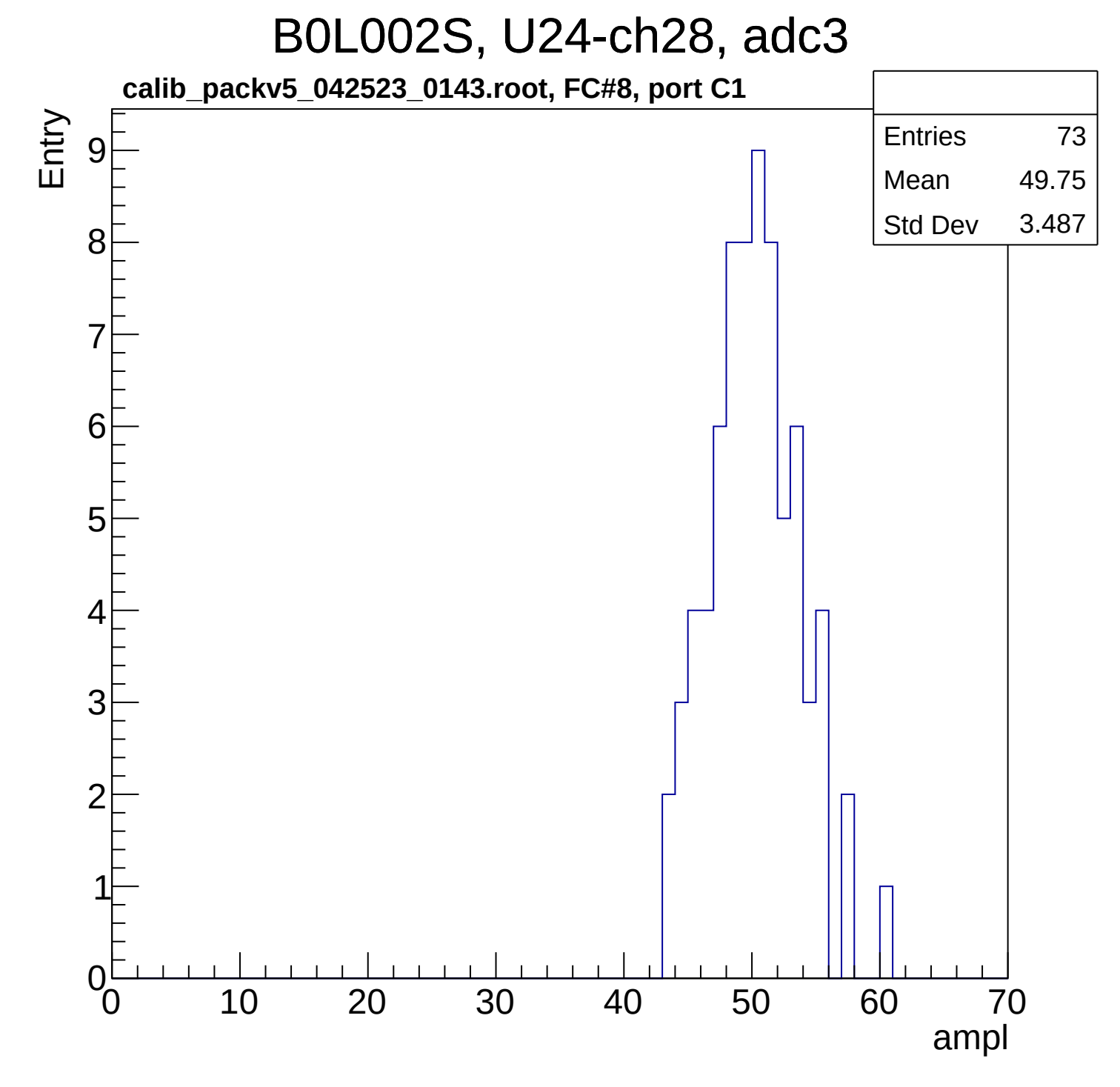
B0L002S, U24-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 49.75
Std Dev 3.487
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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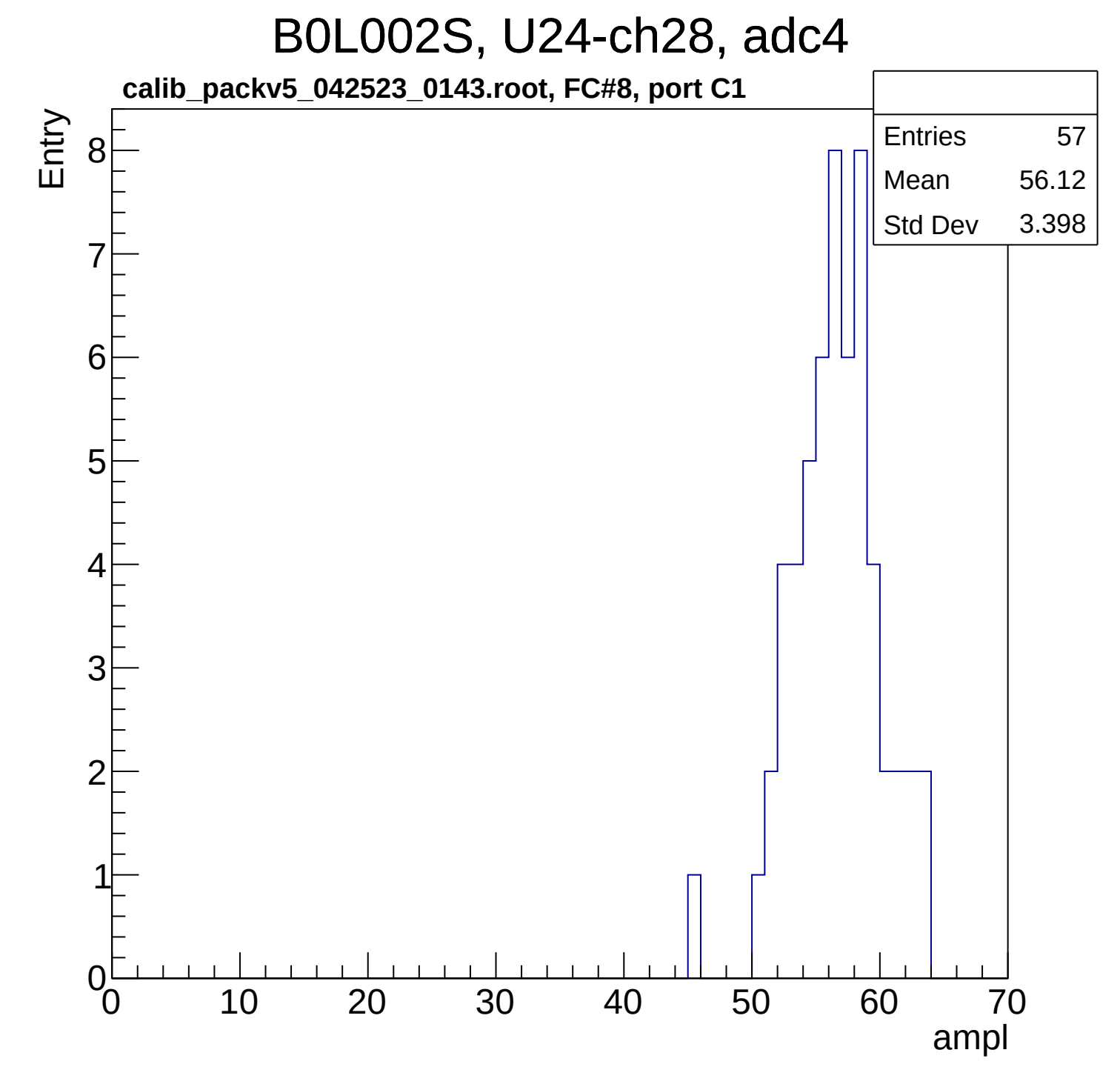
B0L002S, U24-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.12
Std Dev 3.398
Entry
ampl
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1
2
3
4
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0
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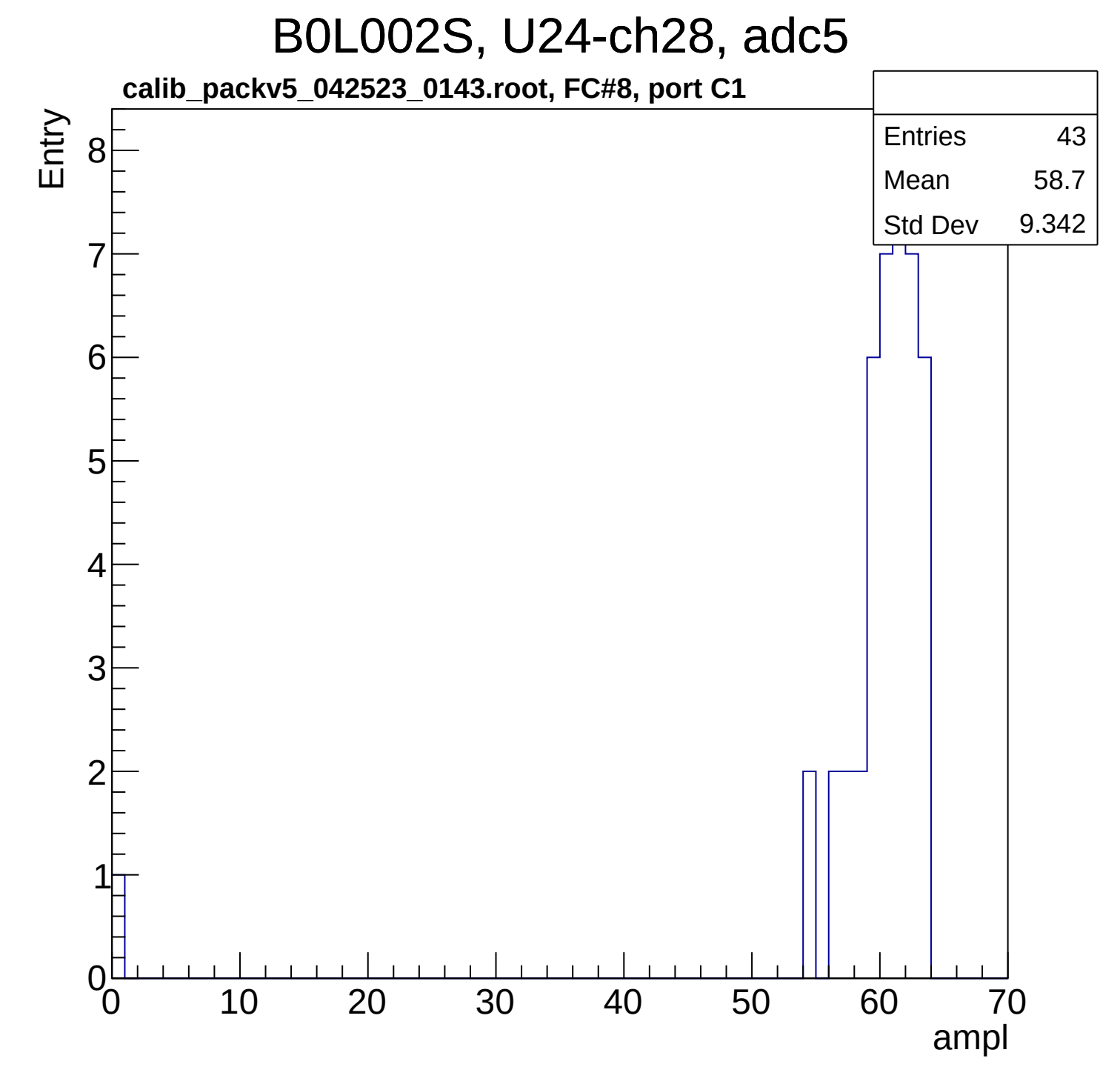
B0L002S, U24-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 58.7
Std Dev 9.342
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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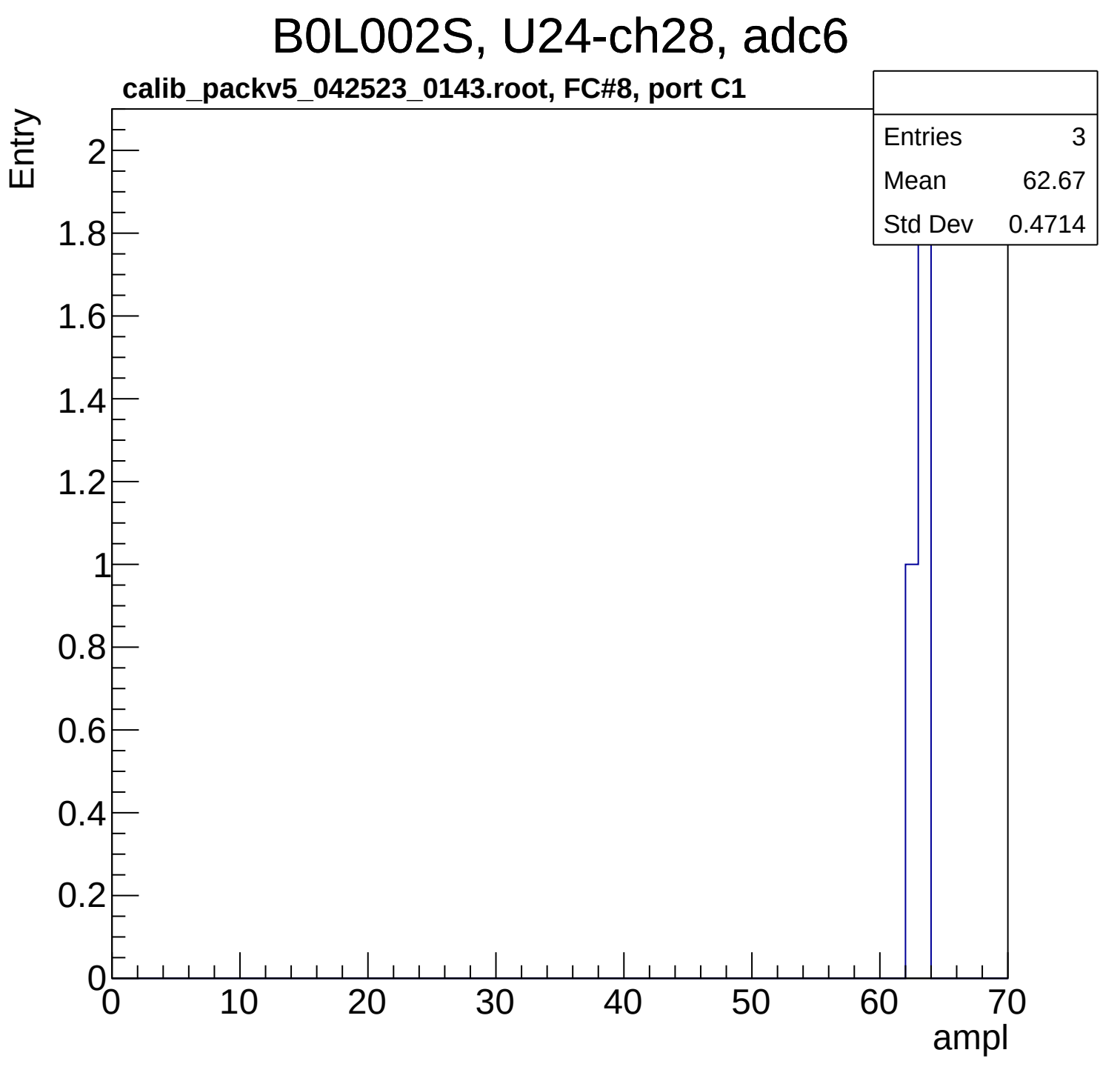
B0L002S, U24-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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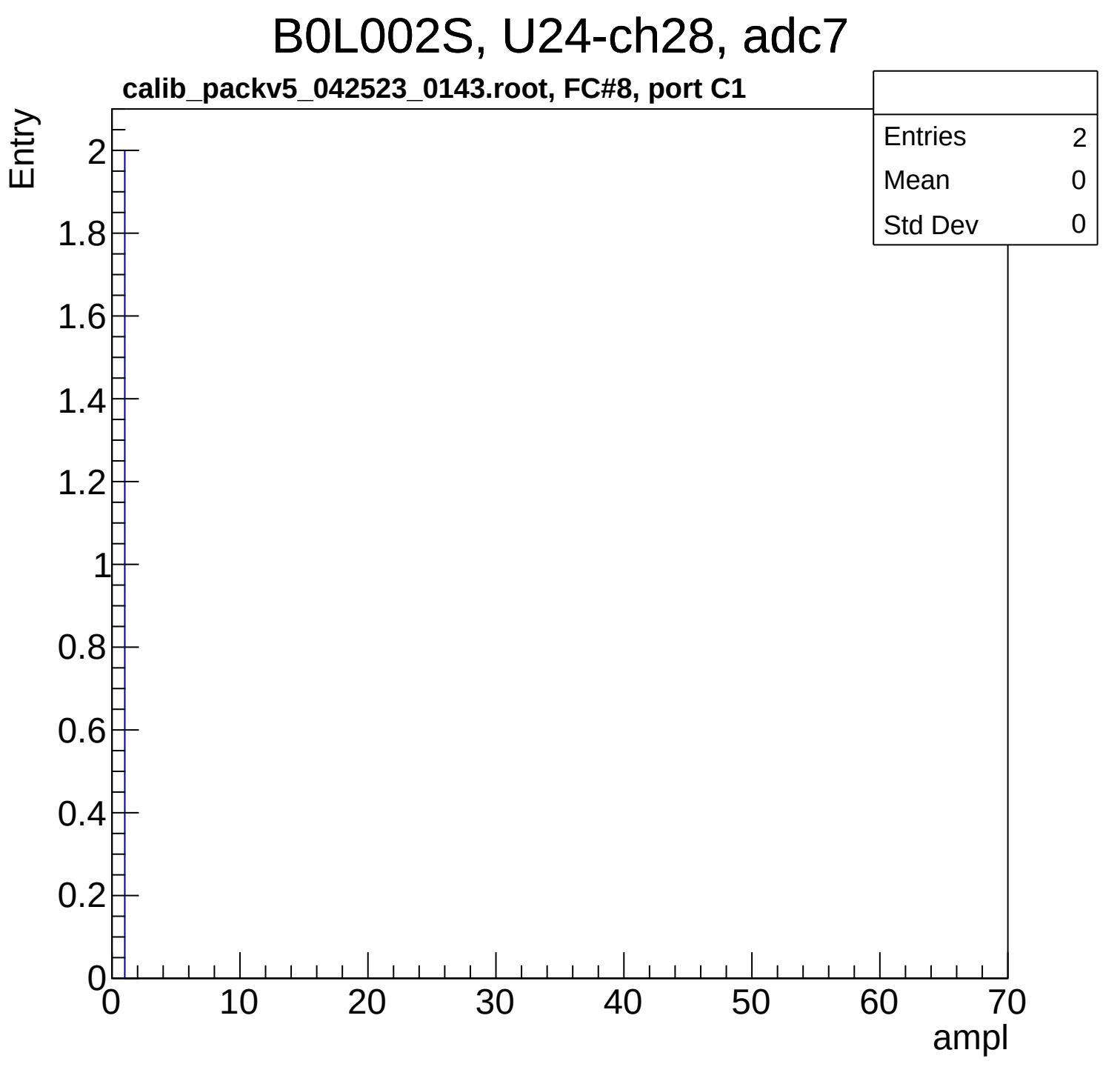
B0L002S, U24-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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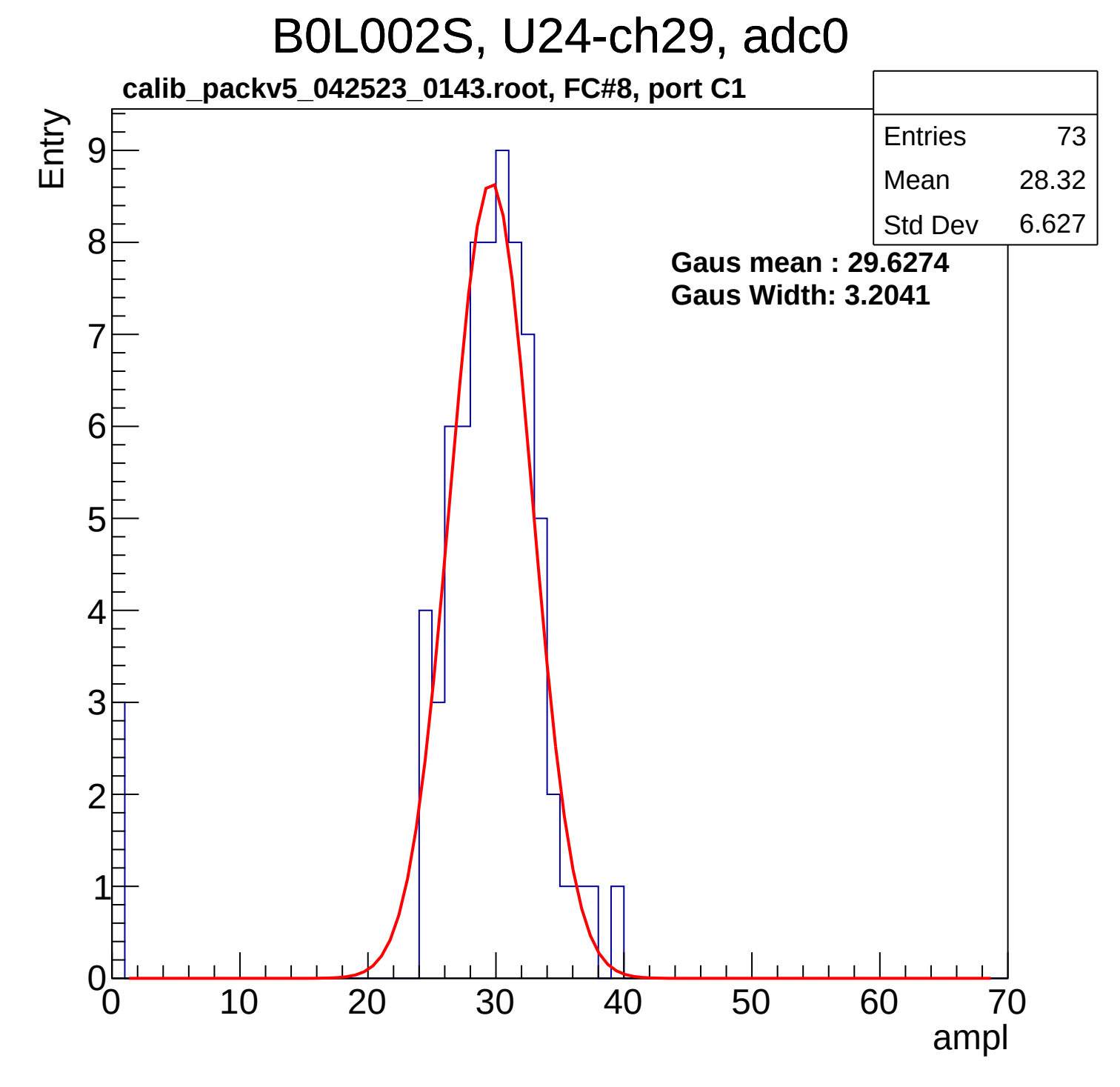
B0L002S, U24-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 28.32
Std Dev 6.627
Gaus mean : 29.6274
Gaus Width: 3.2041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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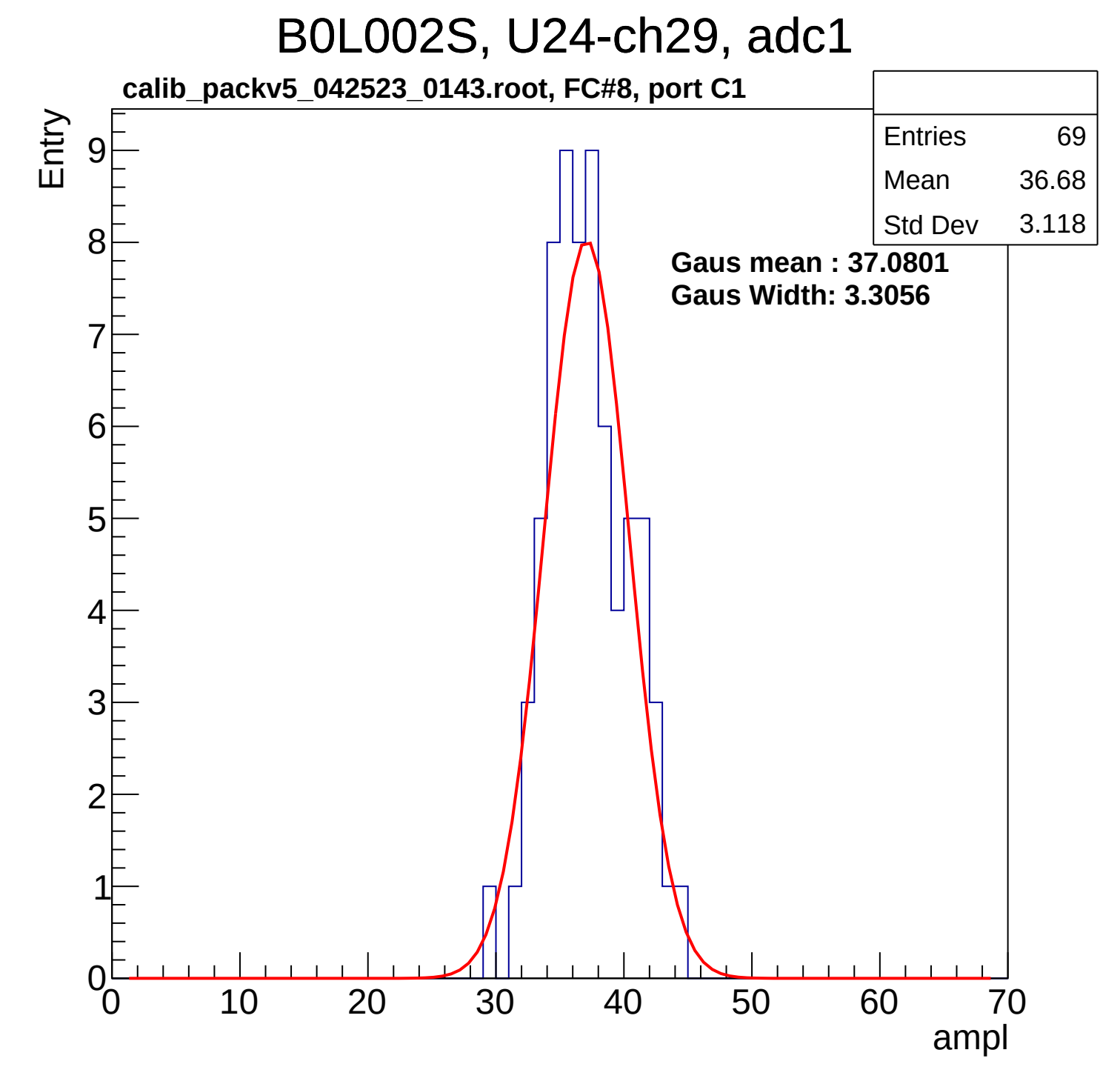
B0L002S, U24-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.68
Std Dev 3.118
Gaus mean : 37.0801
Gaus Width: 3.3056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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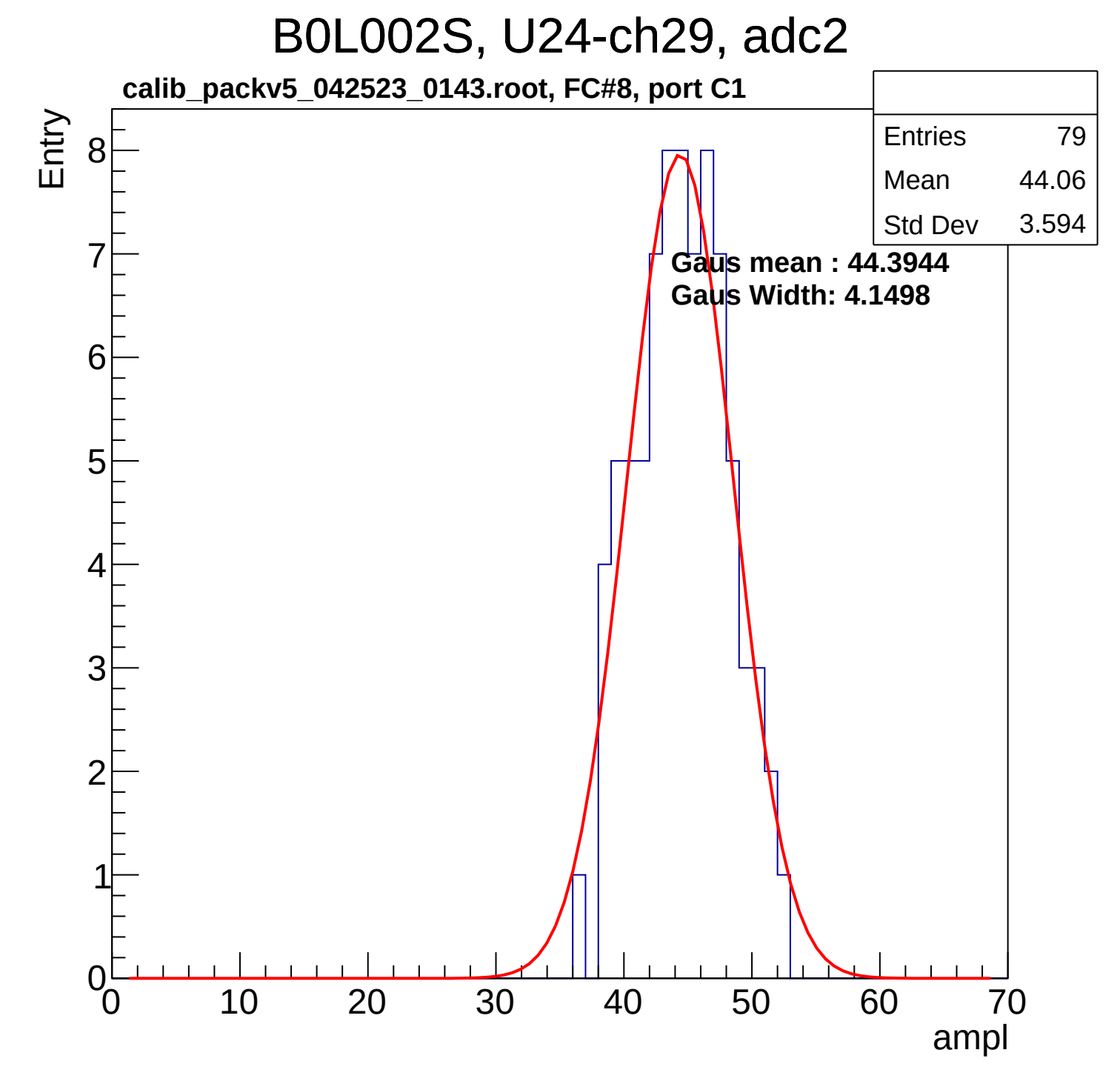
B0L002S, U24-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 44.06
Std Dev 3.594
Gaus mean : 44.3944
Gaus Width: 4.1498
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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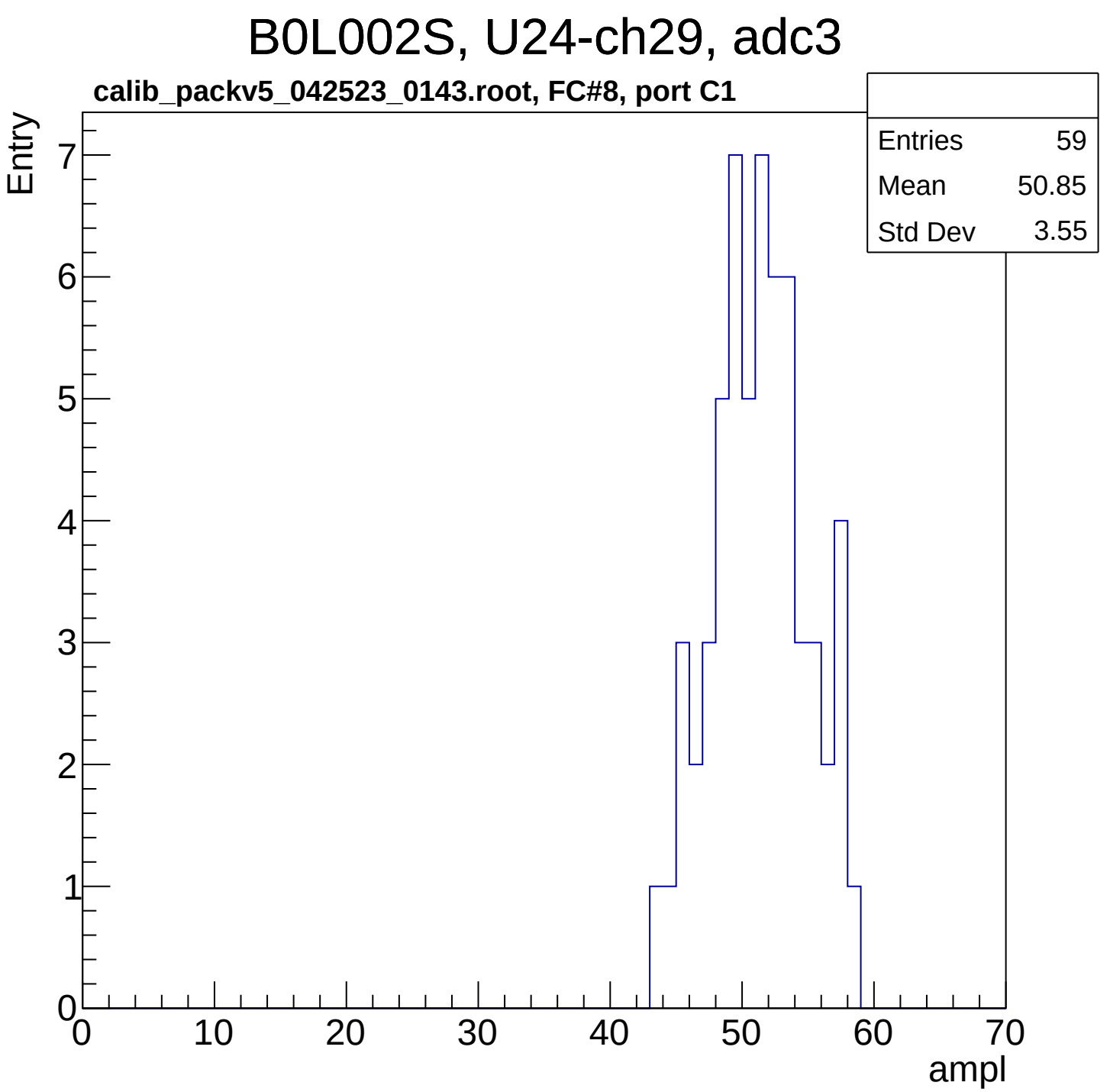
B0L002S, U24-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.85
Std Dev 3.55
Entry
ampl
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3
4
5
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0
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20
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40
50
60
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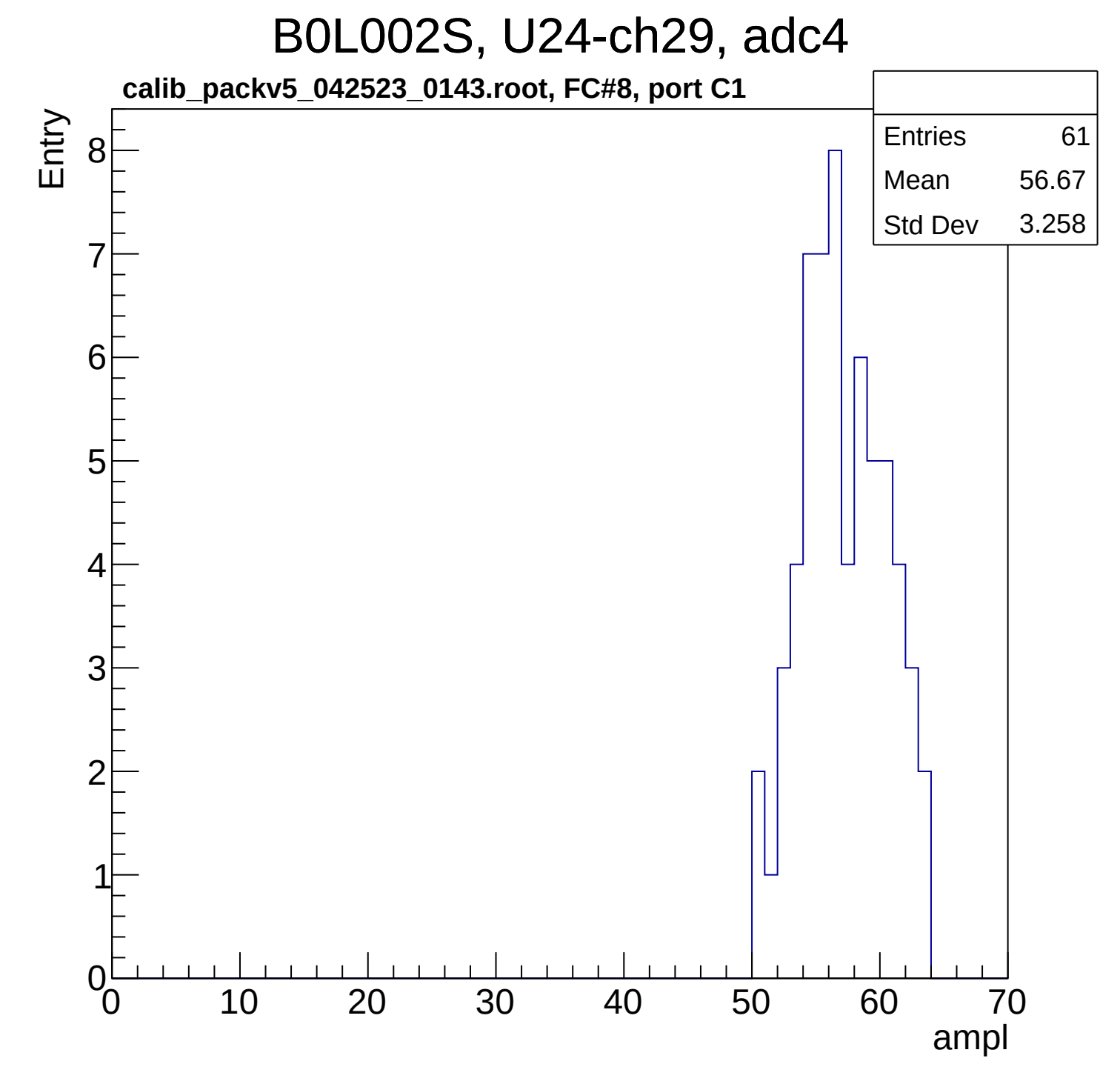
B0L002S, U24-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.67
Std Dev 3.258
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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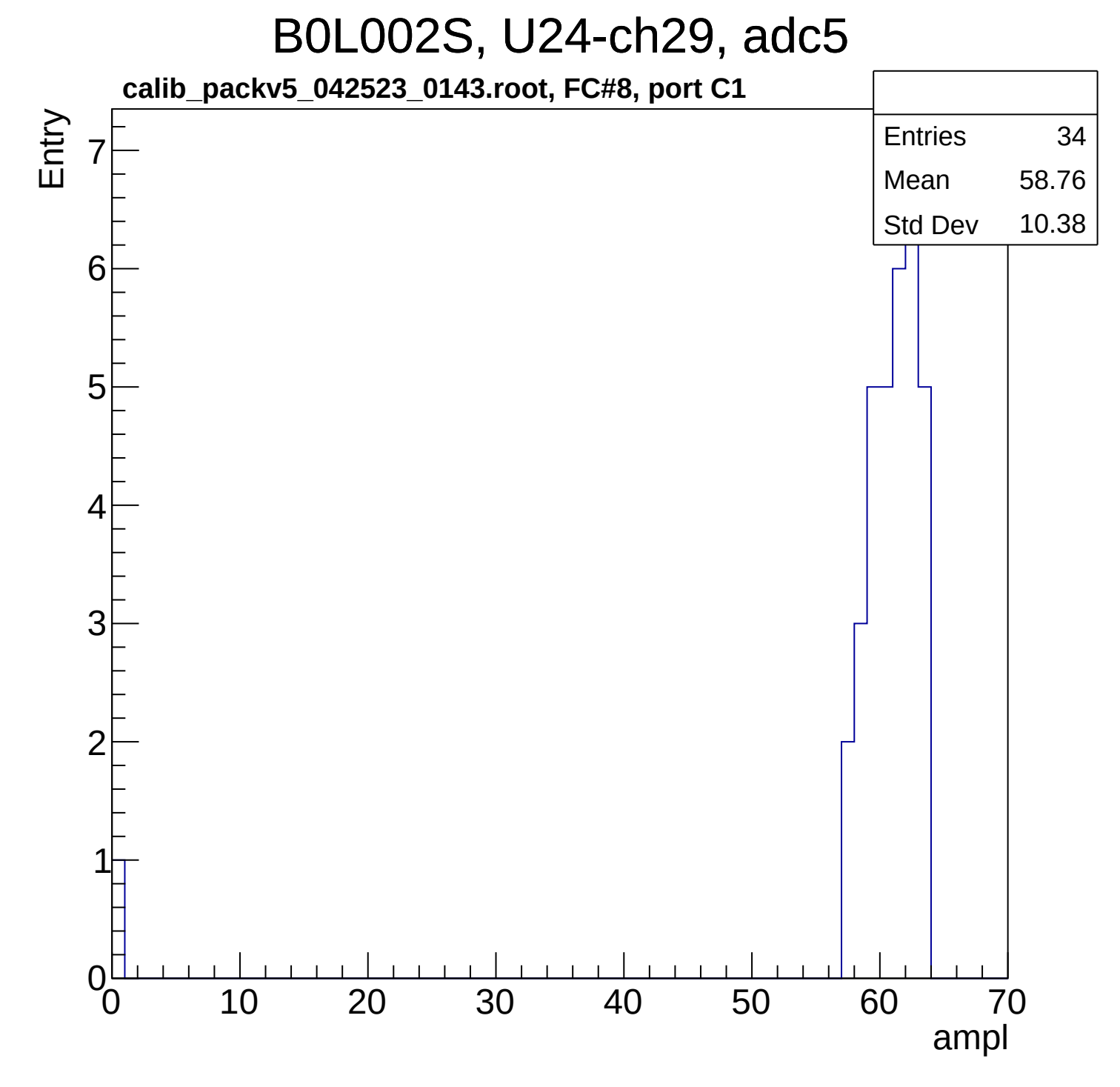
B0L002S, U24-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.76
Std Dev 10.38
Entry
ampl
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1
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4
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0
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40
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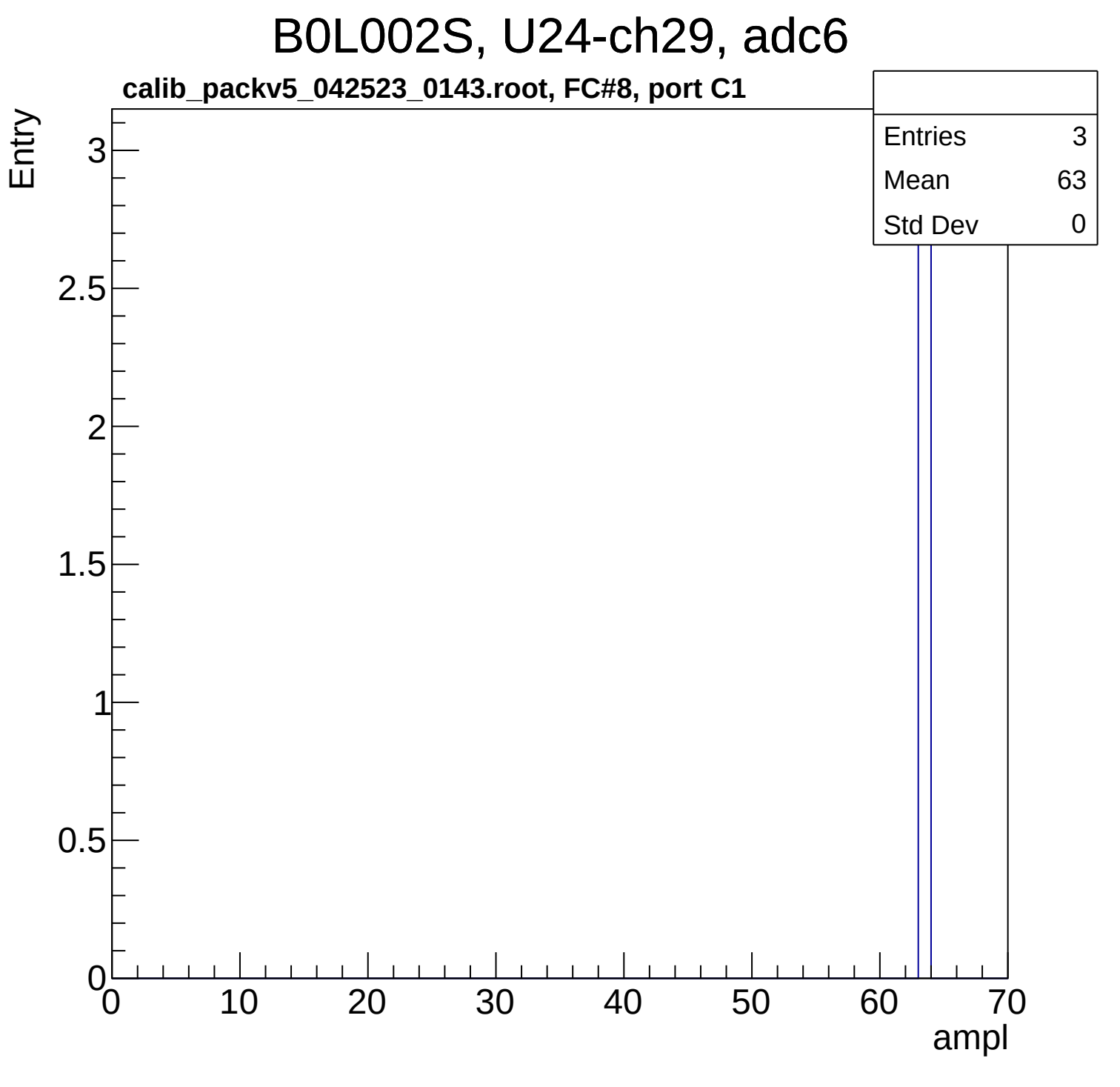
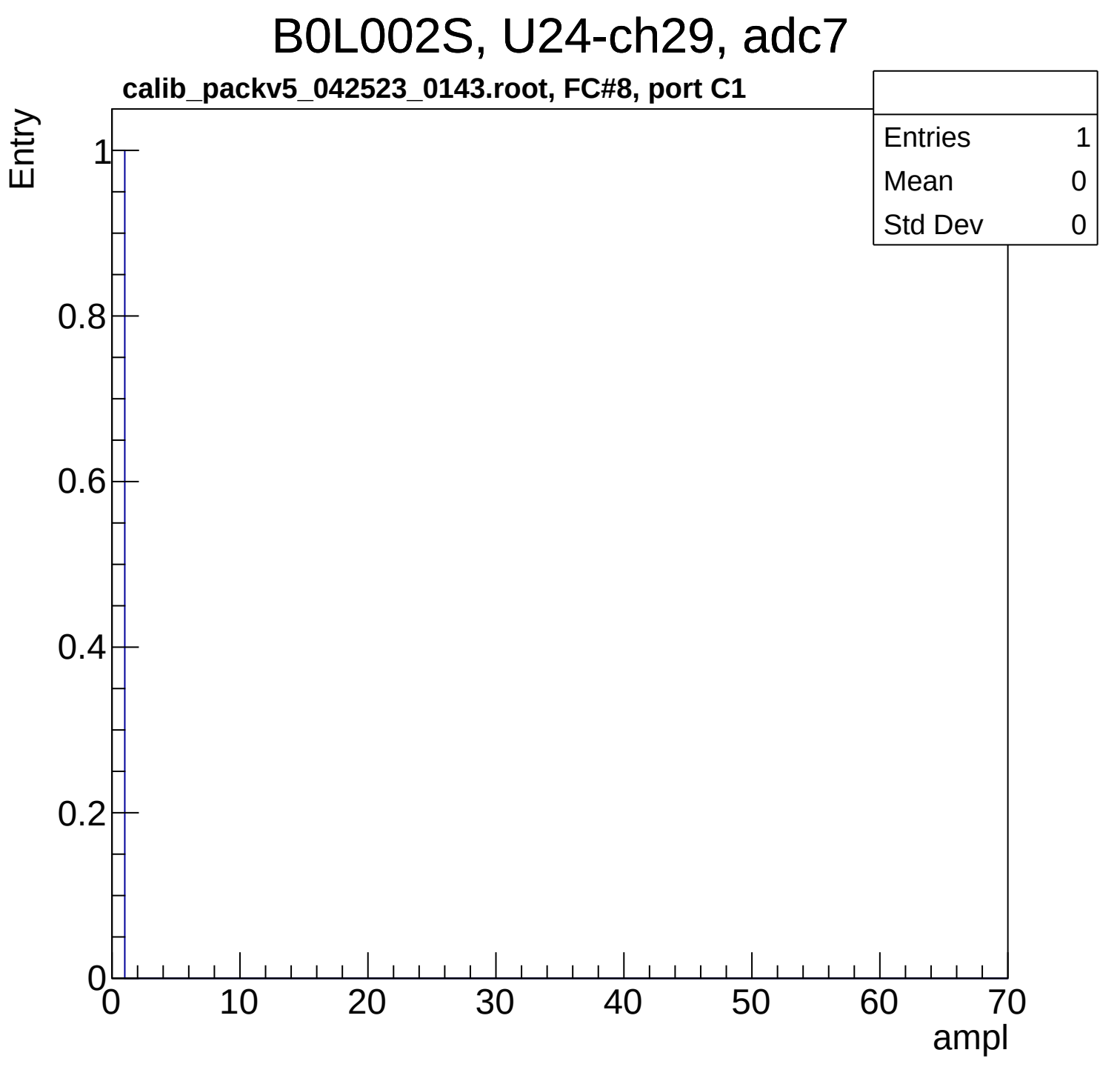

B0L002S, U24-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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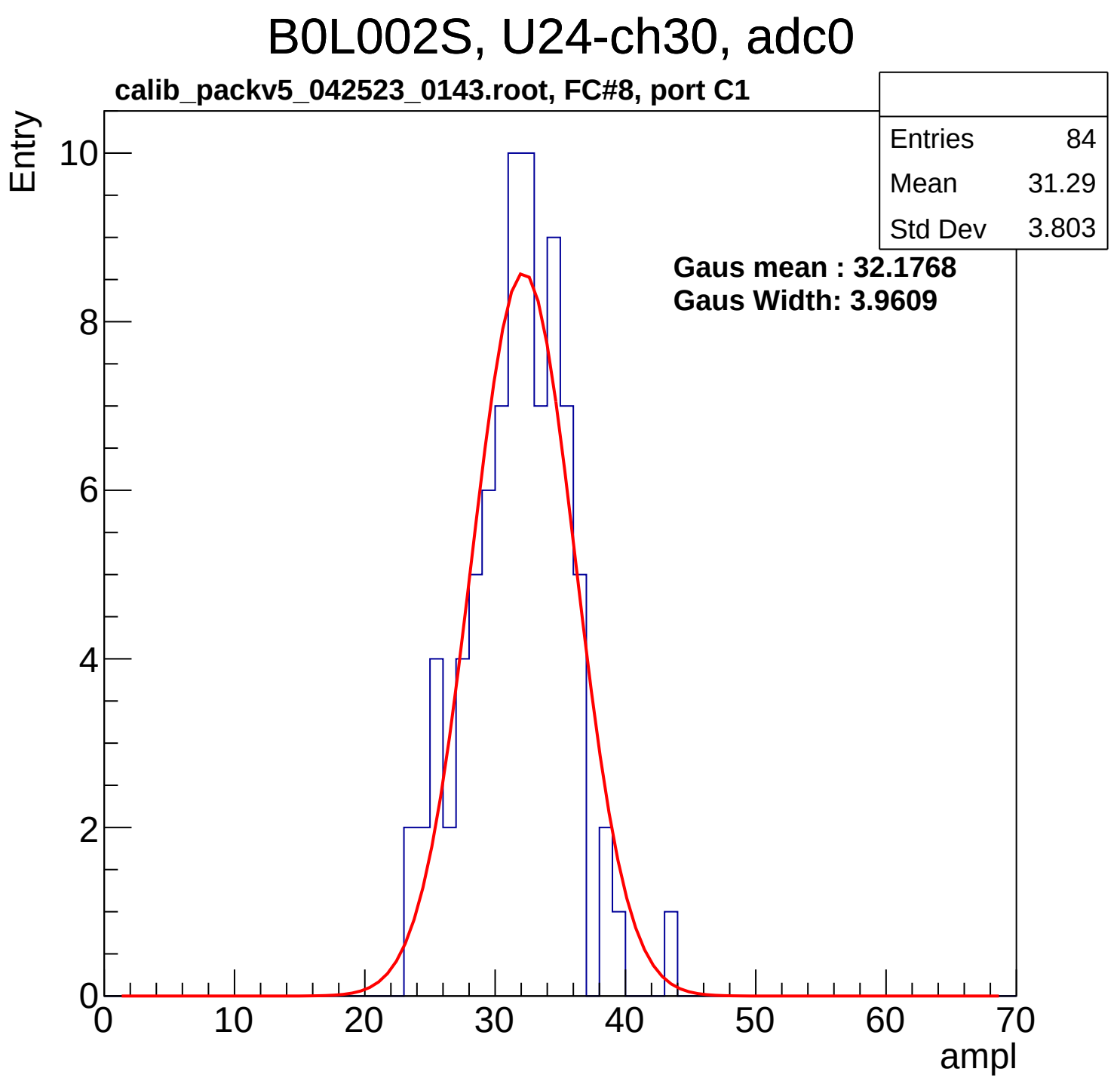
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 31.29
Std Dev 3.803
Gaus mean : 32.1768
Gaus Width: 3.9609
Entry
ampl
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2
4
6
8
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0
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60
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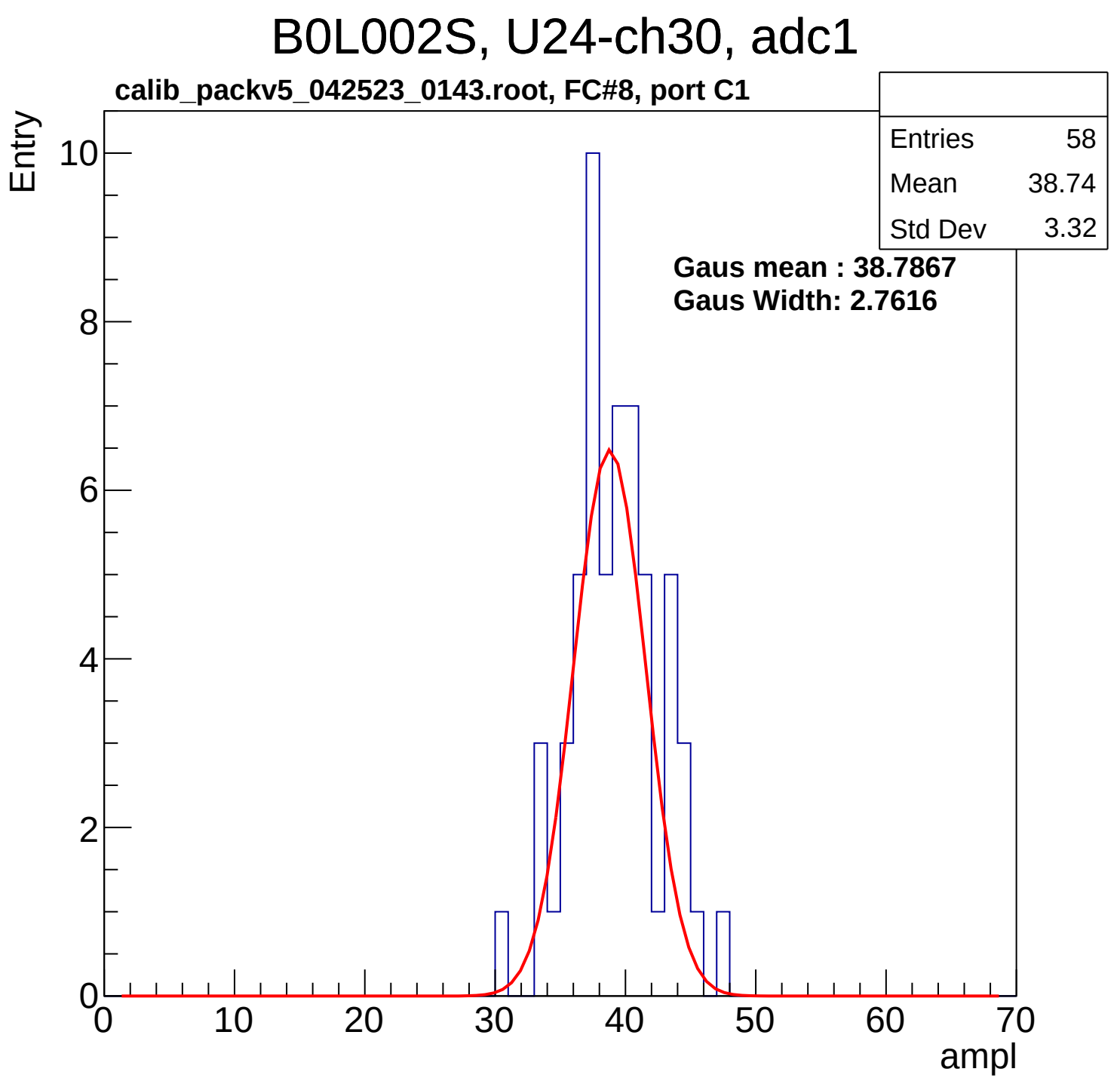
B0L002S, U24-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 38.74
Std Dev 3.32
Gaus mean : 38.7867
Gaus Width: 2.7616
Entry
ampl
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4
6
8
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0
10
20
30
40
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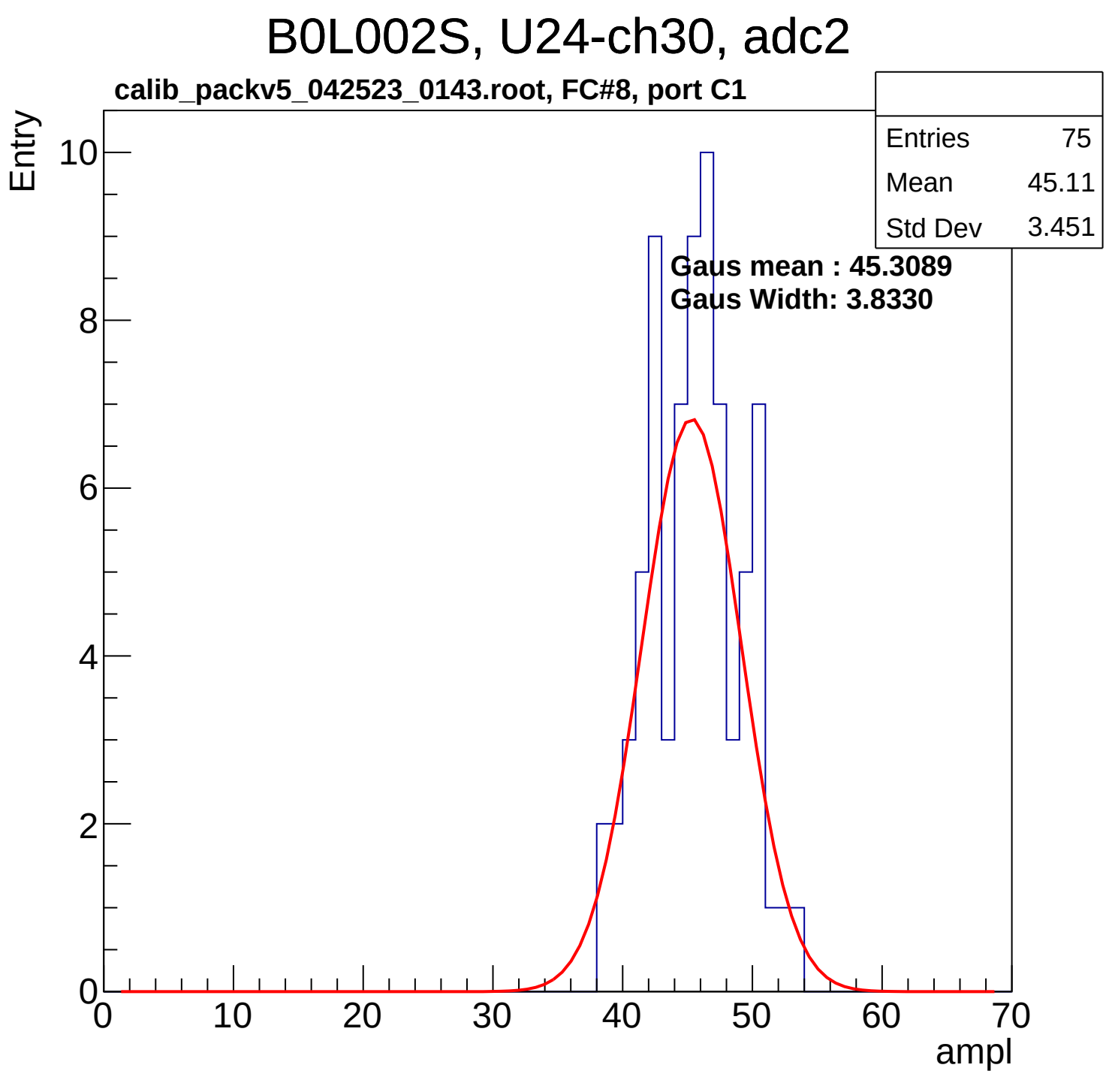
B0L002S, U24-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.11
Std Dev 3.451
Gaus mean : 45.3089
Gaus Width: 3.8330
Entry
ampl
0
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4
6
8
10
0
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40
50
60
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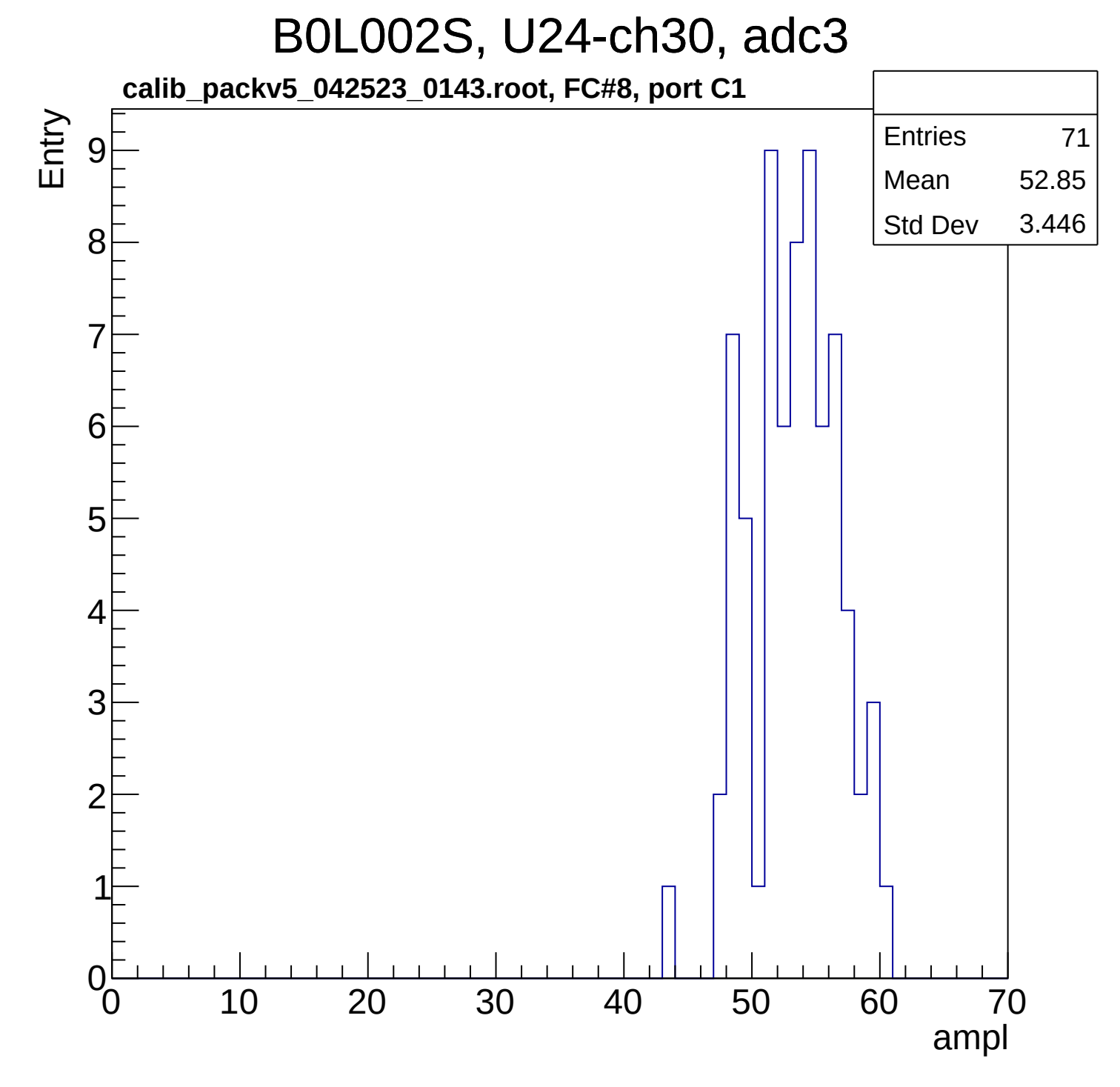
B0L002S, U24-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.85
Std Dev 3.446
Entry
ampl
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4
5
6
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0
10
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60
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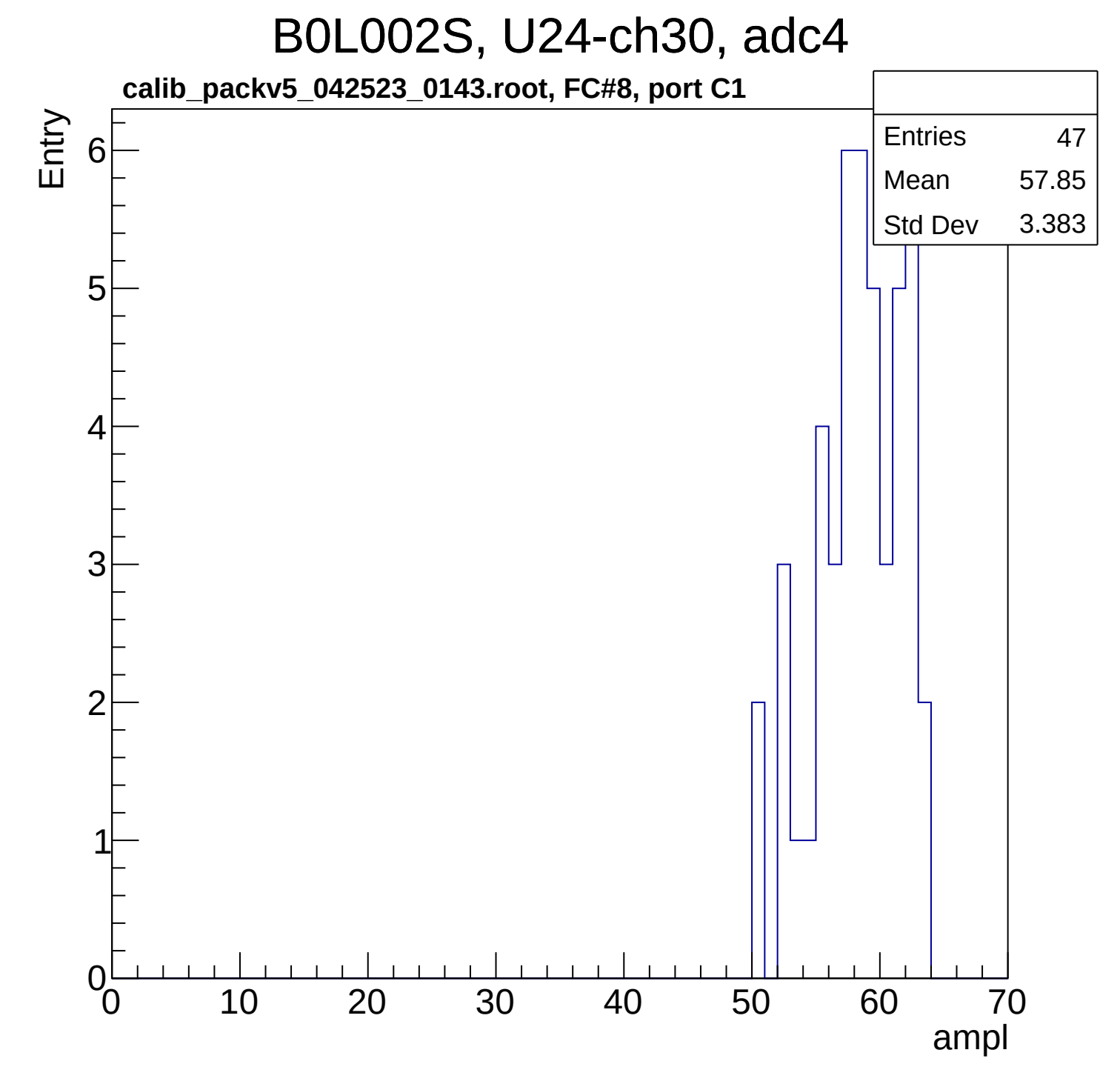
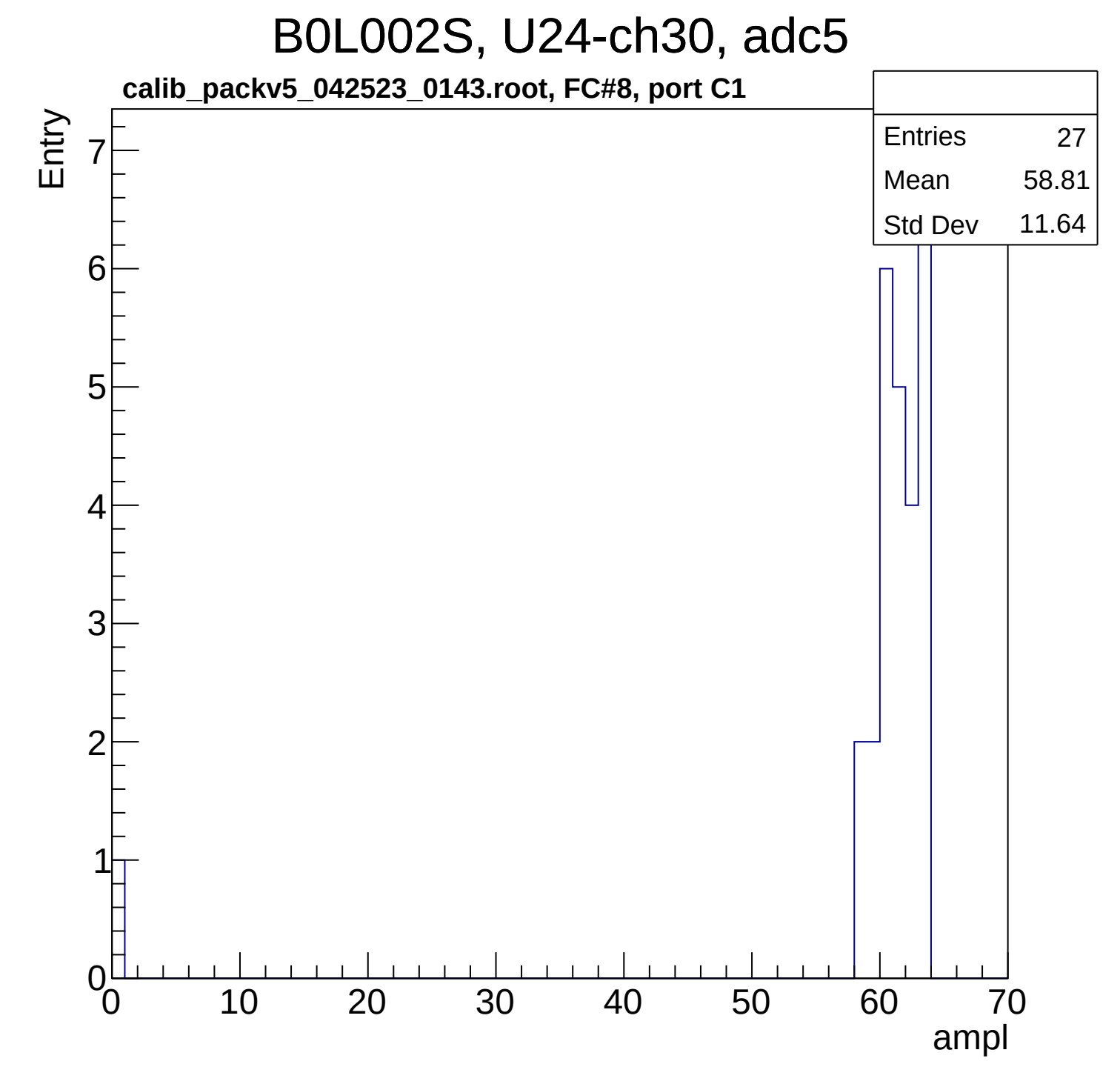

B0L002S, U24-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.85
Std Dev 3.383
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.81
Std Dev 11.64
Entry
ampl
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1
2
3
4
5
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0
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40
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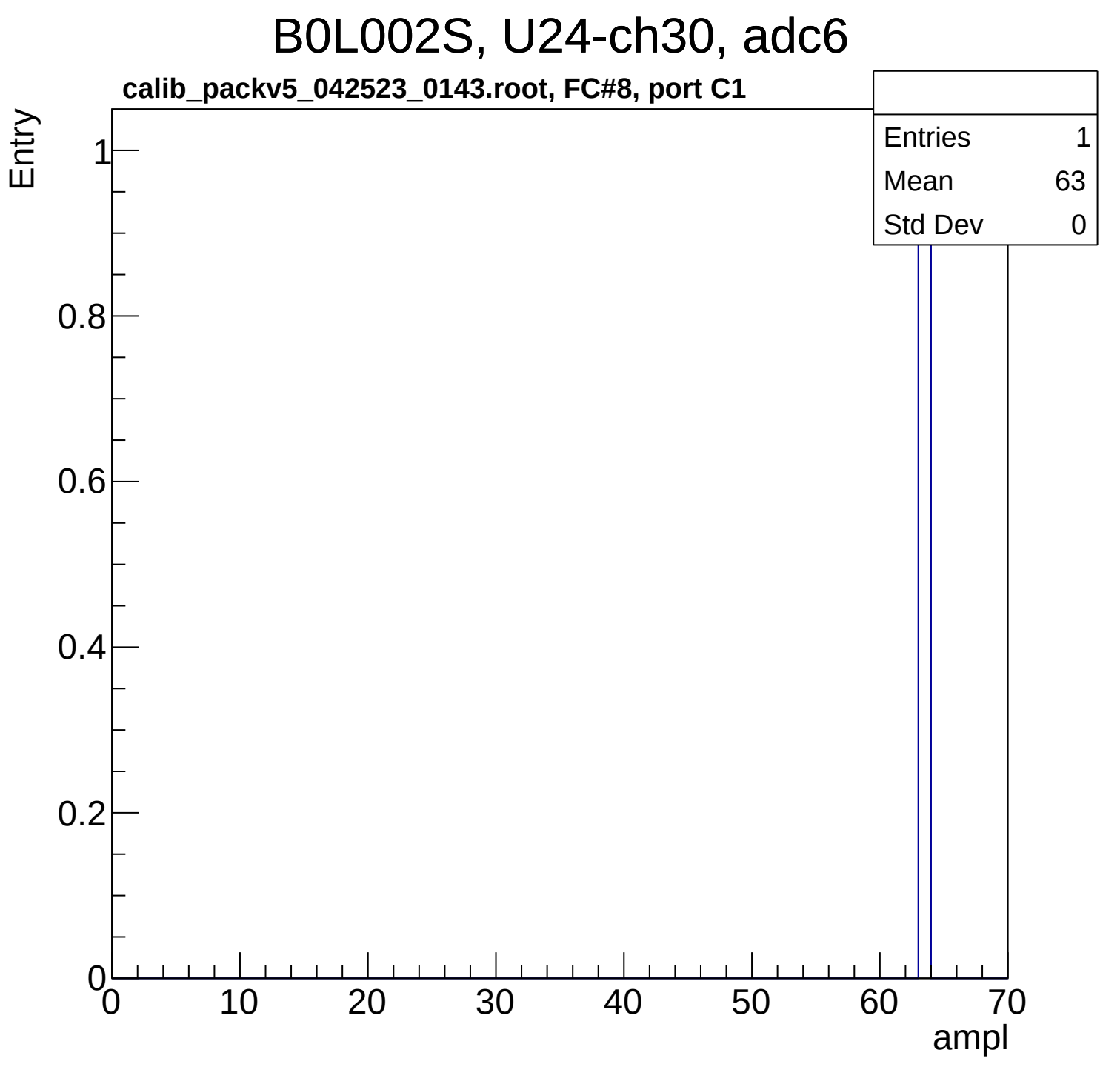
B0L002S, U24-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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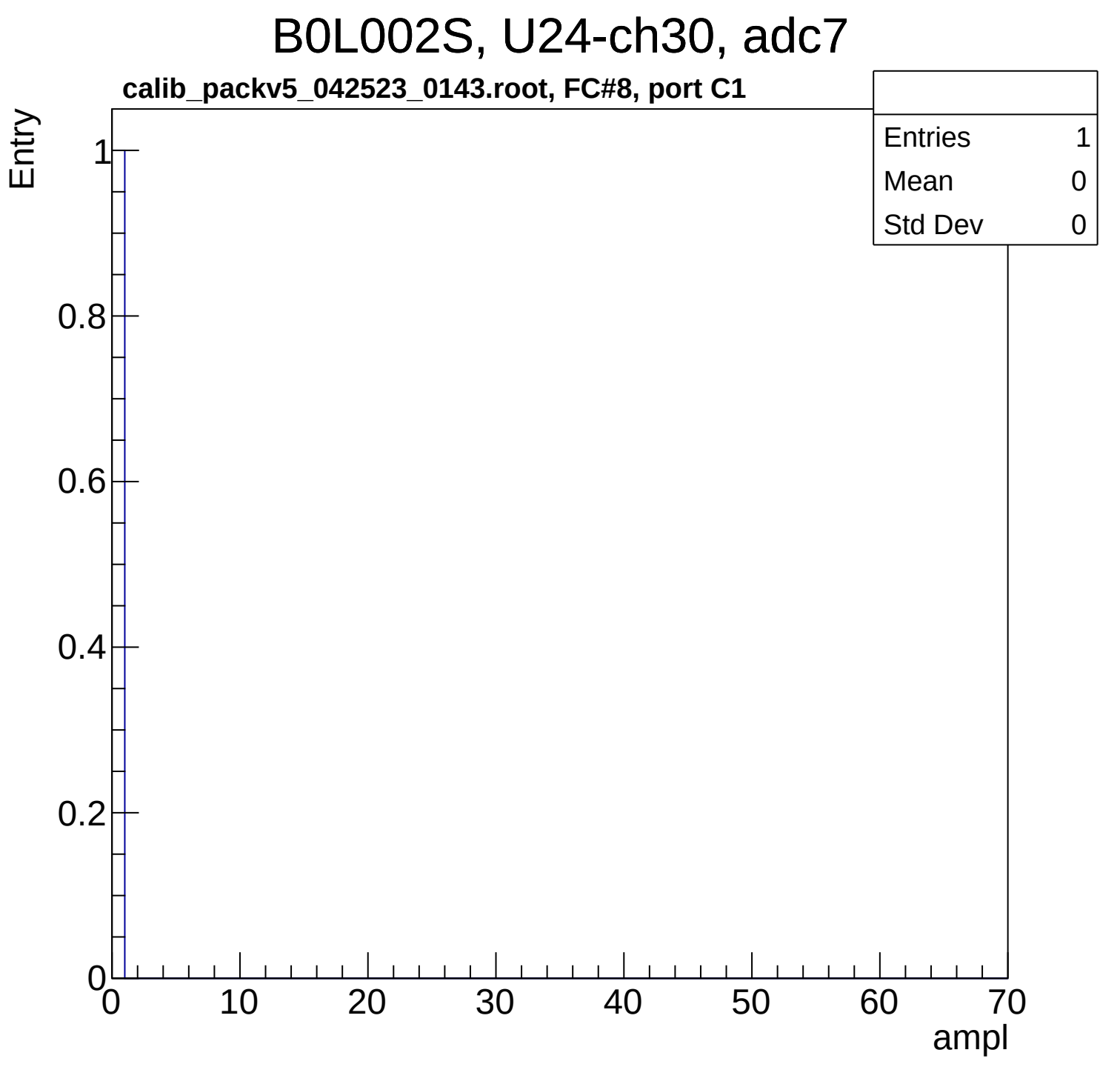
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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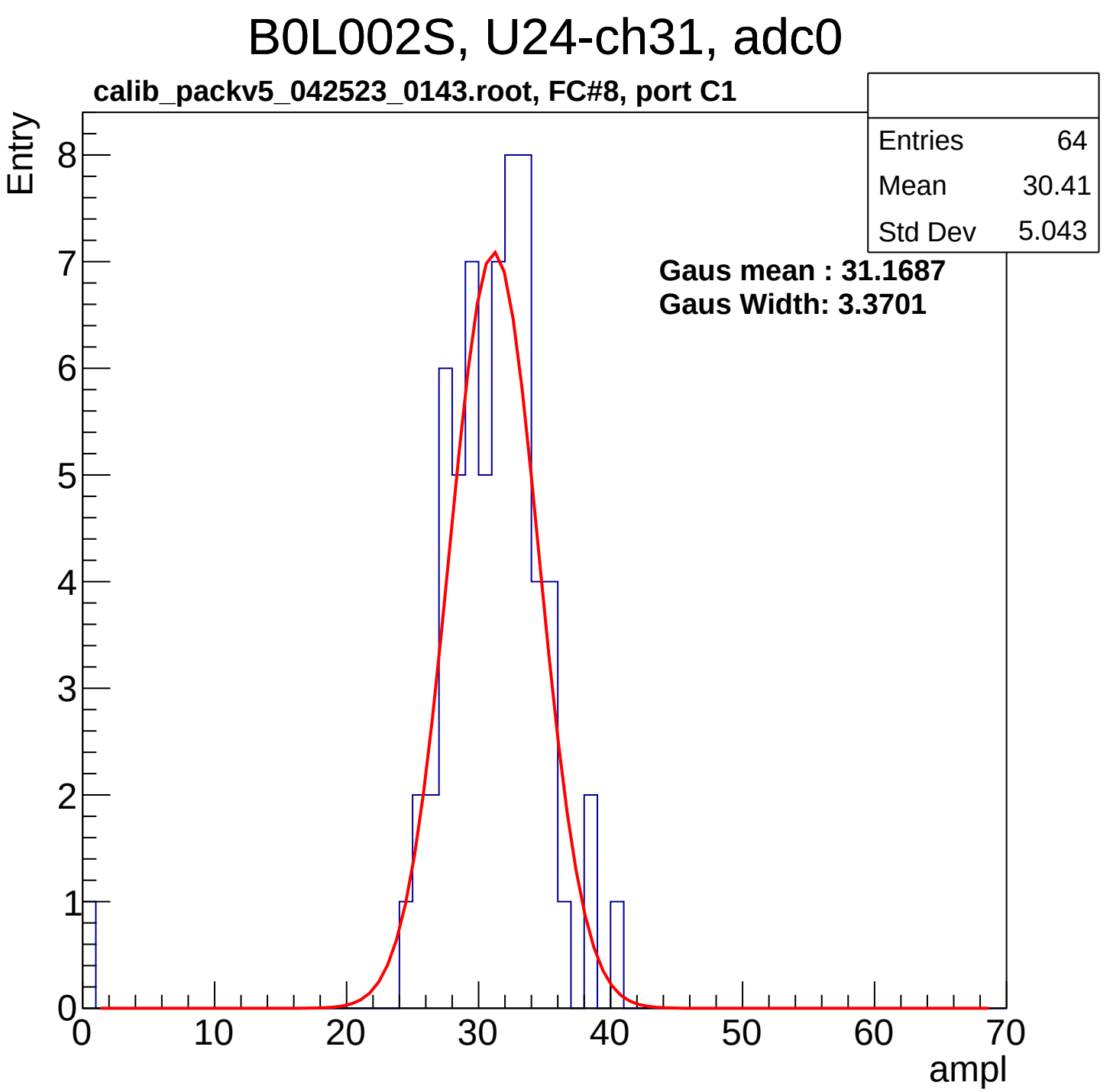
50
60
70
ampl


B0L002S, U24-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.41
Std Dev 5.043
Gaus mean : 31.1687
Gaus Width: 3.3701
Entry
ampl
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1
2
3
4
5
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7
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0
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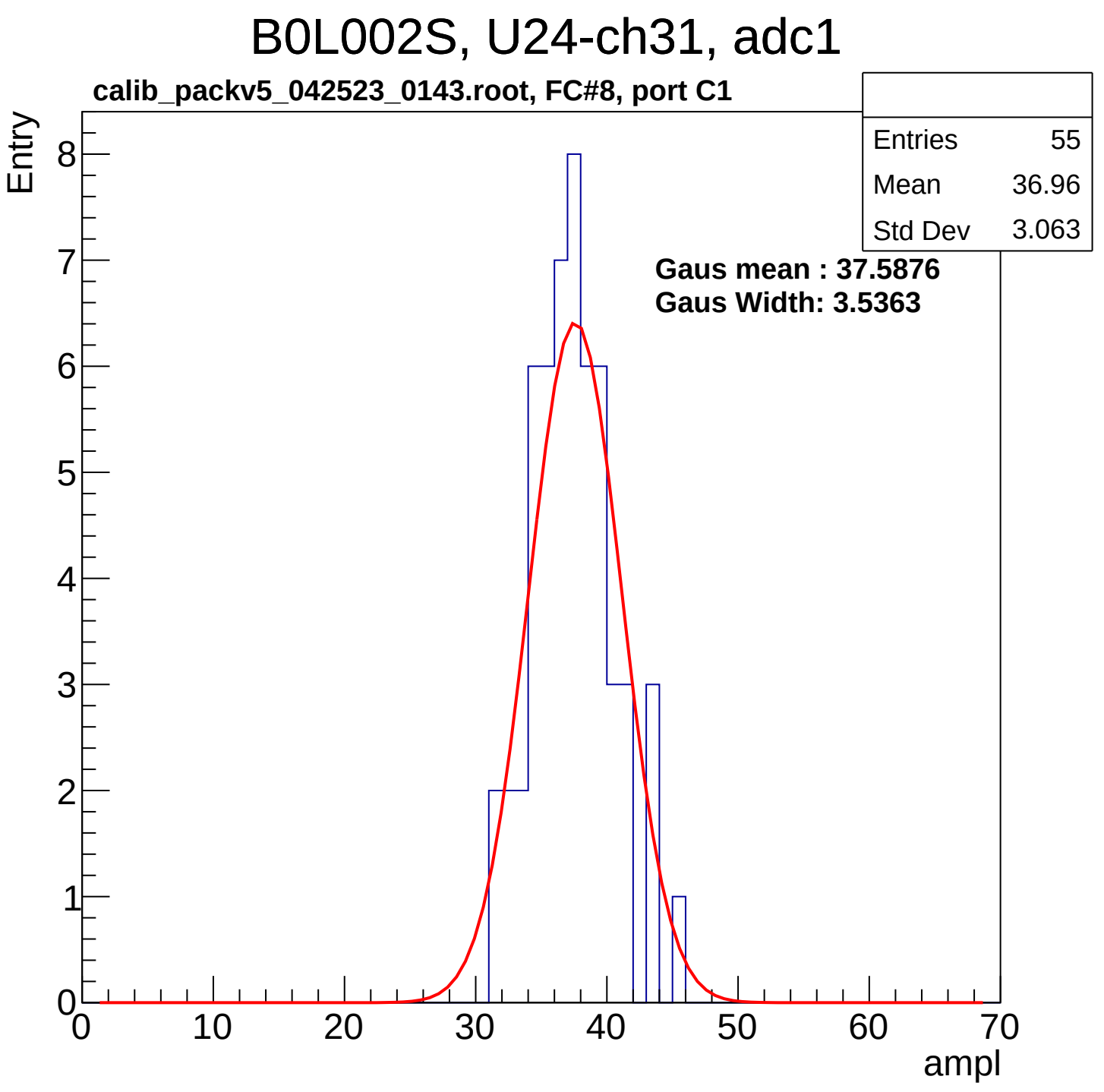
B0L002S, U24-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 36.96
Std Dev 3.063
Gaus mean : 37.5876
Gaus Width: 3.5363
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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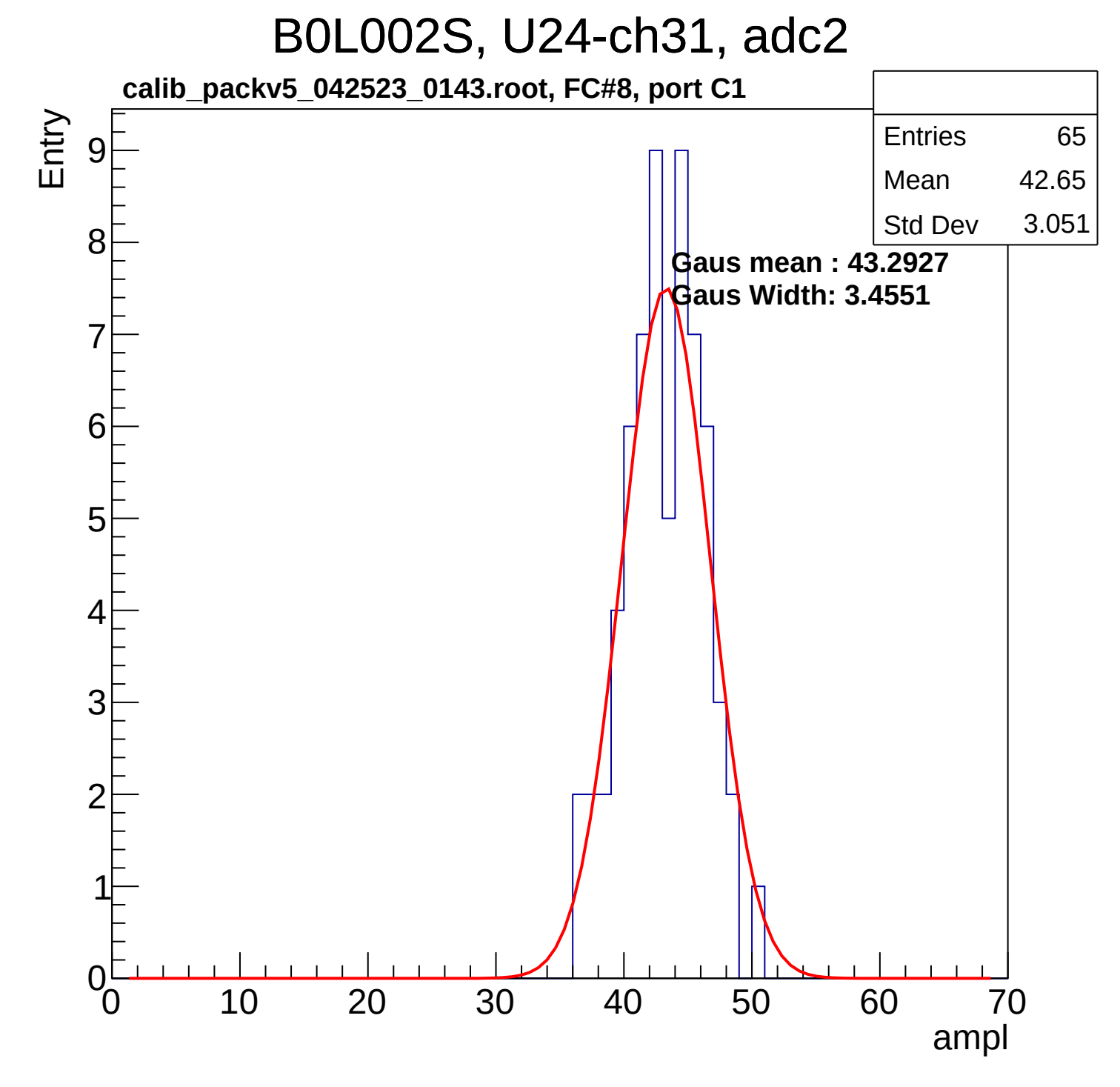
B0L002S, U24-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 42.65
Std Dev 3.051
Gaus mean : 43.2927
Gaus Width: 3.4551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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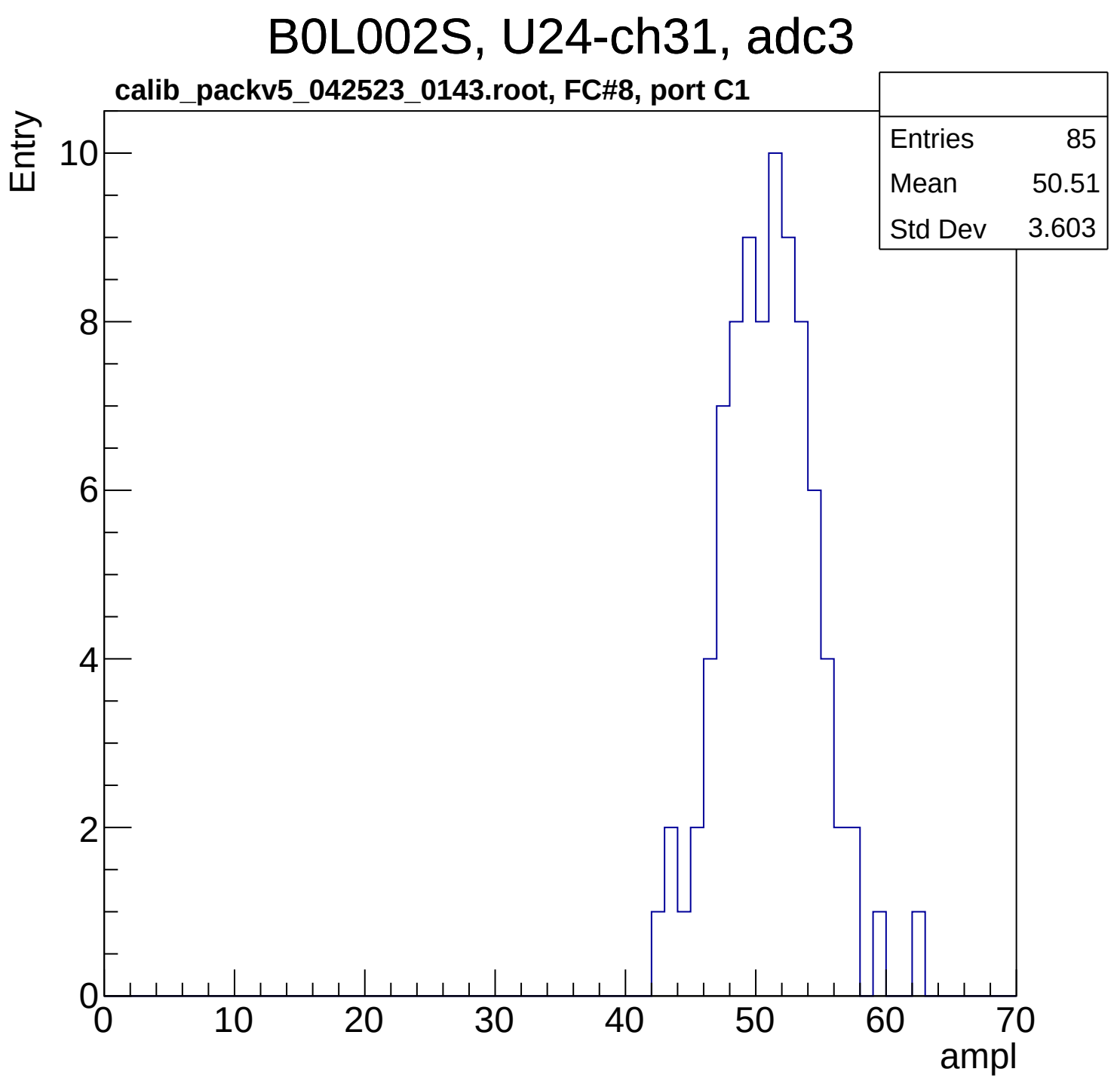
B0L002S, U24-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 50.51
Std Dev 3.603
Entry
ampl
0
2
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0
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40
50
60
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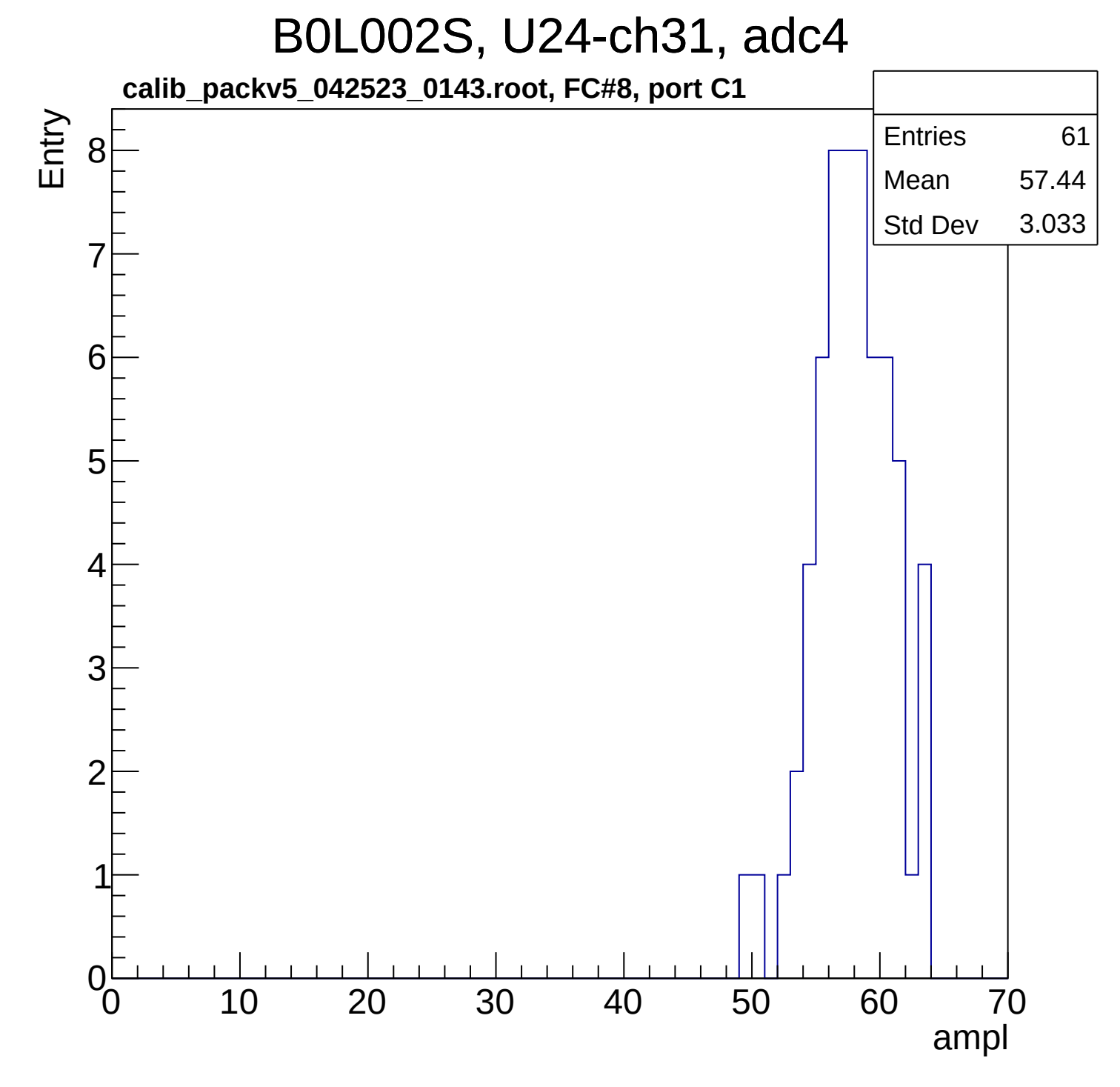
B0L002S, U24-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.44
Std Dev 3.033
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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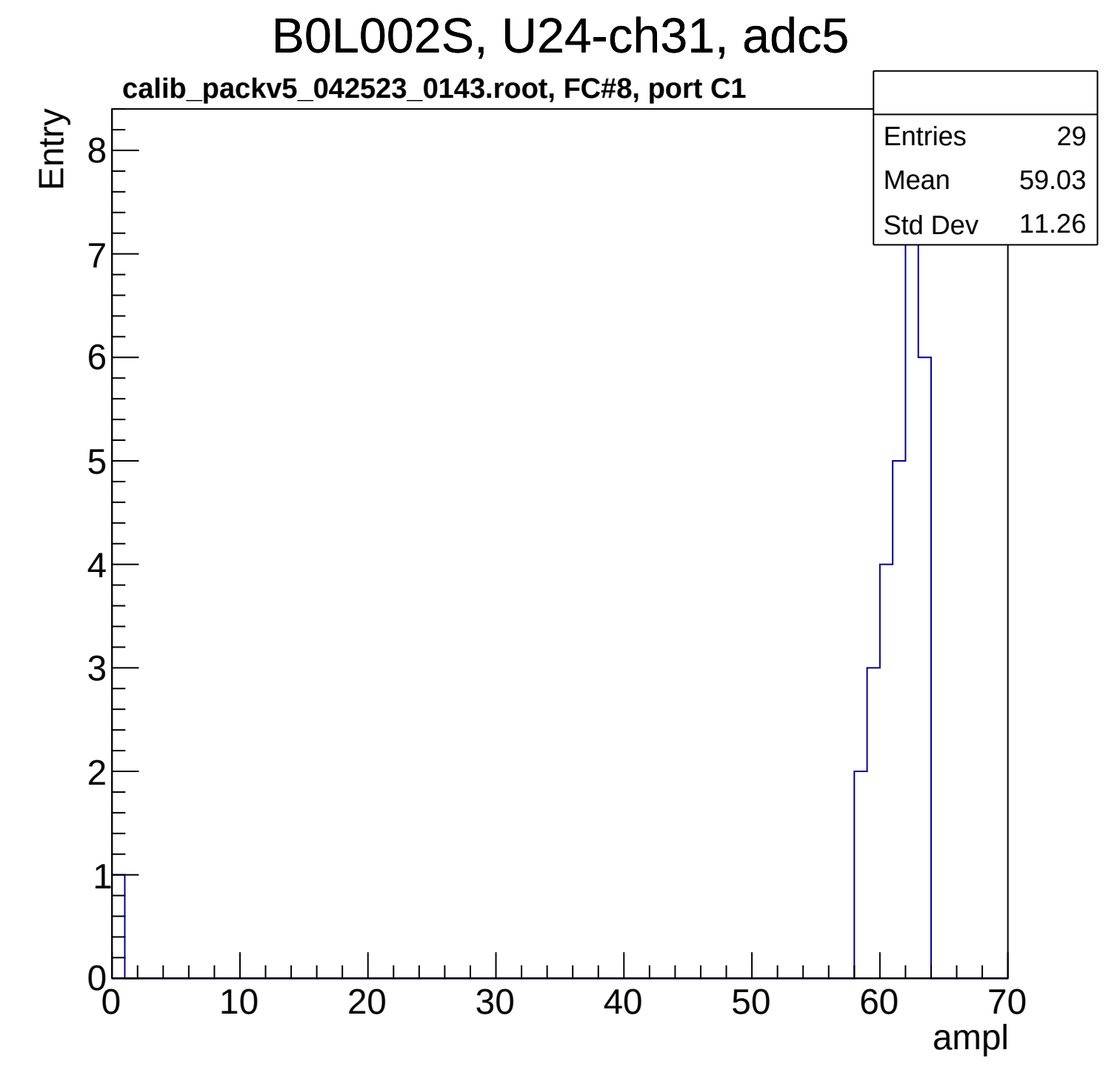
B0L002S, U24-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.03
Std Dev 11.26
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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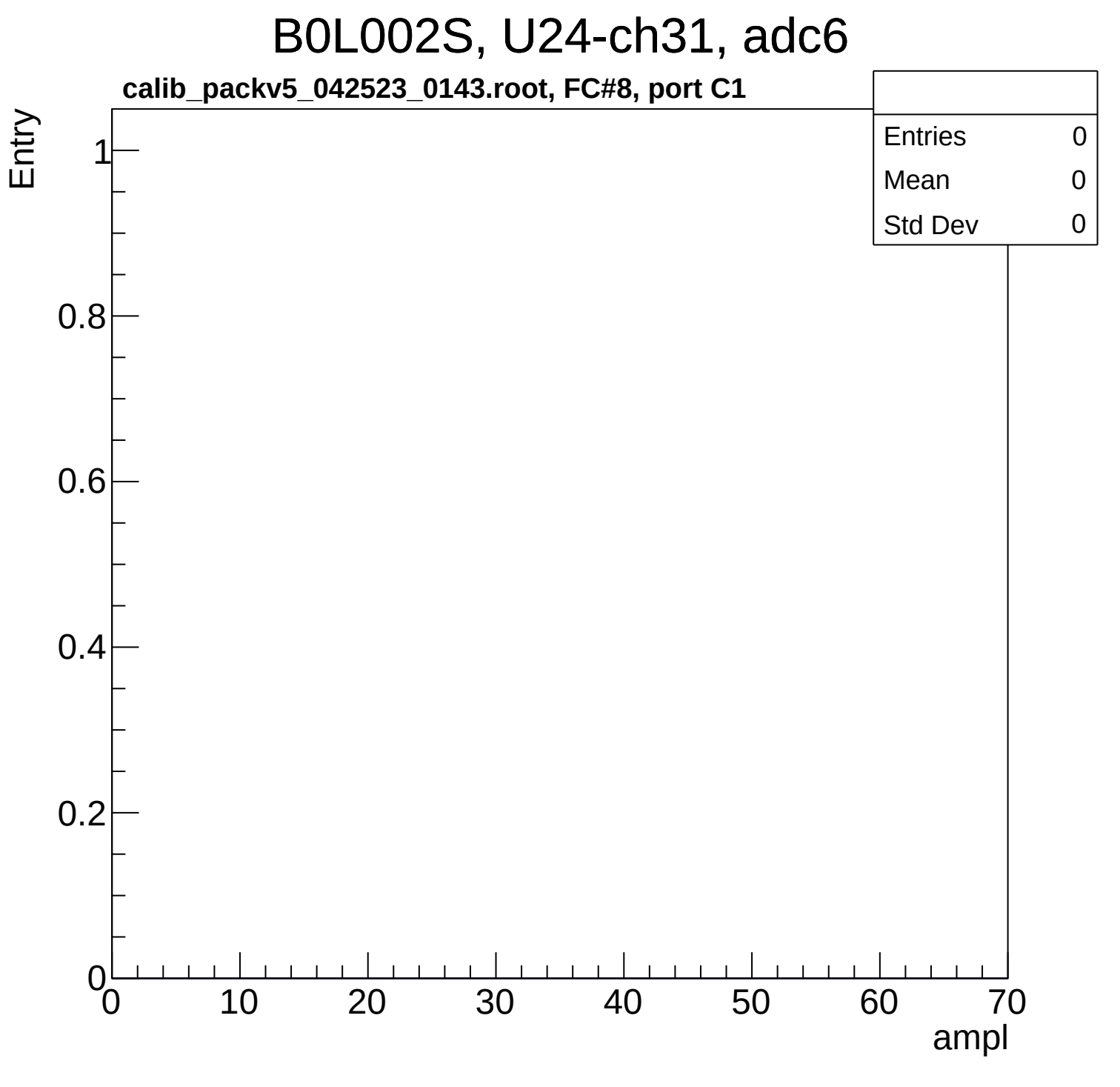
B0L002S, U24-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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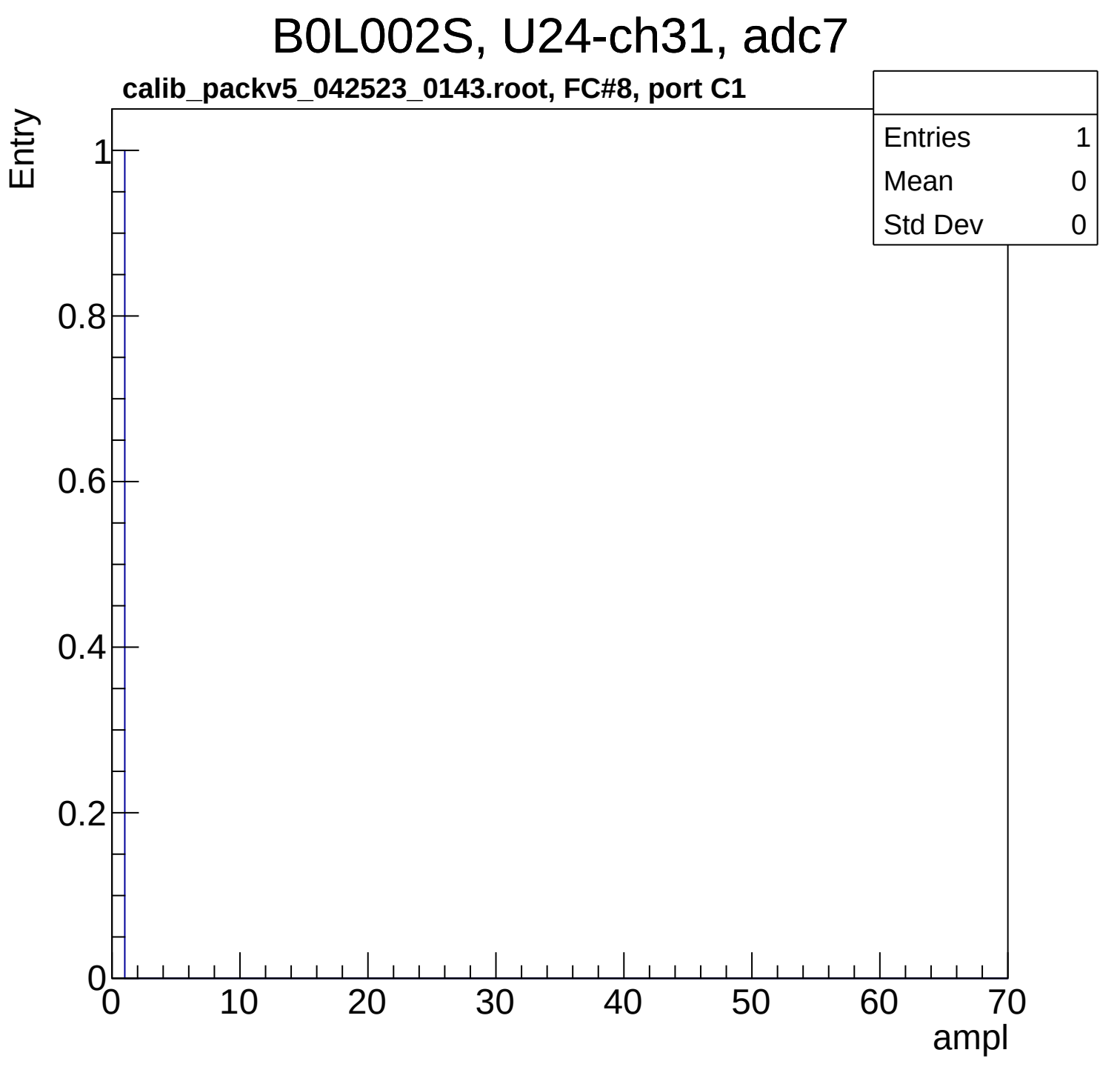
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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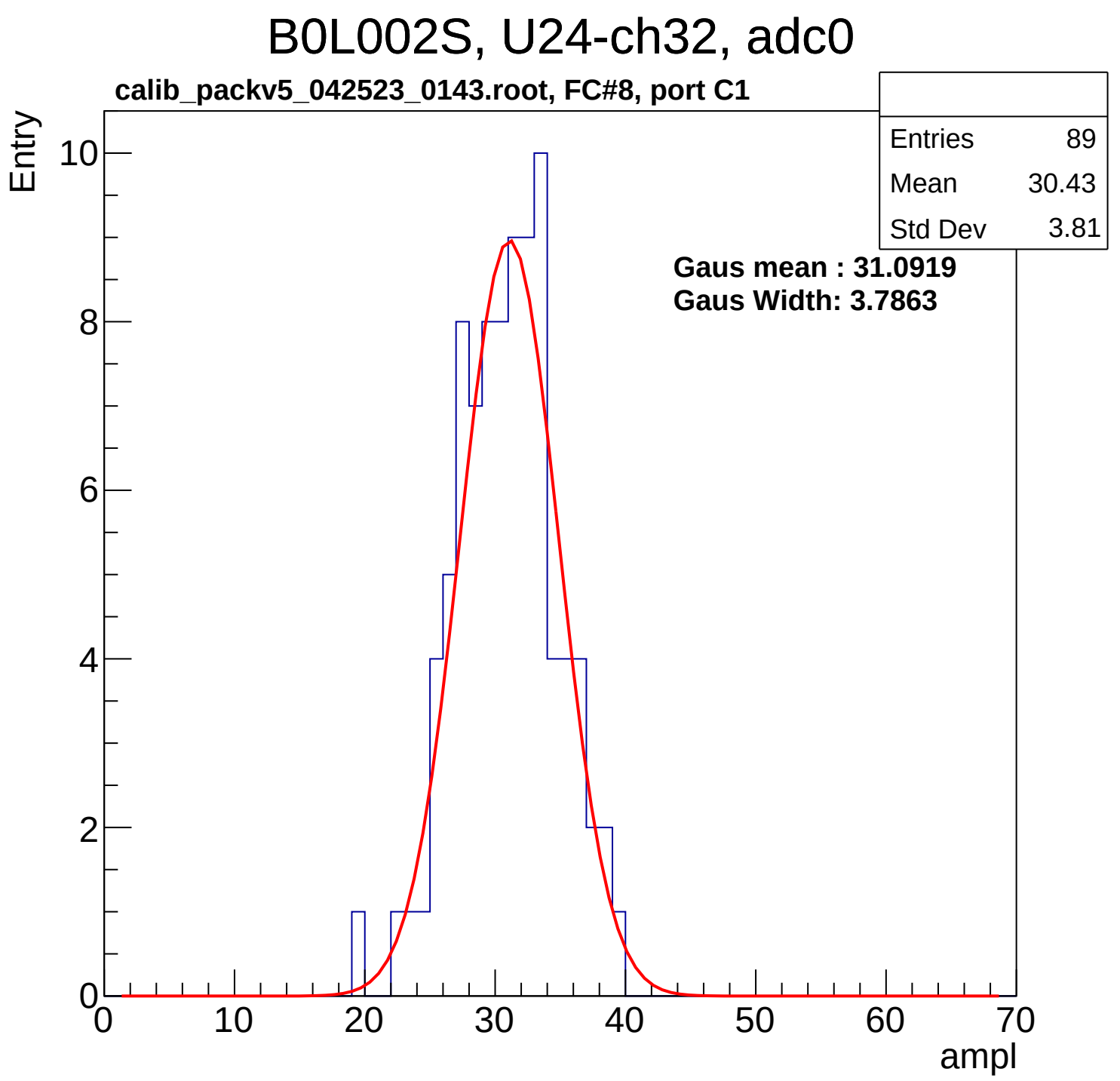
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 30.43
Std Dev 3.81
Gaus mean : 31.0919
Gaus Width: 3.7863
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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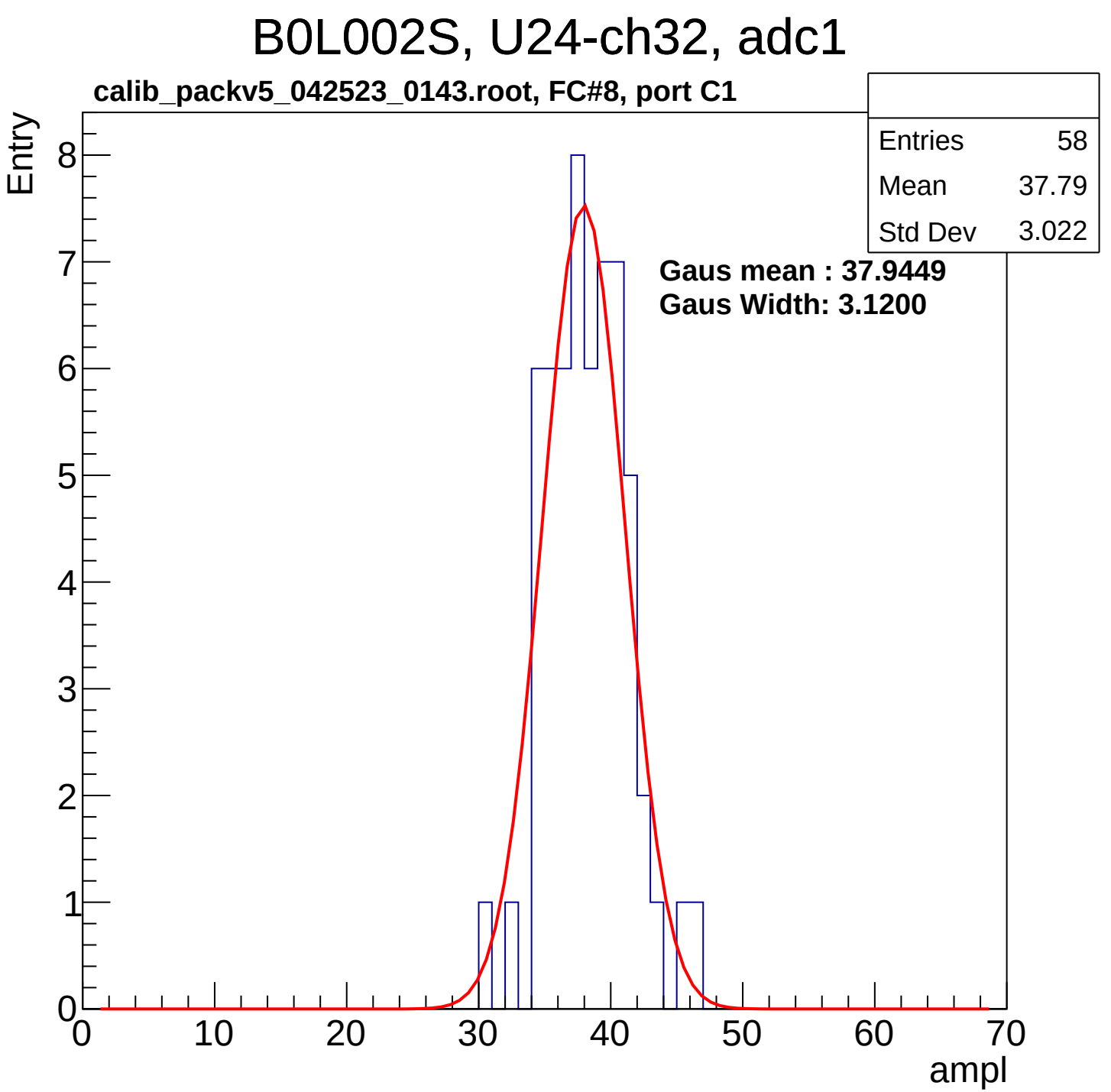
B0L002S, U24-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.79
Std Dev 3.022
Gaus mean : 37.9449
Gaus Width: 3.1200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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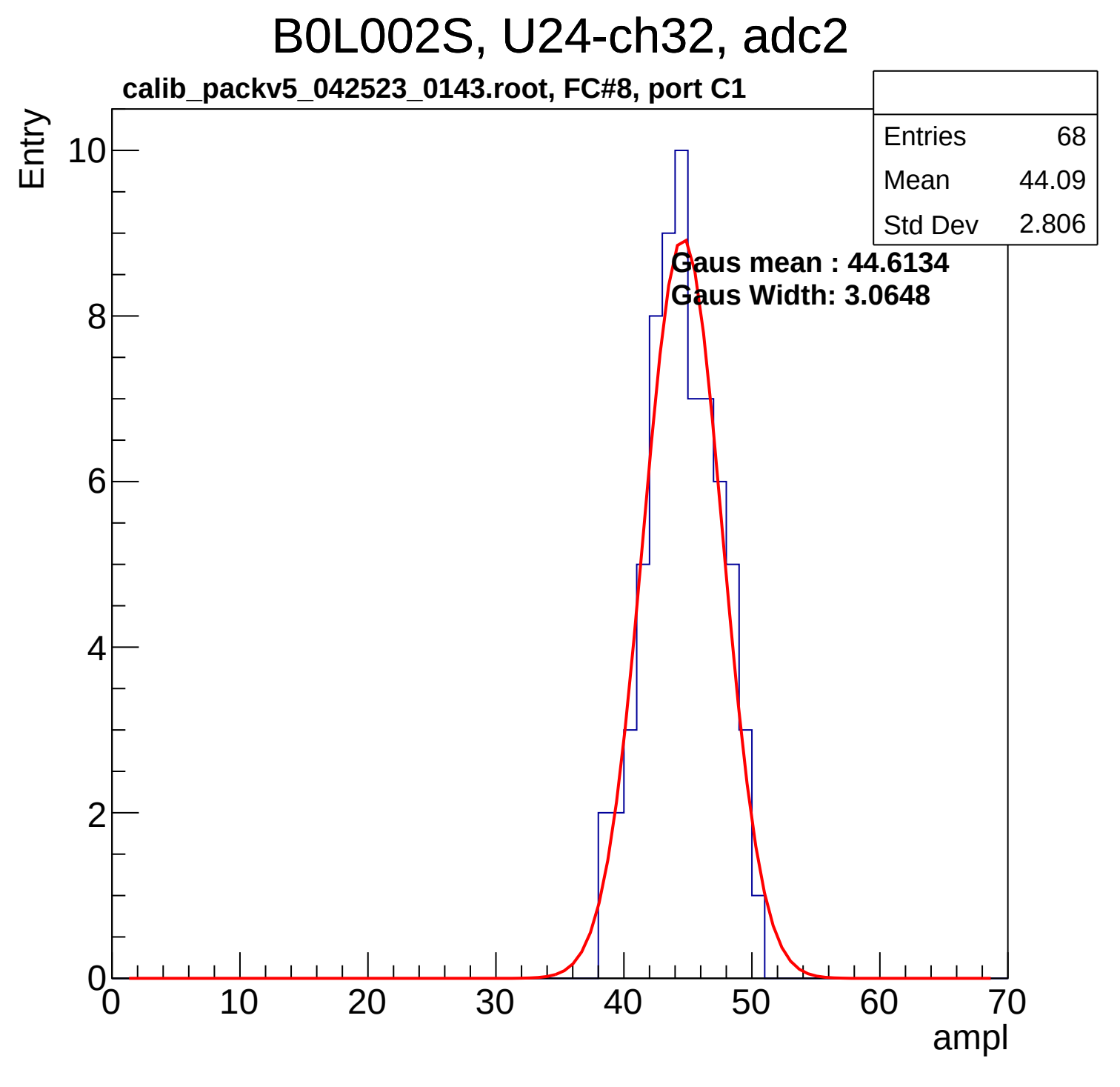
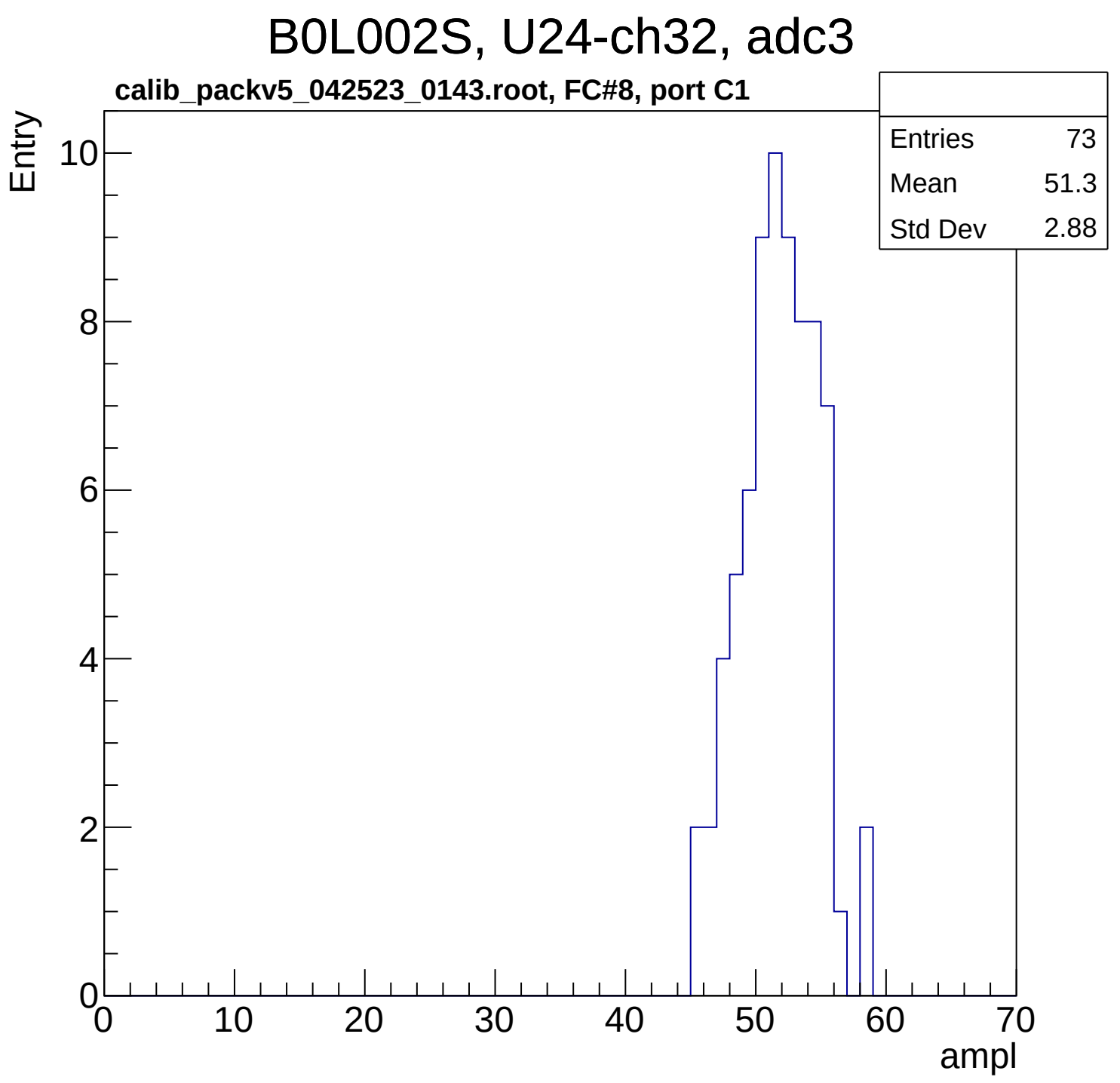
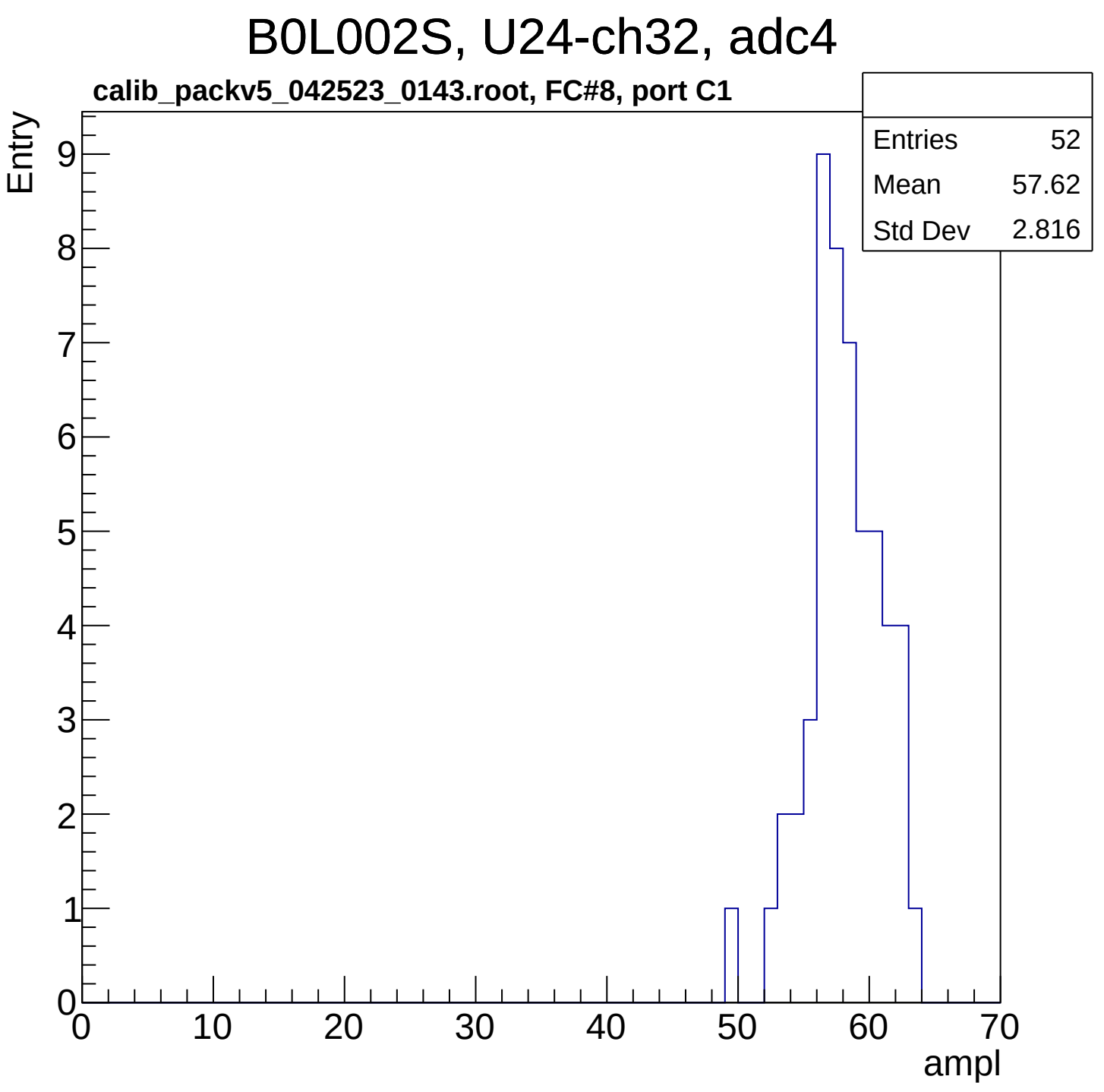

B0L002S, U24-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.09
Std Dev 2.806
Gaus mean : 44.6134
Gaus Width: 3.0648
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.3
Std Dev 2.88
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.62
Std Dev 2.816
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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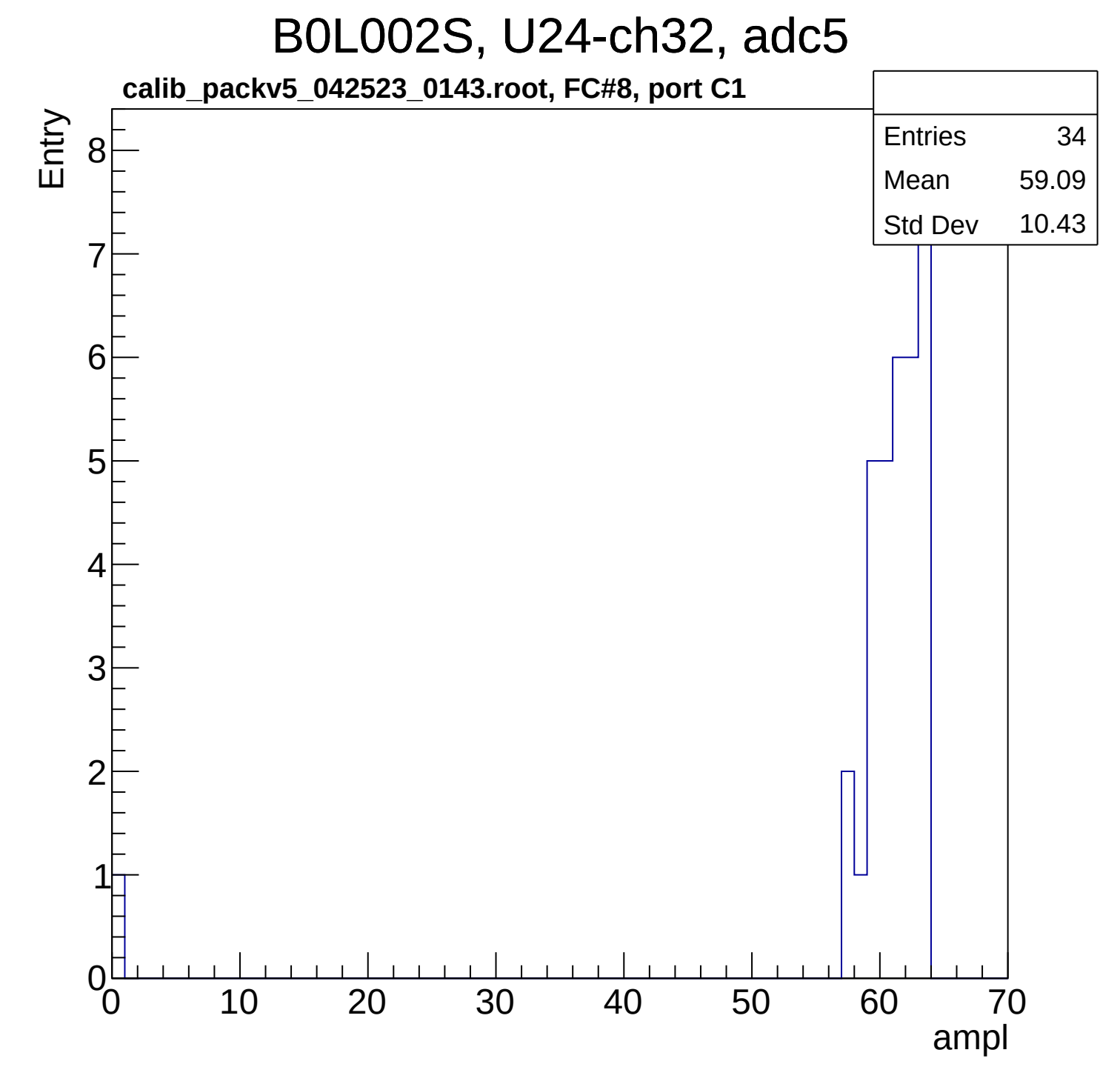
B0L002S, U24-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.09
Std Dev 10.43
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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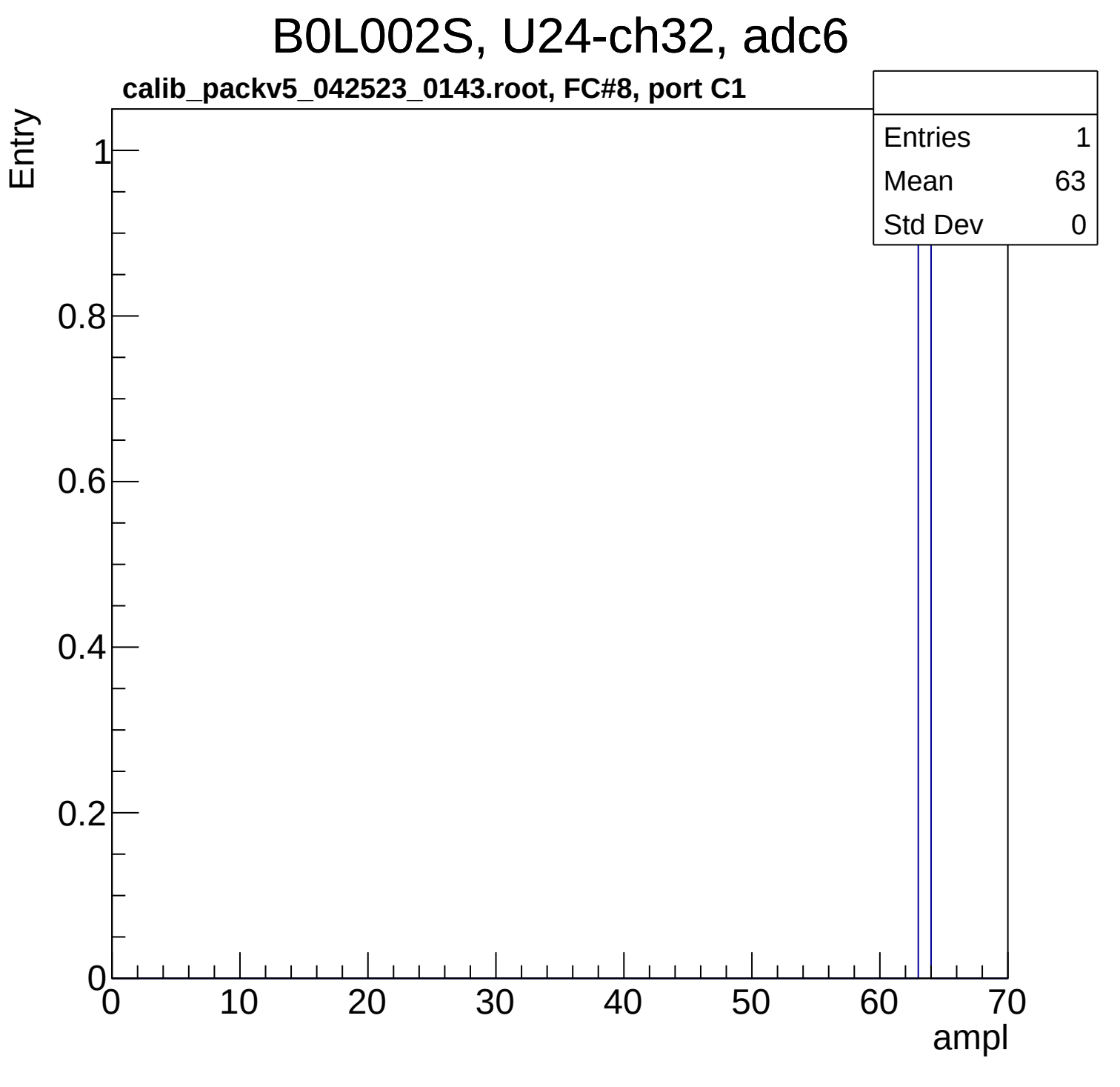
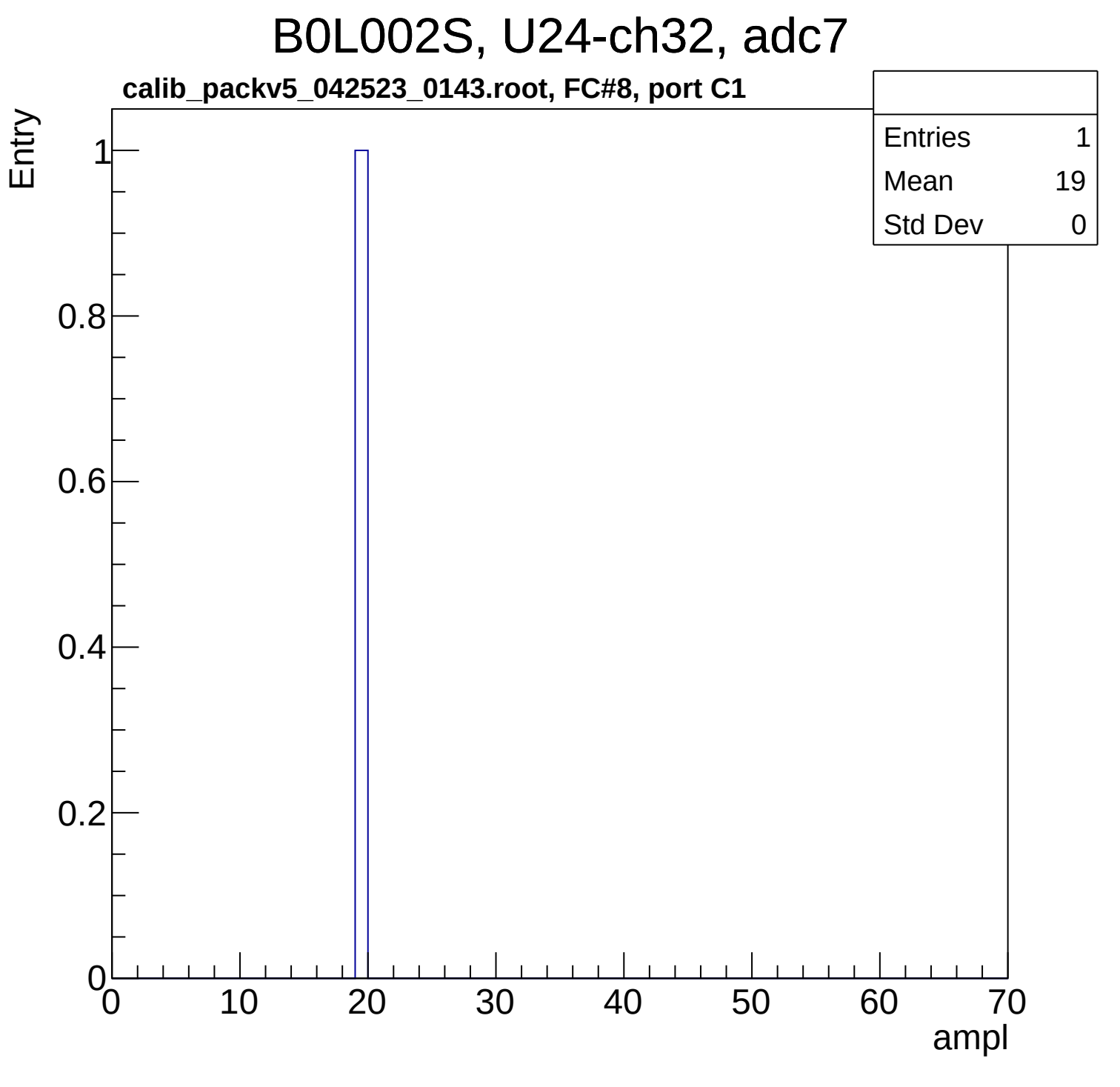

B0L002S, U24-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
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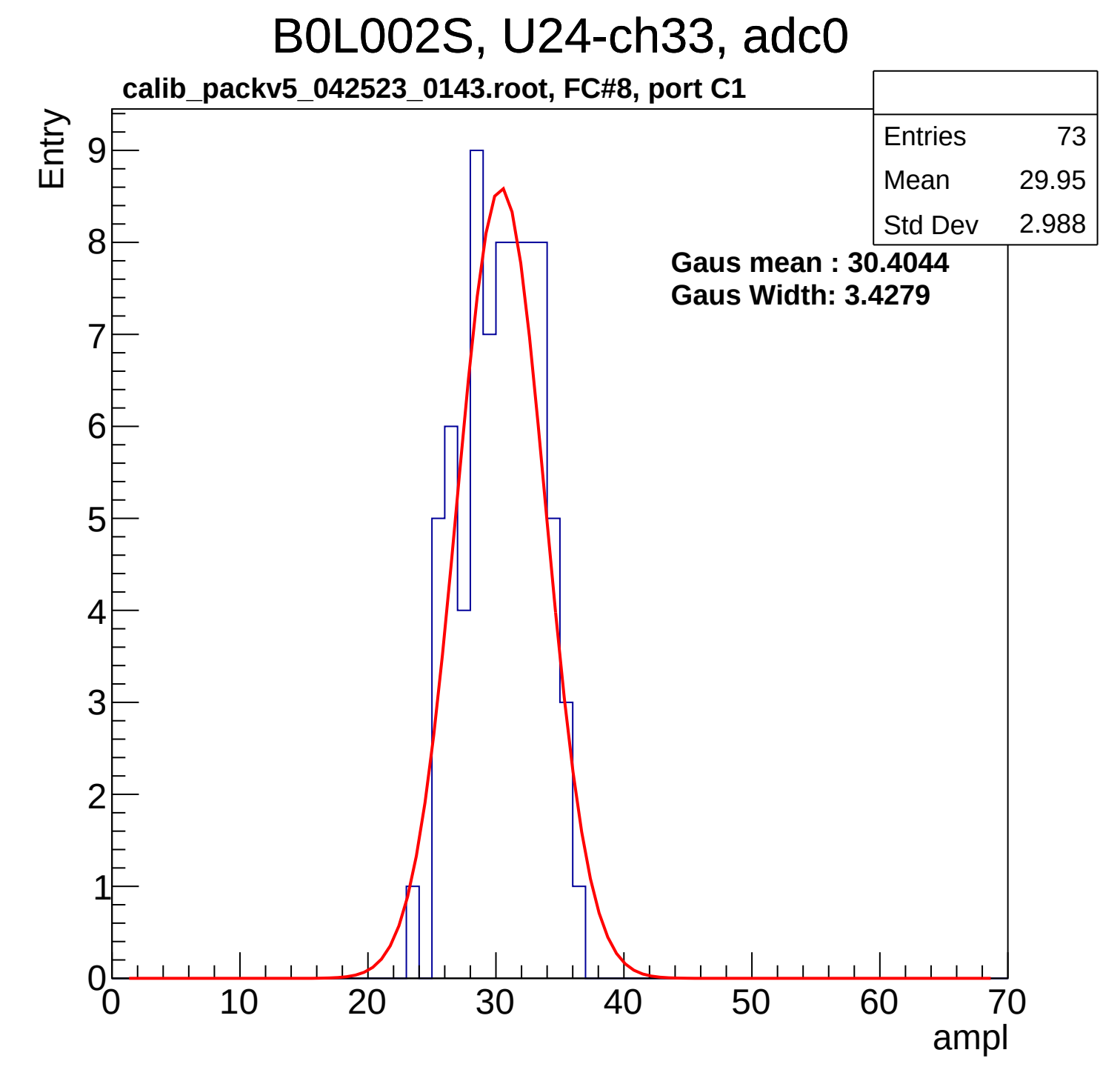
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 29.95
Std Dev 2.988
Gaus mean : 30.4044
Gaus Width: 3.4279
Entry
ampl
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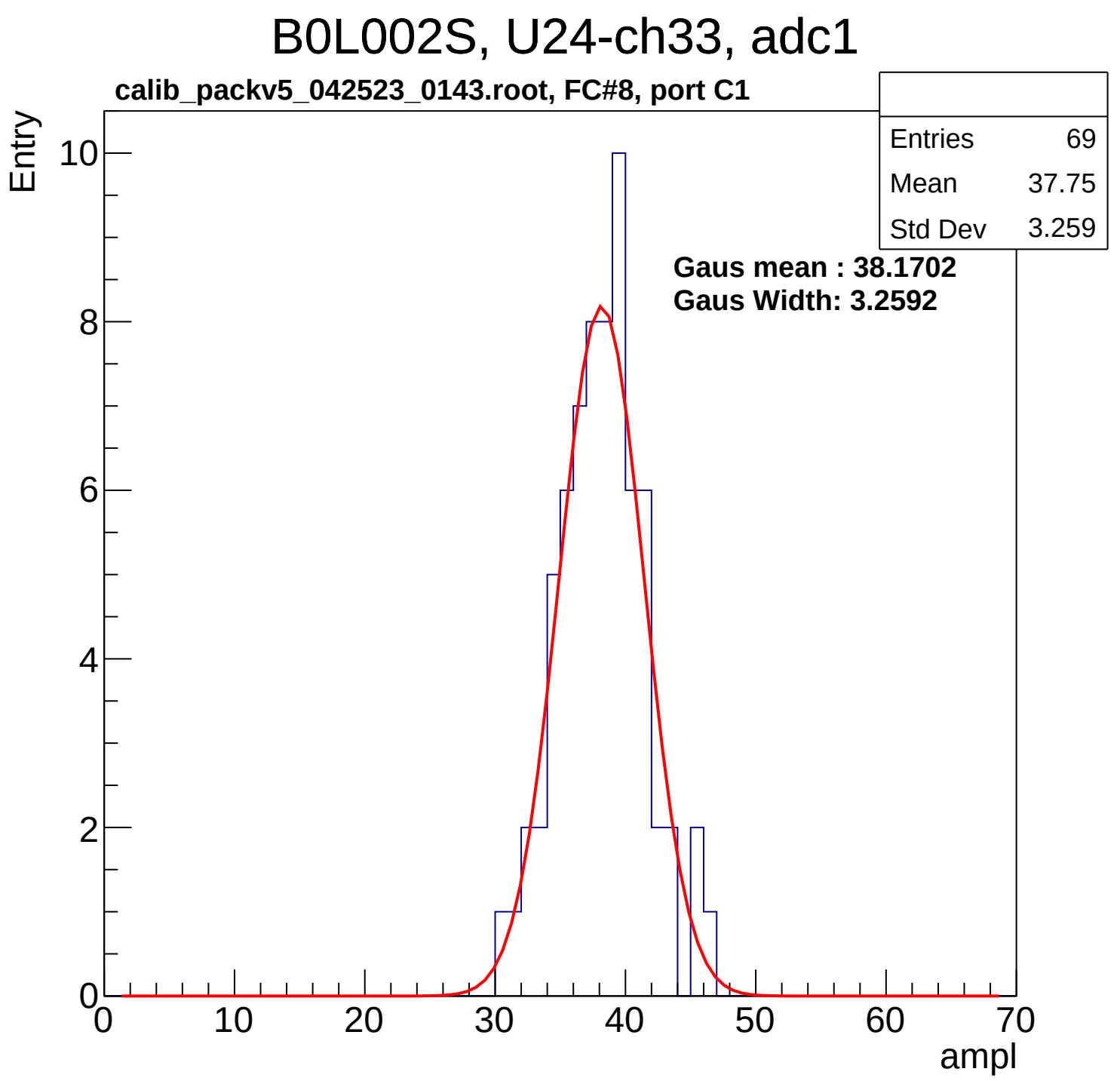
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.75
Std Dev 3.259
Gaus mean : 38.1702
Gaus Width: 3.2592
Entry
ampl
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6
8
10
0
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60
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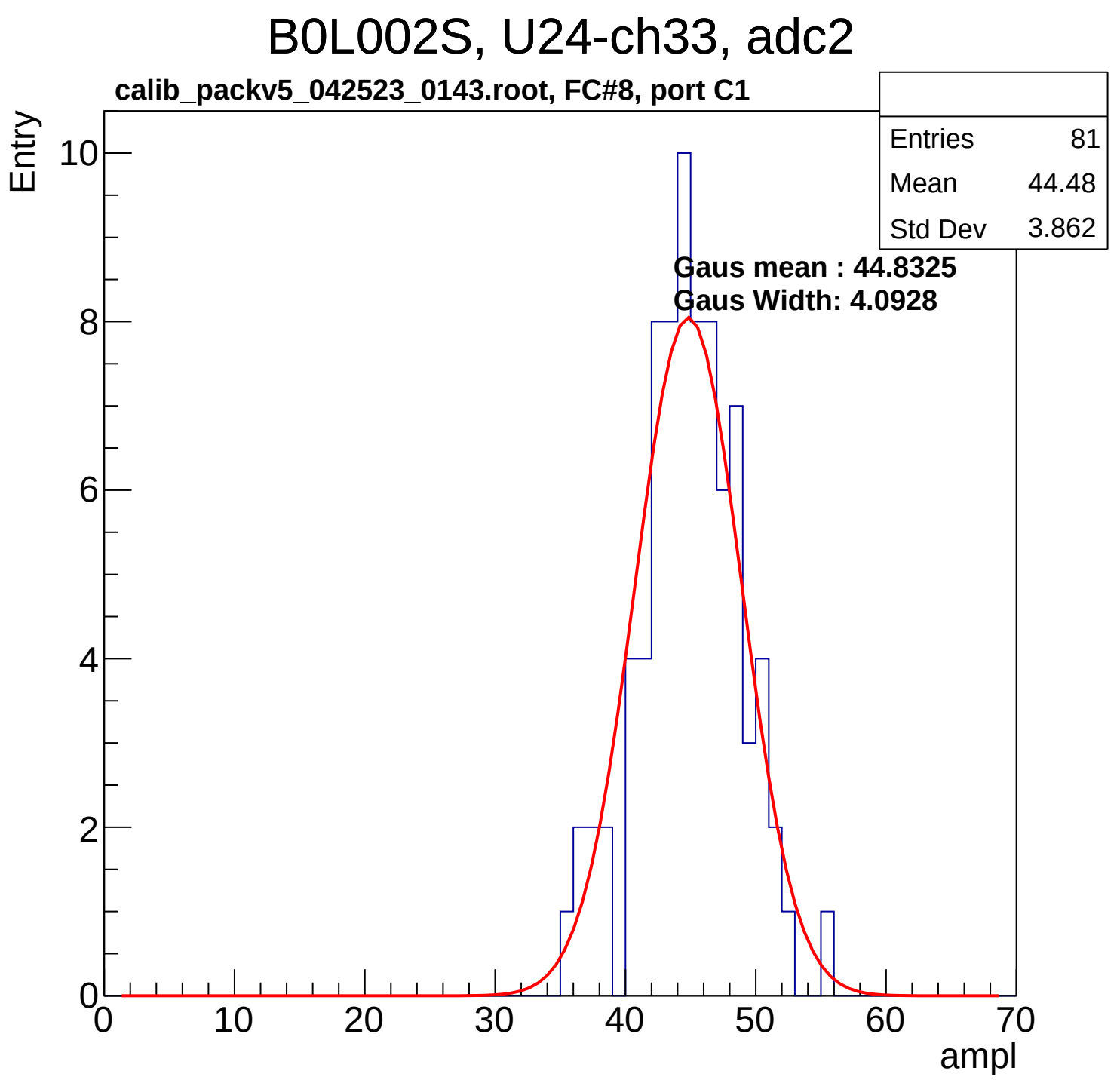
B0L002S, U24-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 44.48
Std Dev 3.862
Gaus mean : 44.8325
Gaus Width: 4.0928
Entry
ampl
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0
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40
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60
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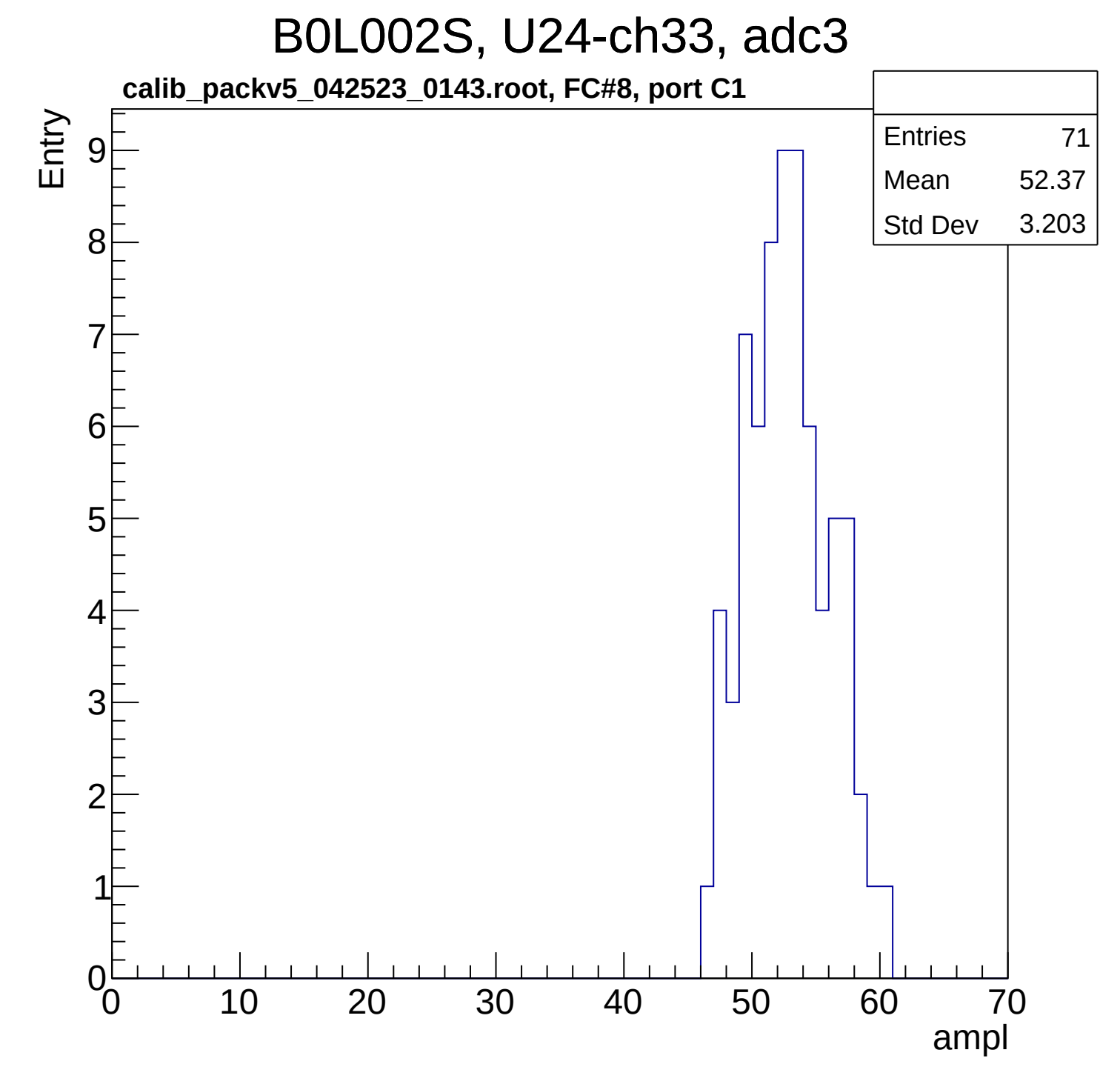
B0L002S, U24-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.37
Std Dev 3.203
Entry
ampl
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0
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60
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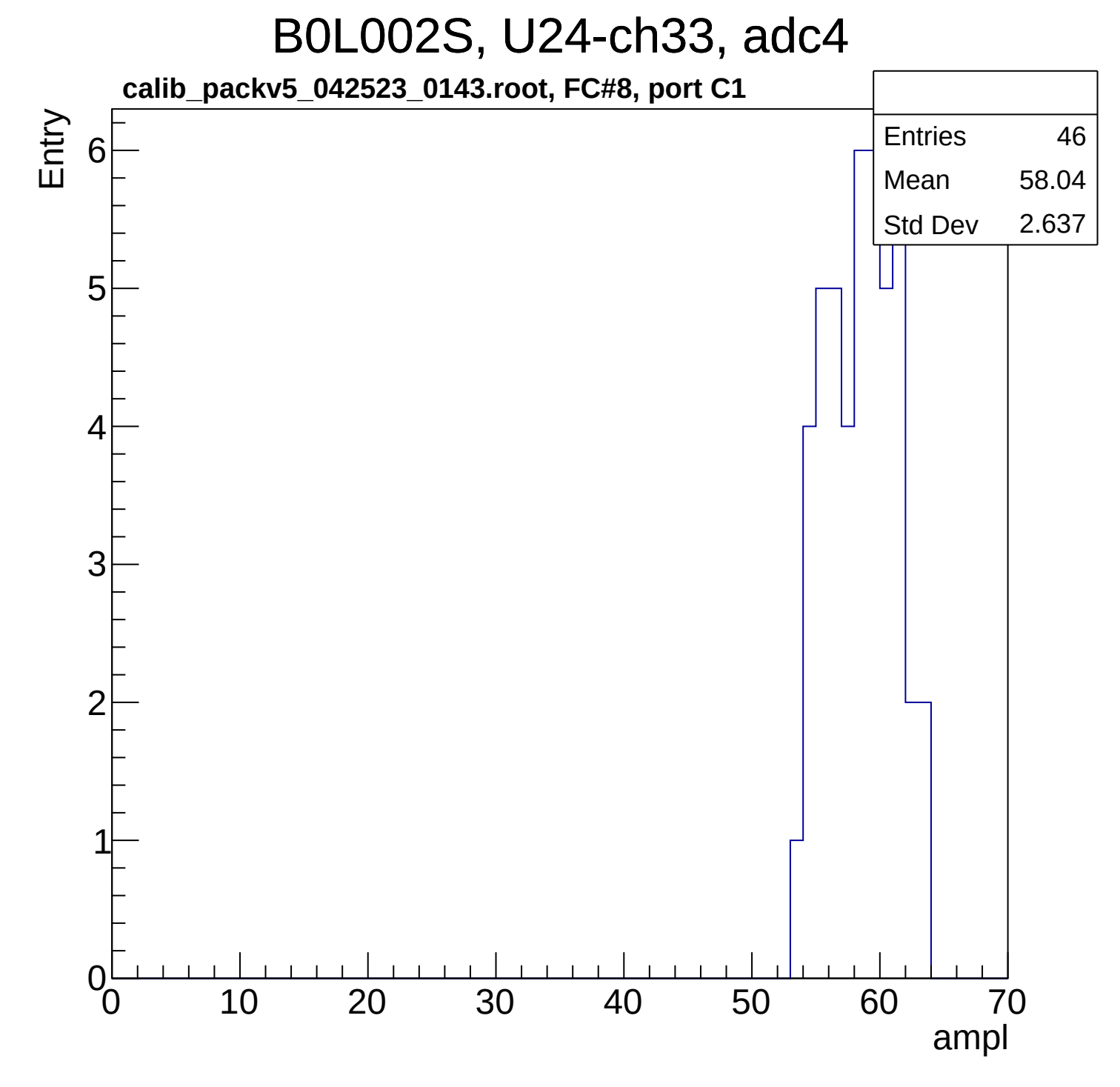
B0L002S, U24-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 58.04
Std Dev 2.637
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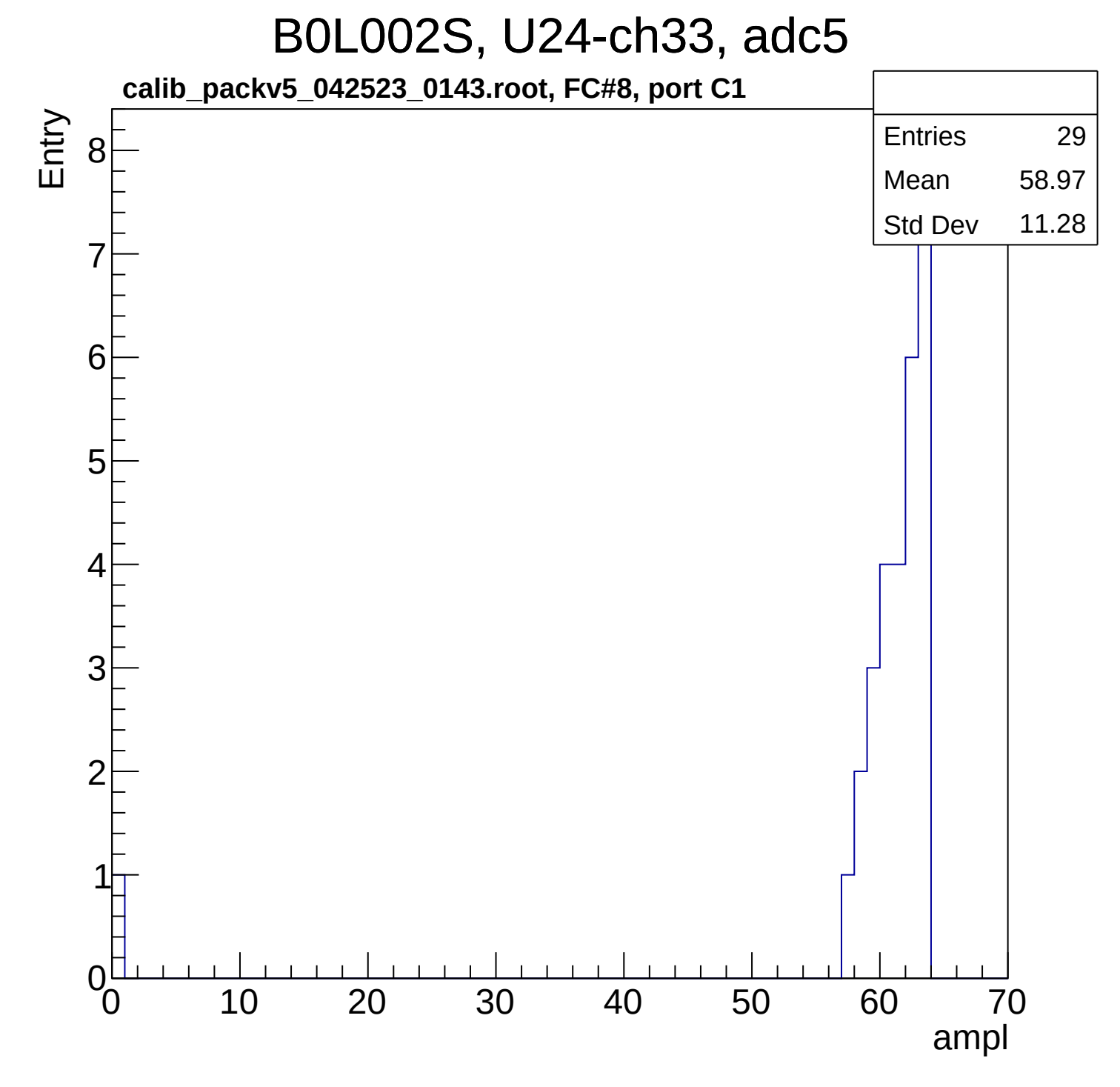
60
70
ampl


B0L002S, U24-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.97
Std Dev 11.28
Entry
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4
5
6
7
8
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10
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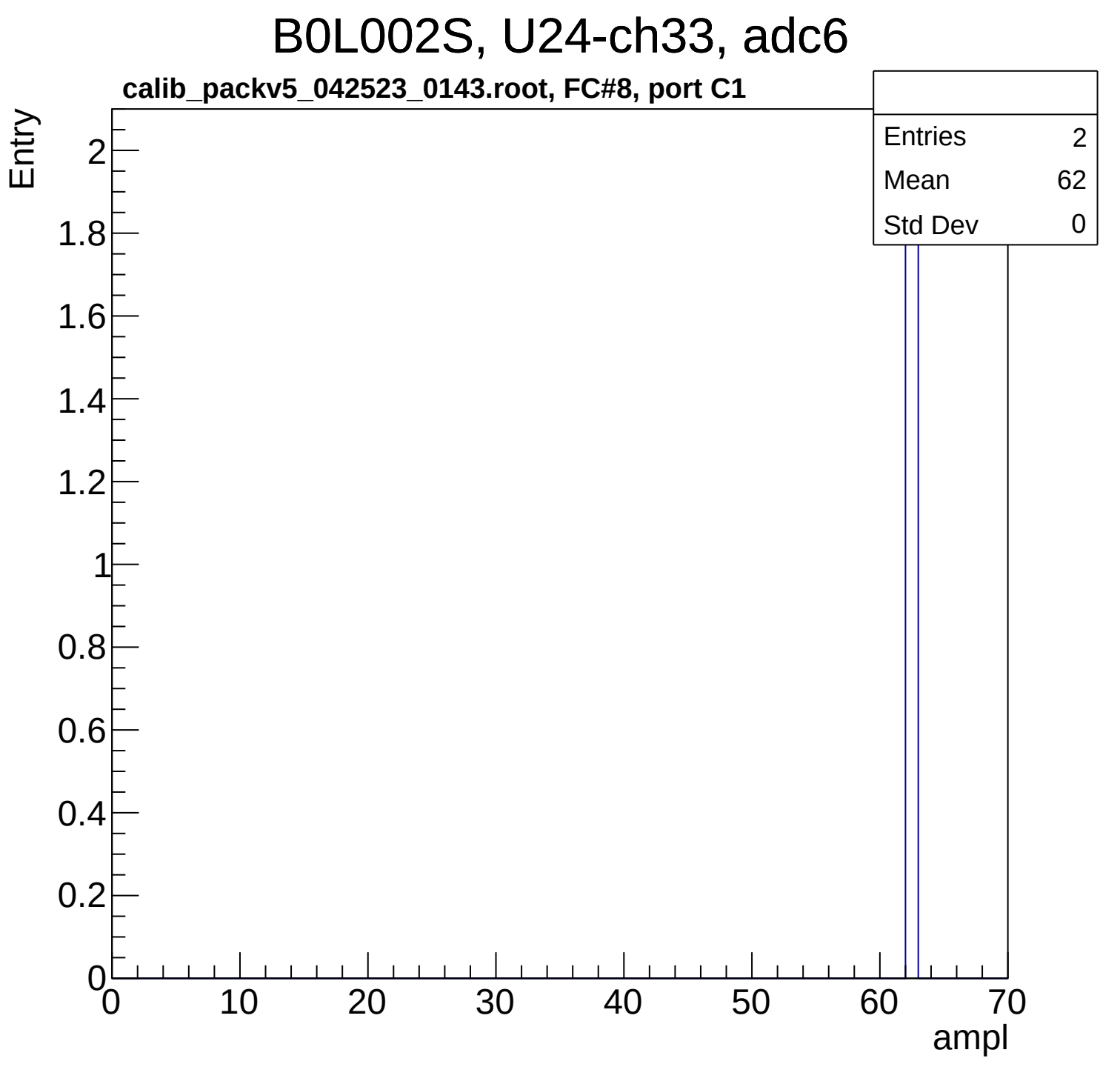
50
60
70
ampl


B0L002S, U24-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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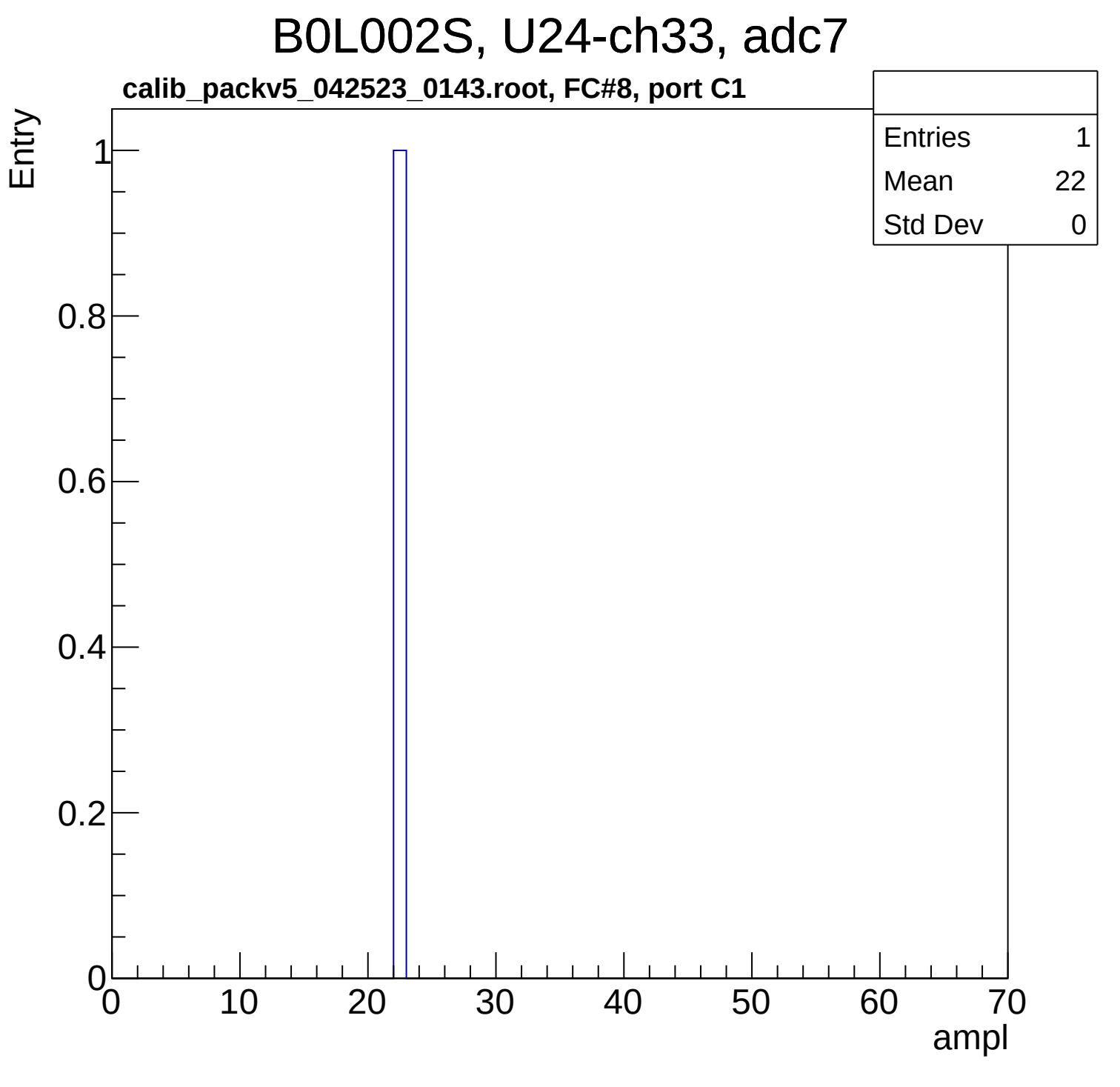
B0L002S, U24-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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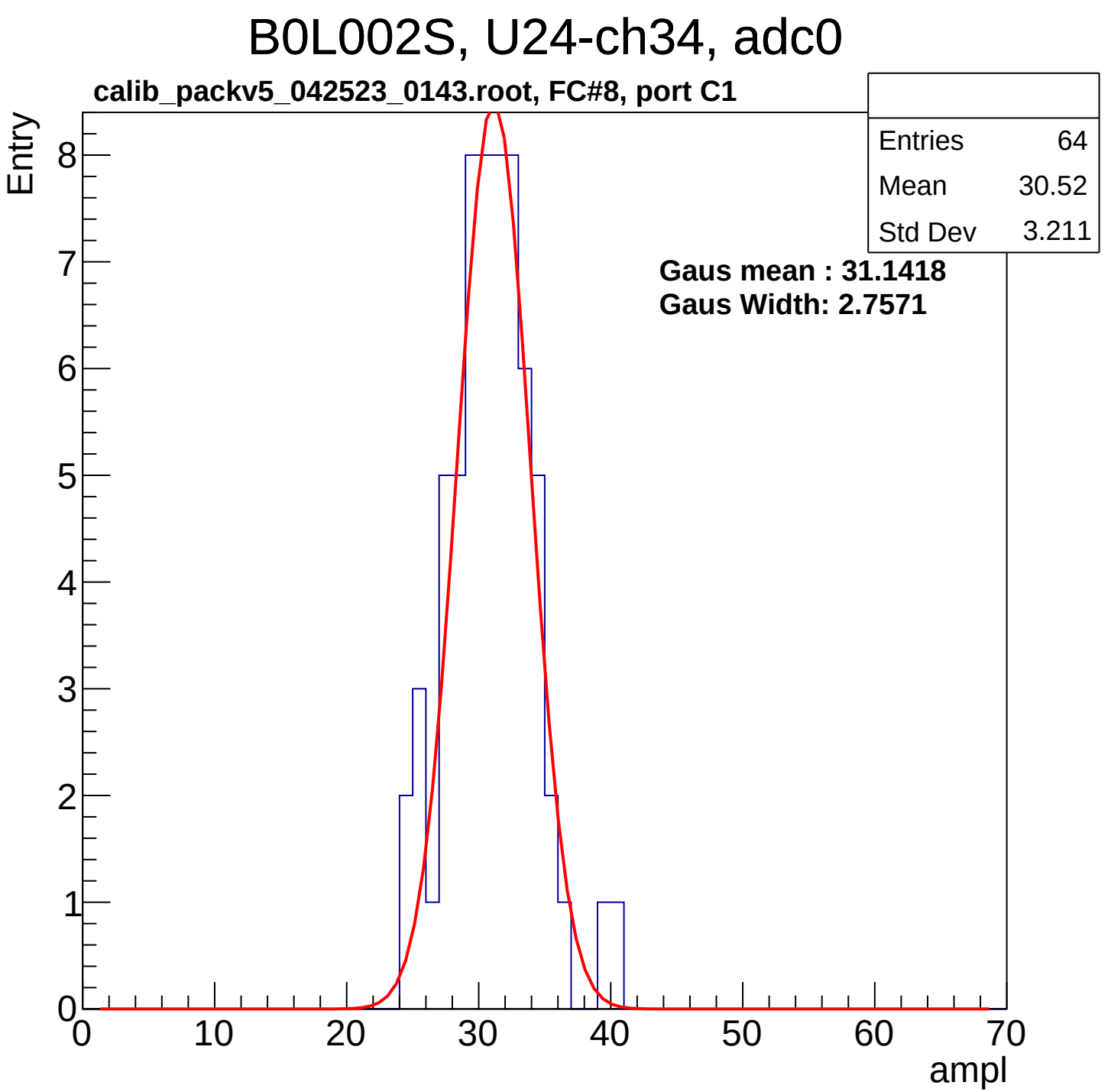
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.52
Std Dev 3.211
Gaus mean : 31.1418
Gaus Width: 2.7571
Entry
ampl
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1
2
3
4
5
6
7
8
0
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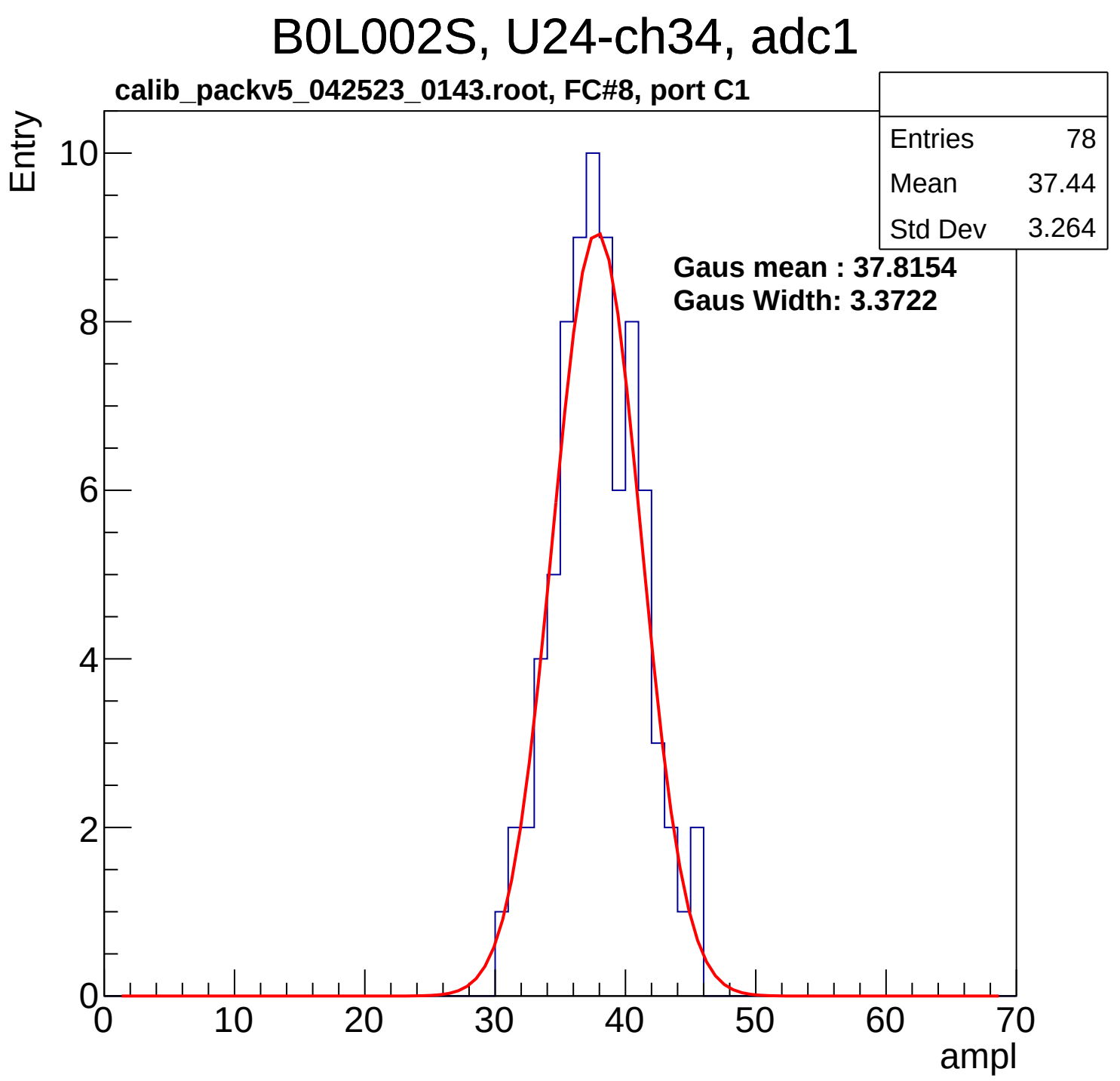
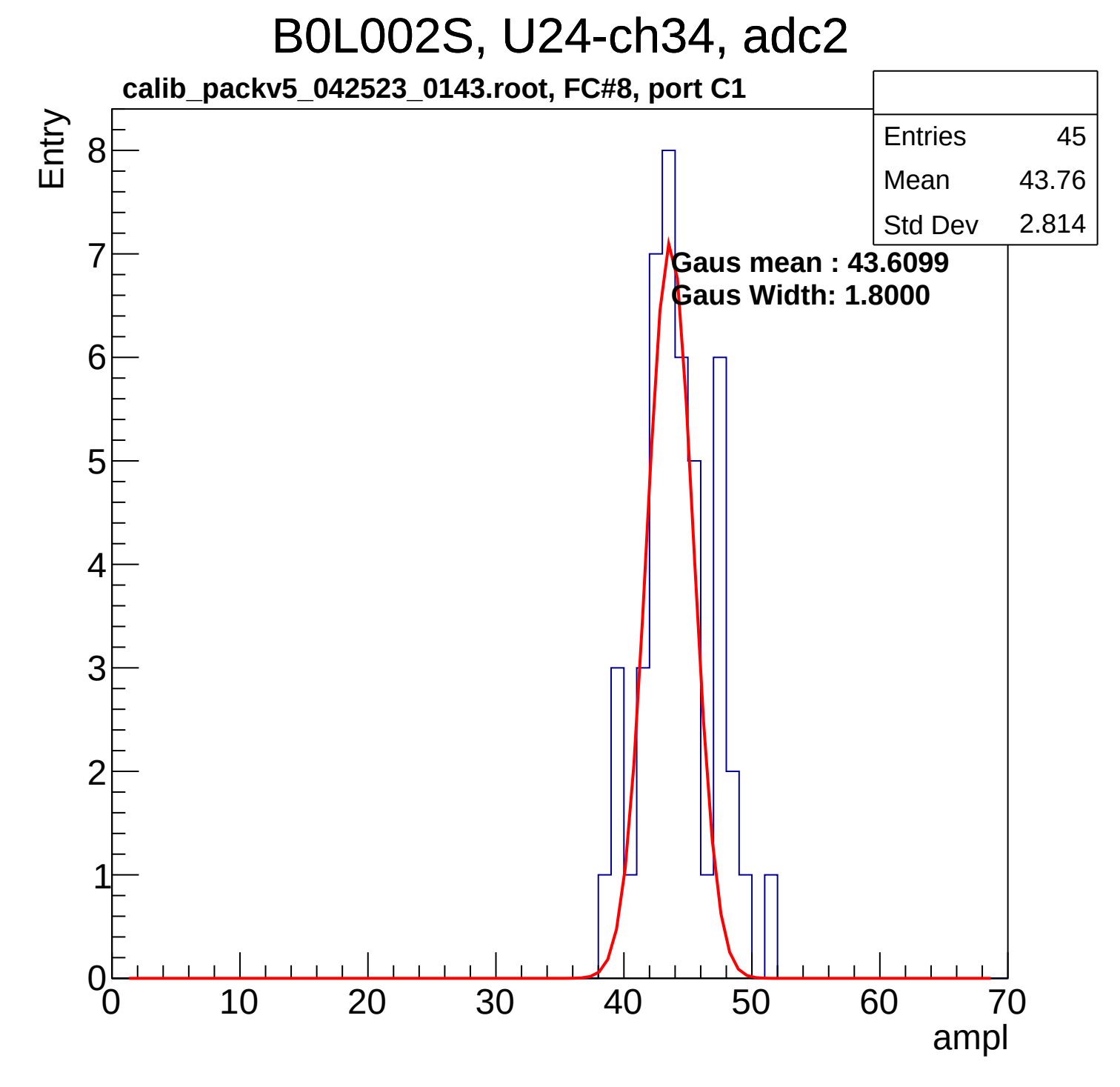
20
30
40
50
60
70


B0L002S, U24-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.44
Std Dev 3.264
Gaus mean : 37.8154
Gaus Width: 3.3722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 43.76
Std Dev 2.814
Gaus mean : 43.6099
Gaus Width: 1.8000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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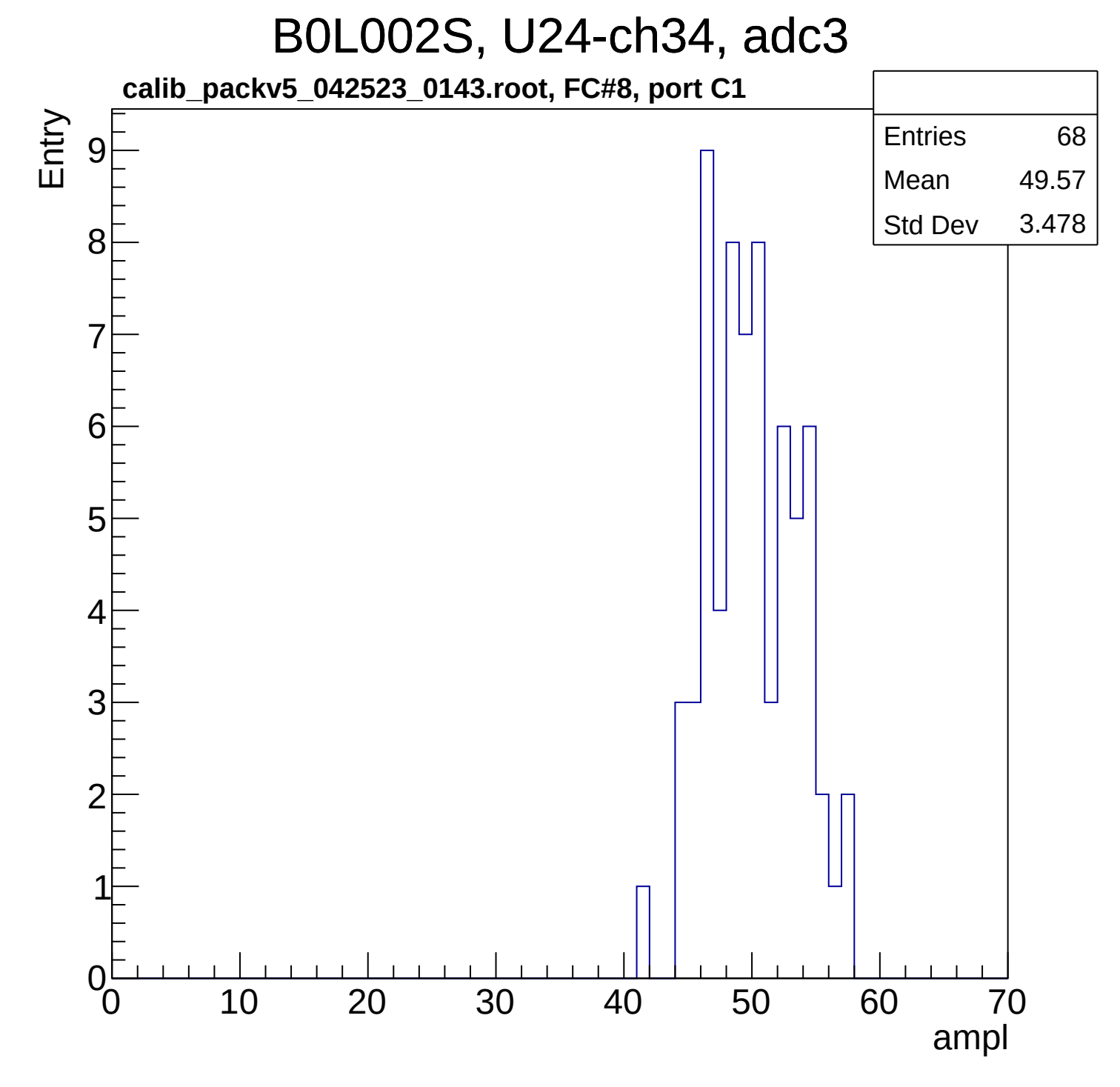
B0L002S, U24-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 49.57
Std Dev 3.478
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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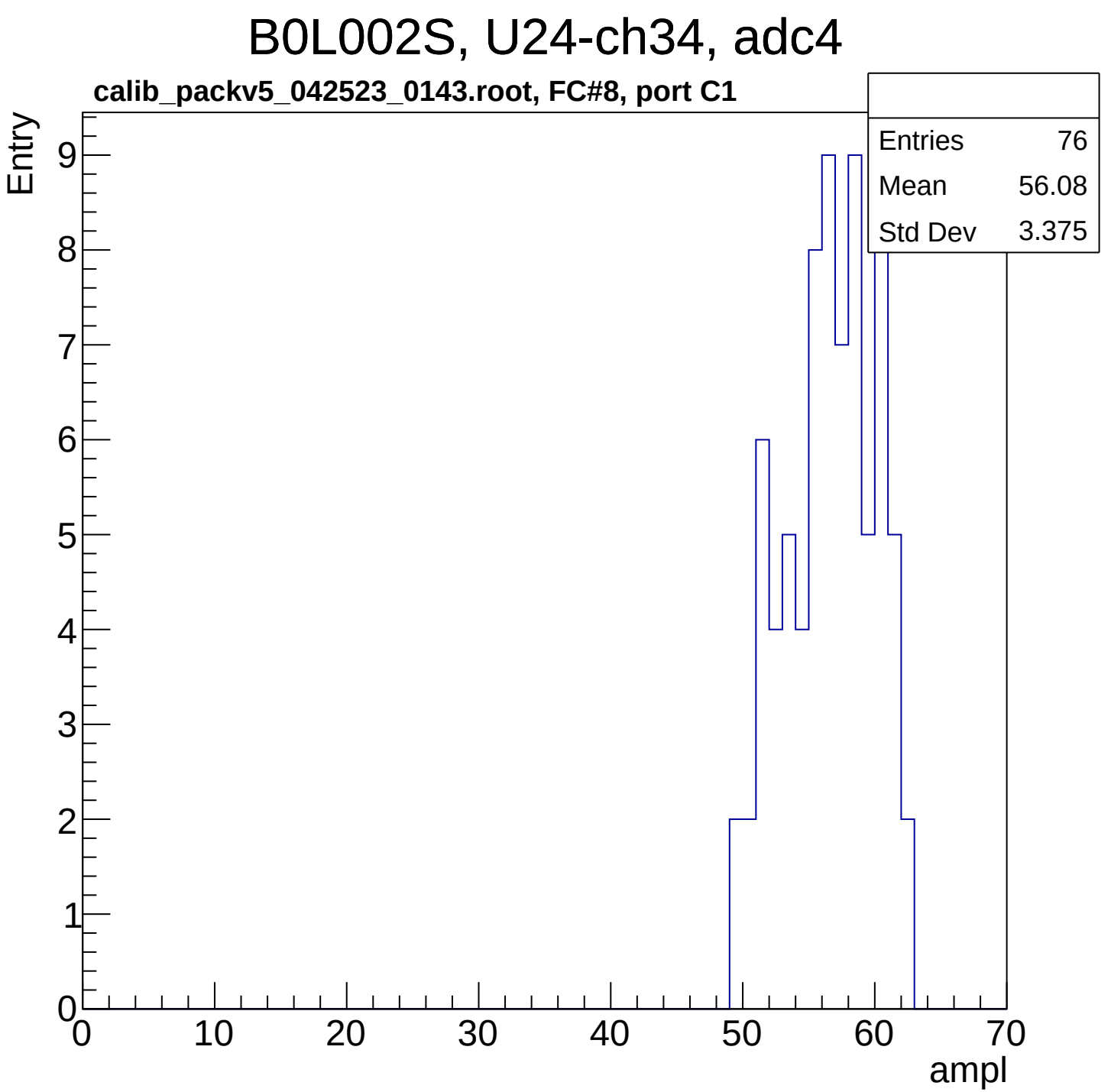
B0L002S, U24-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 56.08
Std Dev 3.375
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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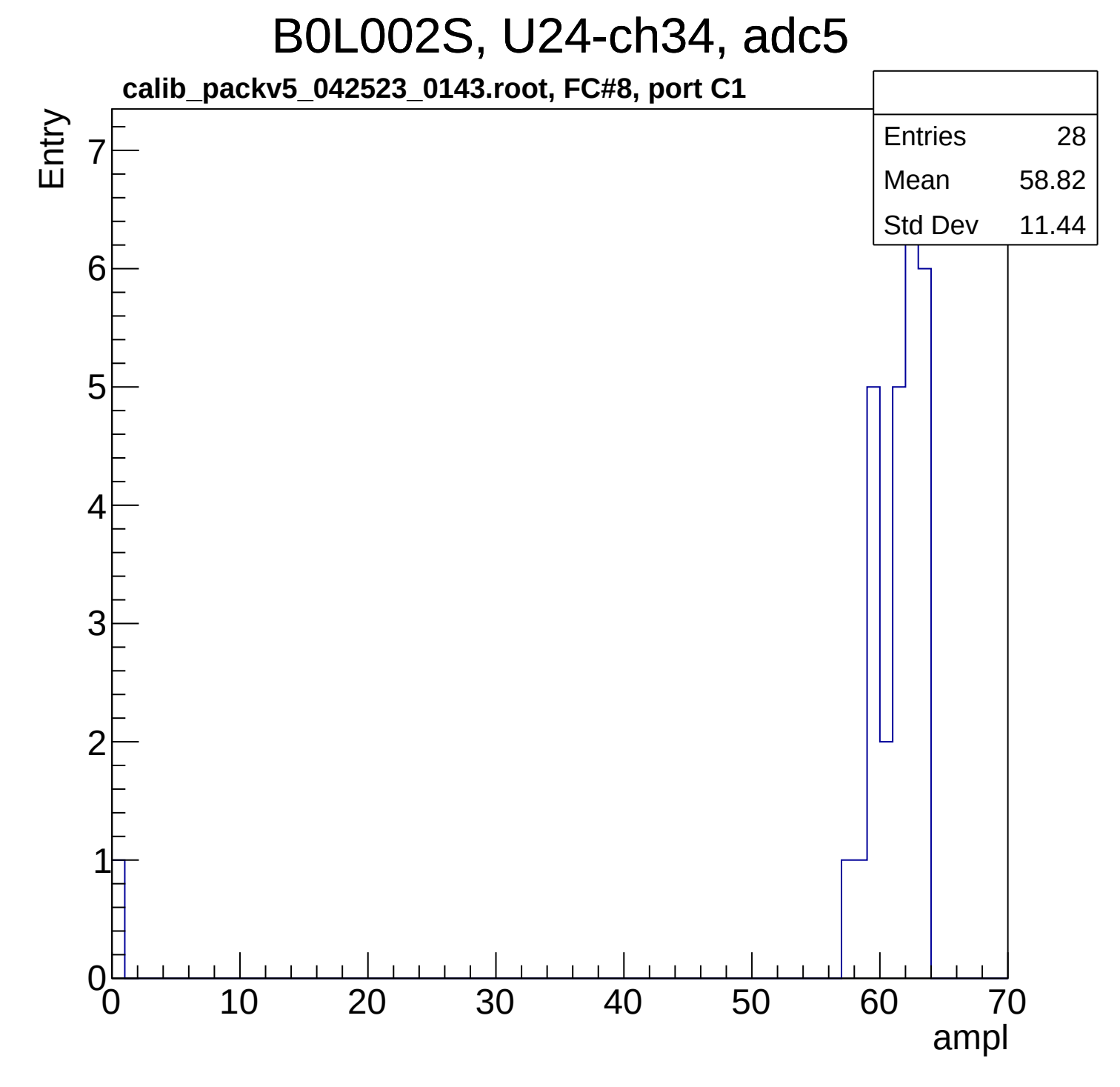
B0L002S, U24-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.82
Std Dev 11.44
Entry
ampl
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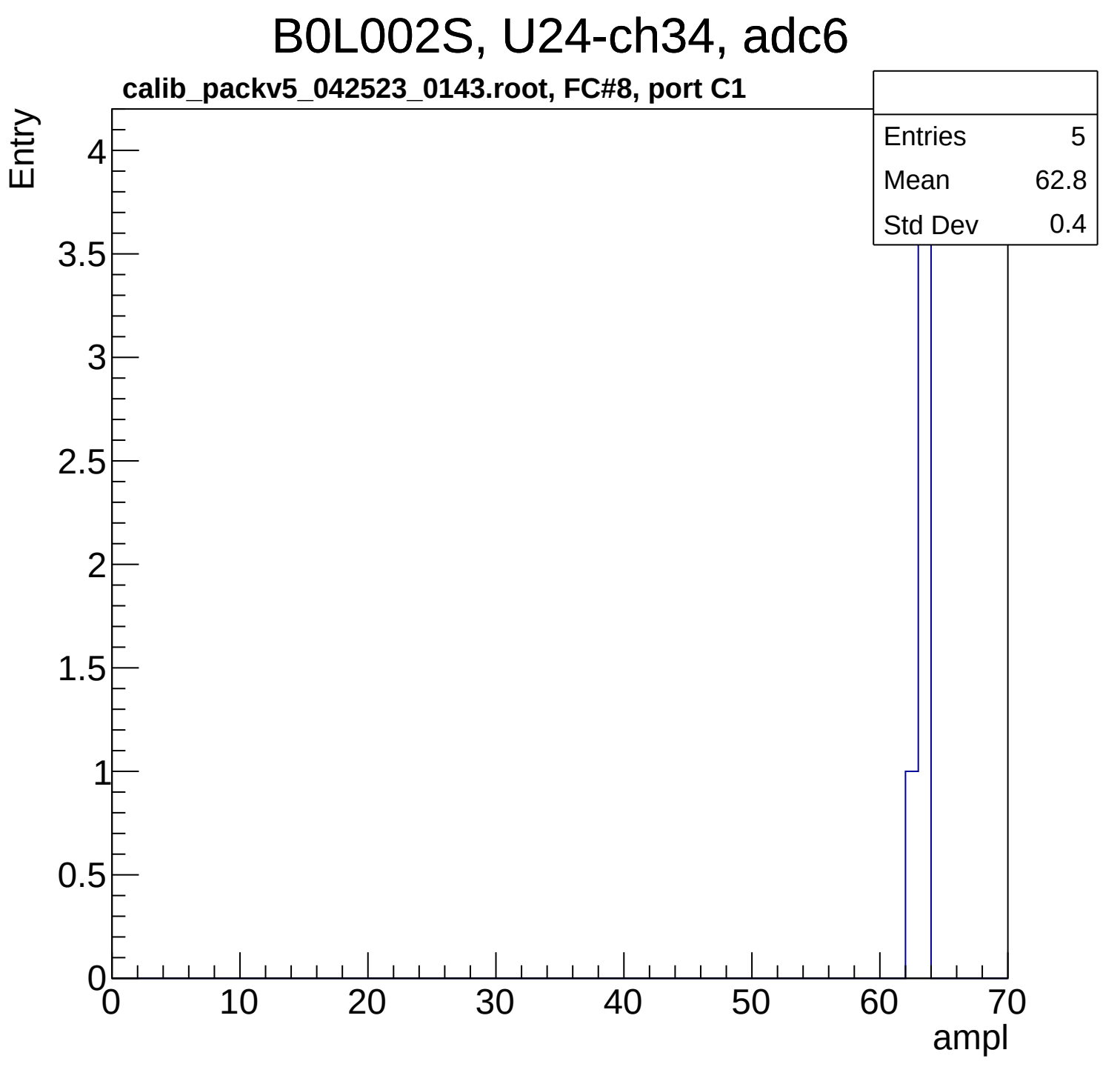
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
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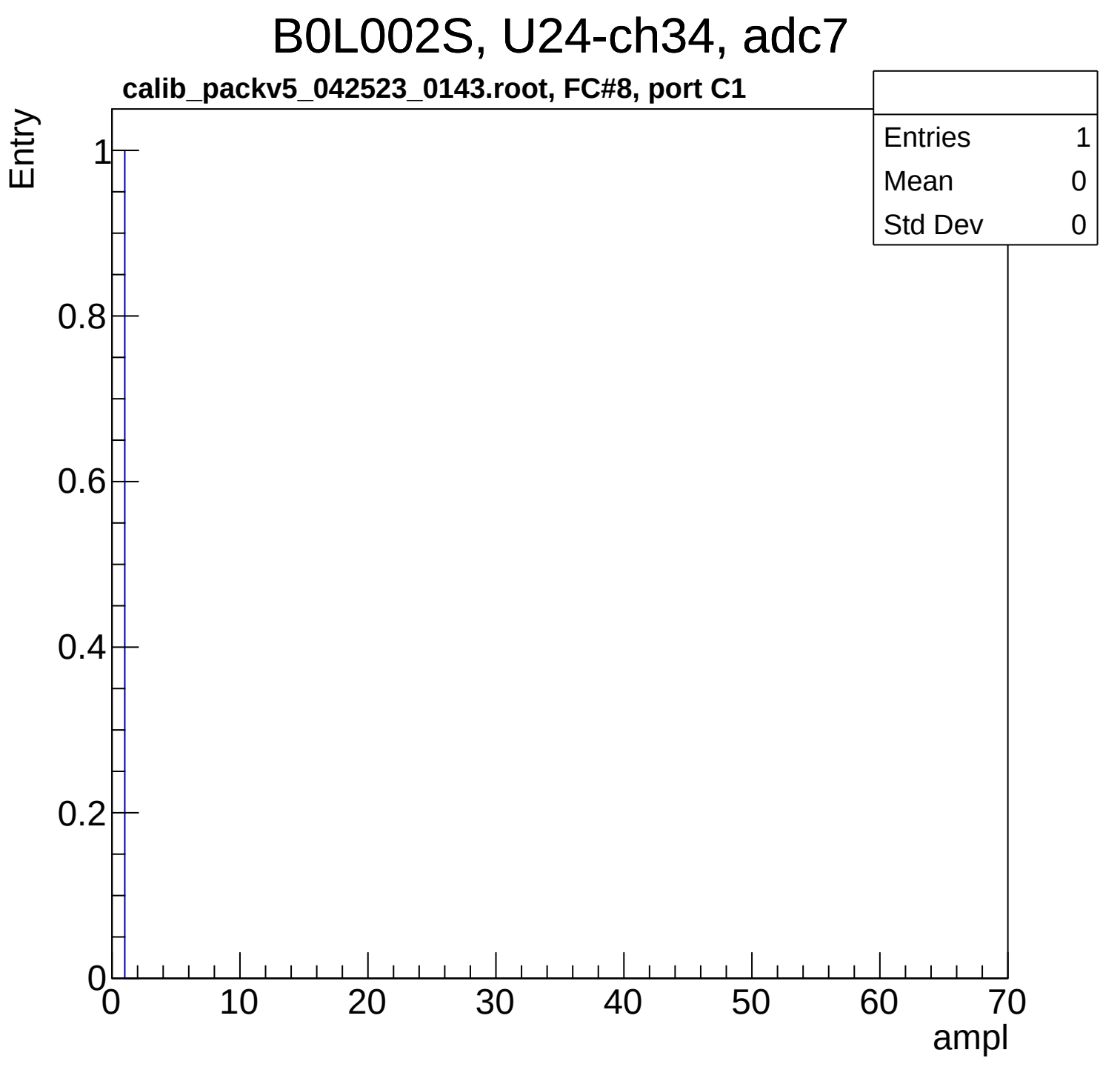
B0L002S, U24-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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10
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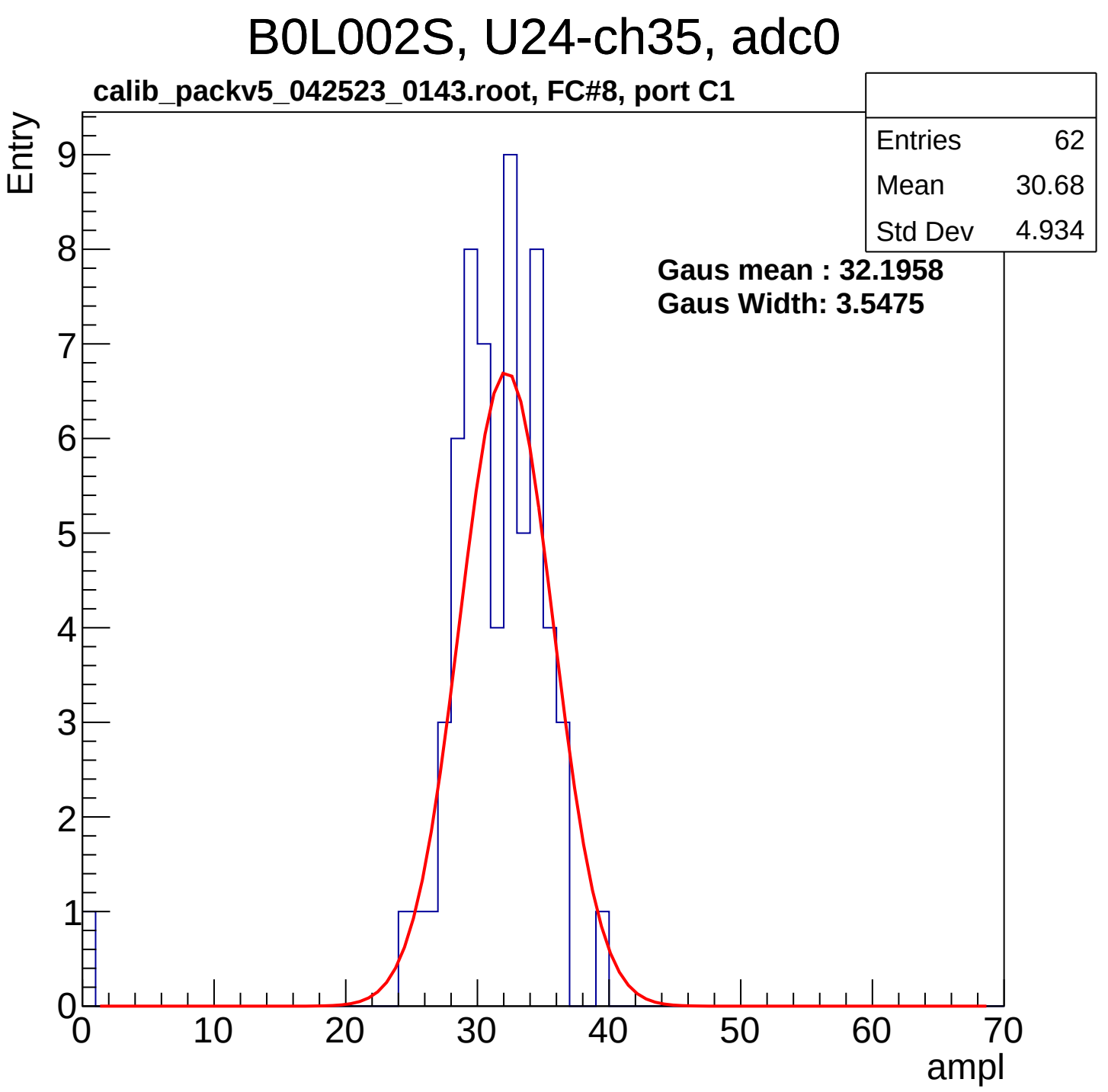
70
ampl


B0L002S, U24-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.68
Std Dev 4.934
Gaus mean : 32.1958
Gaus Width: 3.5475
Entry
ampl
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2
3
4
5
6
7
8
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0
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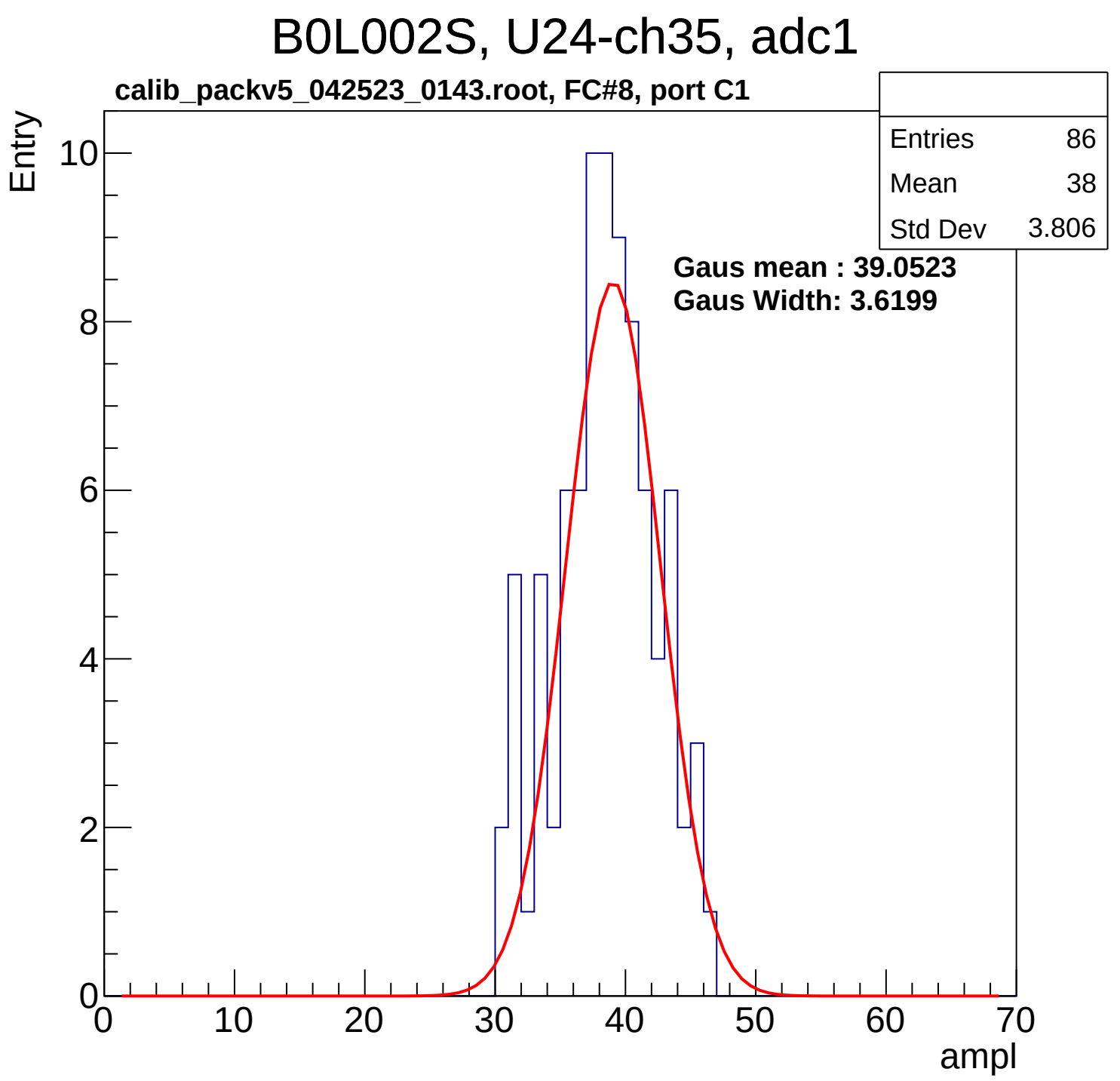
B0L002S, U24-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 38
Std Dev 3.806
Gaus mean : 39.0523
Gaus Width: 3.6199
Entry
ampl
0
2
4
6
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0
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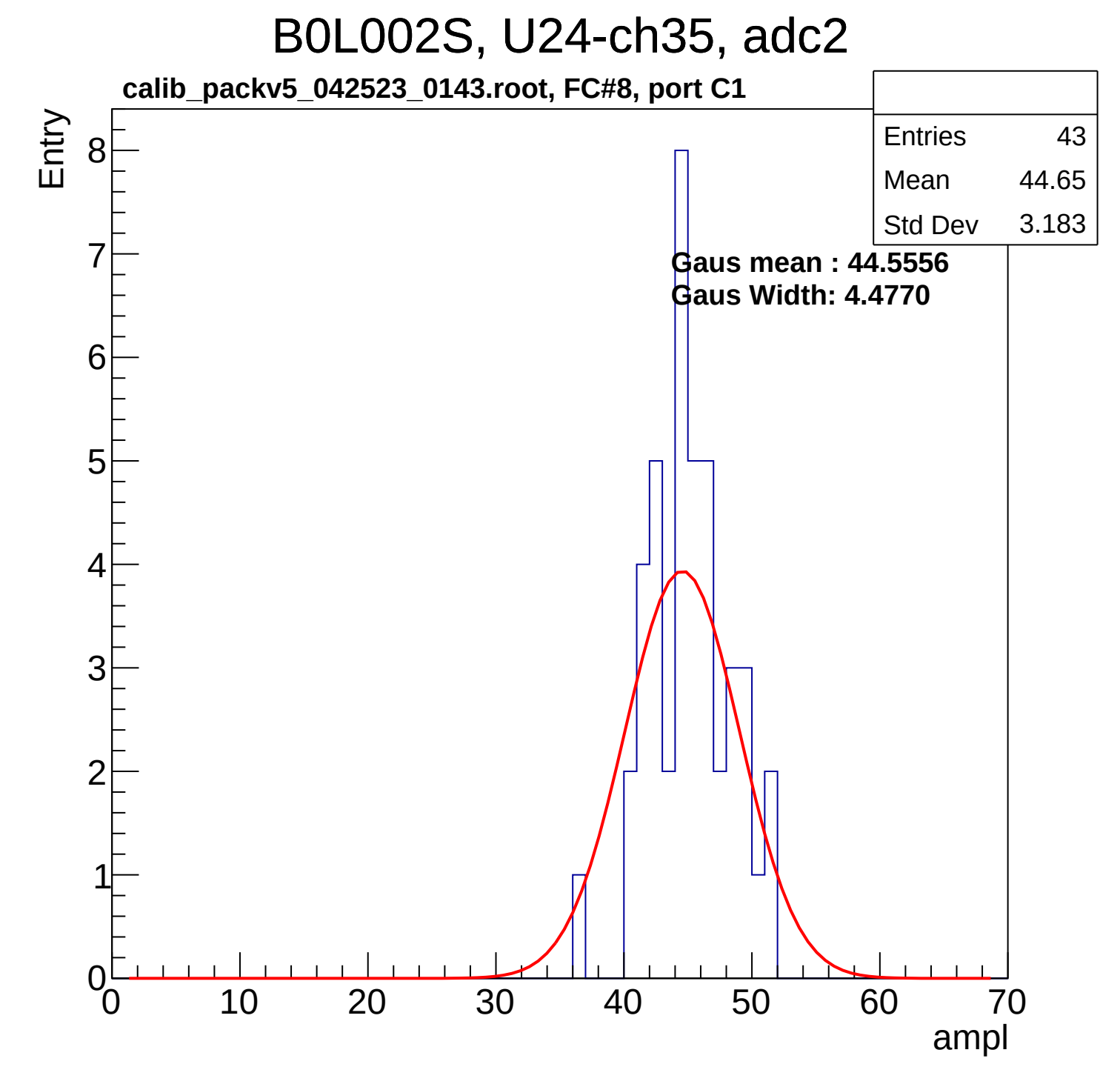
B0L002S, U24-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 44.65
Std Dev 3.183
Gaus mean : 44.5556
Gaus Width: 4.4770
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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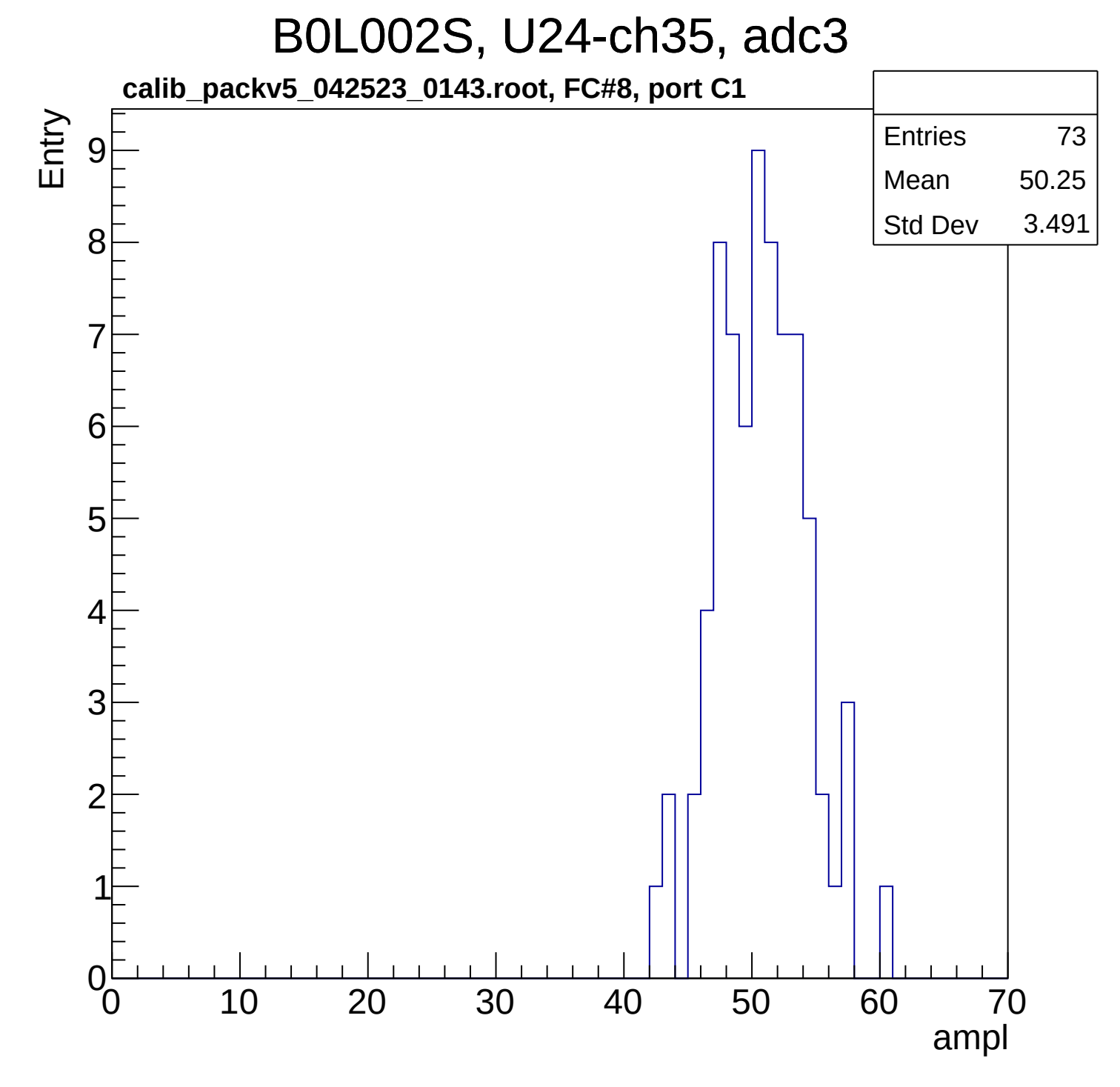
B0L002S, U24-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.25
Std Dev 3.491
Entry
ampl
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2
3
4
5
6
7
8
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0
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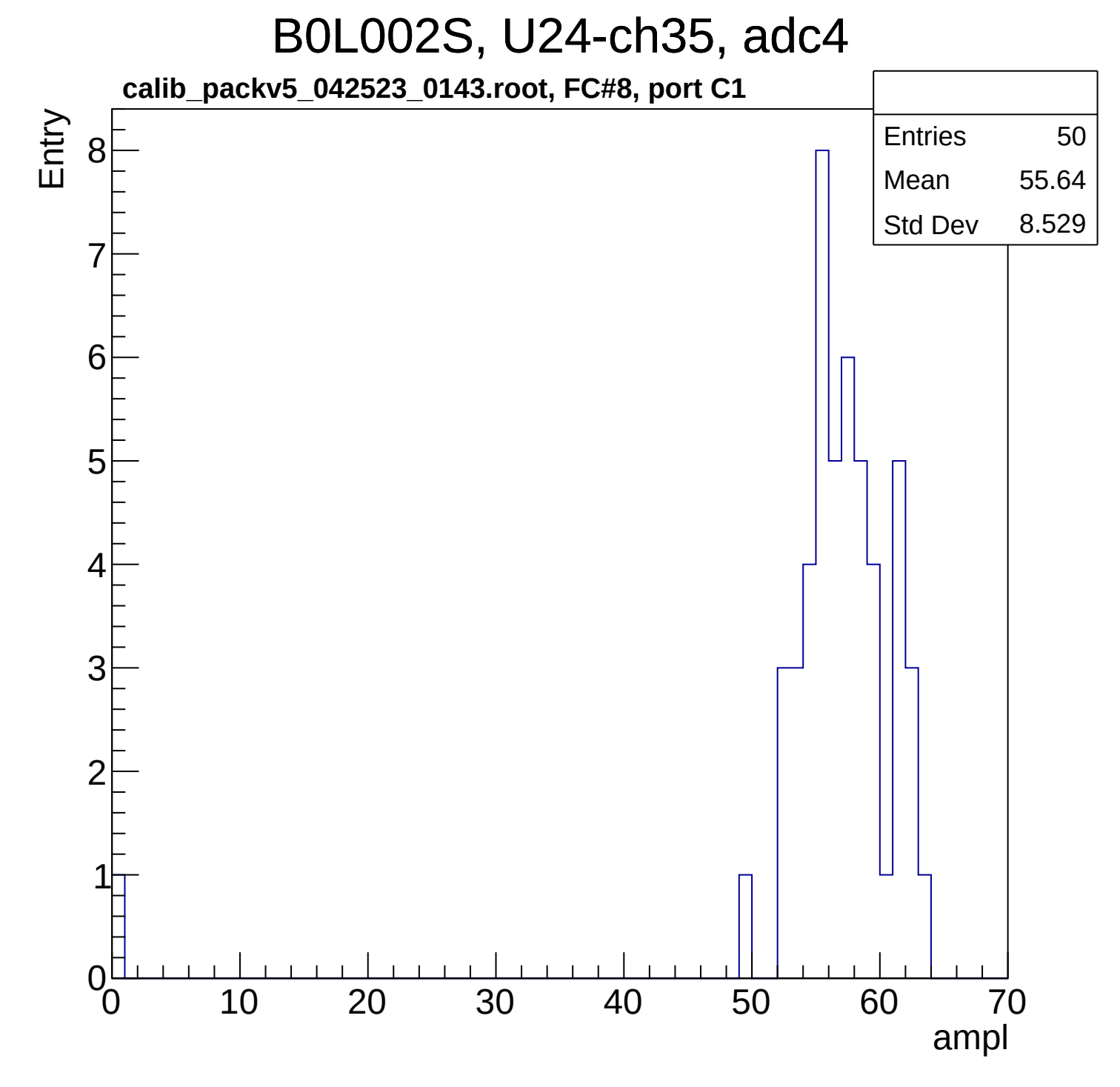
B0L002S, U24-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 55.64
Std Dev 8.529
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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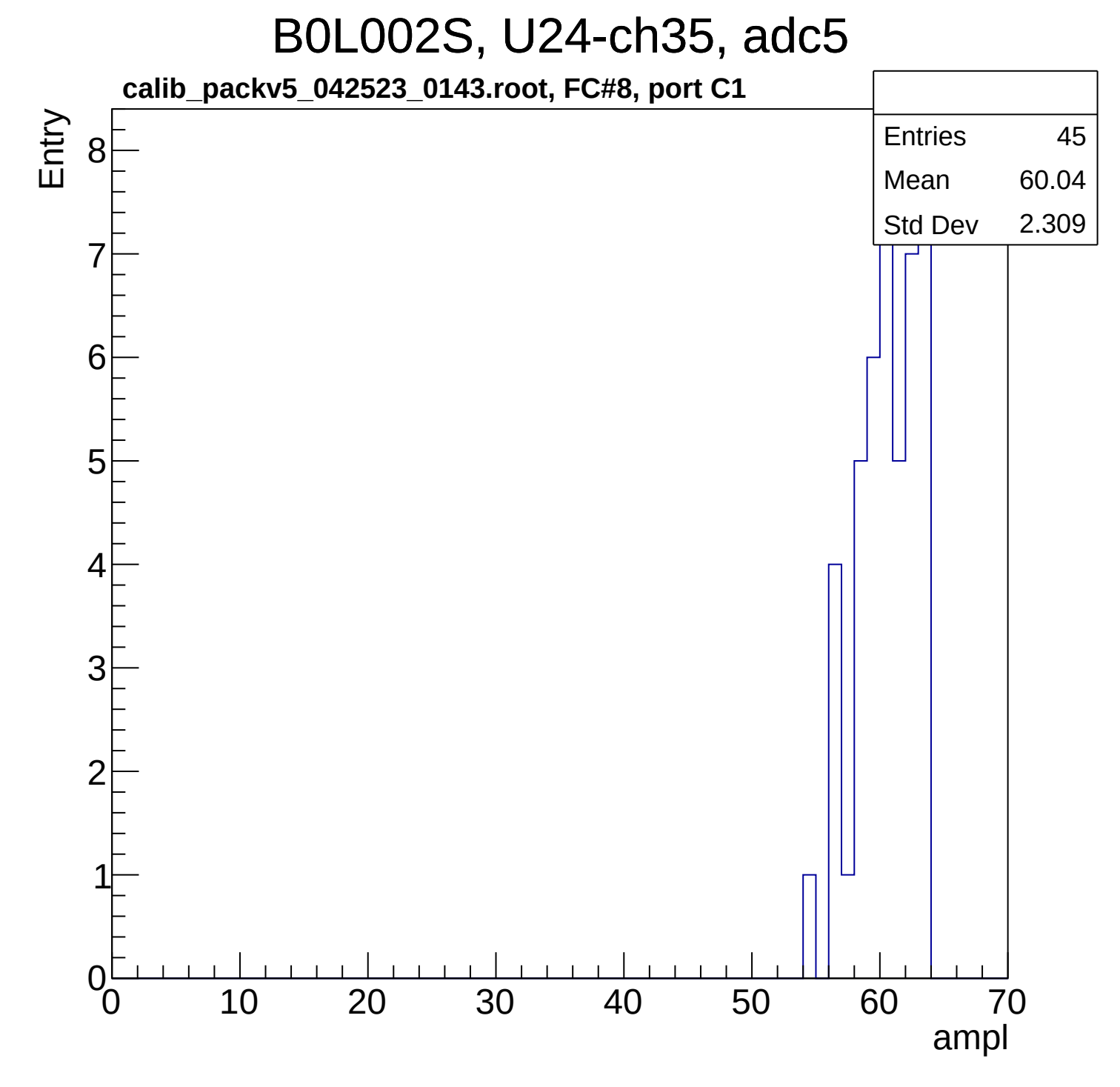
B0L002S, U24-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 60.04
Std Dev 2.309
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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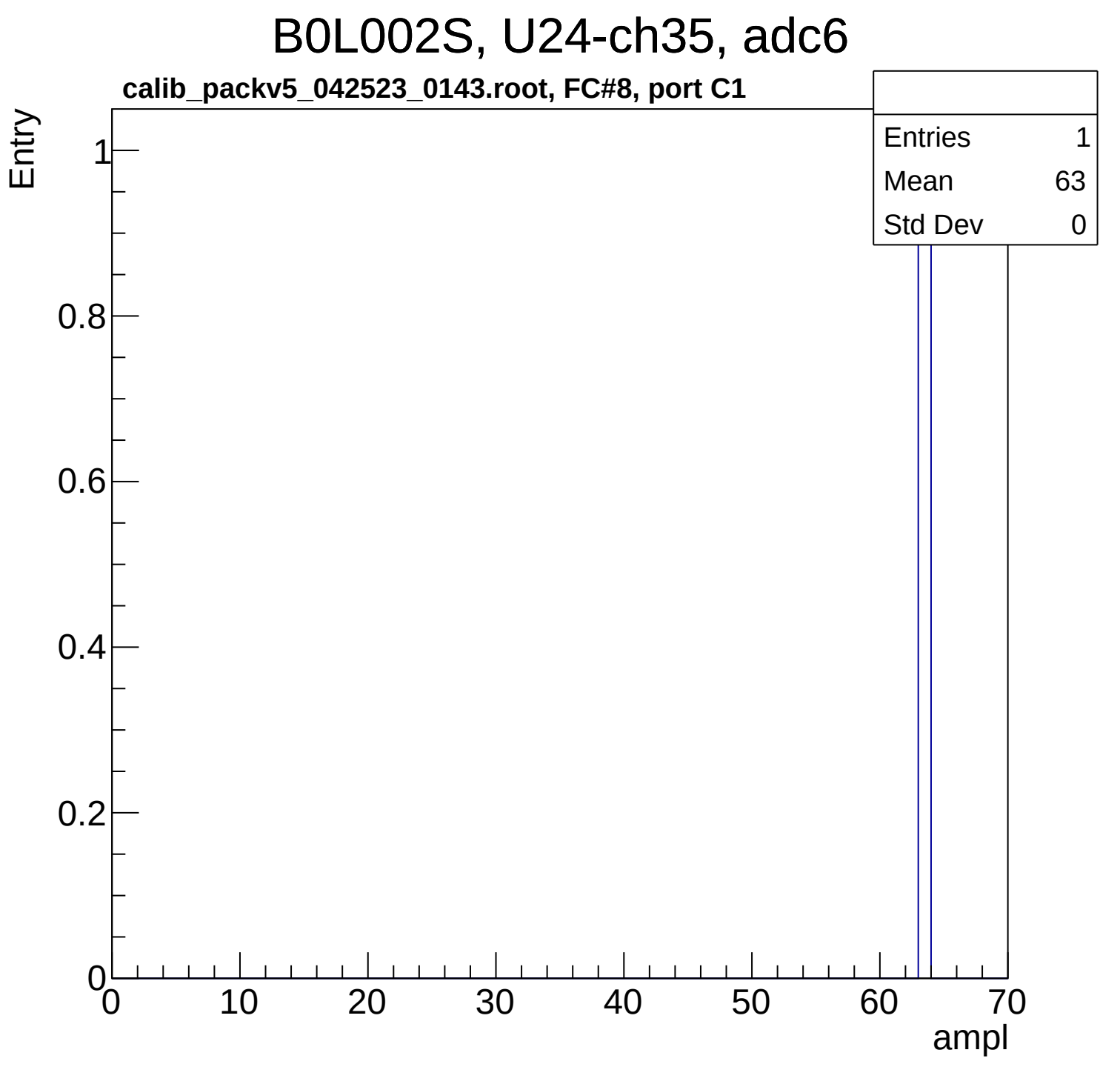
B0L002S, U24-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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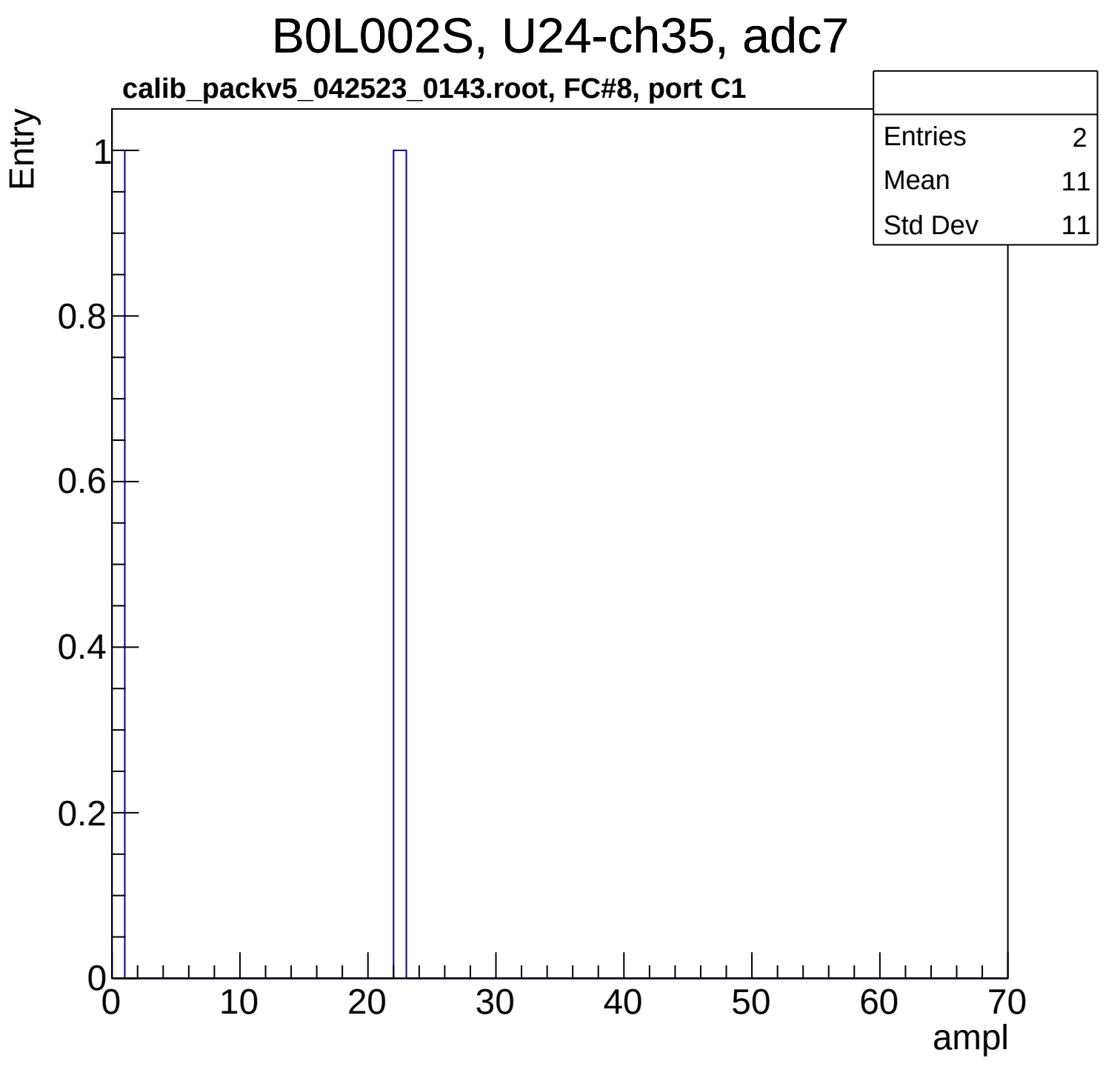
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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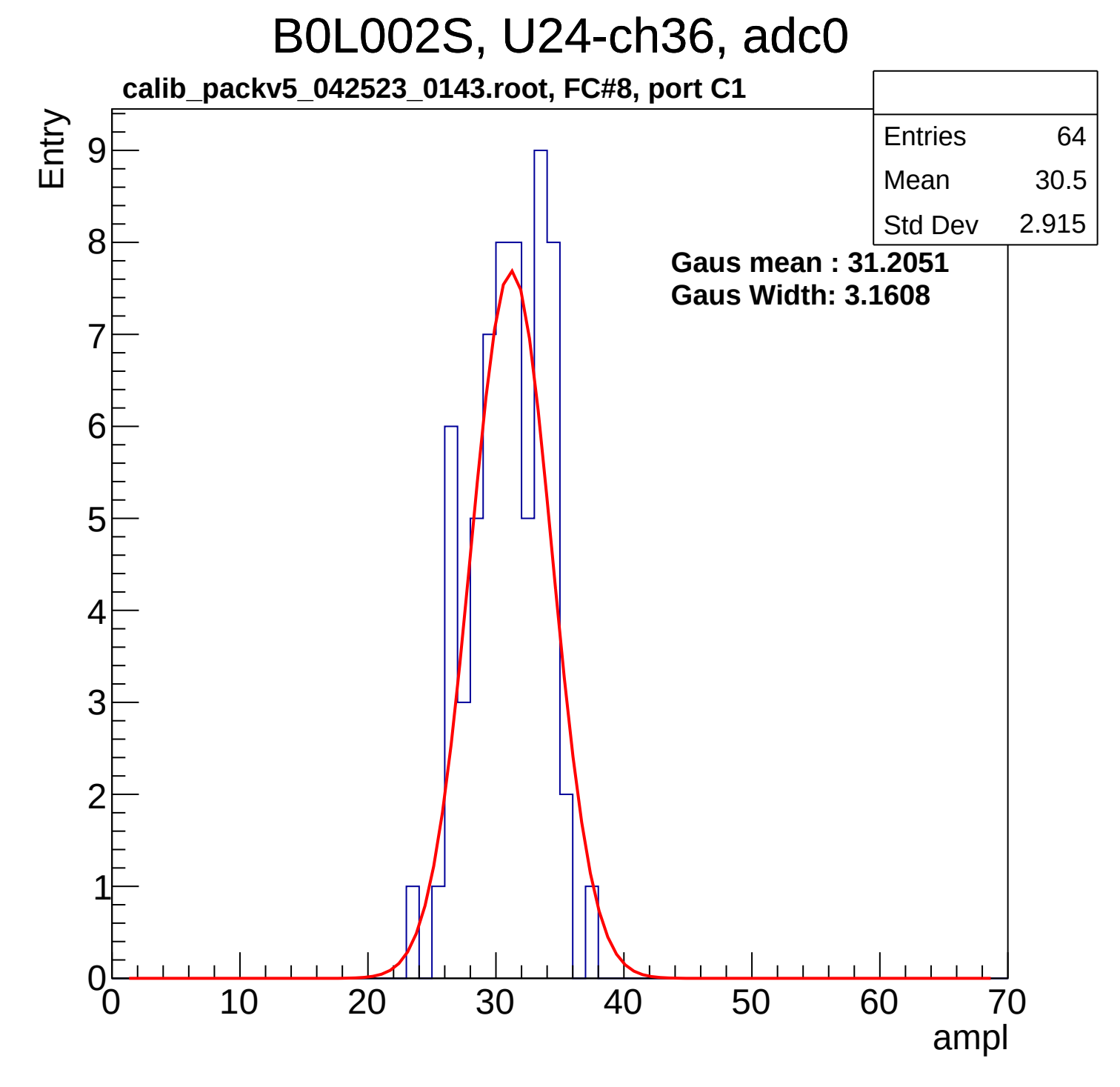
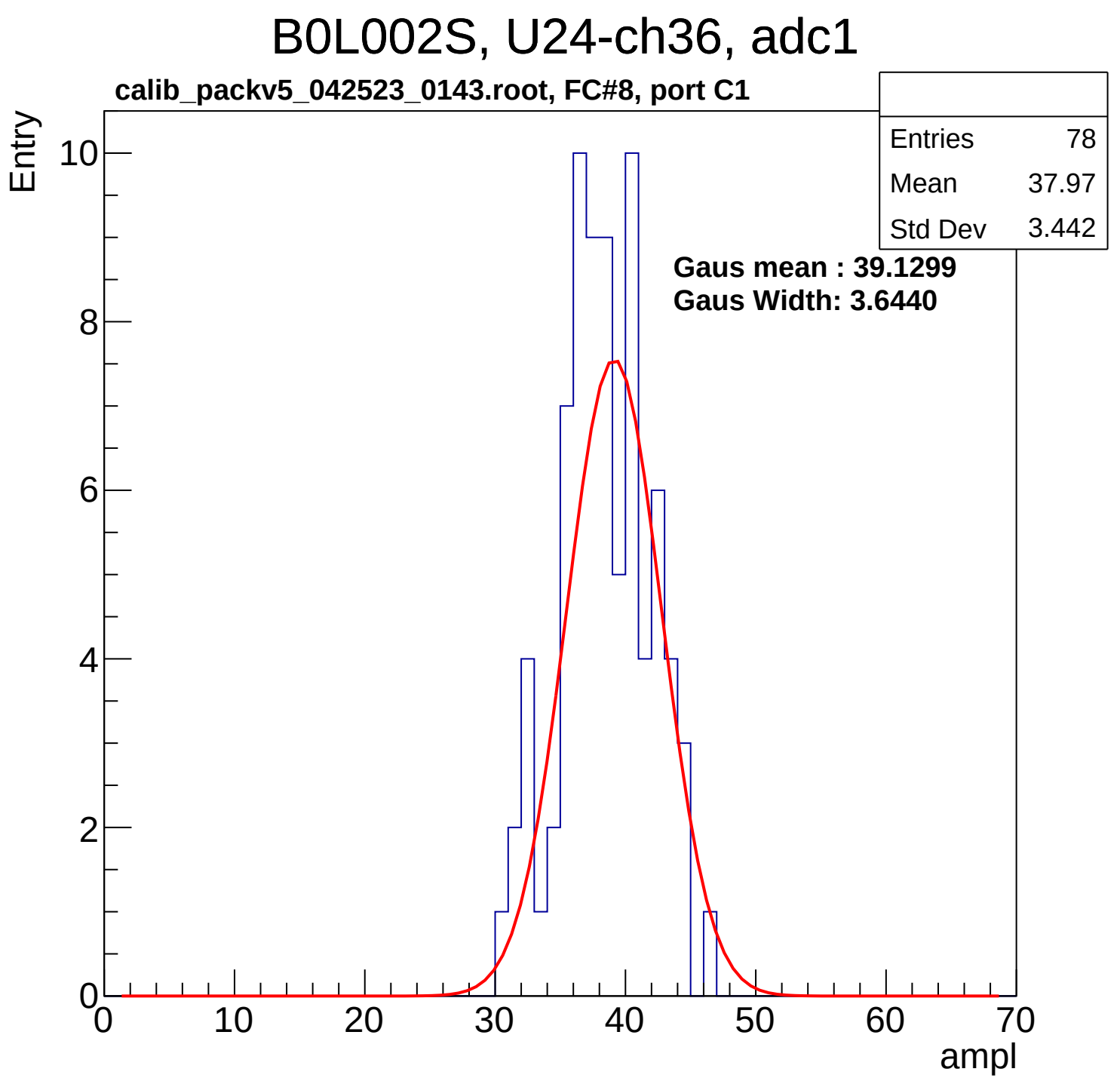
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.5
Std Dev 2.915
Gaus mean : 31.2051
Gaus Width: 3.1608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.97
Std Dev 3.442
Gaus mean : 39.1299
Gaus Width: 3.6440
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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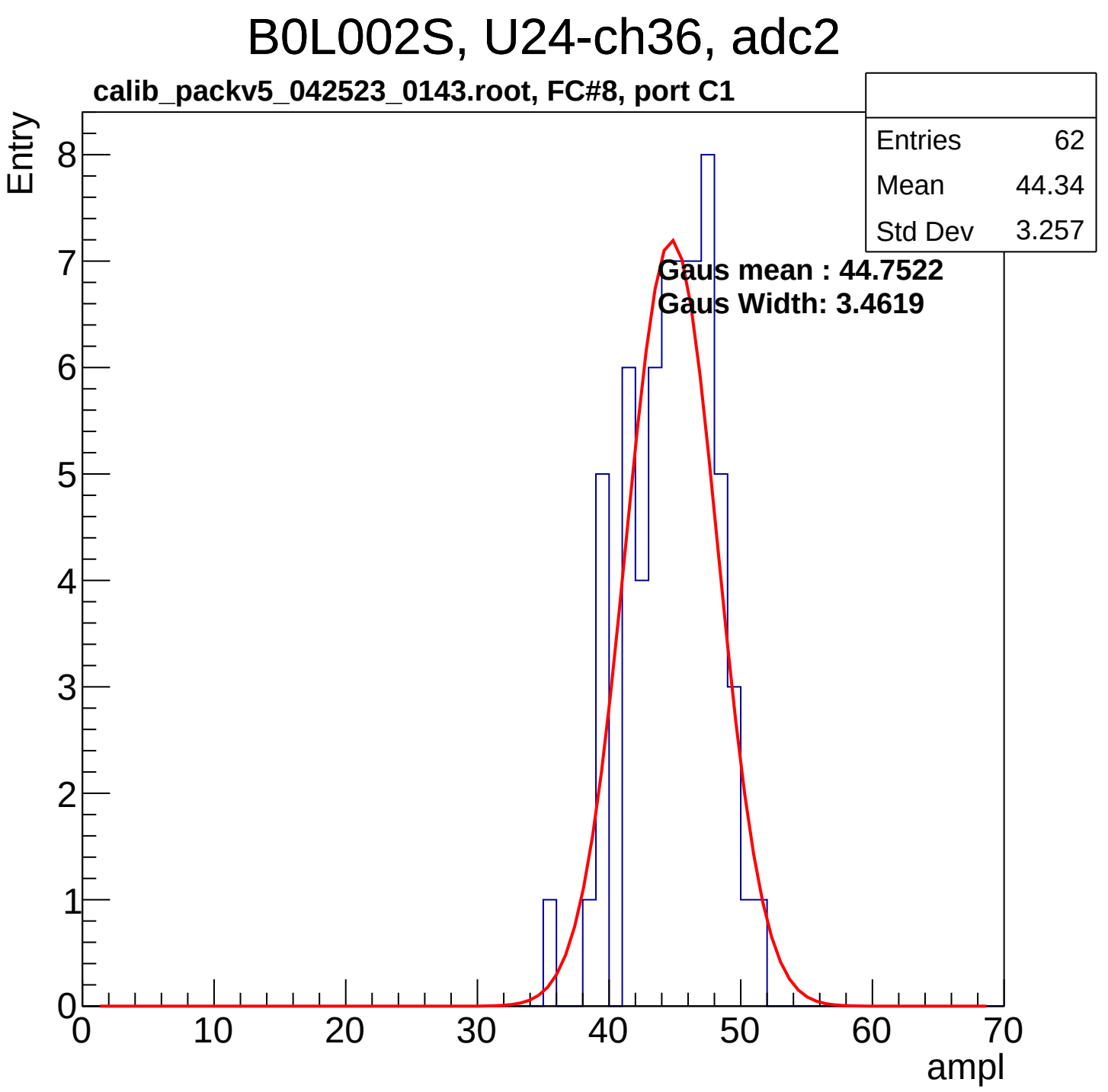
B0L002S, U24-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.34
Std Dev 3.257
Gaus mean : 44.7522
Gaus Width: 3.4619
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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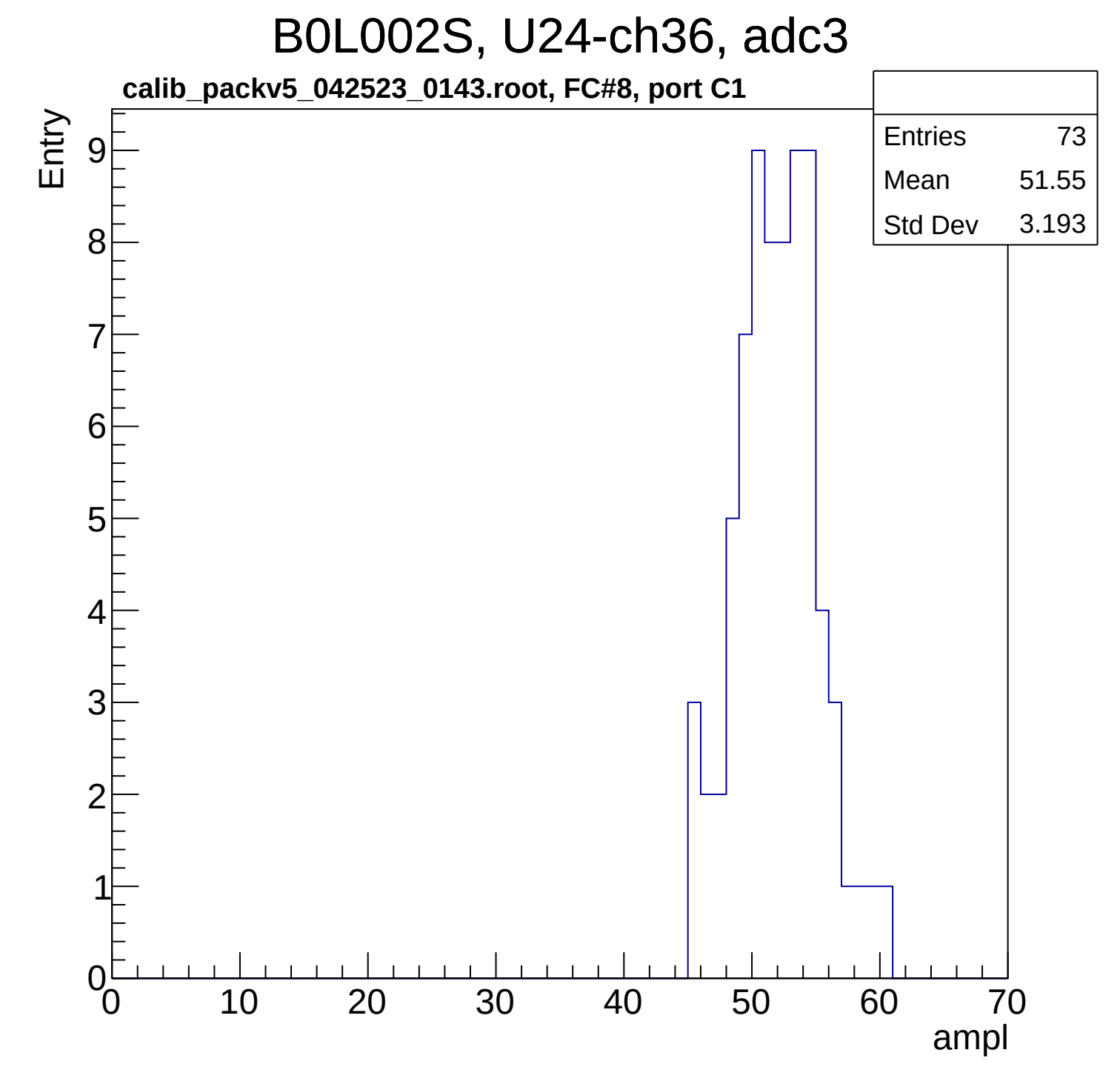
B0L002S, U24-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.55
Std Dev 3.193
Entry
ampl
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2
3
4
5
6
7
8
9
0
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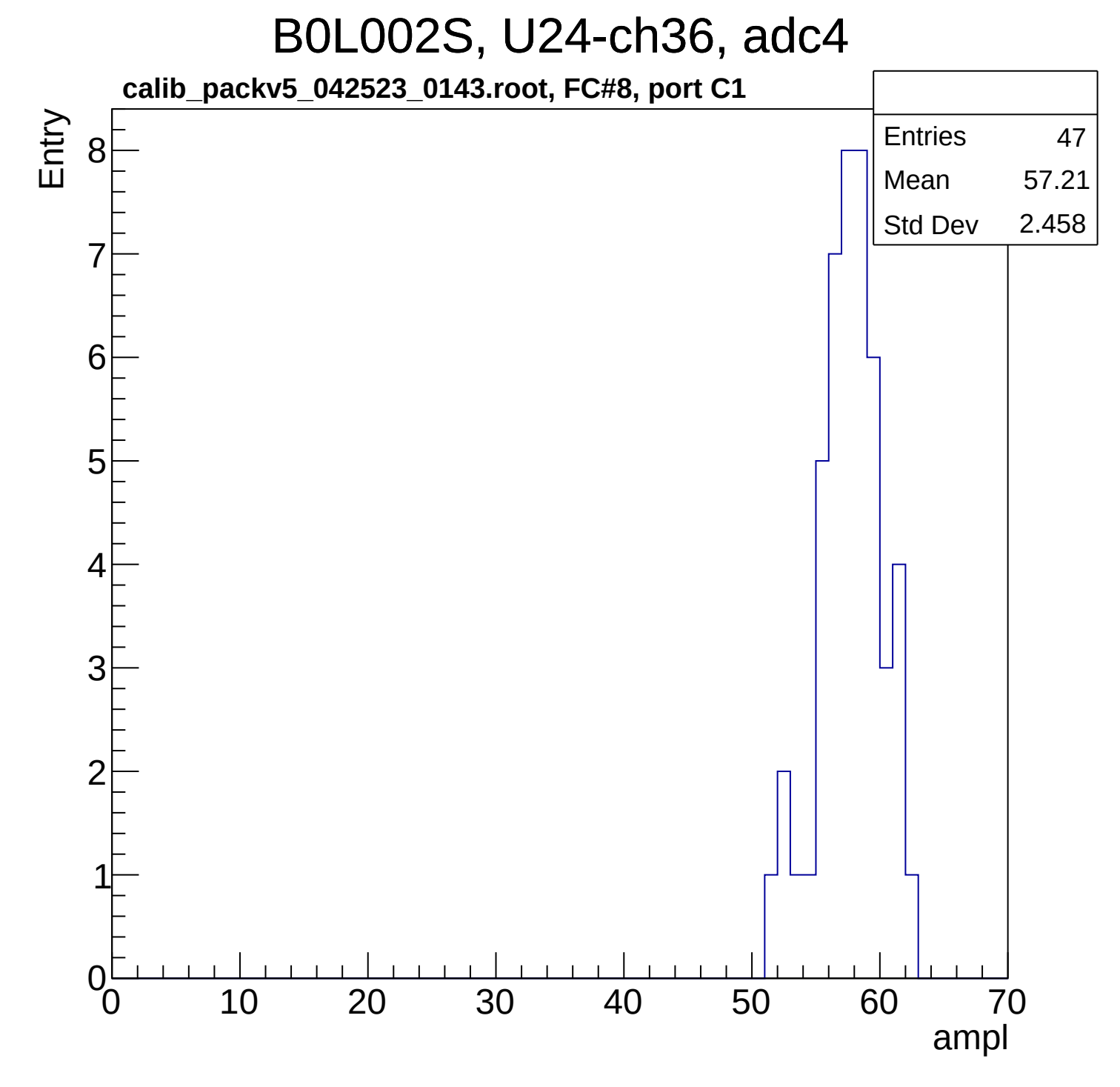
B0L002S, U24-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.21
Std Dev 2.458
Entry
ampl
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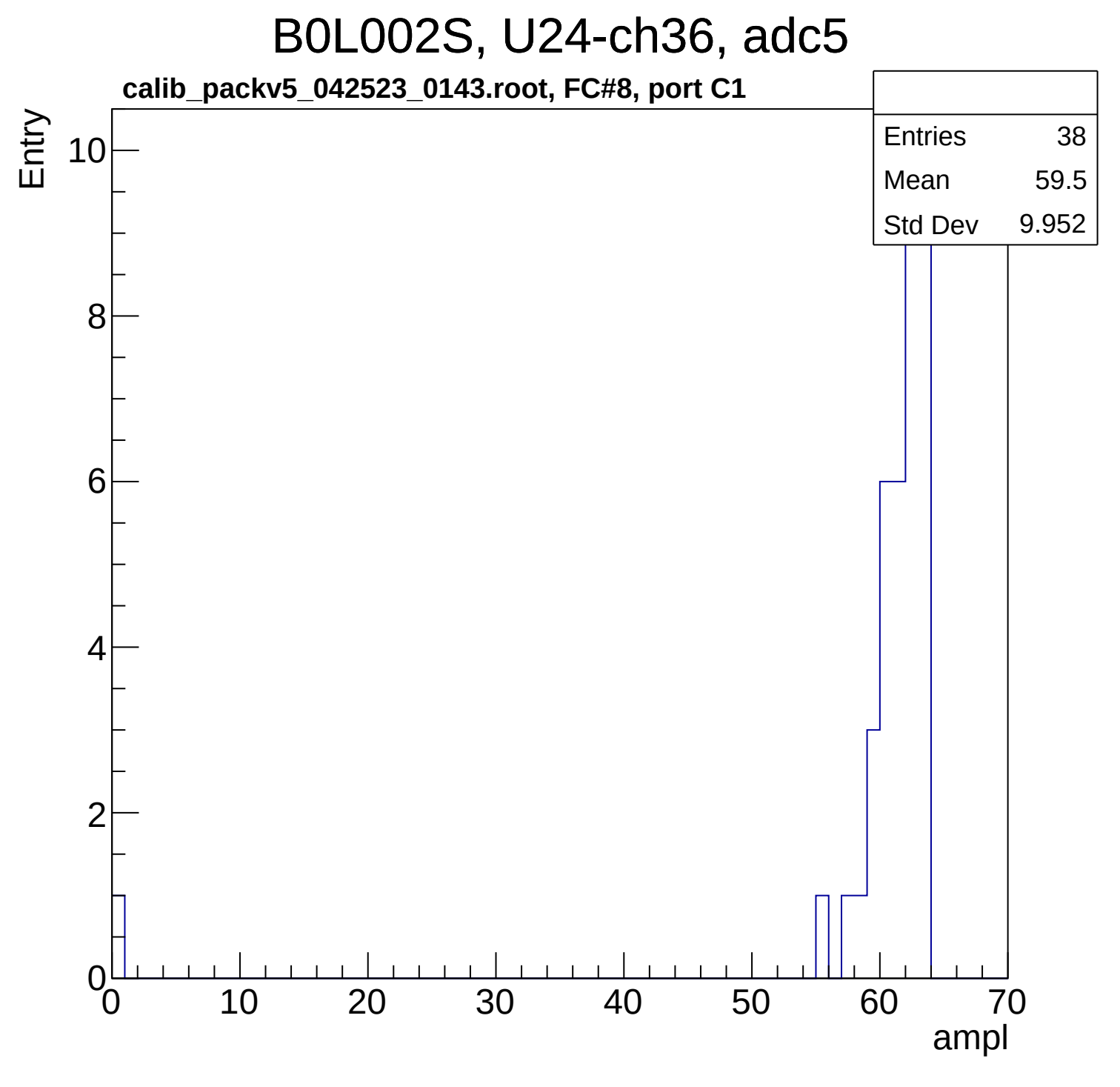
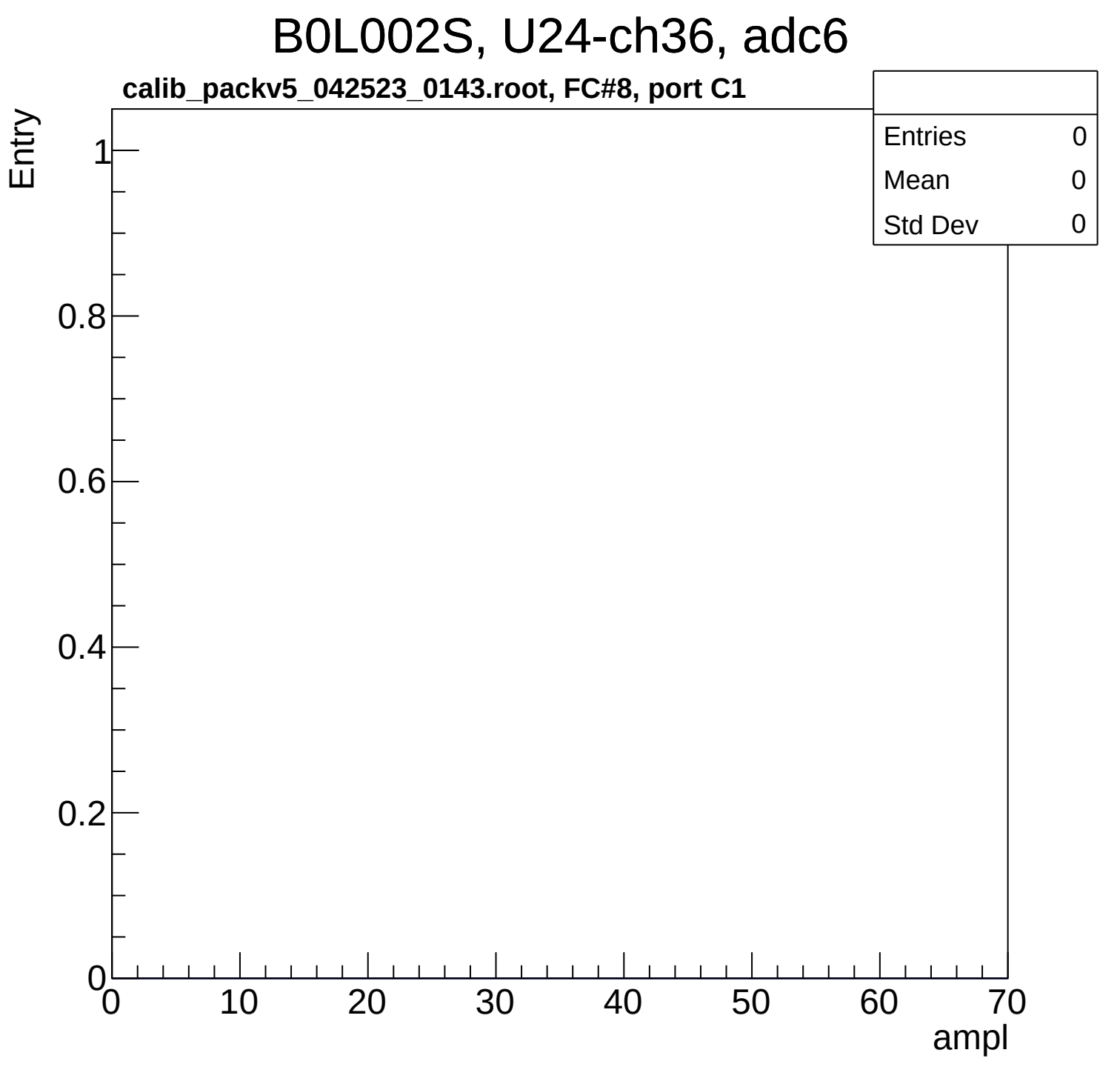
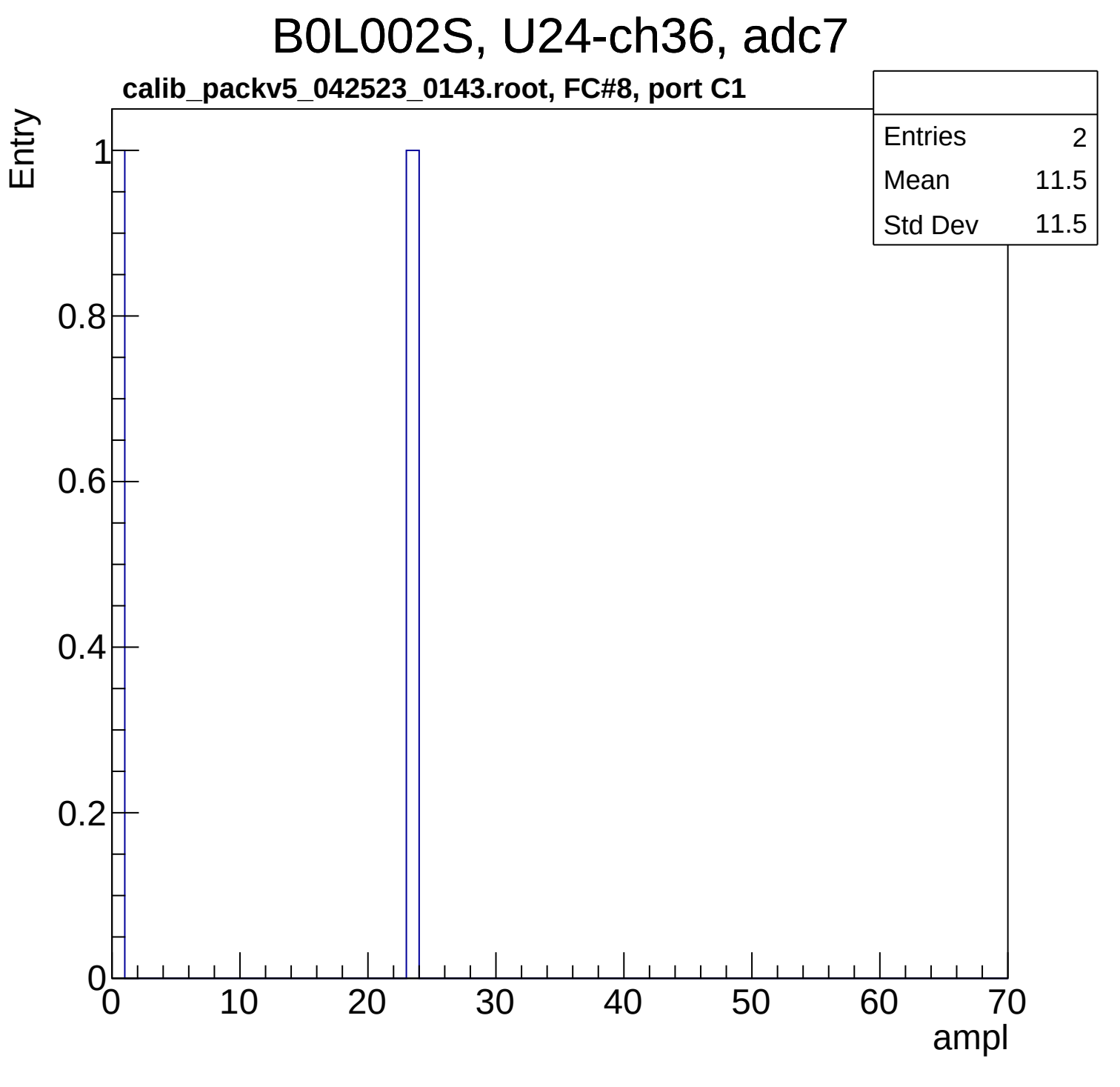
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.5
Std Dev 9.952
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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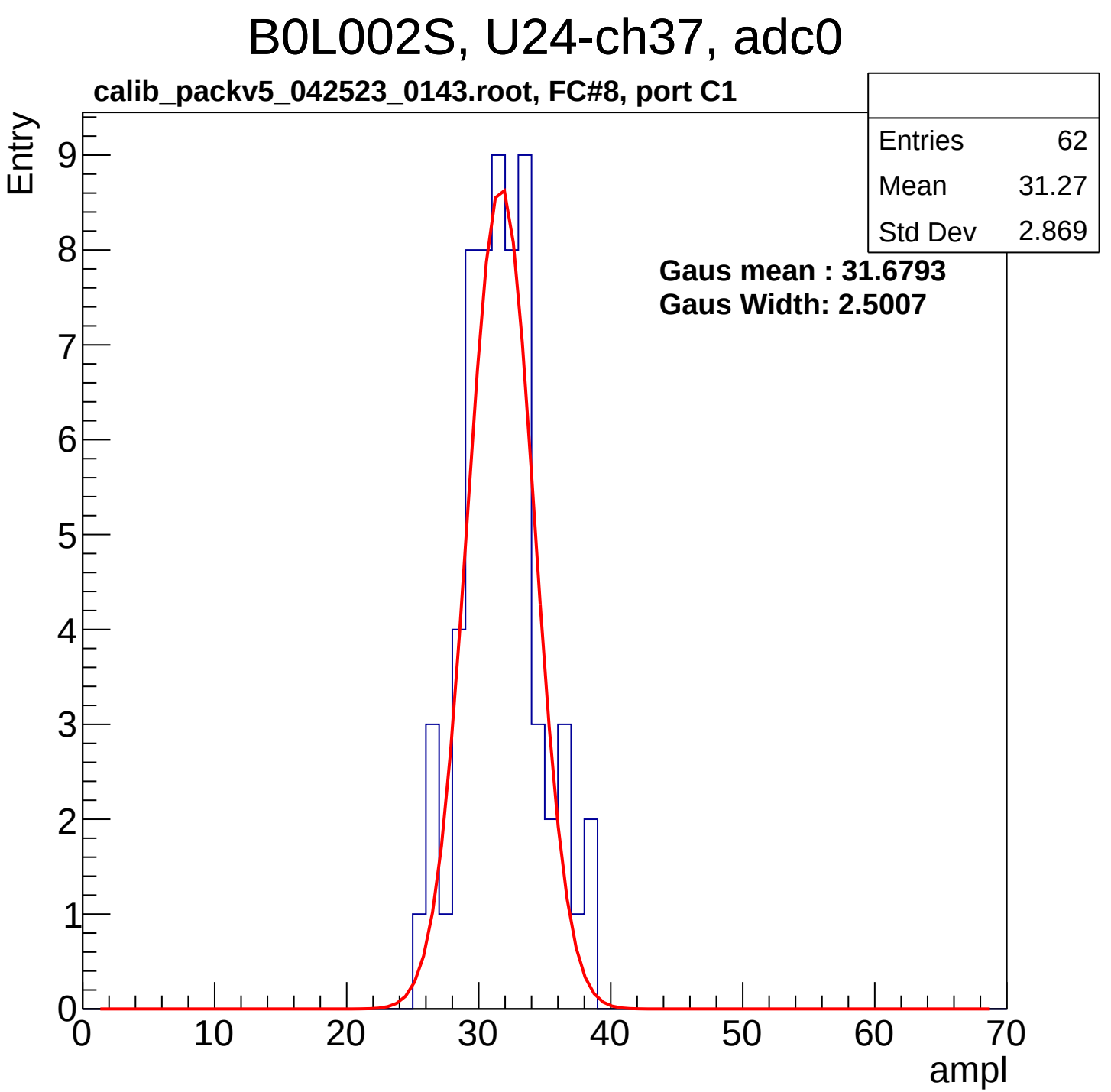
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.27
Std Dev 2.869
Gaus mean : 31.6793
Gaus Width: 2.5007
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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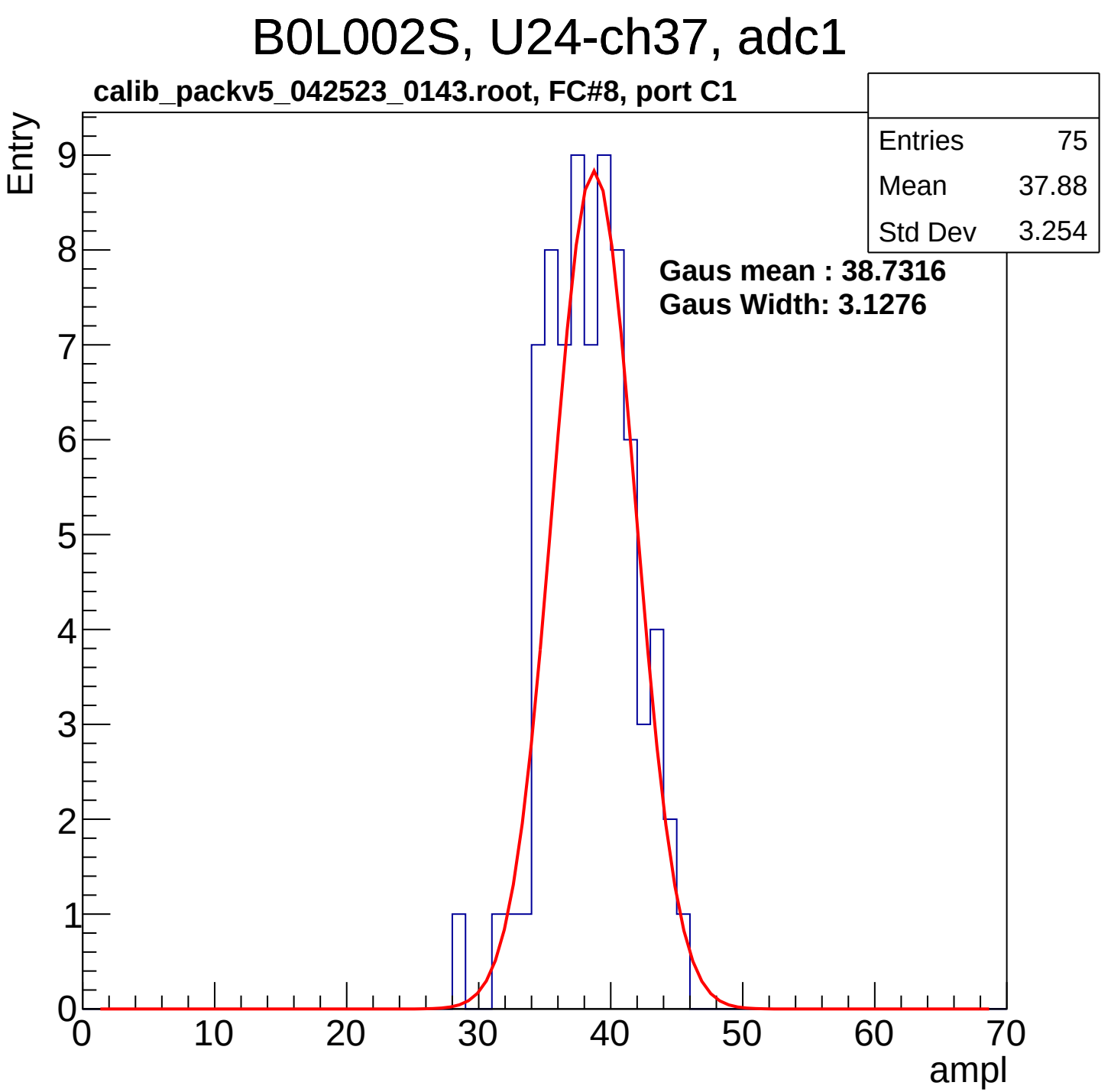
B0L002S, U24-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.88
Std Dev 3.254
Gaus mean : 38.7316
Gaus Width: 3.1276
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
30
40
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60
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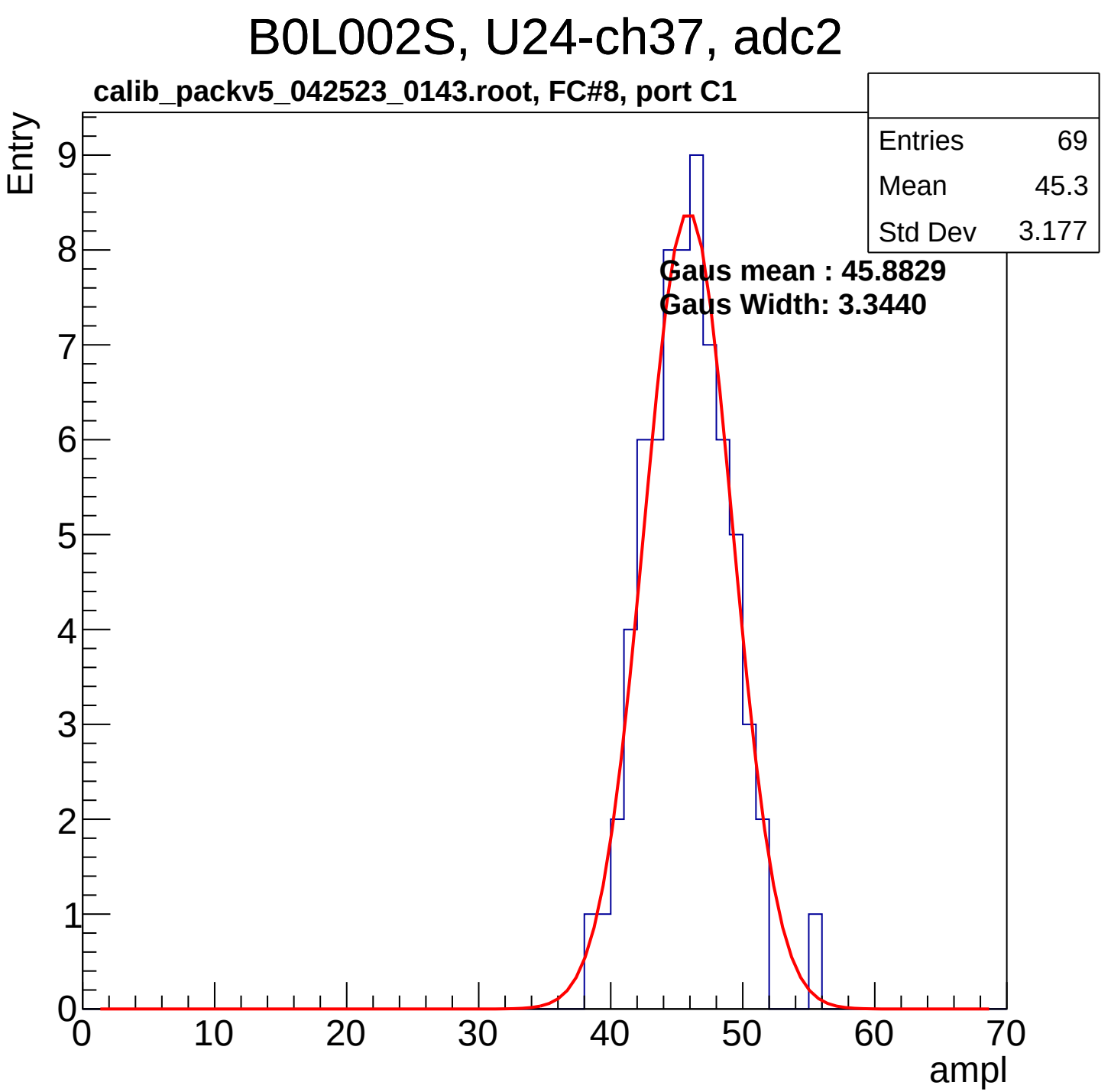
B0L002S, U24-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.3
Std Dev 3.177
Gaus mean : 45.8829
Gaus Width: 3.3440
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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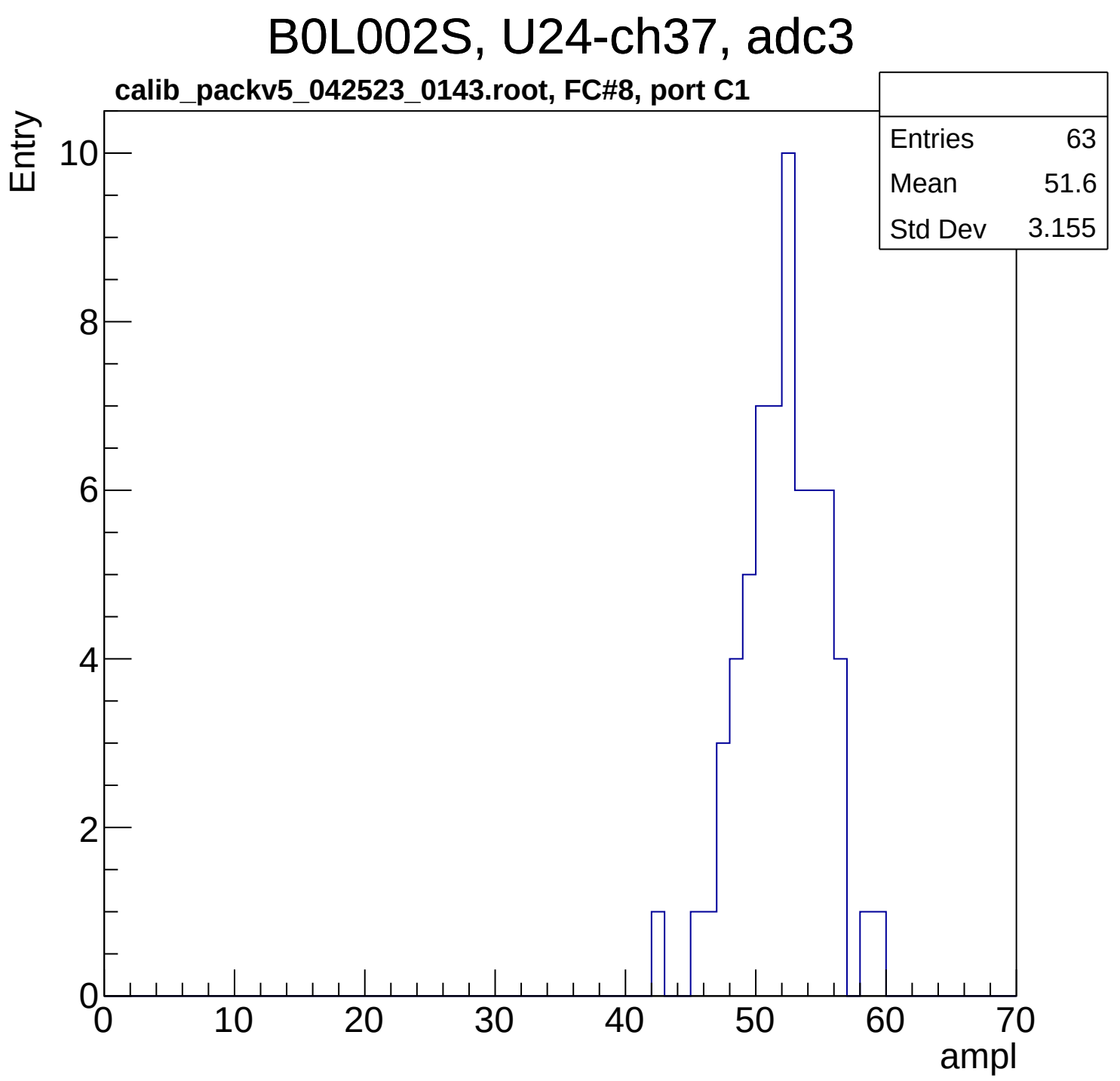
B0L002S, U24-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.6
Std Dev 3.155
Entry
ampl
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0
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60
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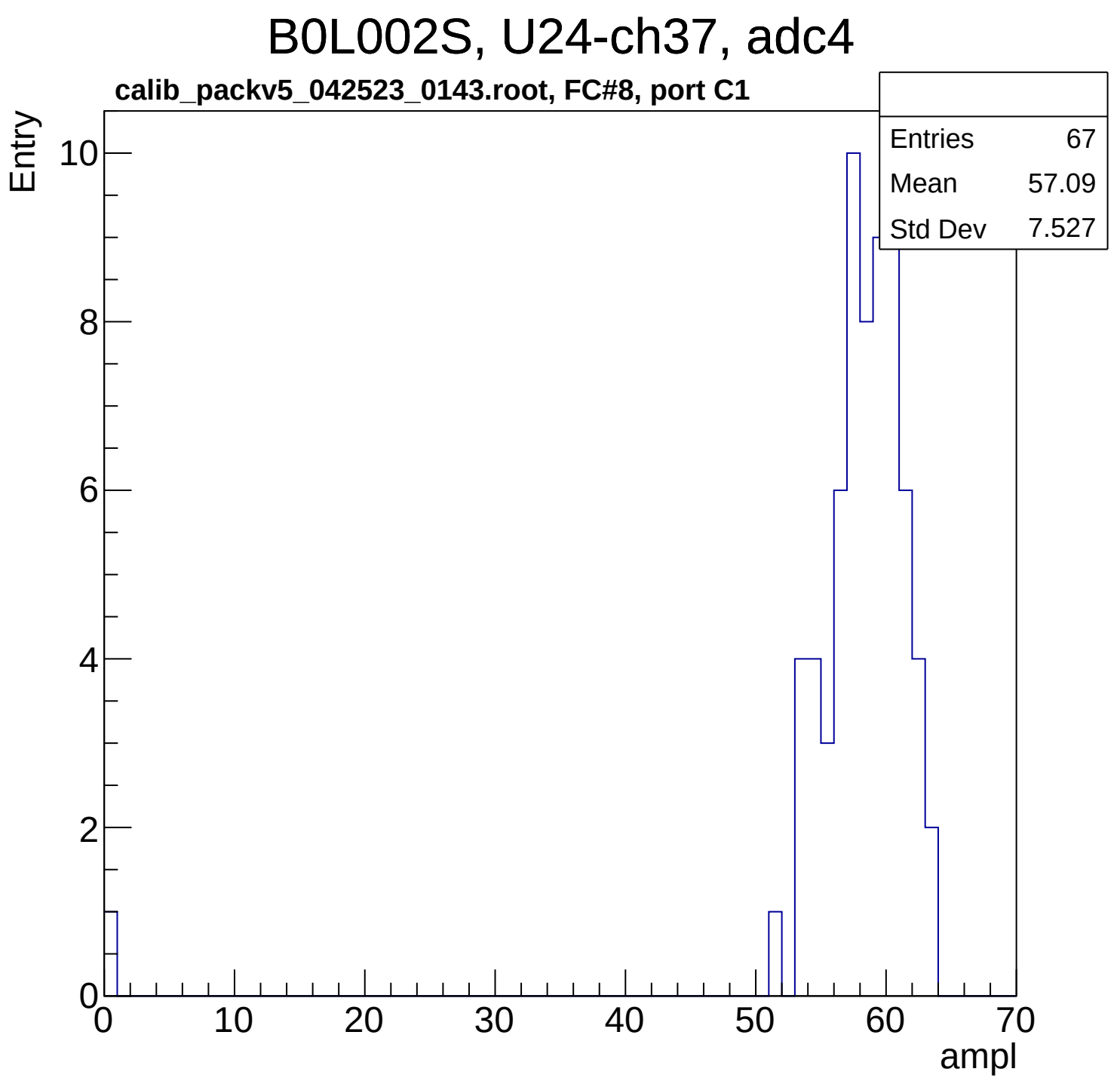
B0L002S, U24-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 57.09
Std Dev 7.527
Entry
ampl
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6
8
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0
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40
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60
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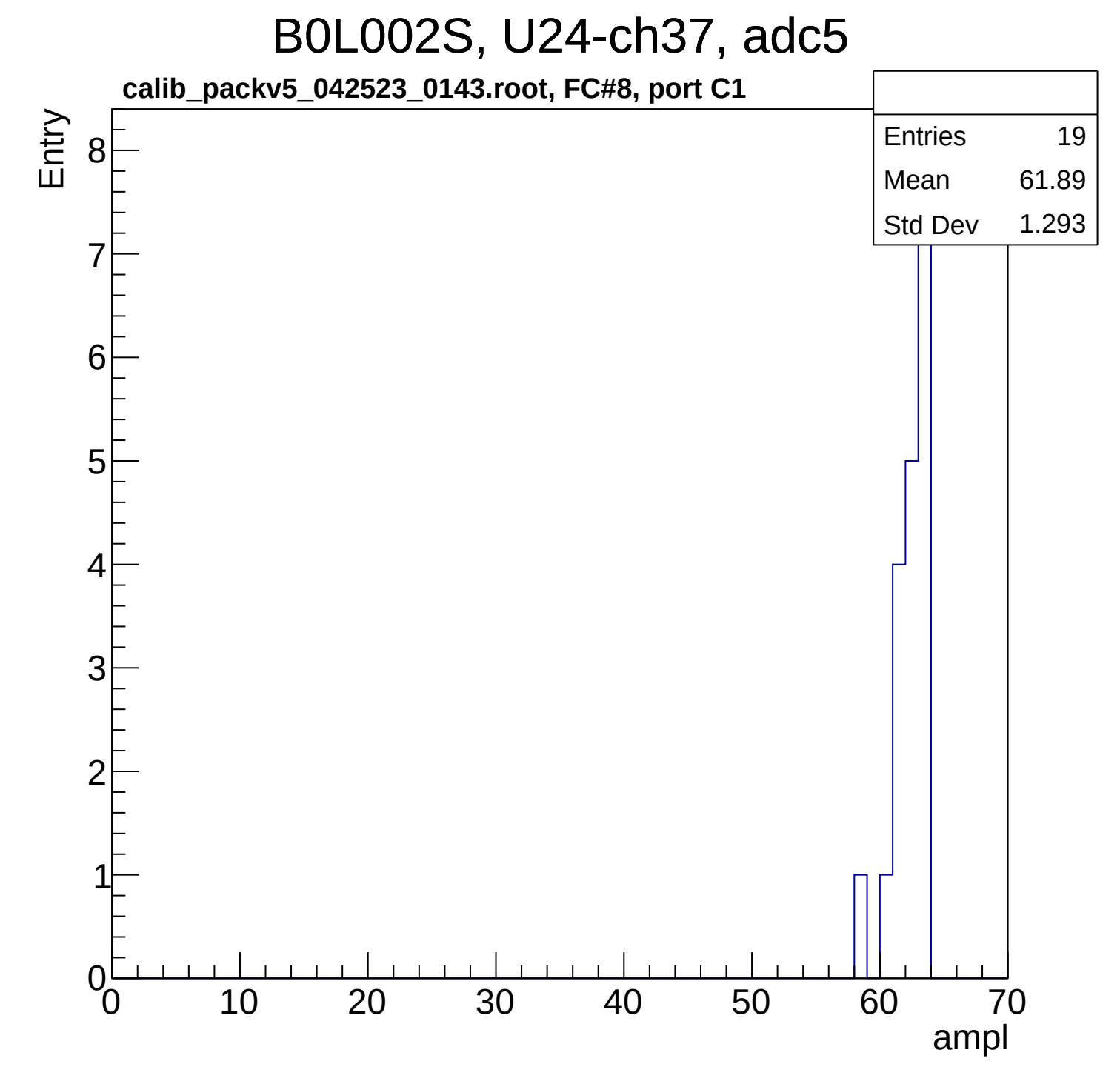
B0L002S, U24-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.89
Std Dev 1.293
Entry
ampl
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4
5
6
7
8
0
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20
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60
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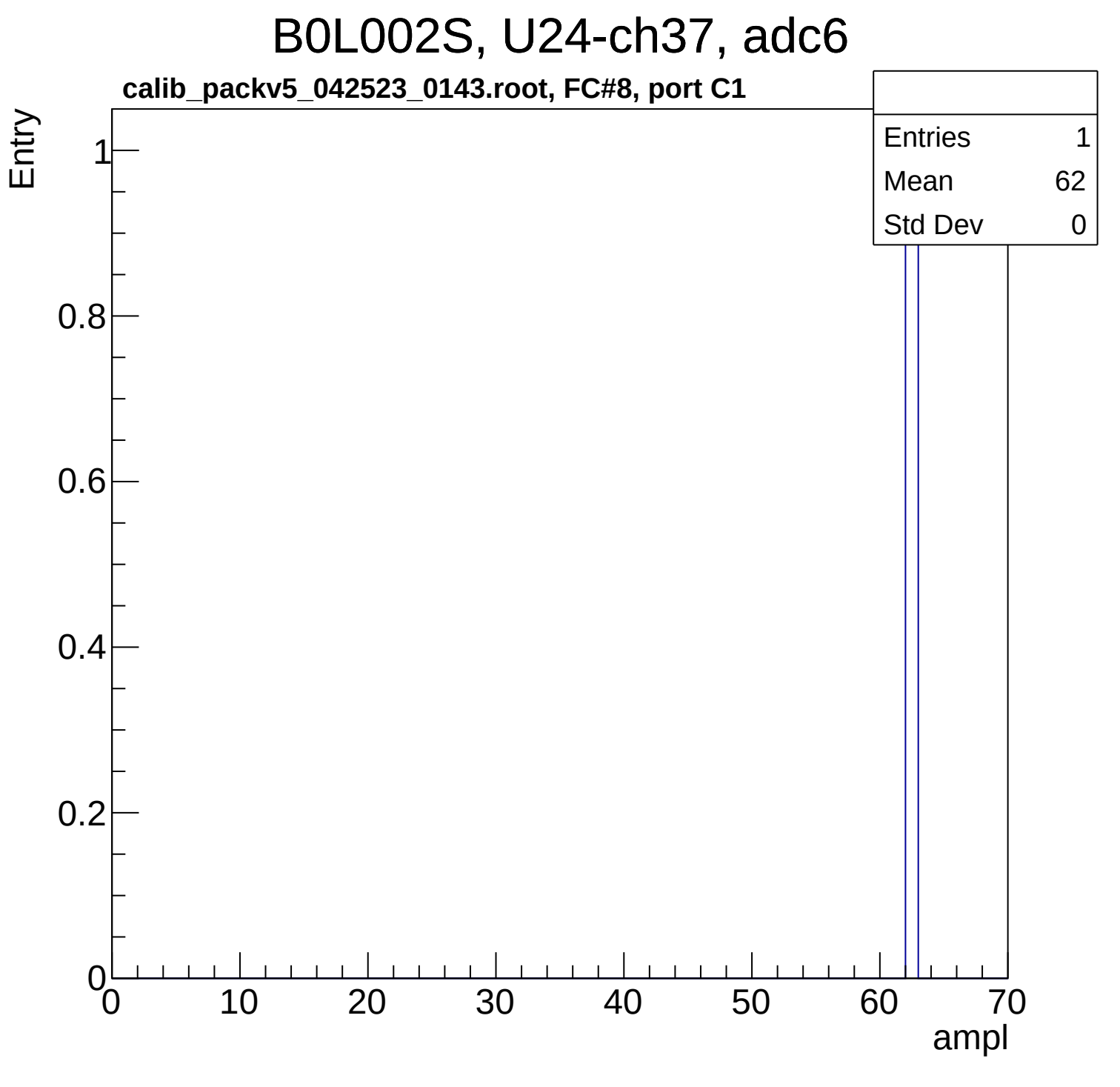
B0L002S, U24-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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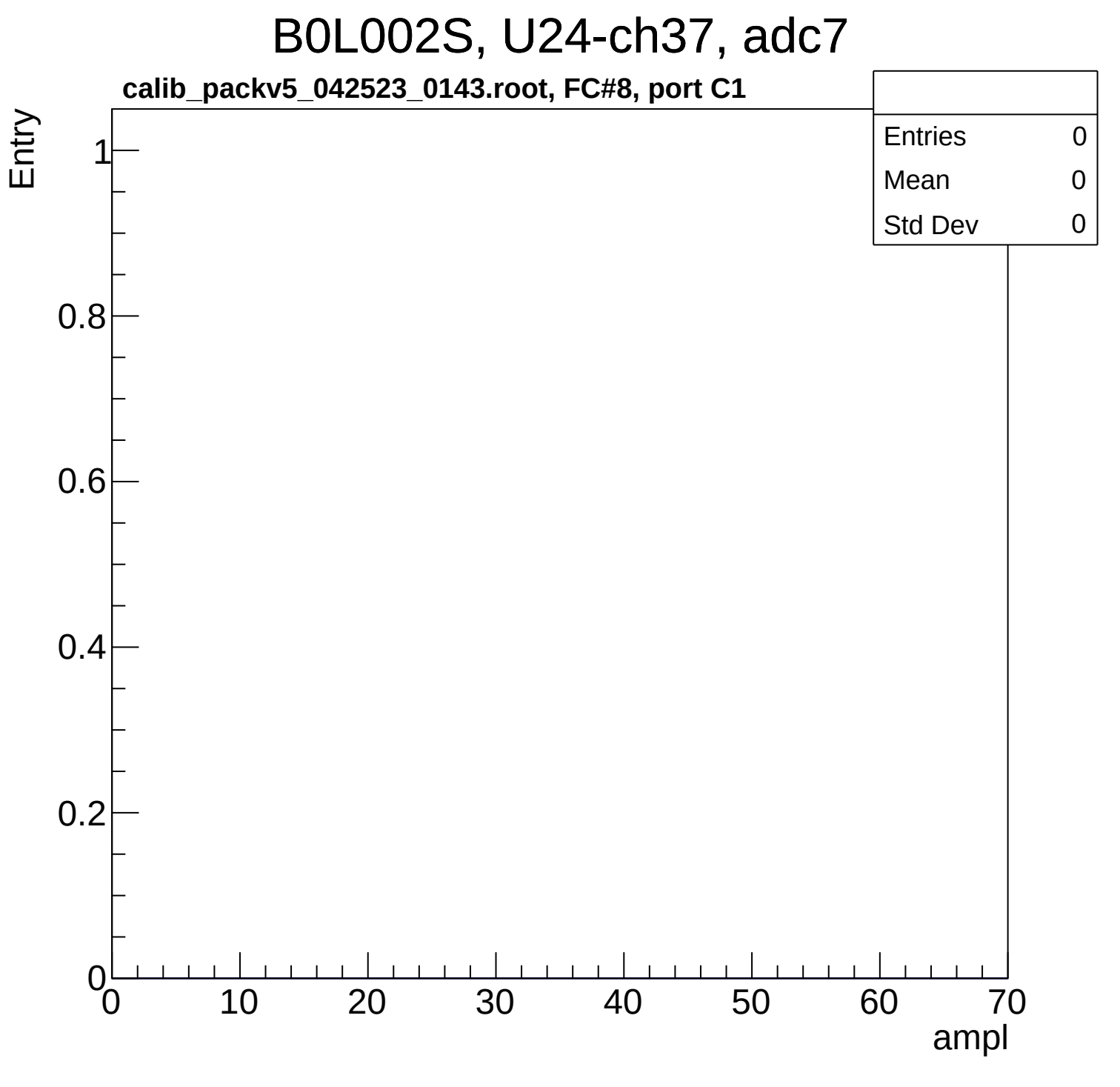
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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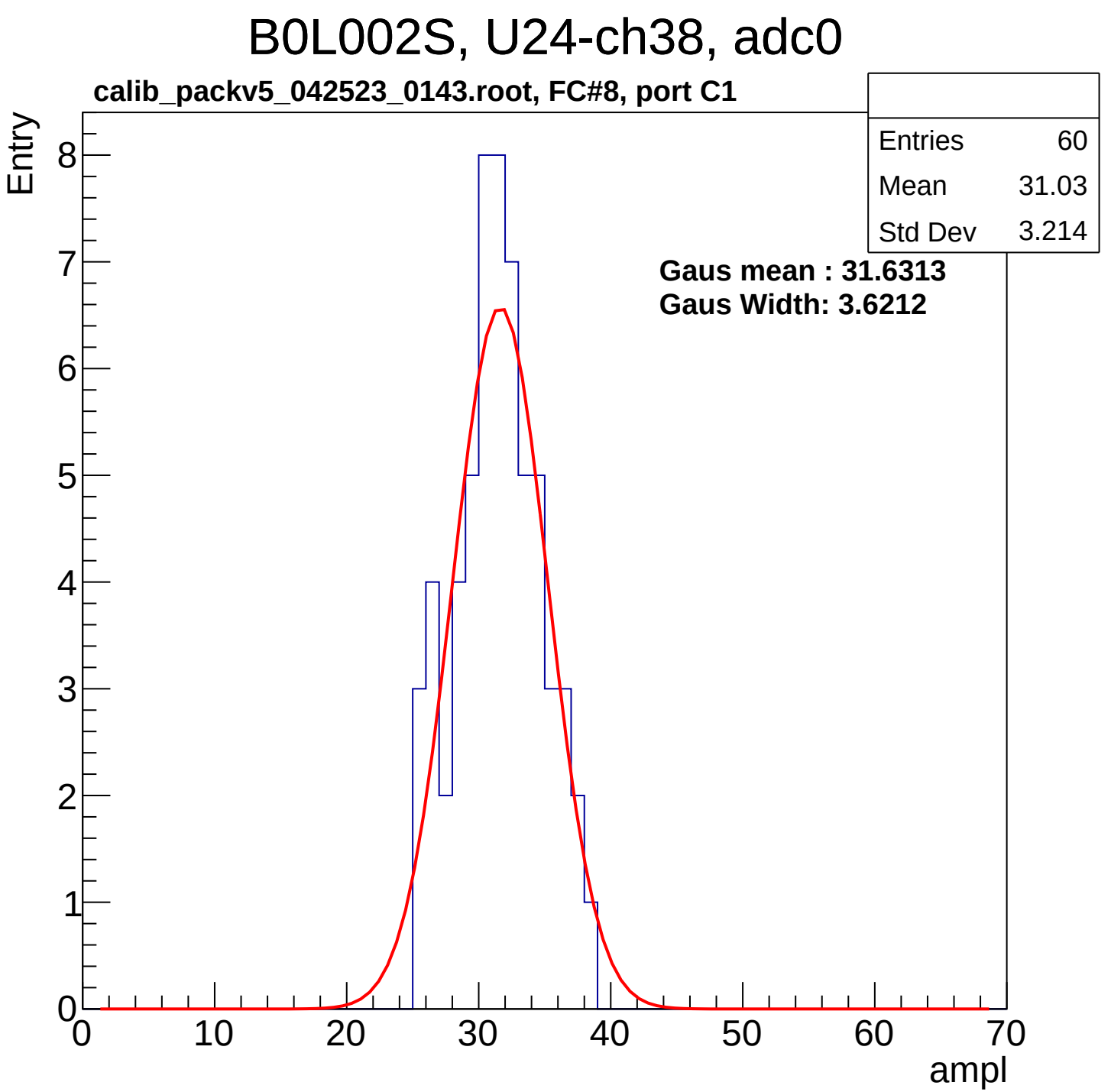
50
60
70
ampl


B0L002S, U24-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.03
Std Dev 3.214
Gaus mean : 31.6313
Gaus Width: 3.6212
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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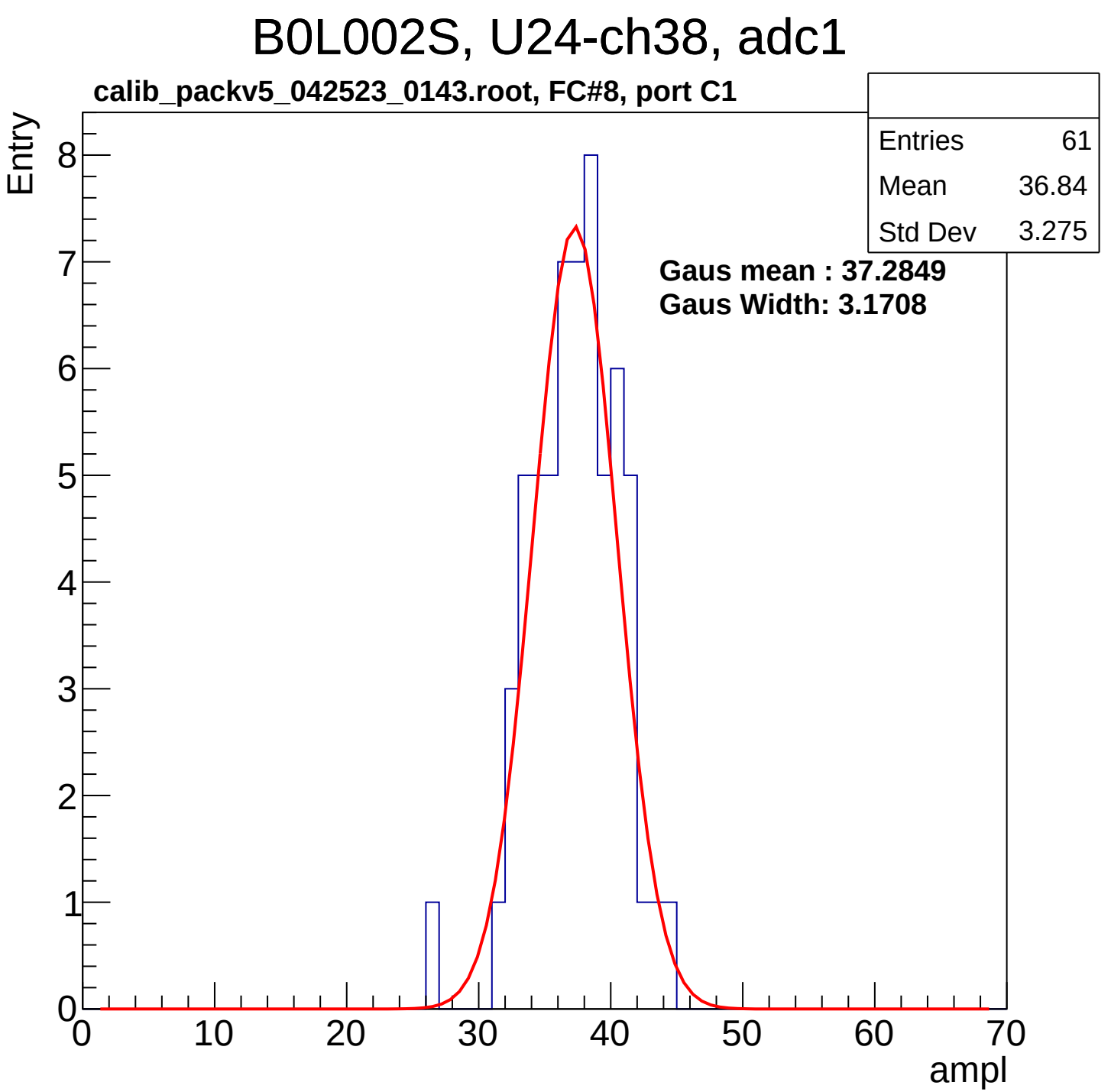
B0L002S, U24-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.84
Std Dev 3.275
Gaus mean : 37.2849
Gaus Width: 3.1708
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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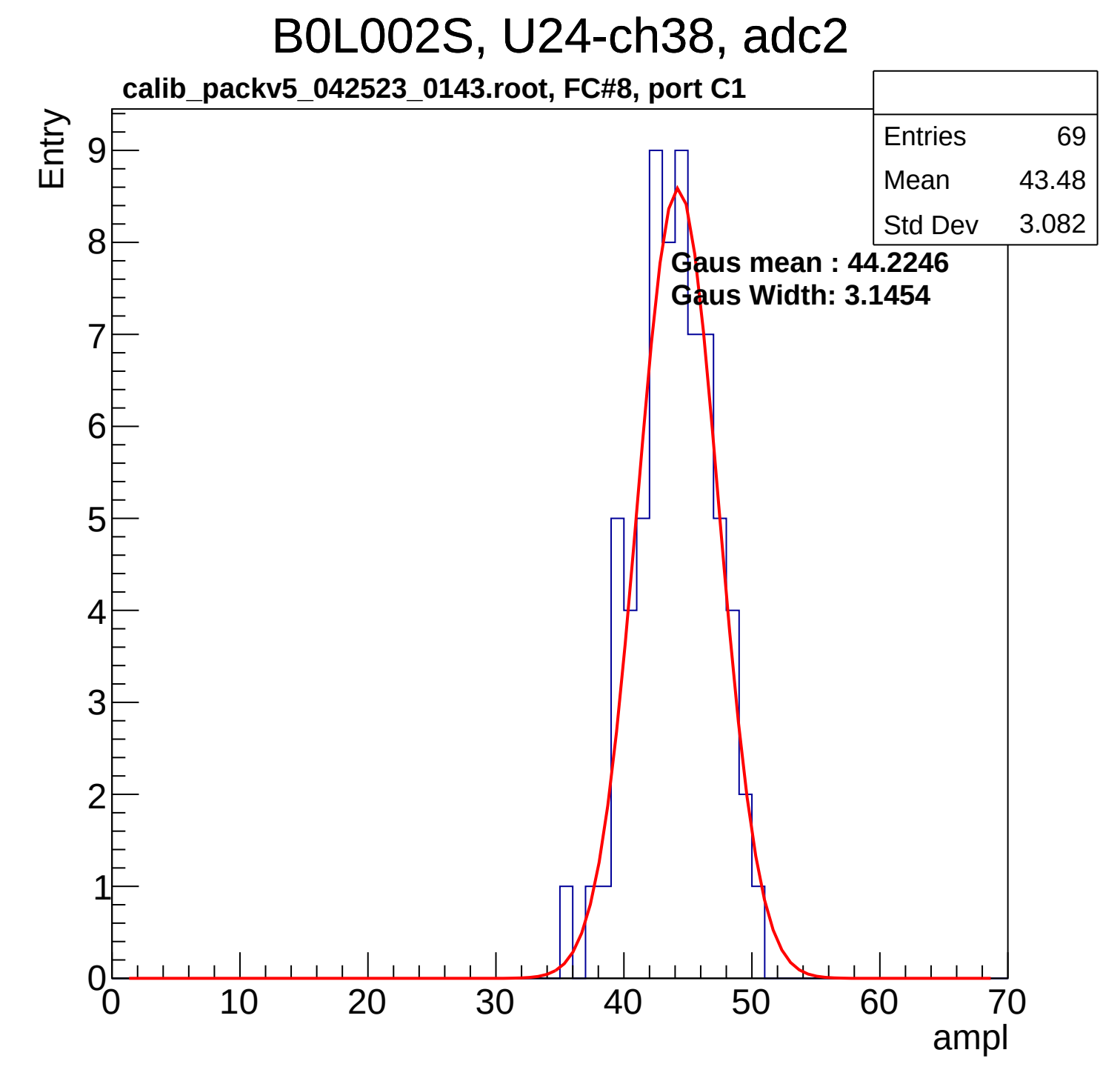
B0L002S, U24-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.48
Std Dev 3.082
Gaus mean : 44.2246
Gaus Width: 3.1454
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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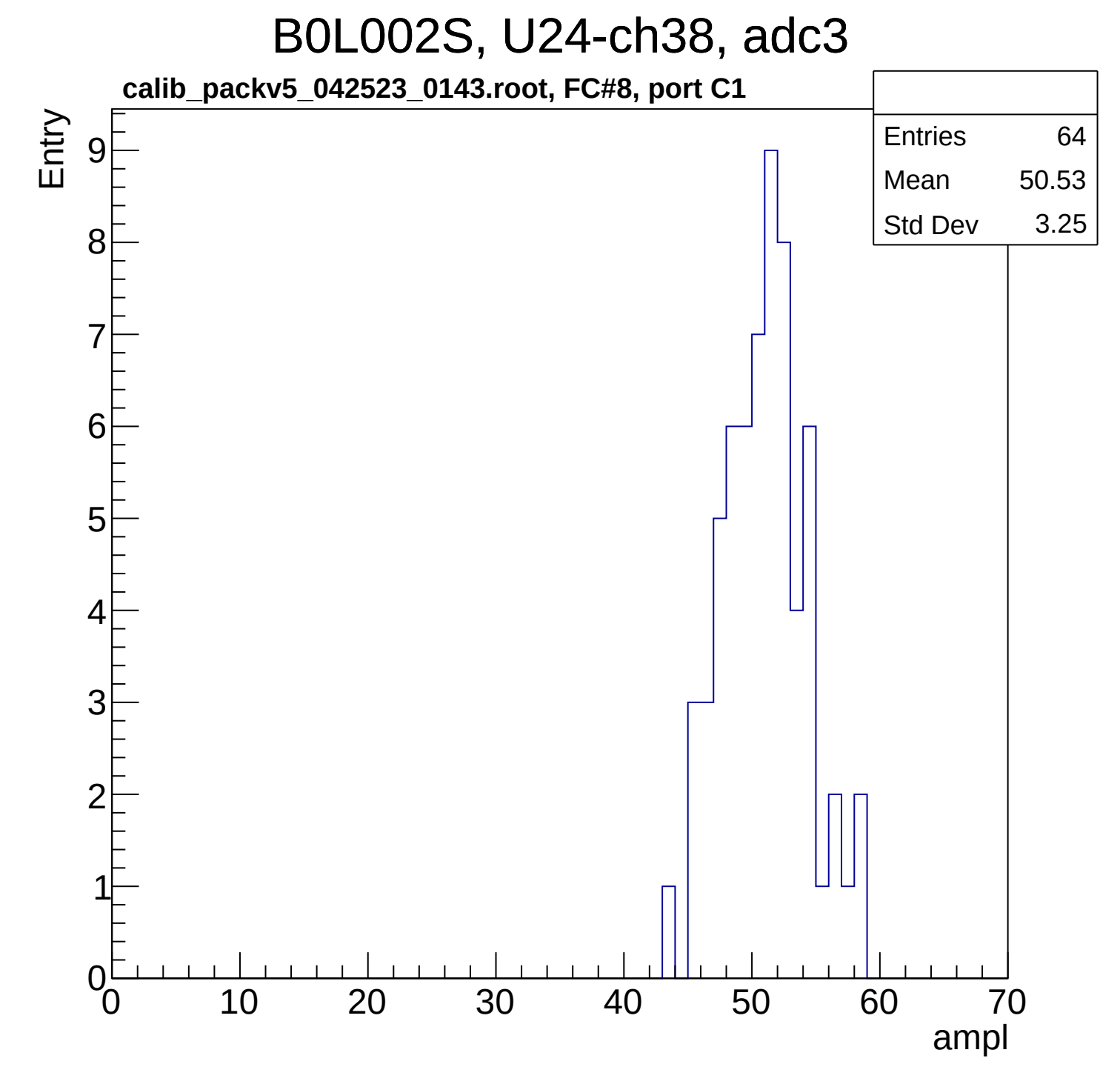
B0L002S, U24-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.53
Std Dev 3.25
Entry
ampl
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3
4
5
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0
10
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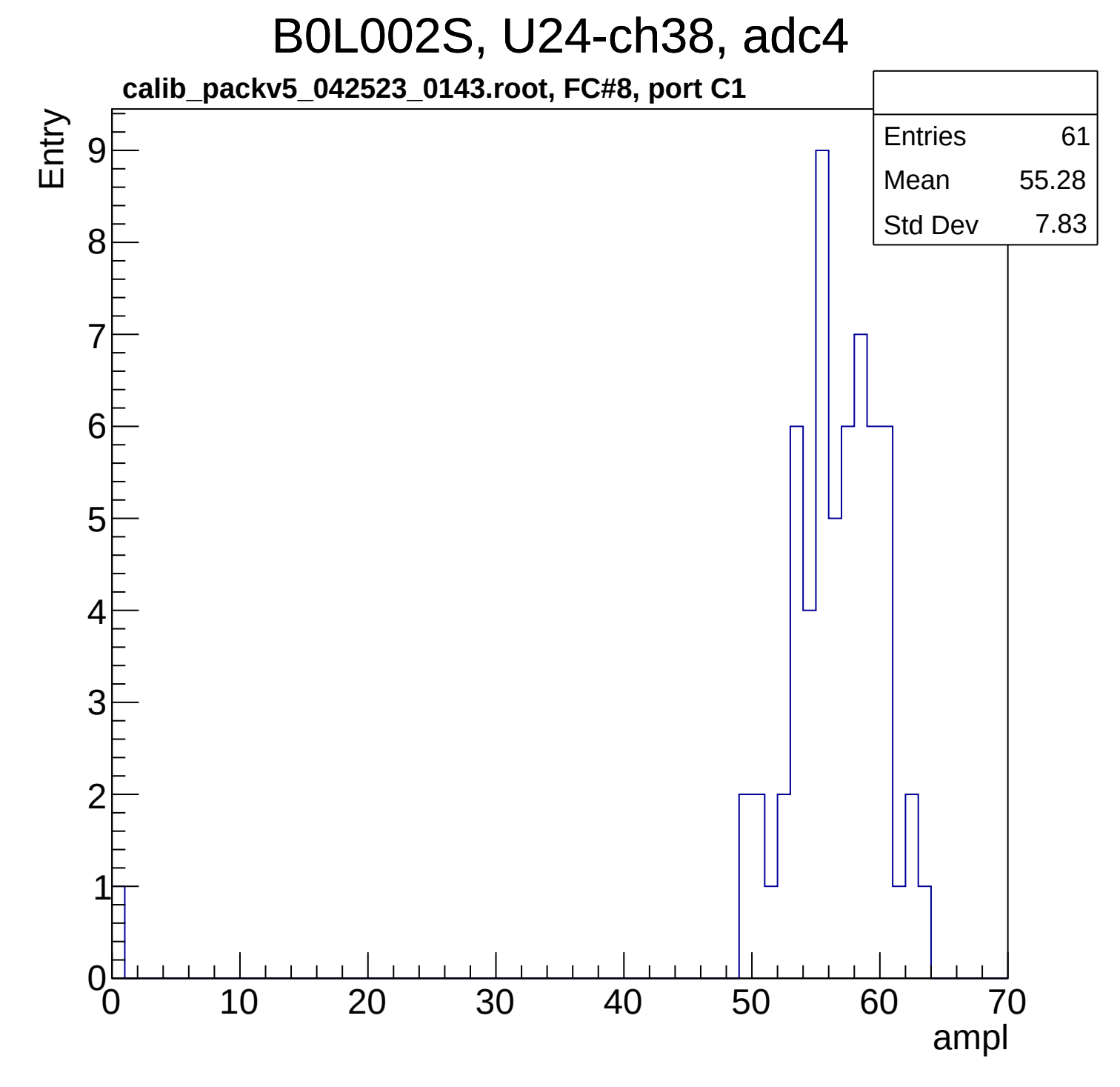
B0L002S, U24-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.28
Std Dev 7.83
Entry
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7
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9
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10
20
30
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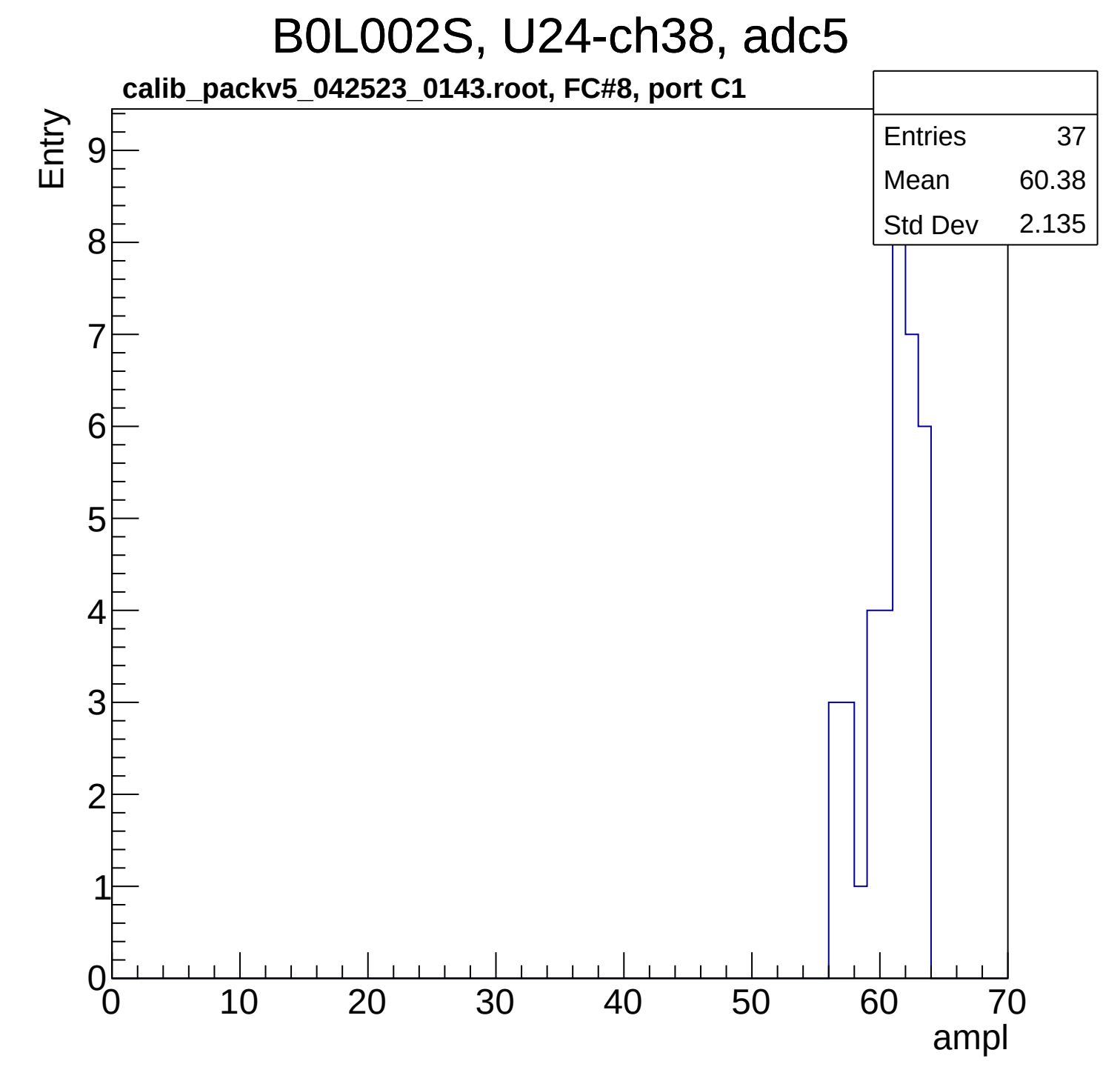
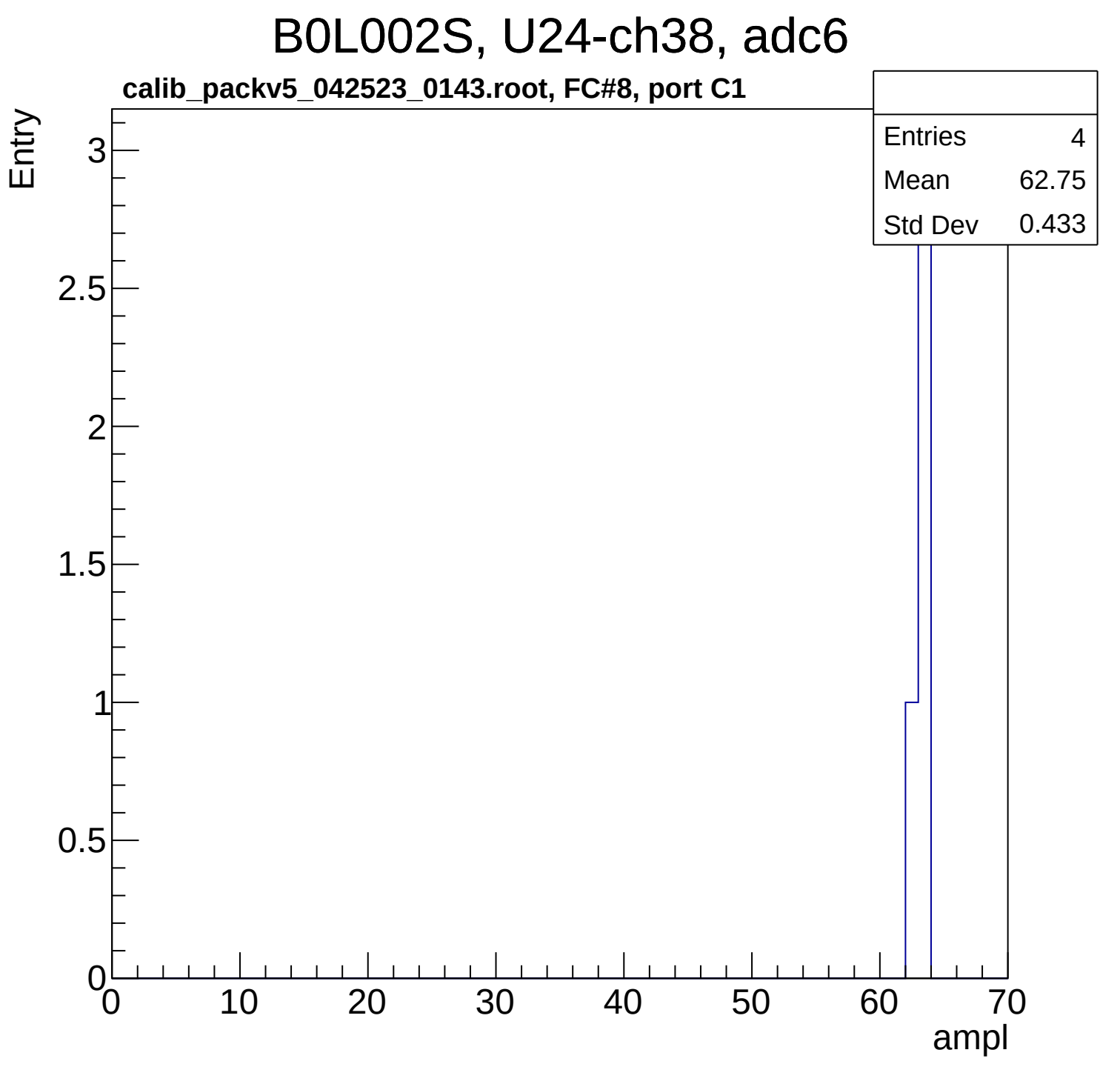
50
60
70
ampl


B0L002S, U24-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 60.38
Std Dev 2.135
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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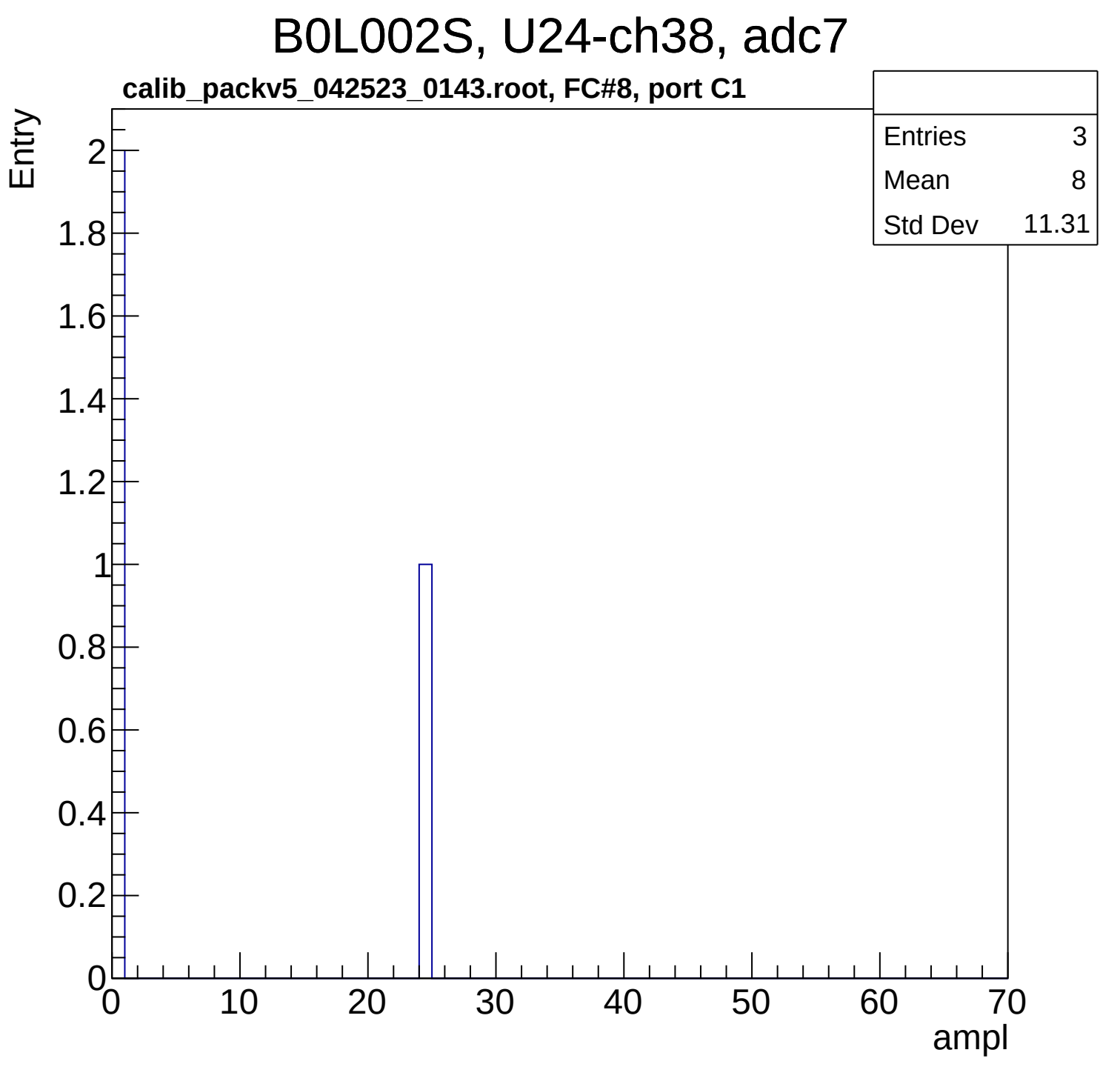
B0L002S, U24-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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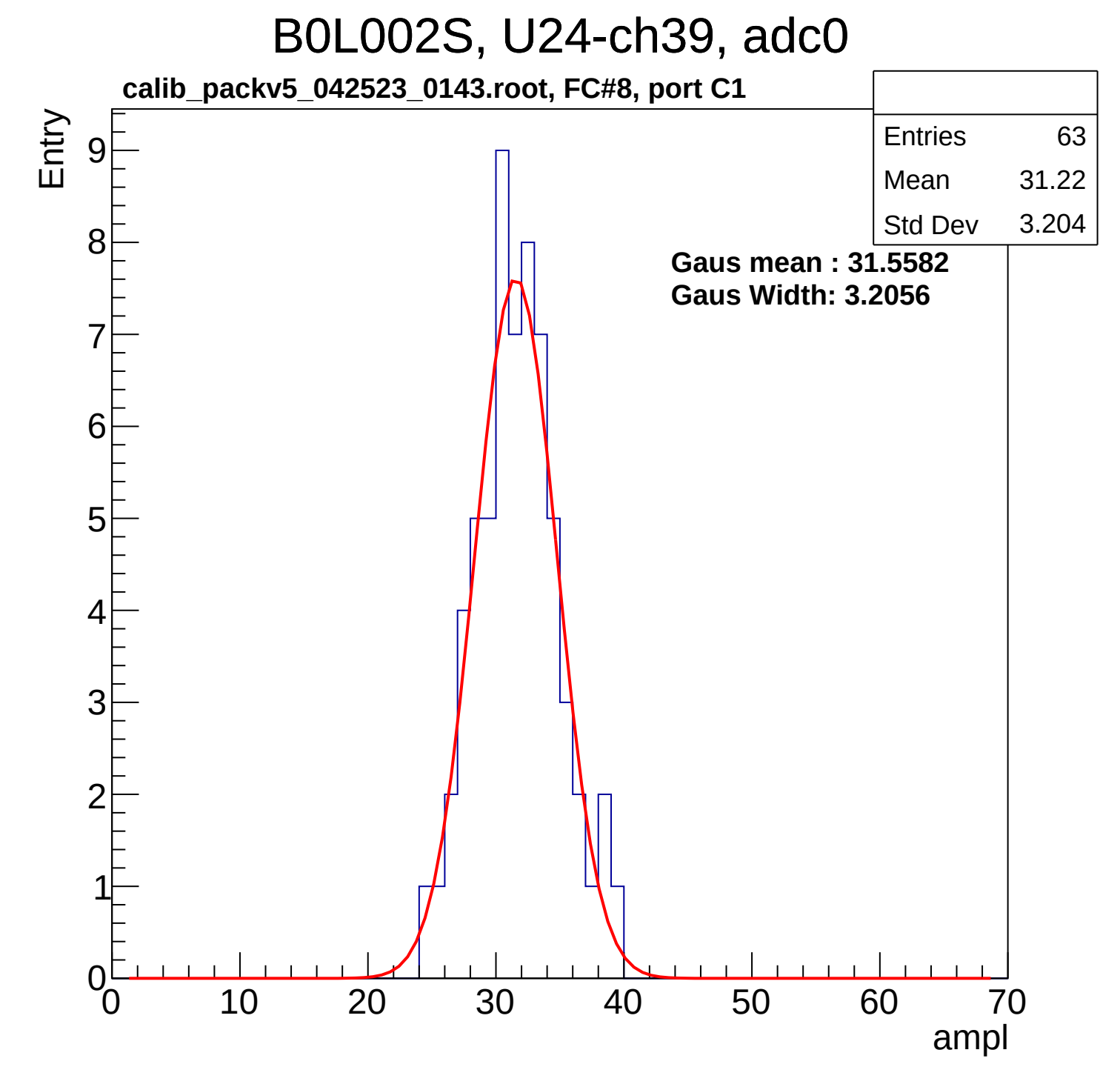
B0L002S, U24-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.22
Std Dev 3.204
Gaus mean : 31.5582
Gaus Width: 3.2056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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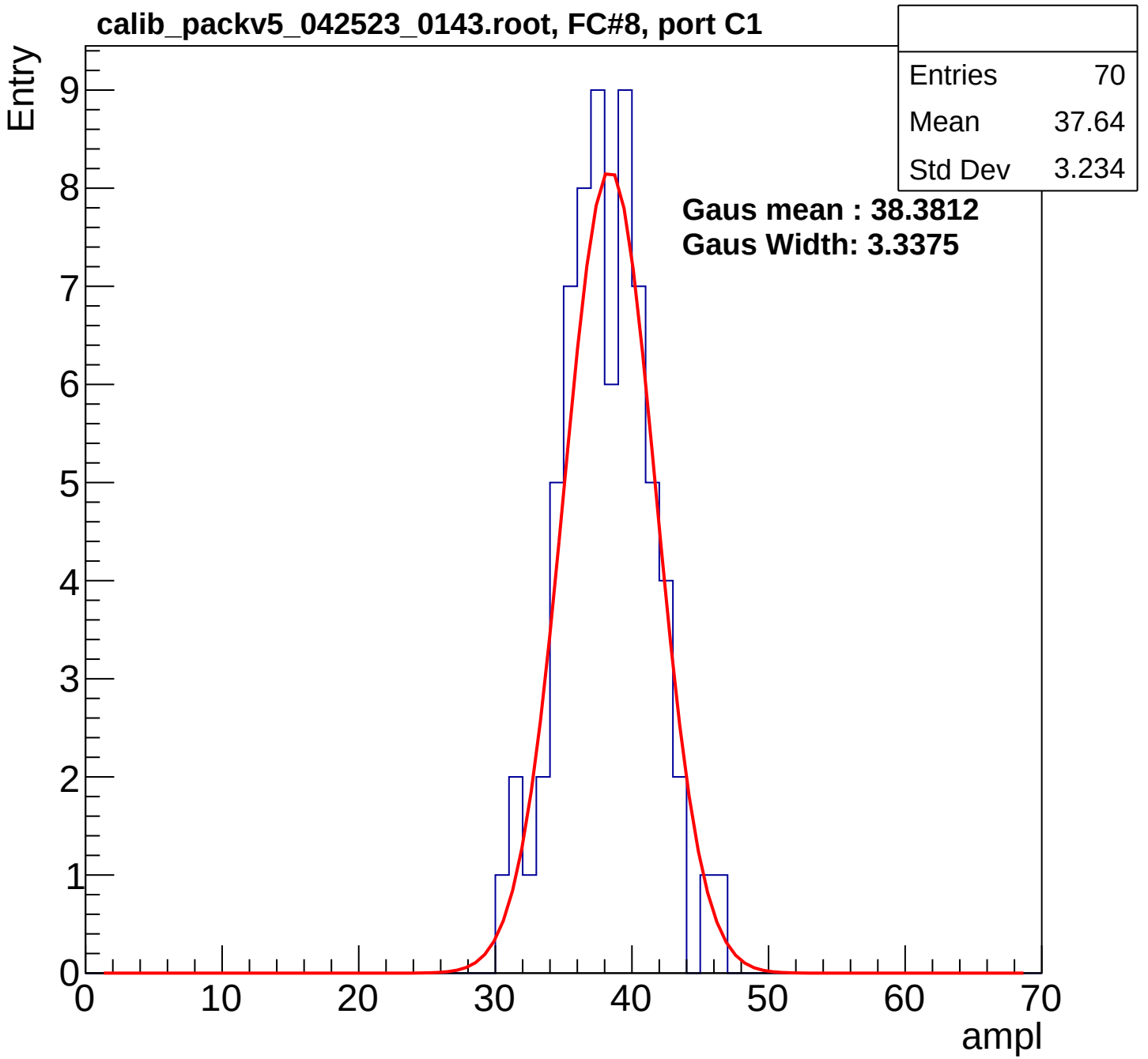
B0L002S, U24-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.64
Std Dev 3.234
Gaus mean : 38.3812
Gaus Width: 3.3375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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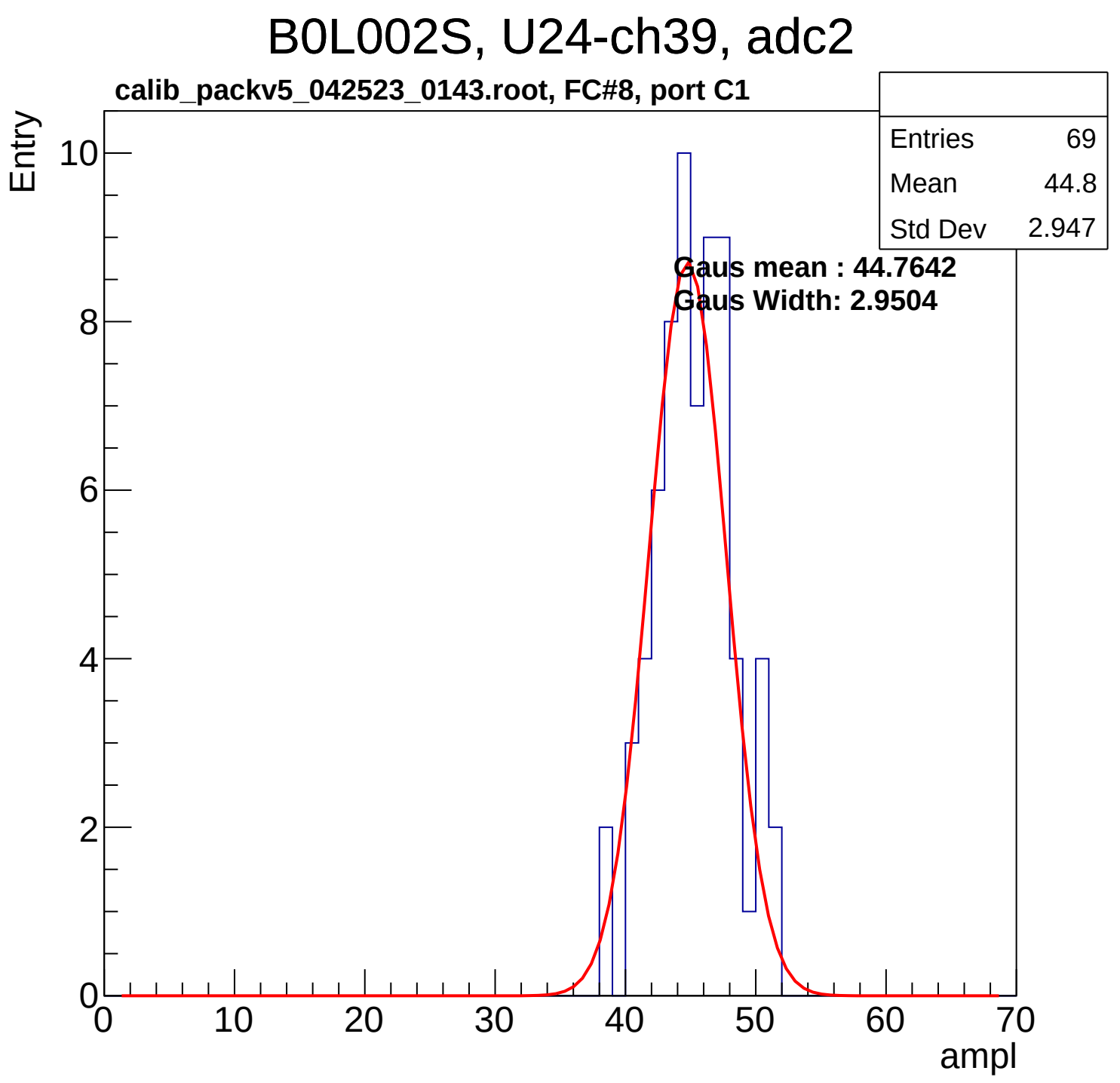
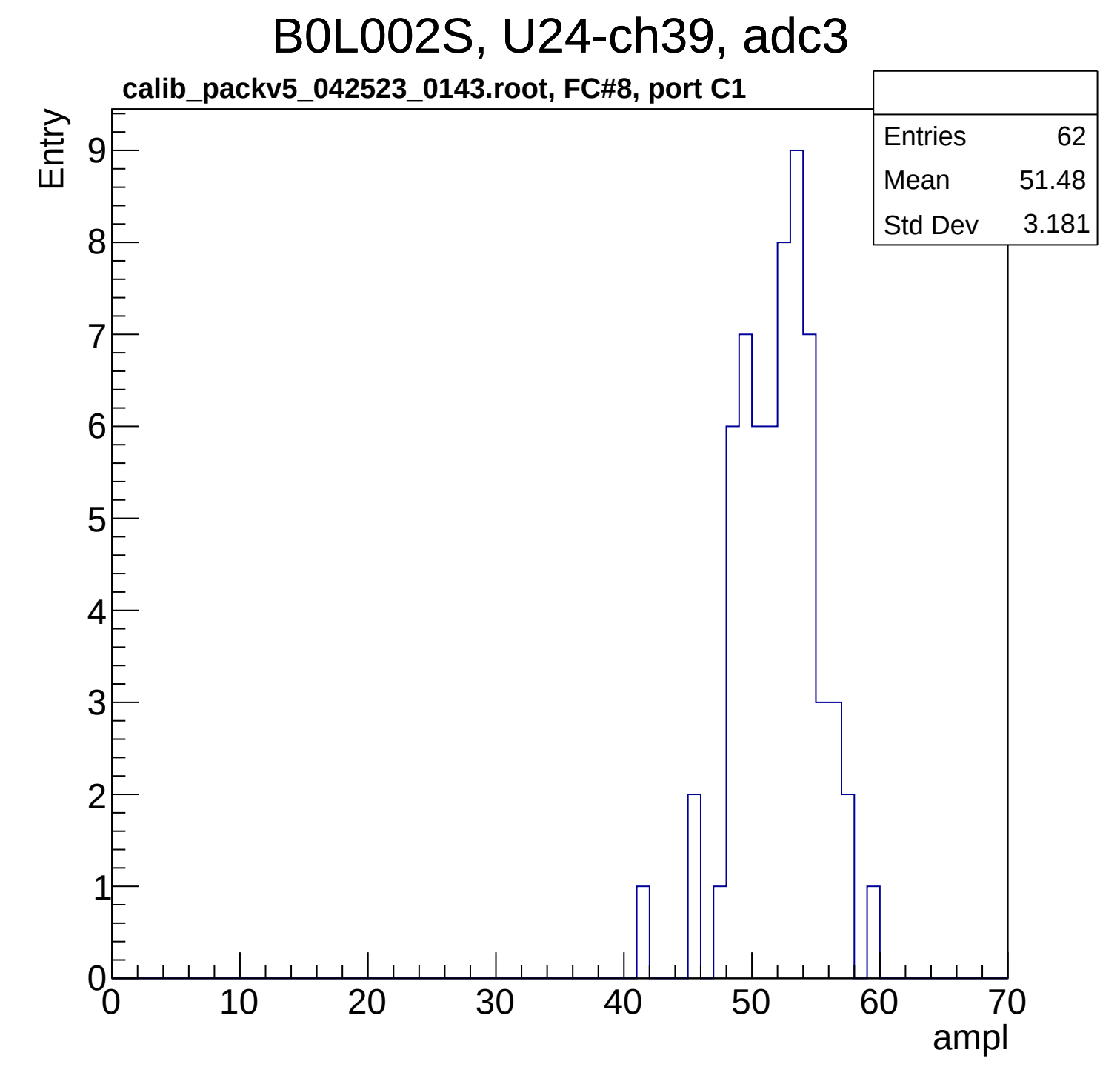
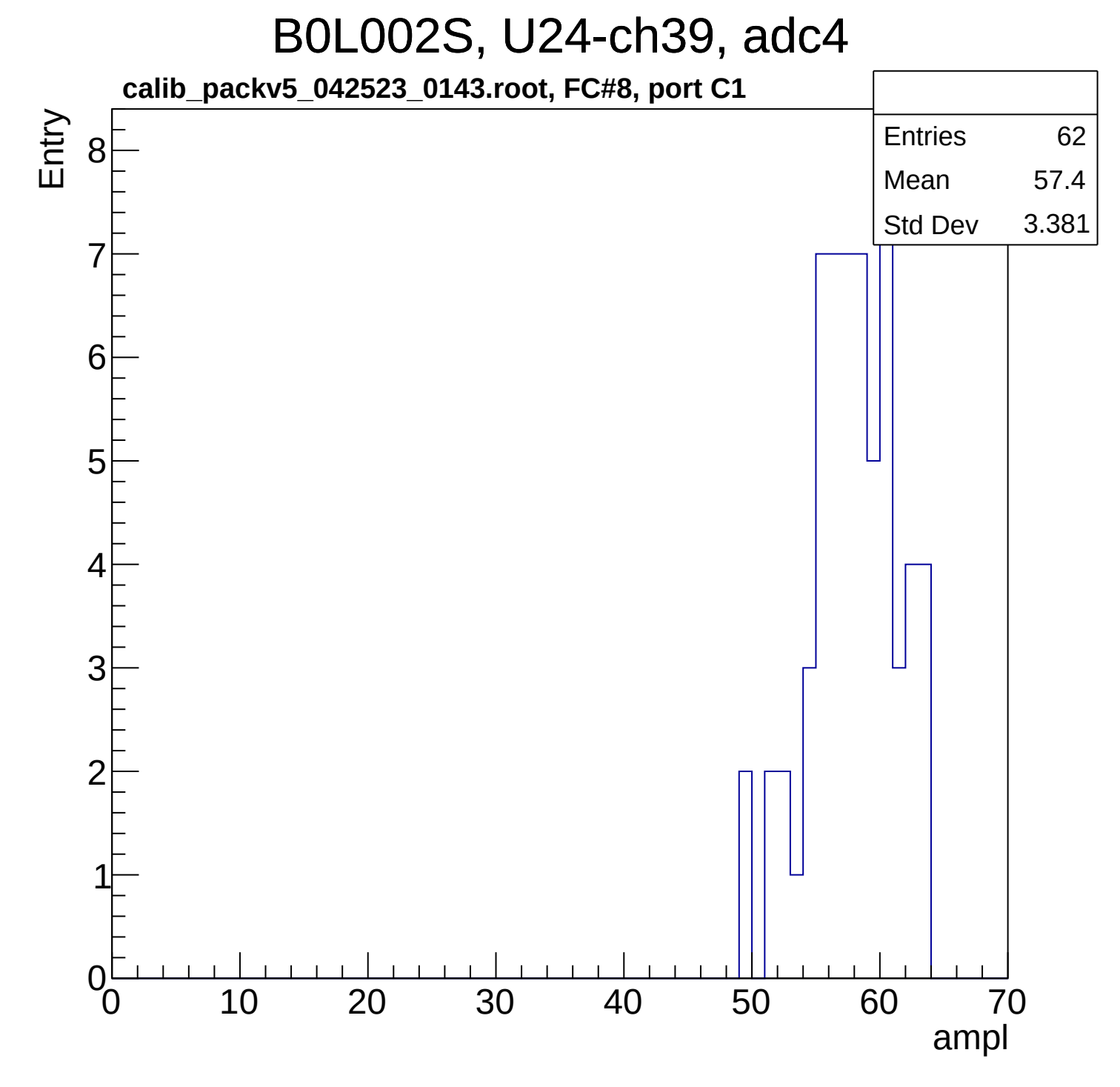
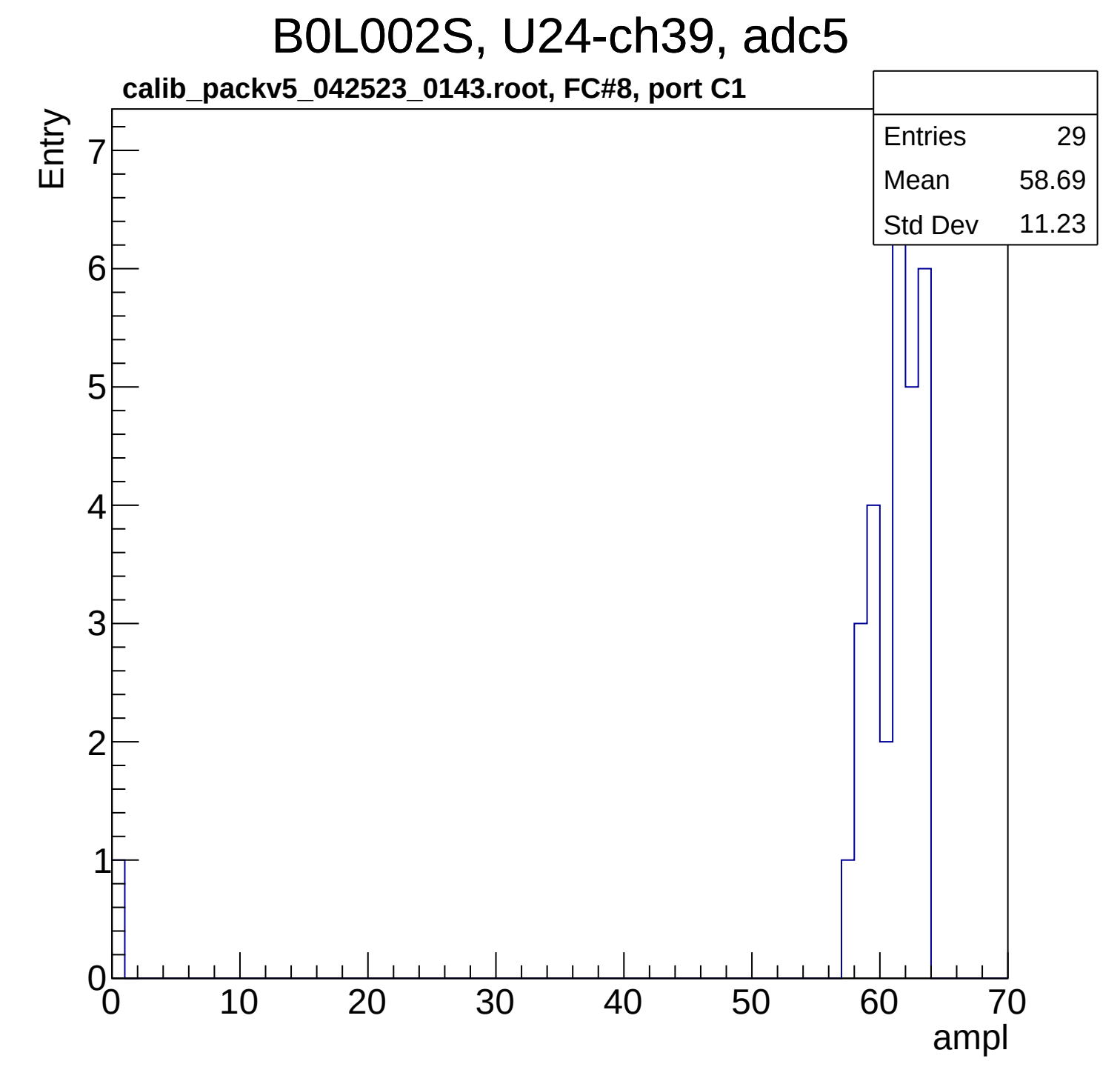
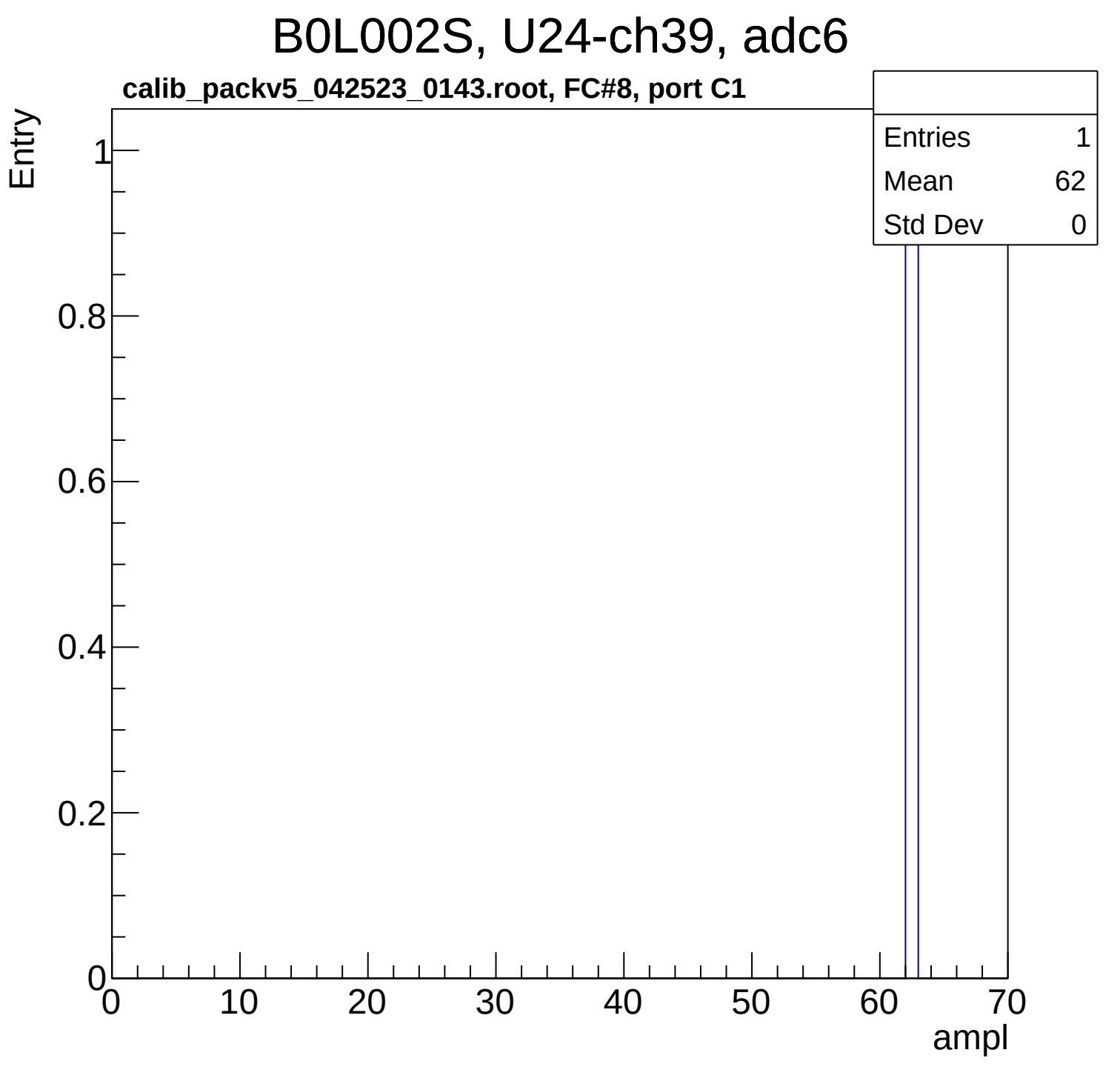
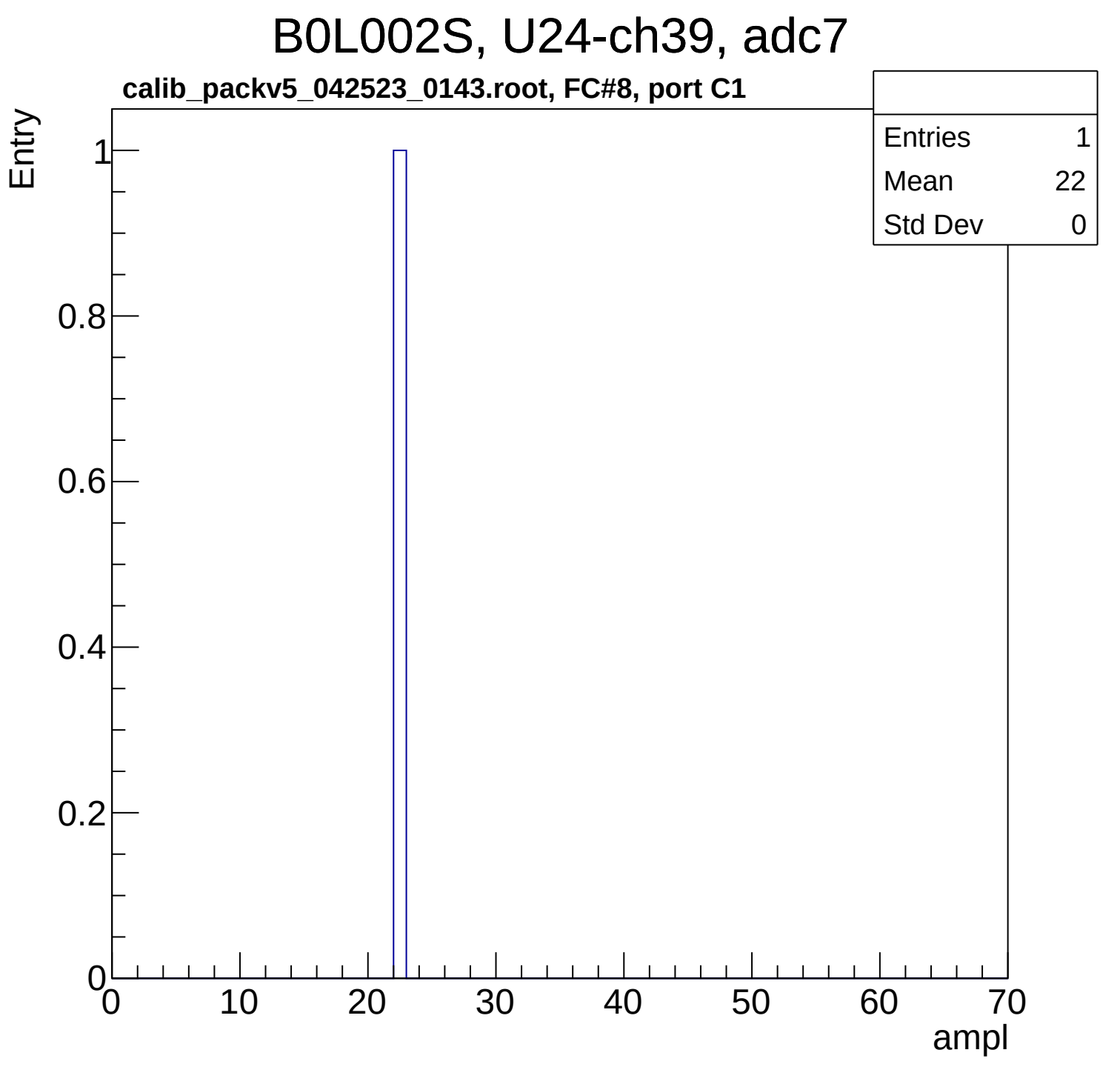

B0L002S, U24-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.8
Std Dev 2.947
Gaus mean : 44.7642
Gaus Width: 2.9504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

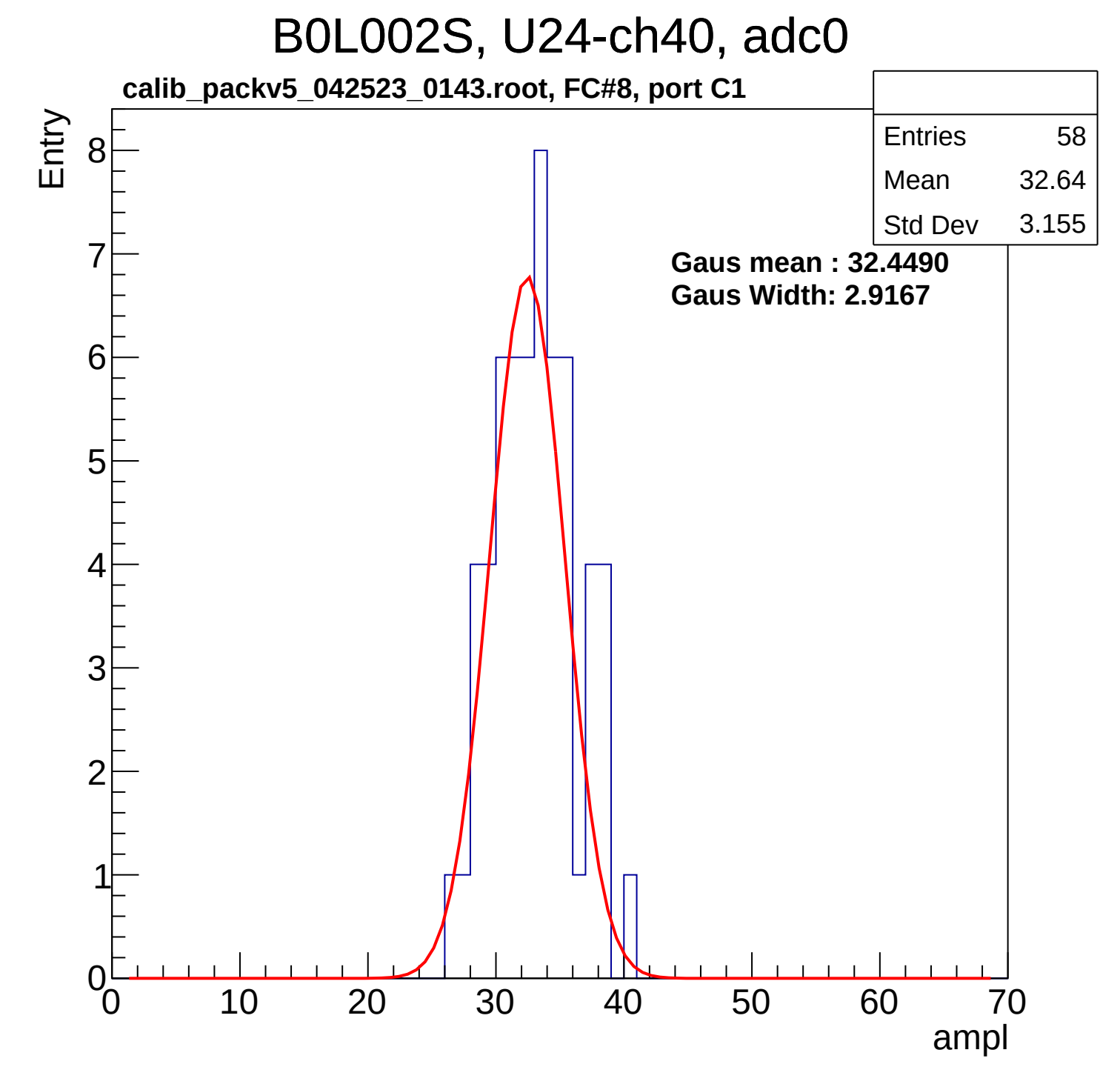

B0L002S, U24-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.48
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

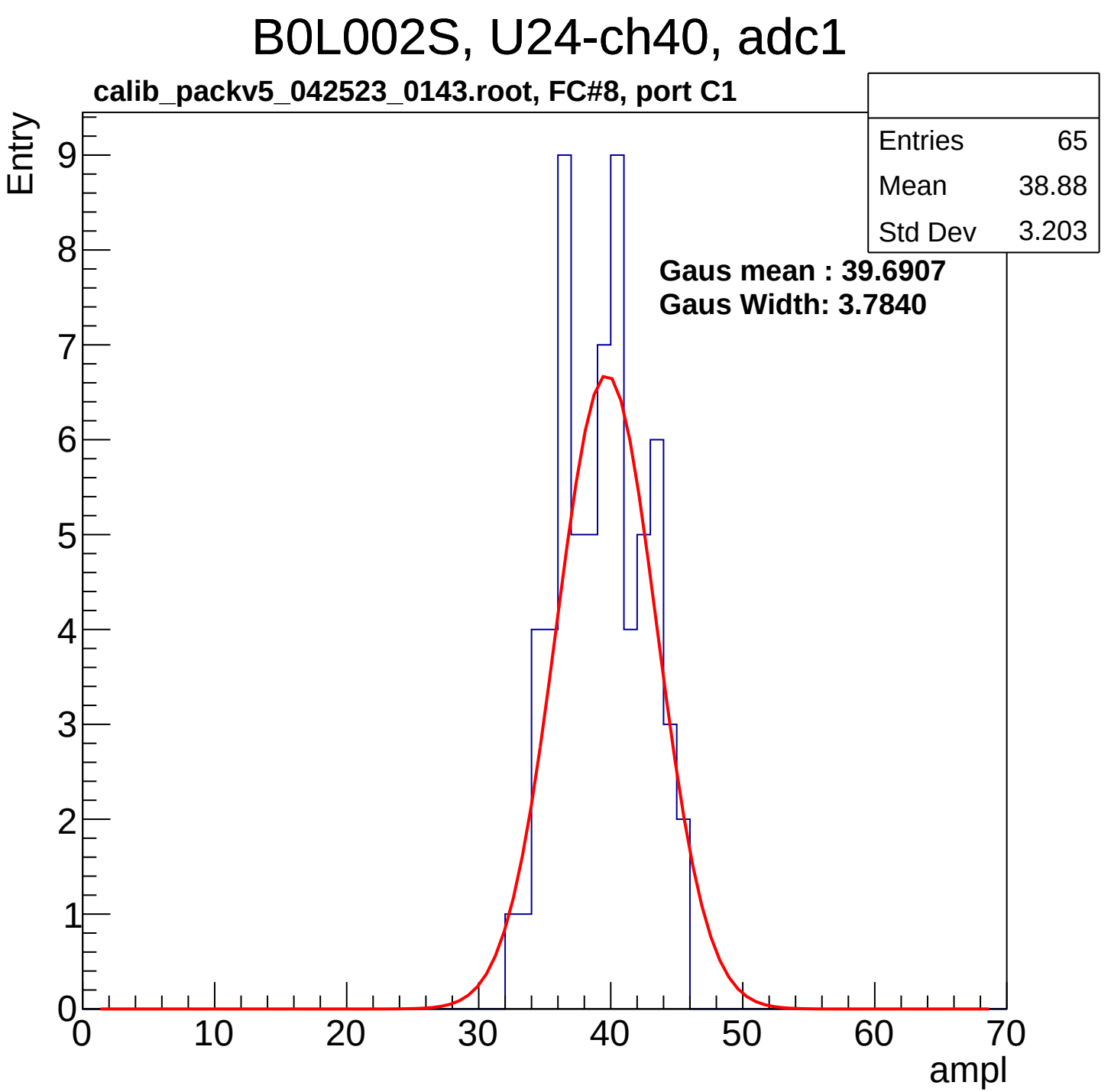

B0L002S, U24-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.4
Std Dev 3.381
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.69
Std Dev 11.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 32.64
Std Dev 3.155
Gaus mean : 32.4490
Gaus Width: 2.9167
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.88
Std Dev 3.203
Gaus mean : 39.6907
Gaus Width: 3.7840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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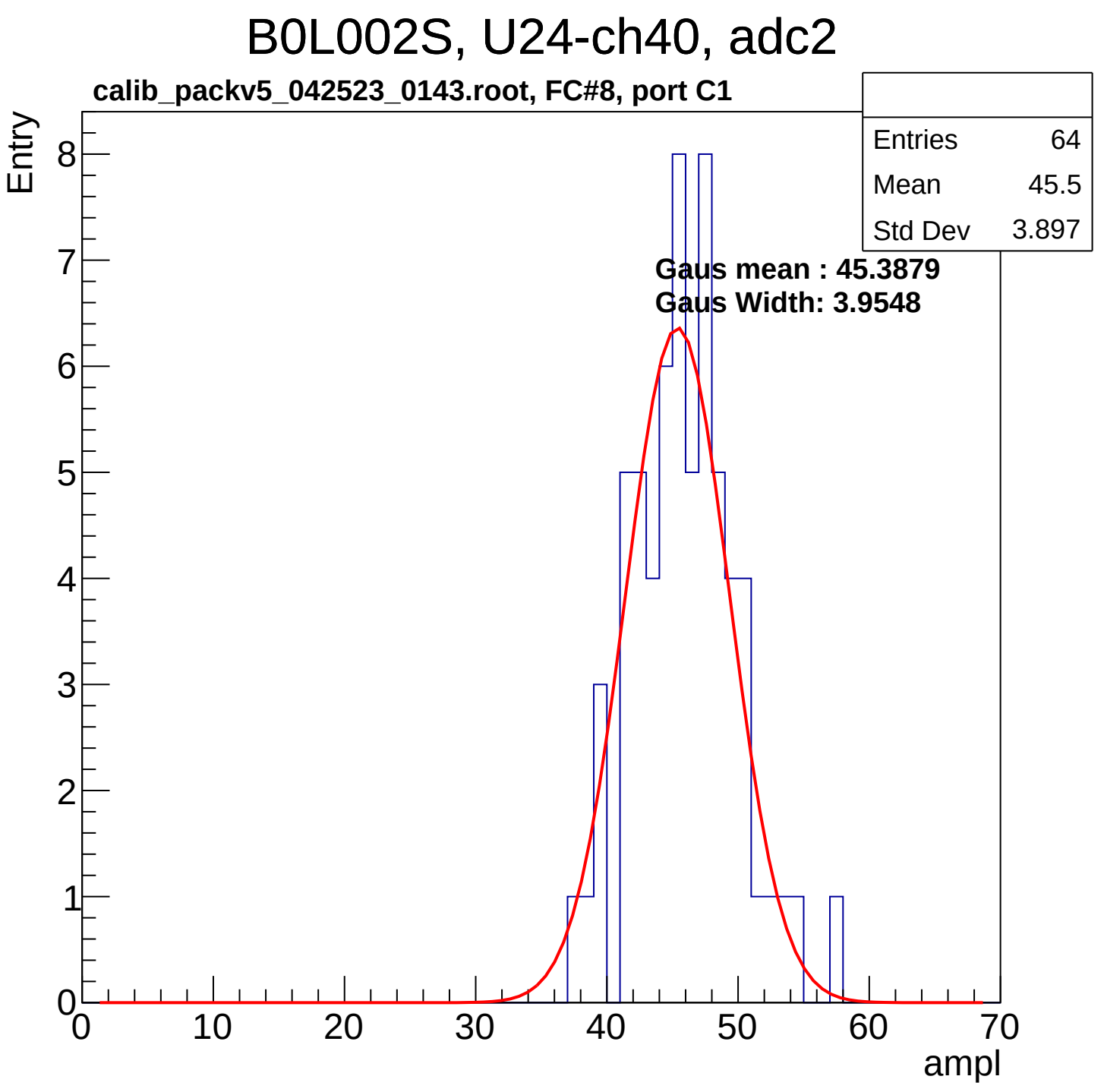
B0L002S, U24-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 45.5
Std Dev 3.897
Gaus mean : 45.3879
Gaus Width: 3.9548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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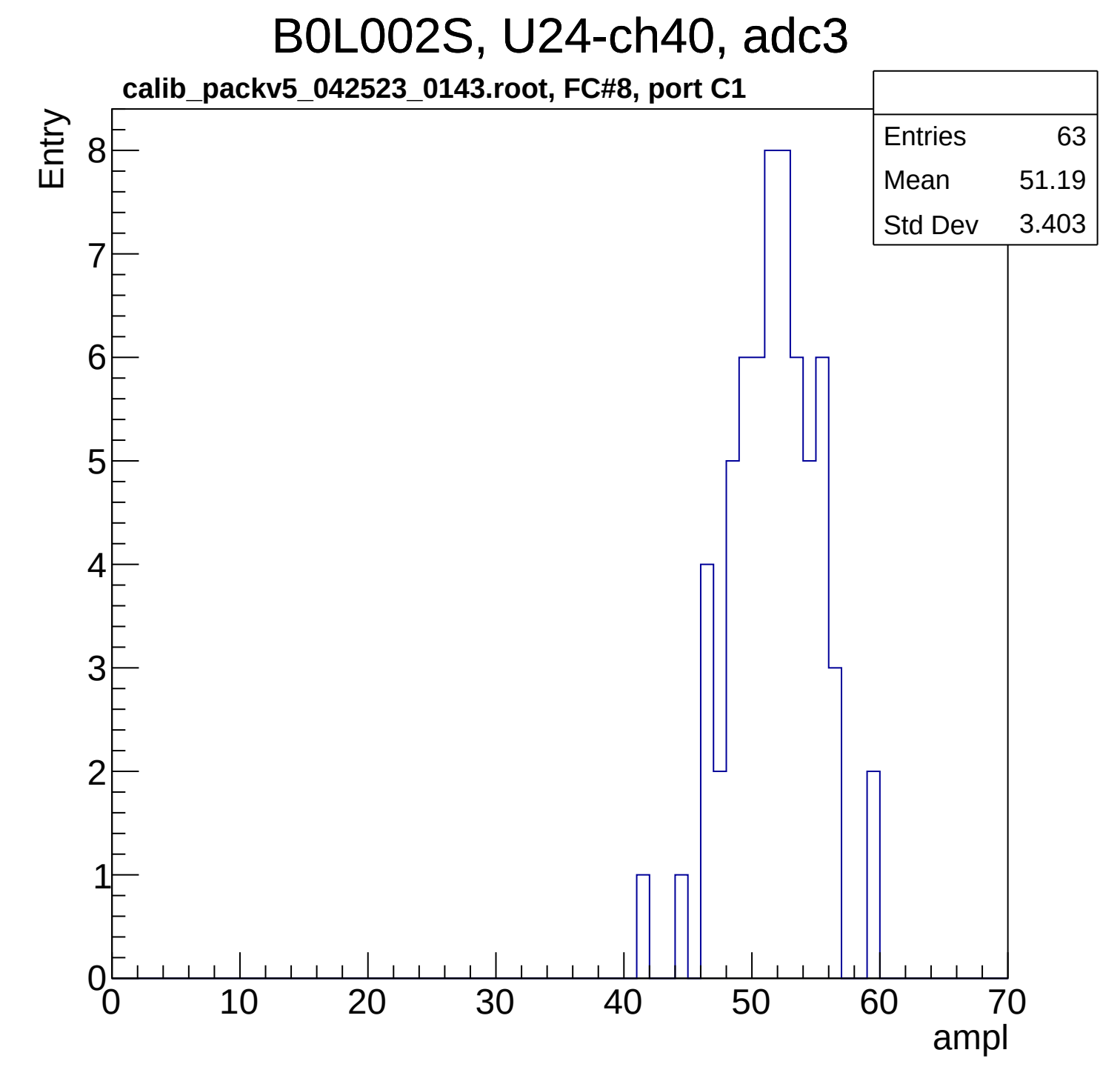
B0L002S, U24-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.19
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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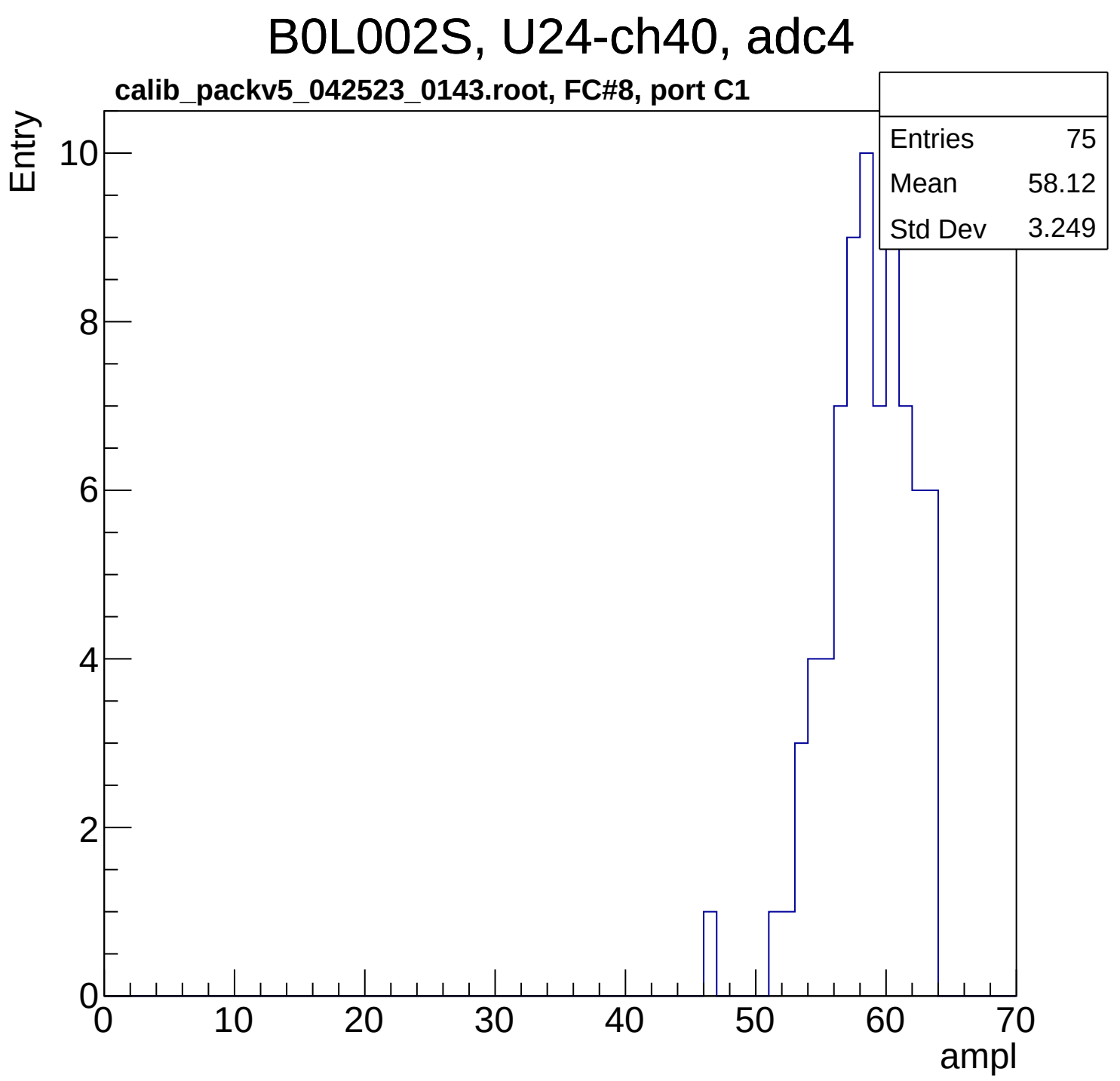
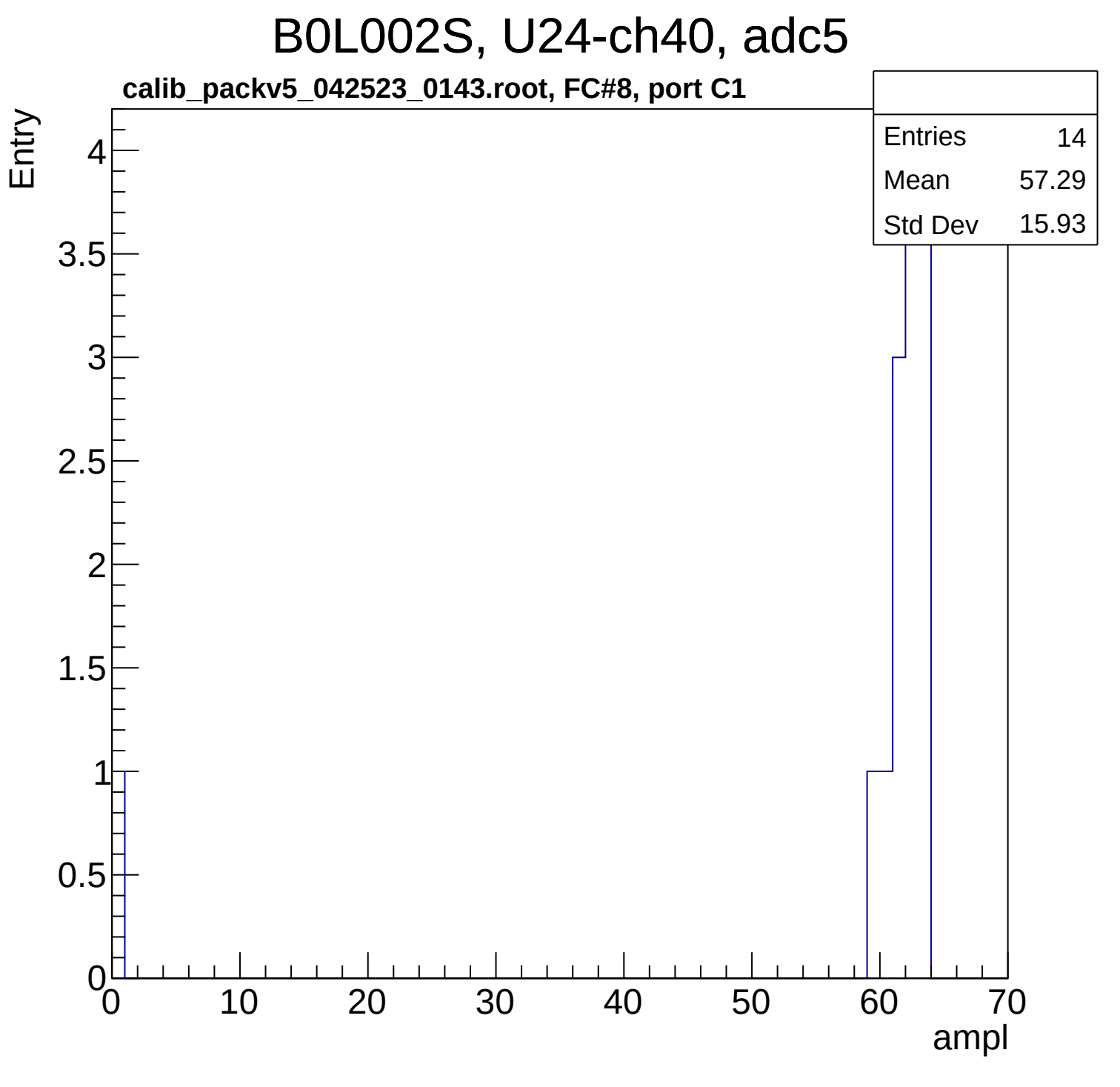
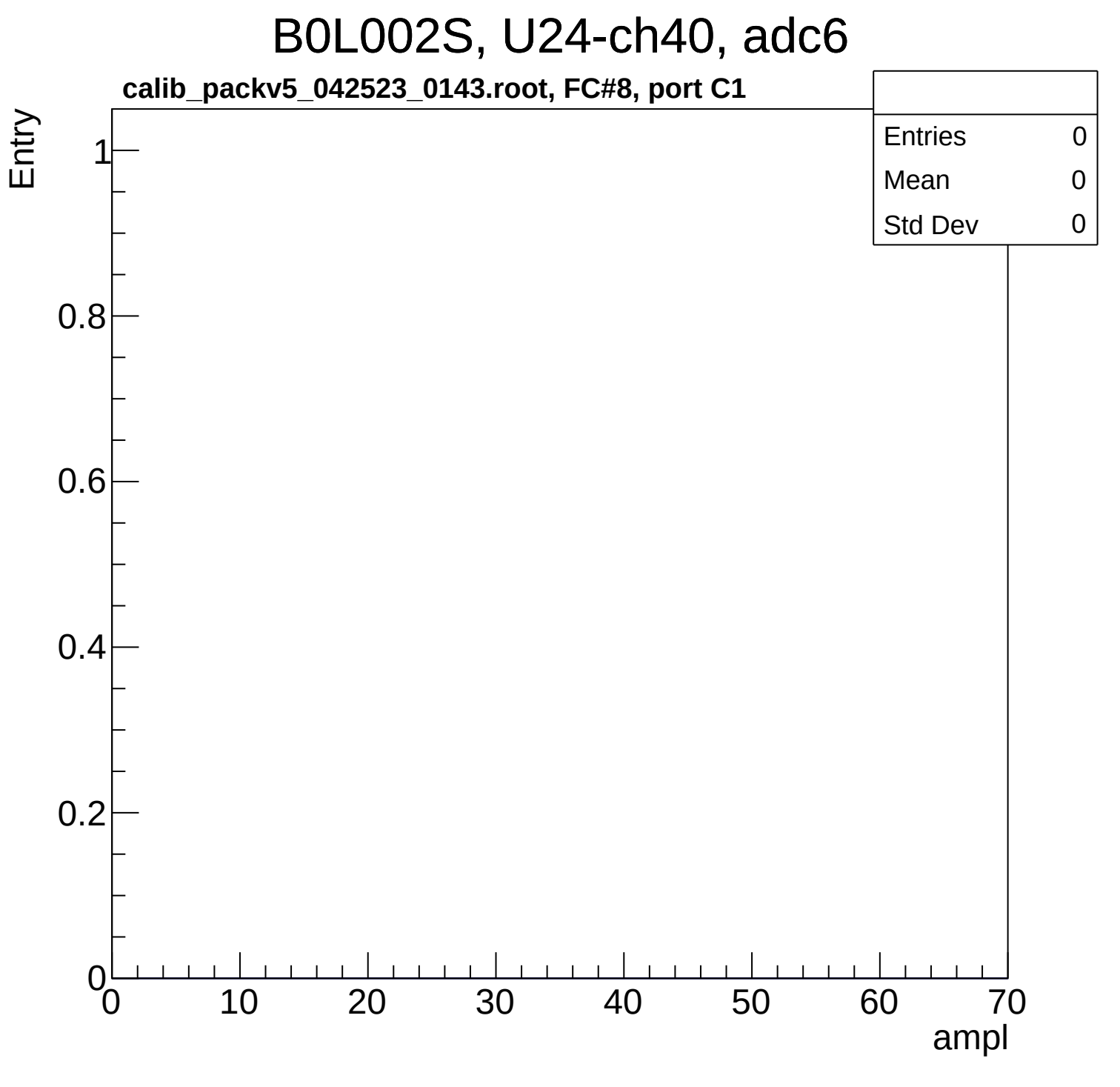
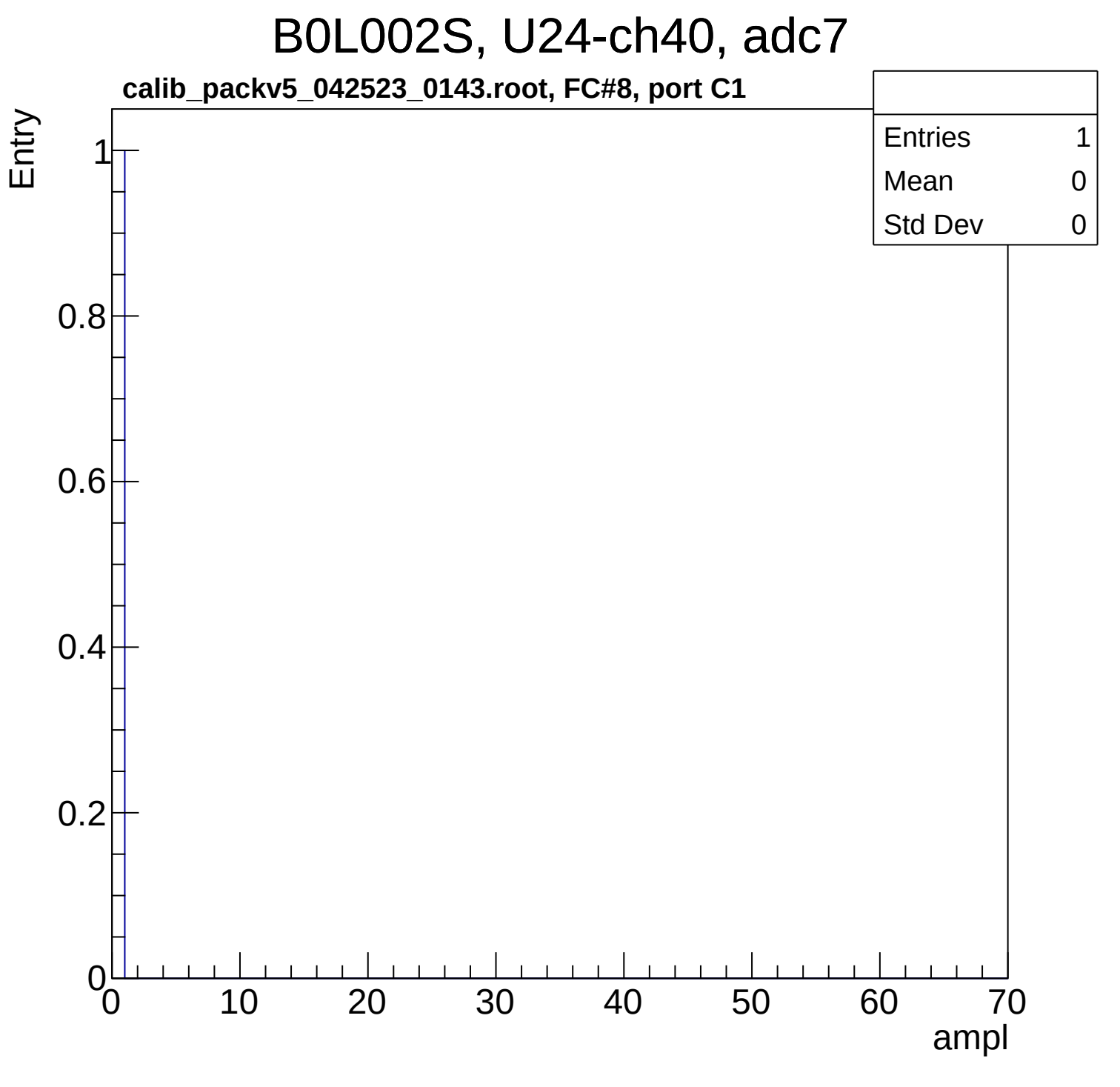
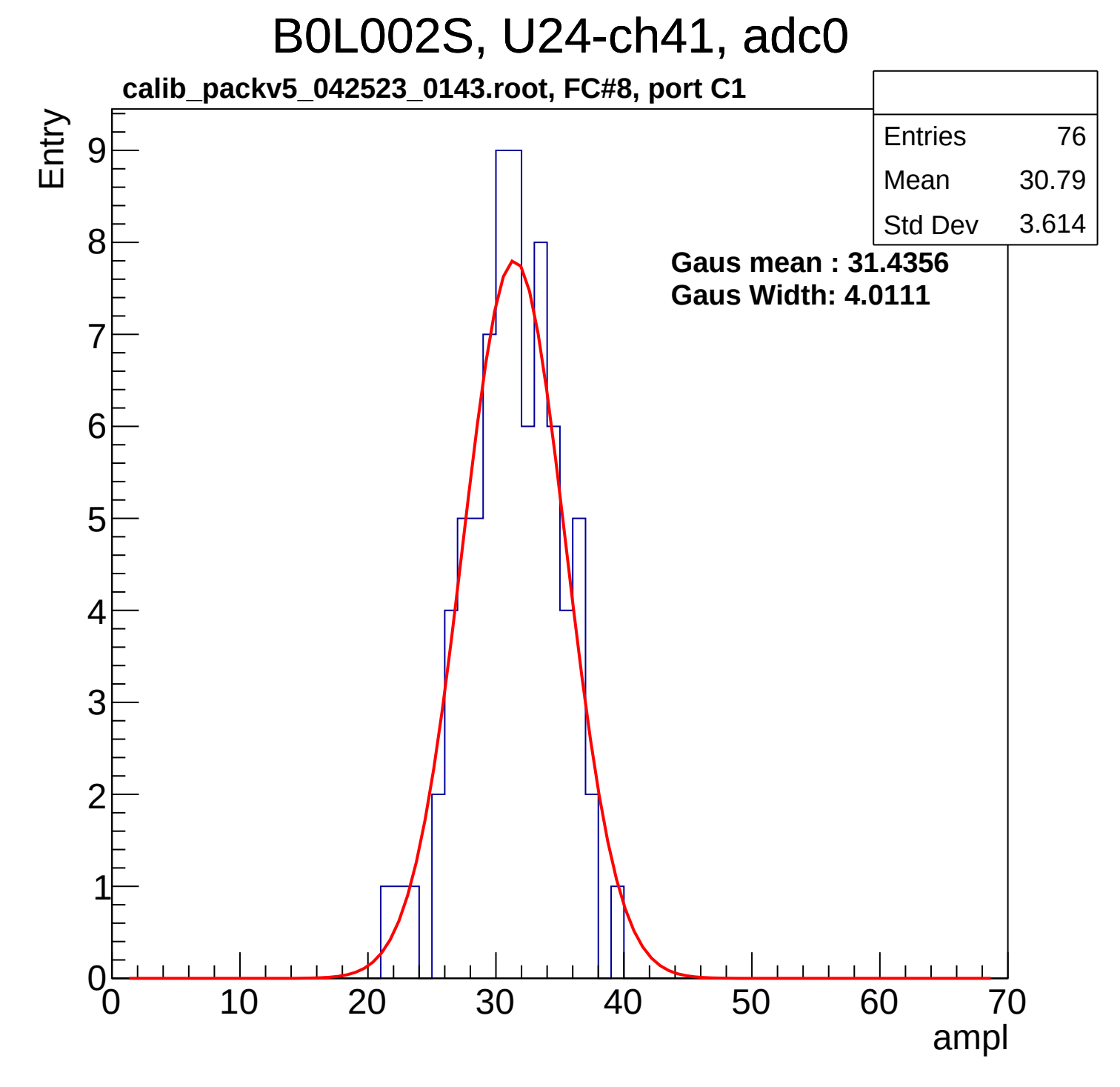
40
50
60
70

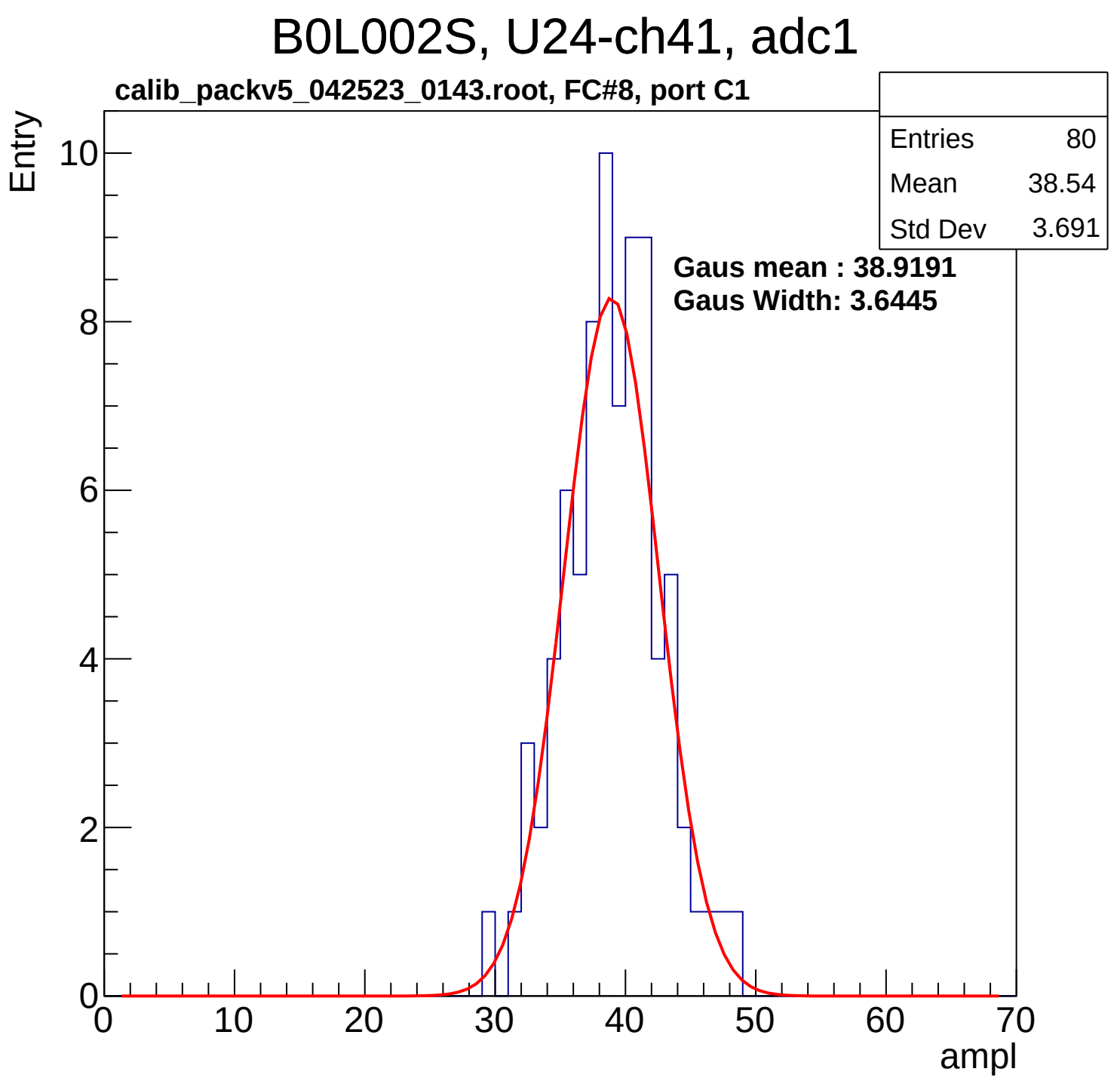

B0L002S, U24-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 58.12
Std Dev 3.249
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

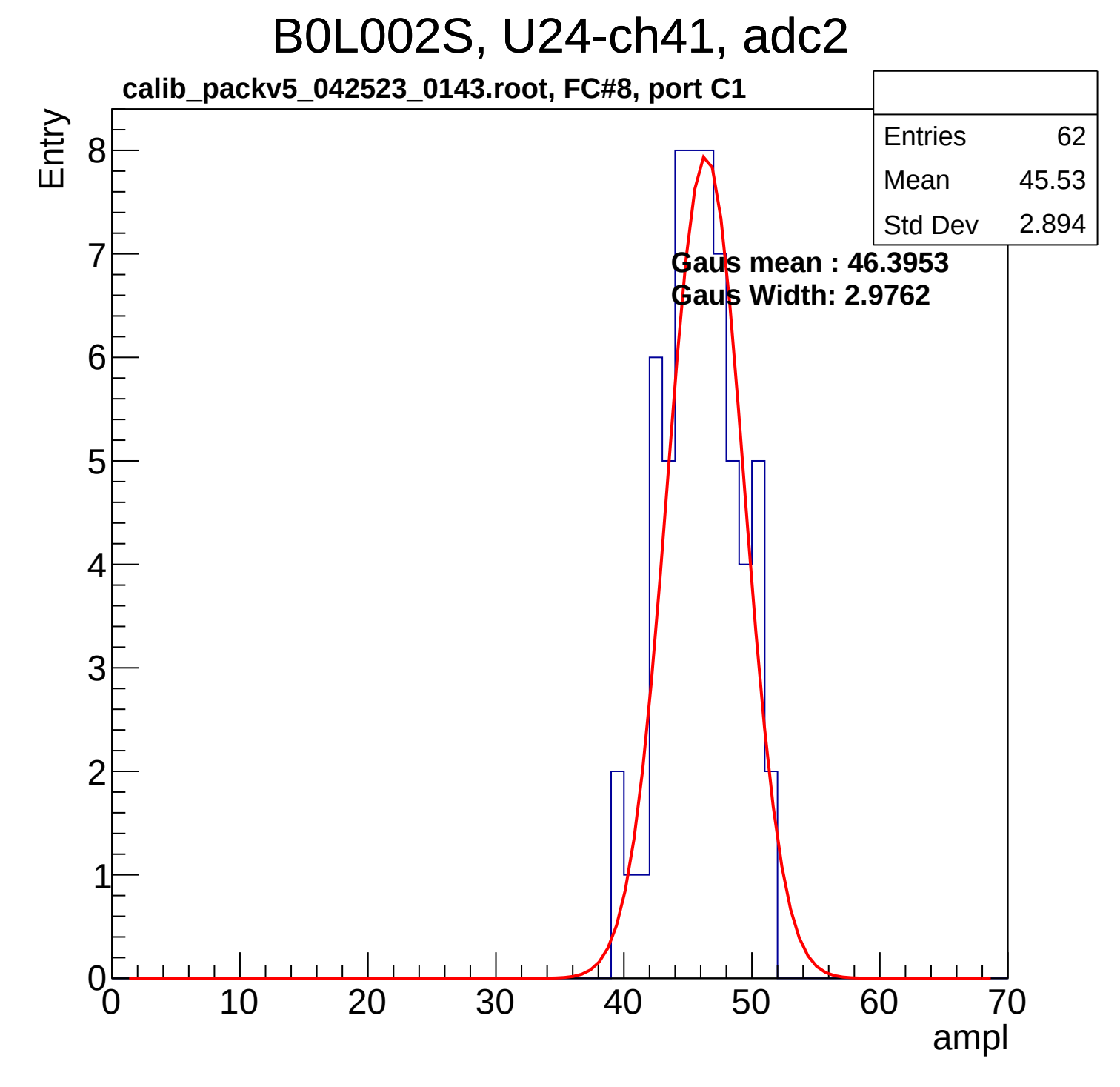

B0L002S, U24-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 57.29
Std Dev 15.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U24-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.79
Std Dev 3.614
Gaus mean : 31.4356
Gaus Width: 4.0111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.54
Std Dev 3.691
Gaus mean : 38.9191
Gaus Width: 3.6445
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 45.53
Std Dev 2.894
Gaus mean : 46.3953
Gaus Width: 2.9762
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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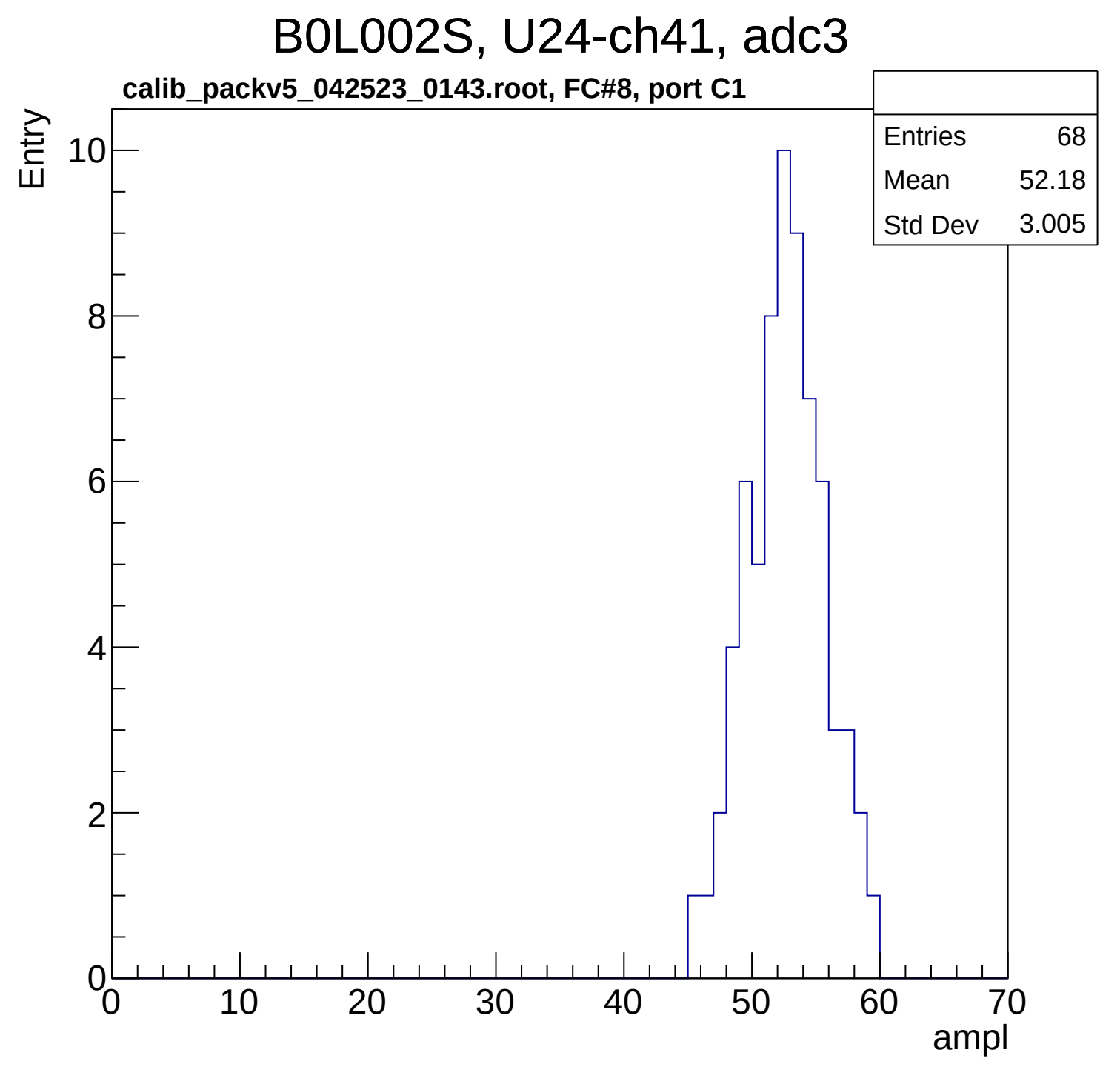
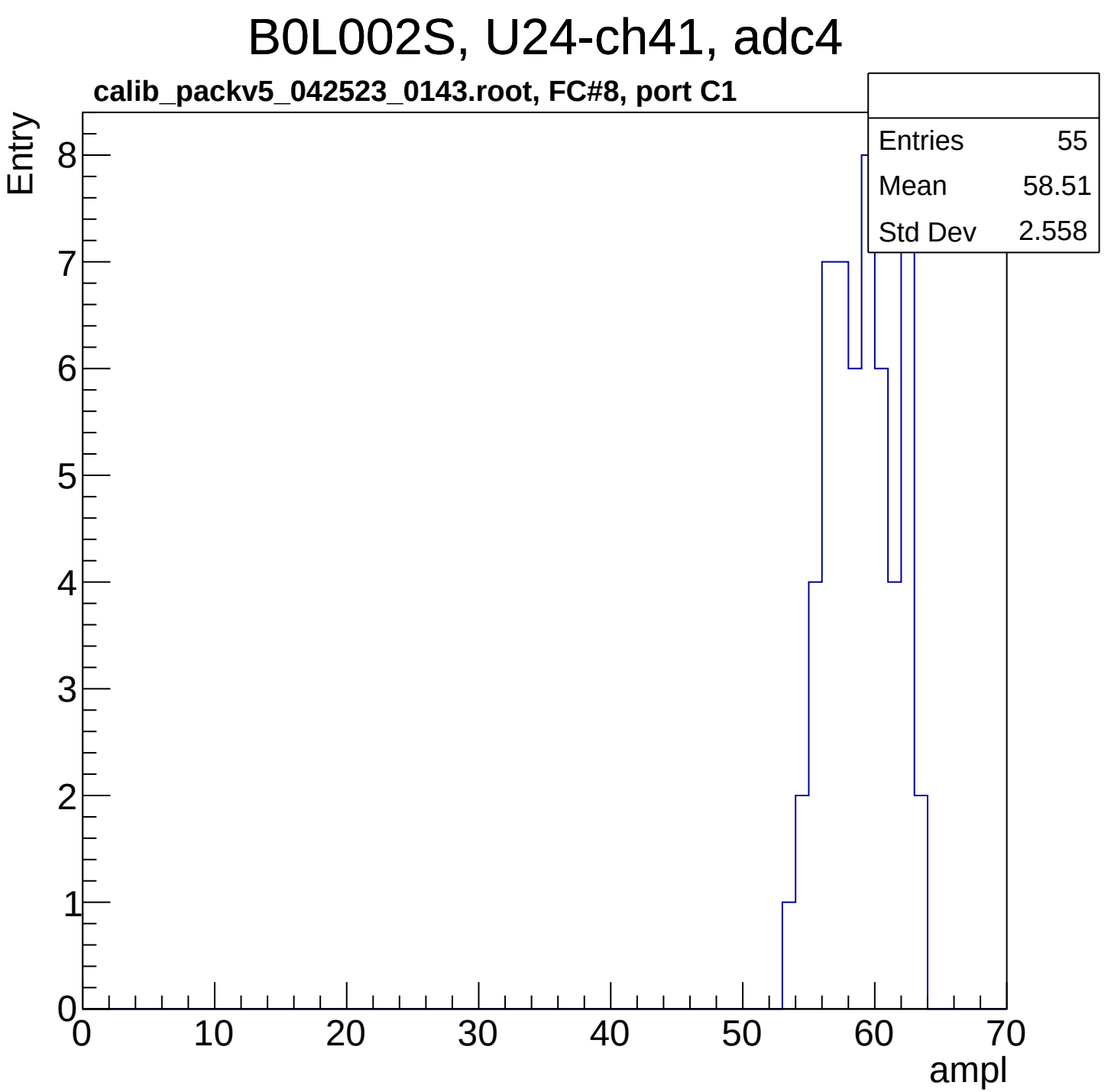

B0L002S, U24-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 52.18
Std Dev 3.005
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 58.51
Std Dev 2.558
Entry
ampl
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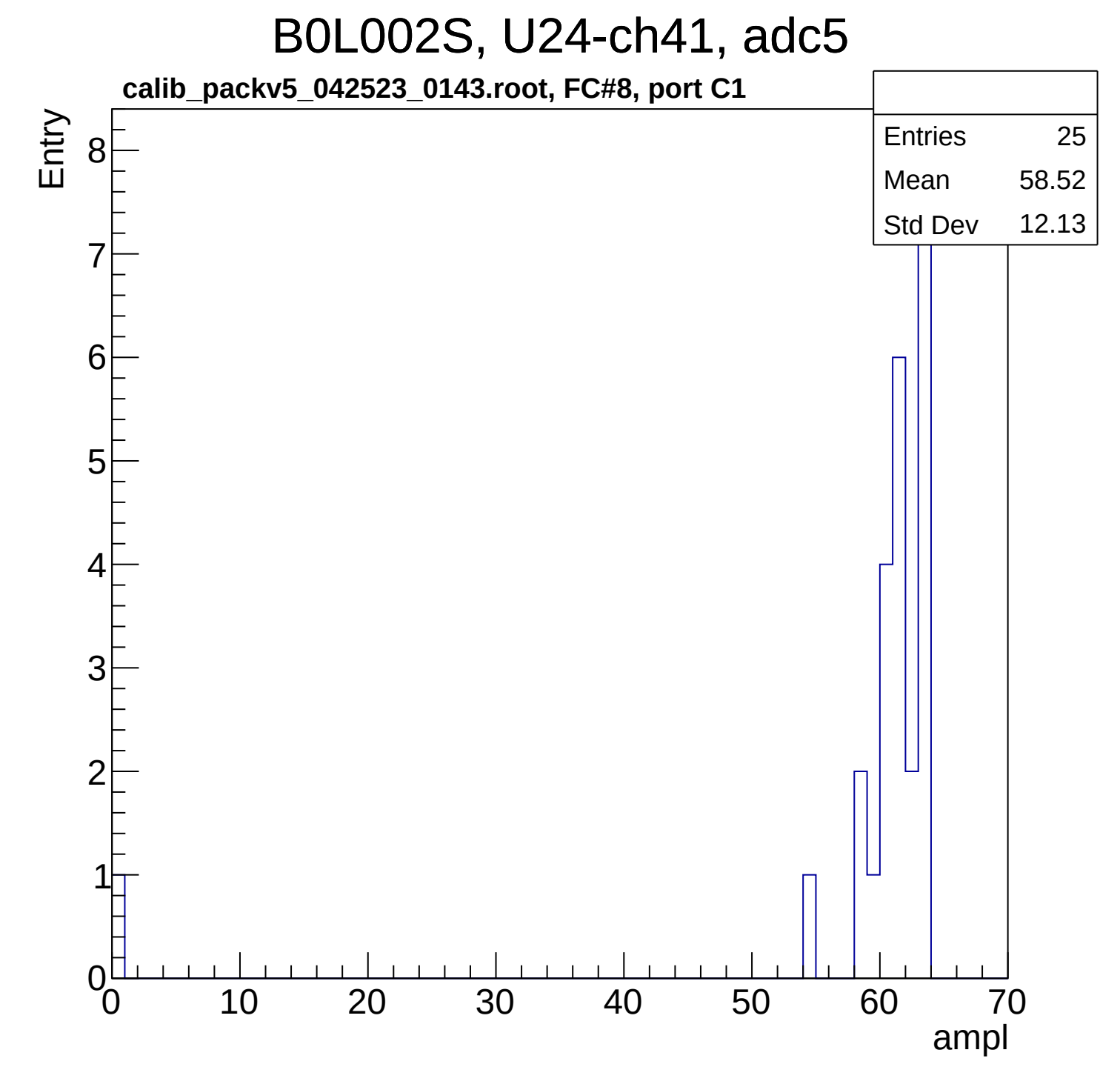
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.52
Std Dev 12.13
Entry
ampl
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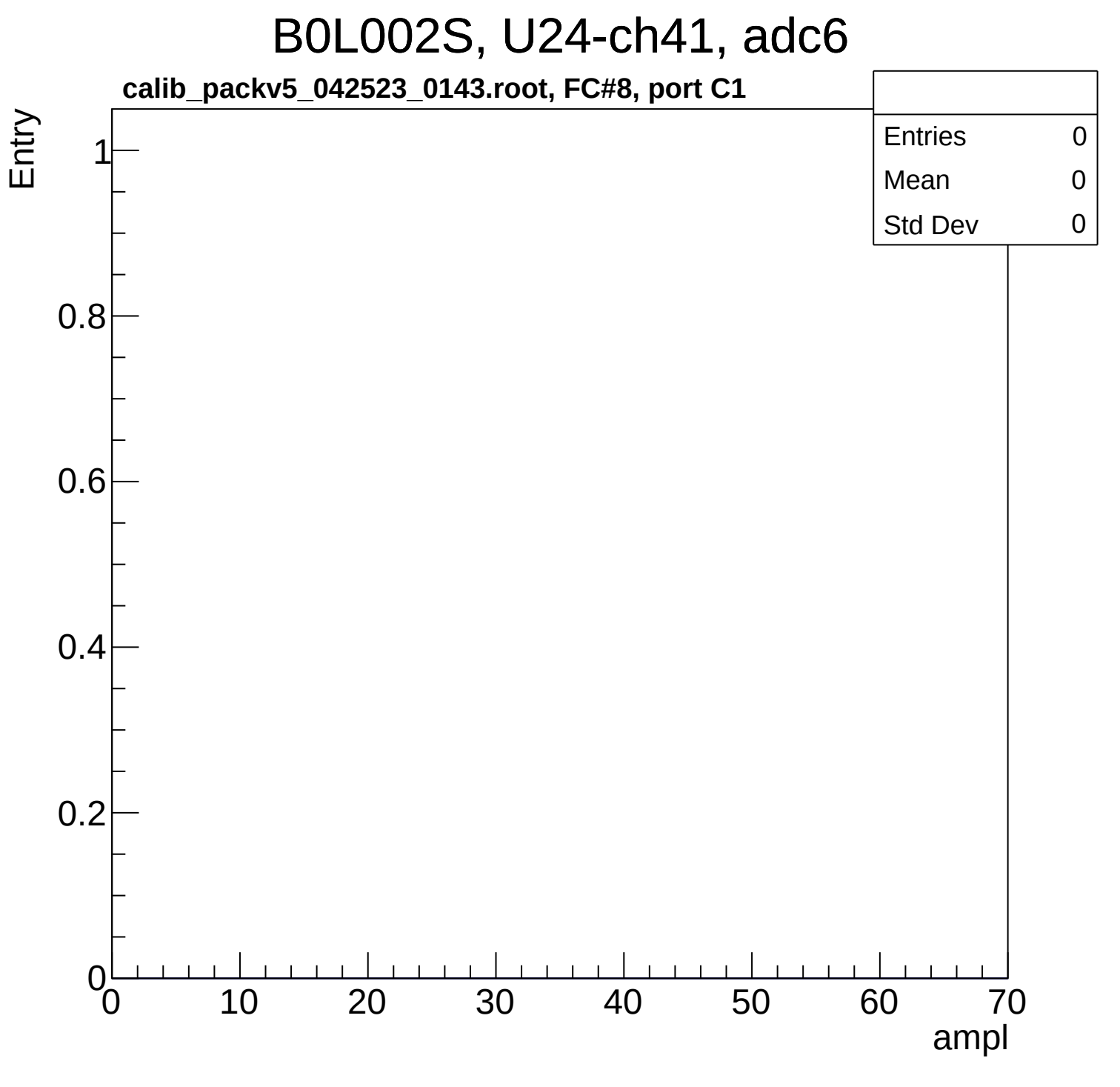
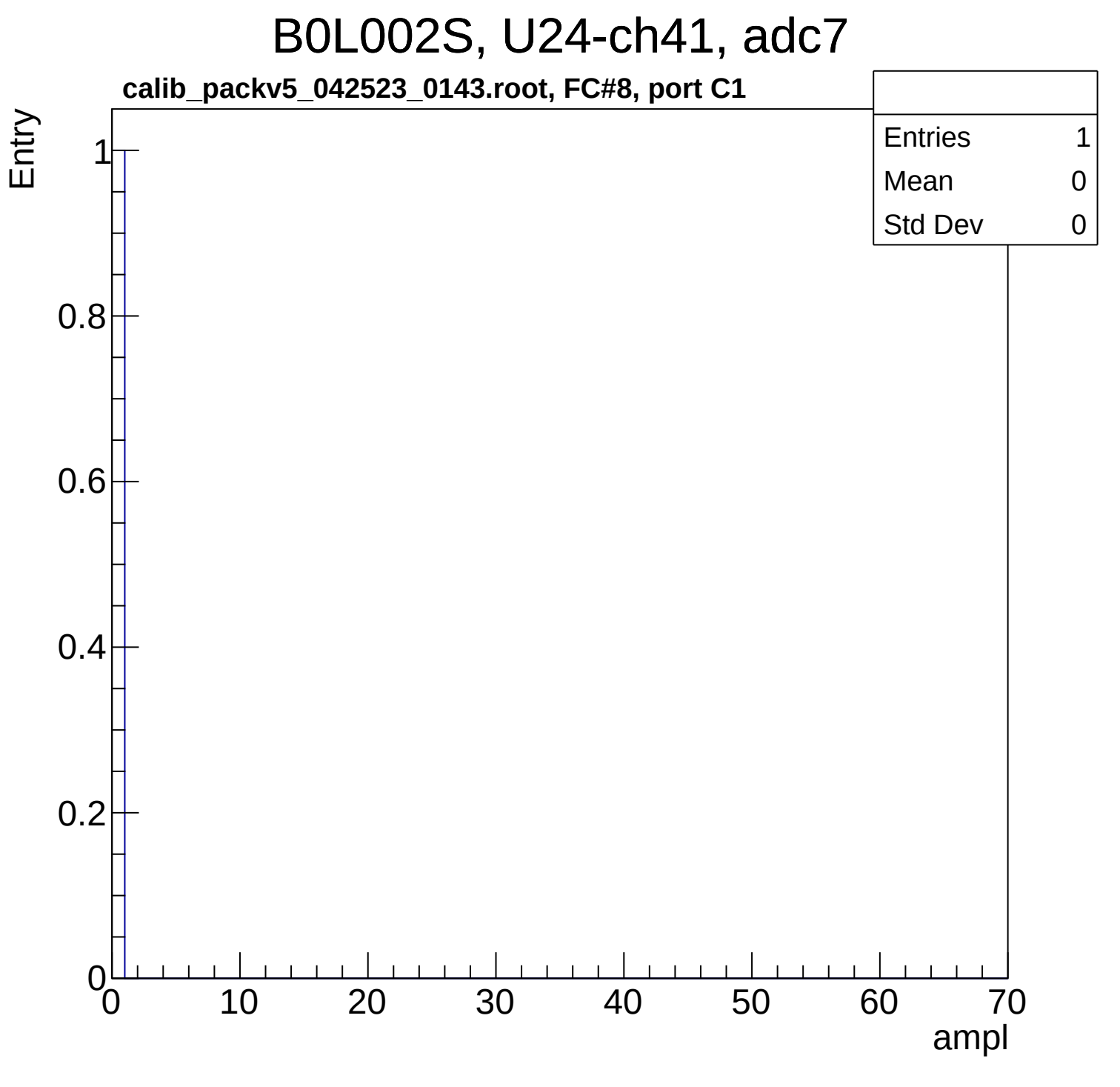
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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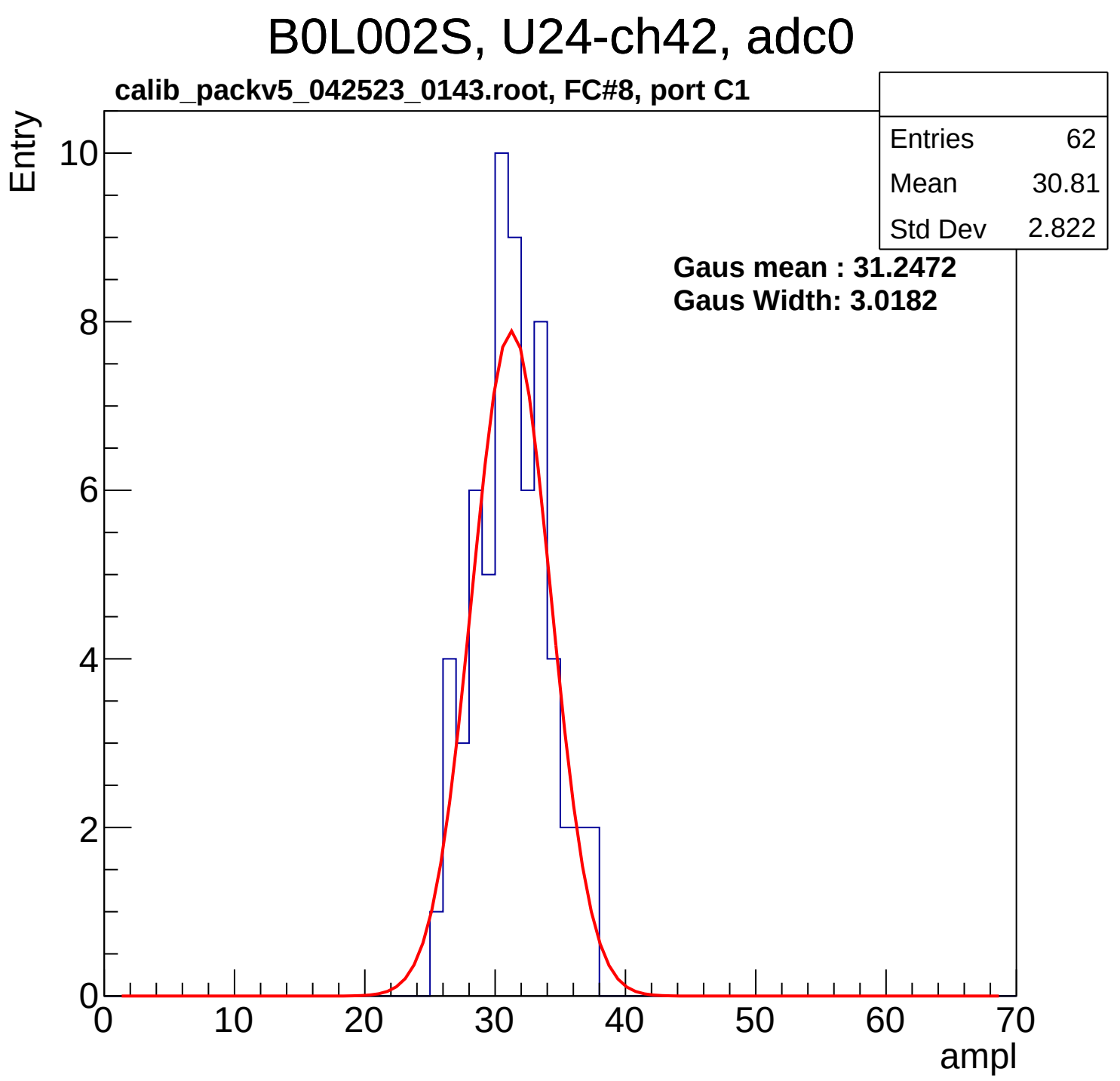
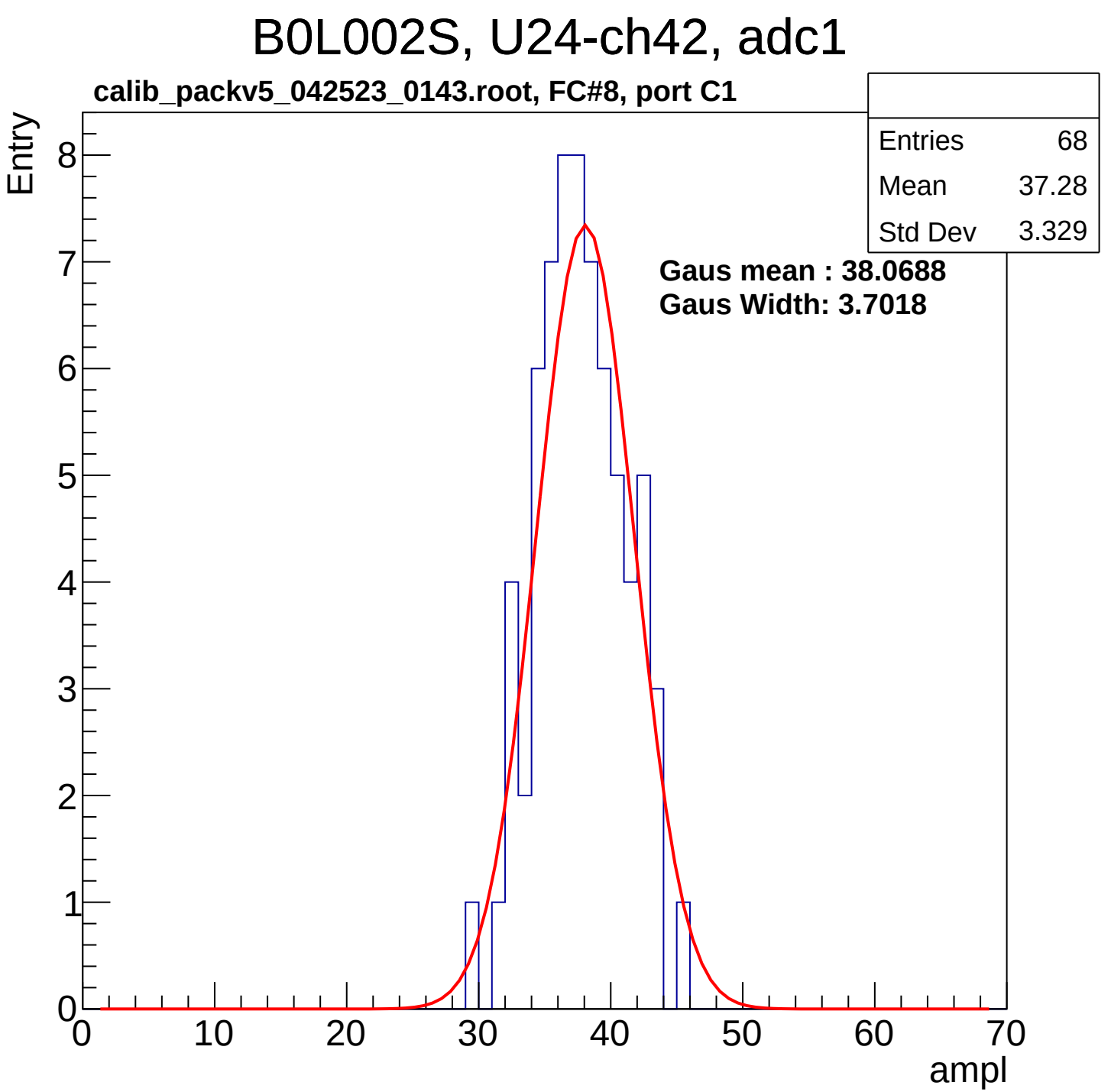
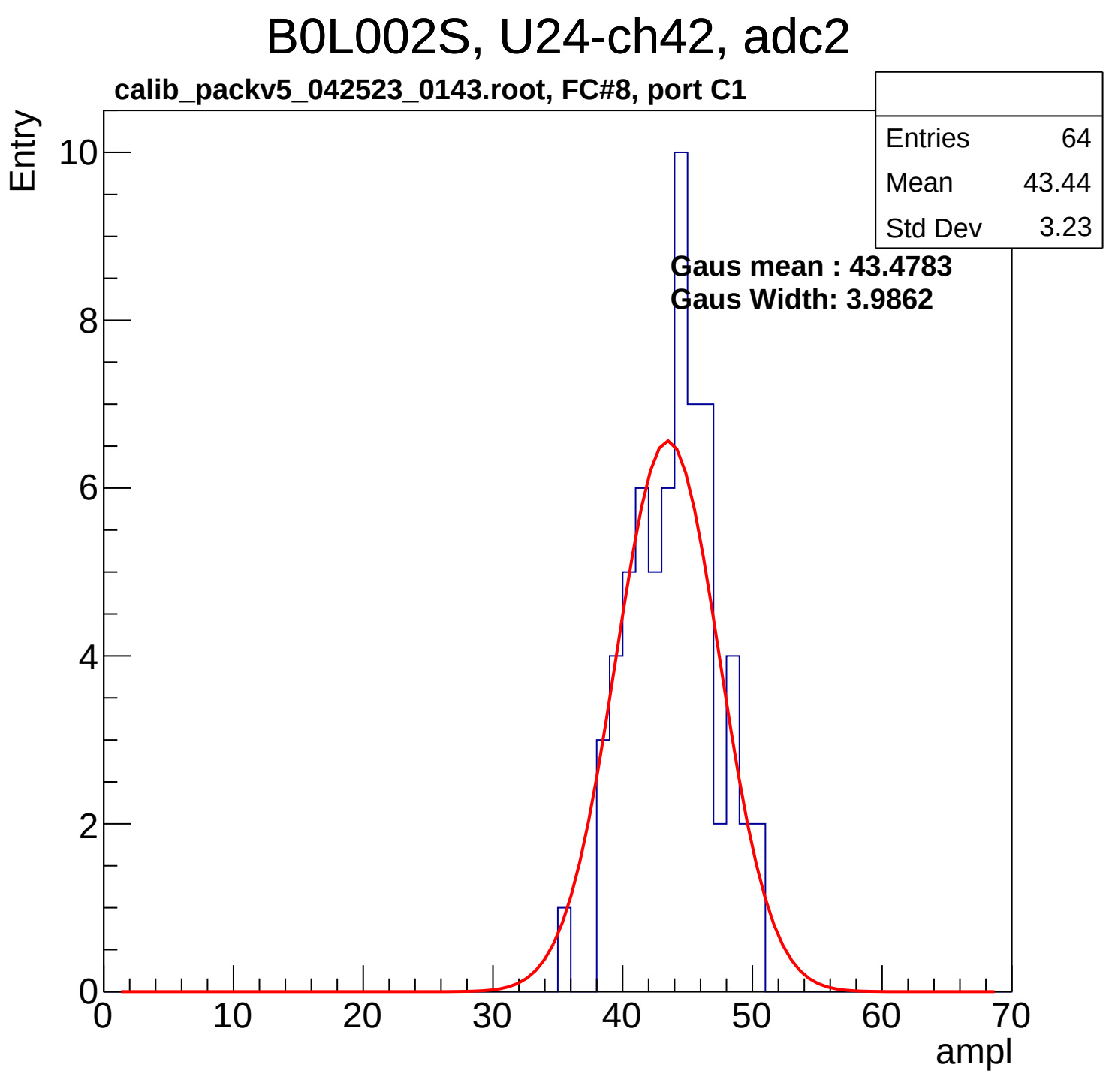
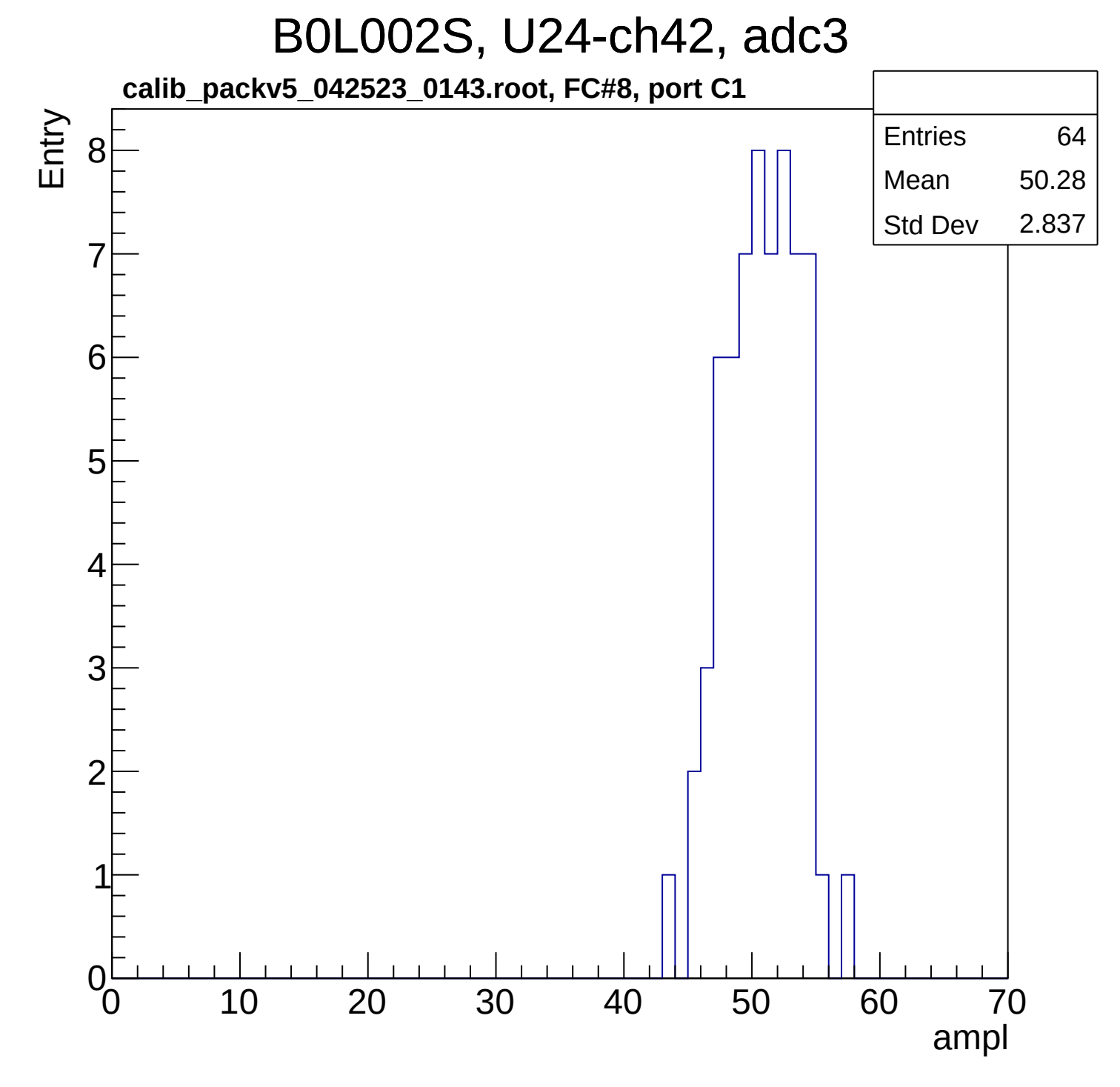
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.81
Std Dev 2.822
Gaus mean : 31.2472
Gaus Width: 3.0182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.28
Std Dev 3.329
Gaus mean : 38.0688
Gaus Width: 3.7018
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.44
Std Dev 3.23
Gaus mean : 43.4783
Gaus Width: 3.9862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.28
Std Dev 2.837
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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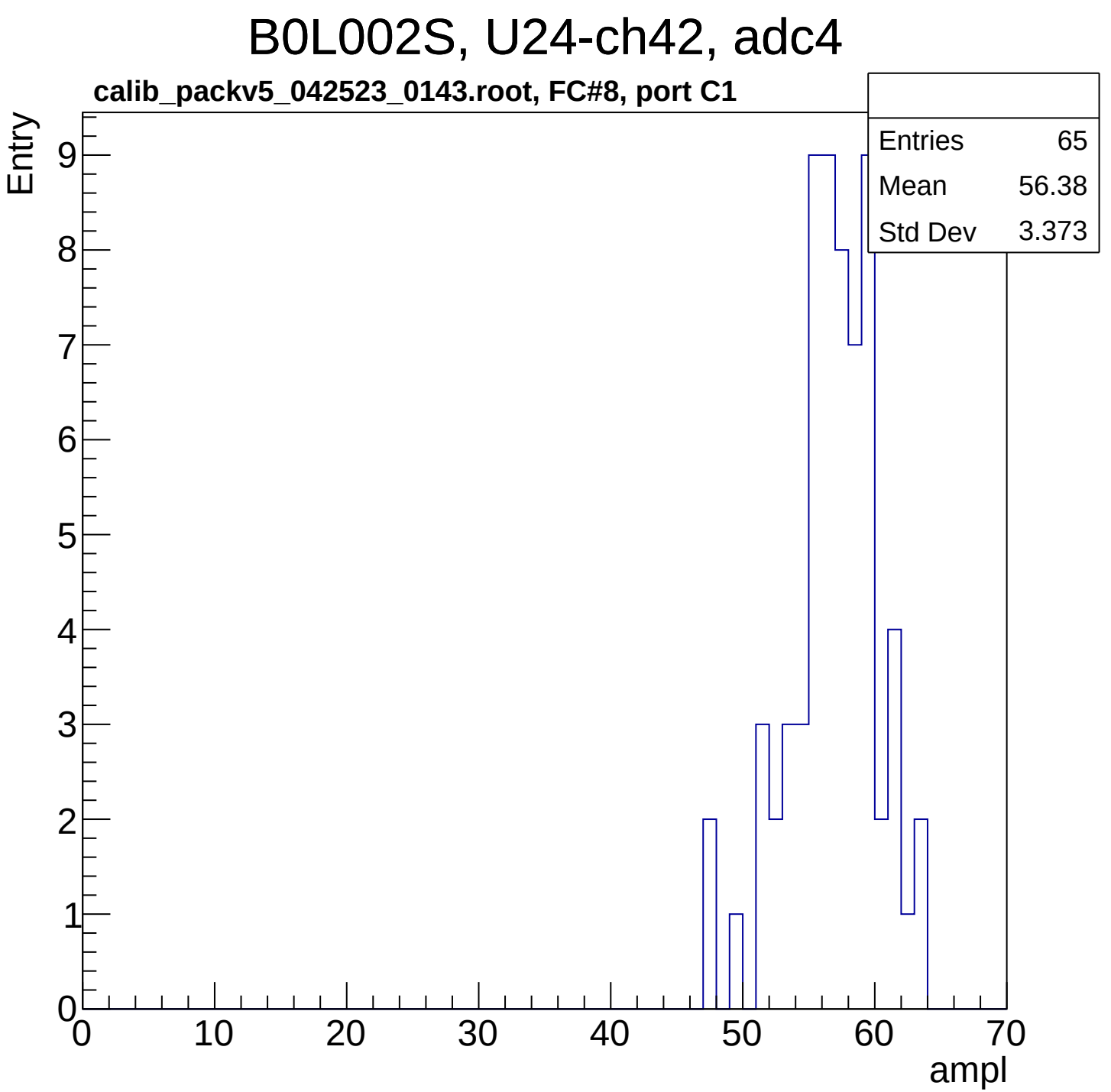
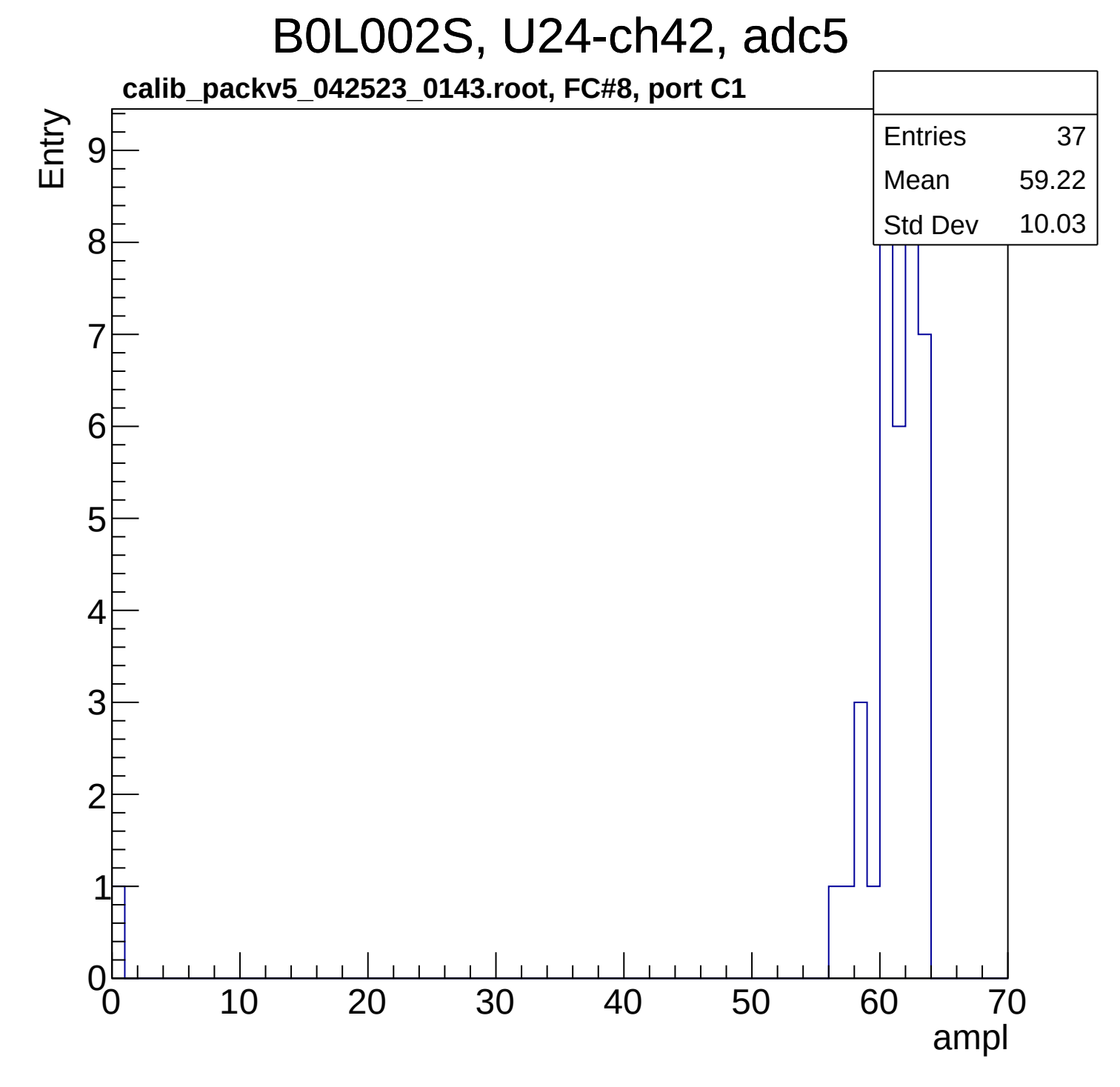
40
50
60
70


B0L002S, U24-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.38
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.22
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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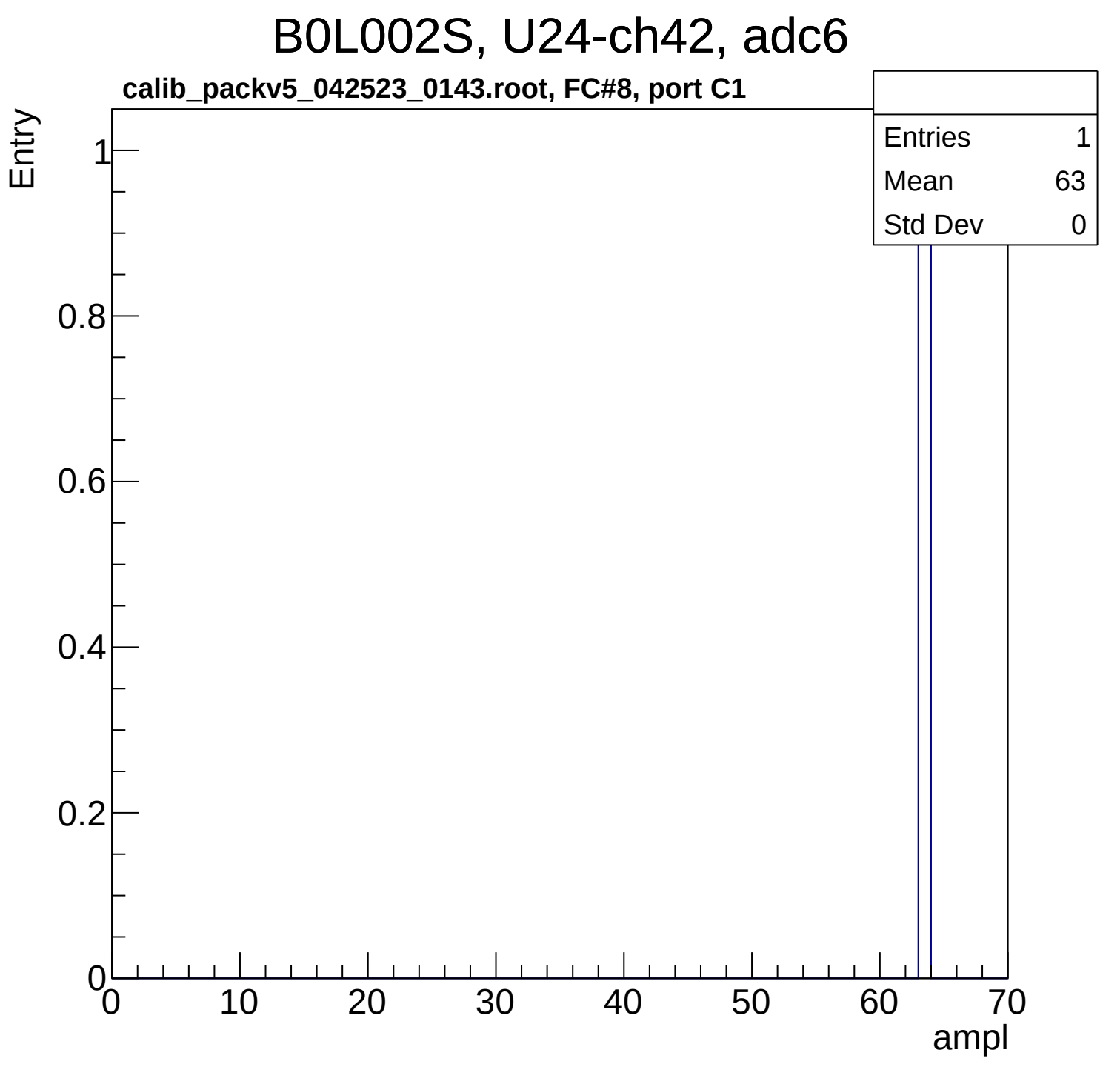
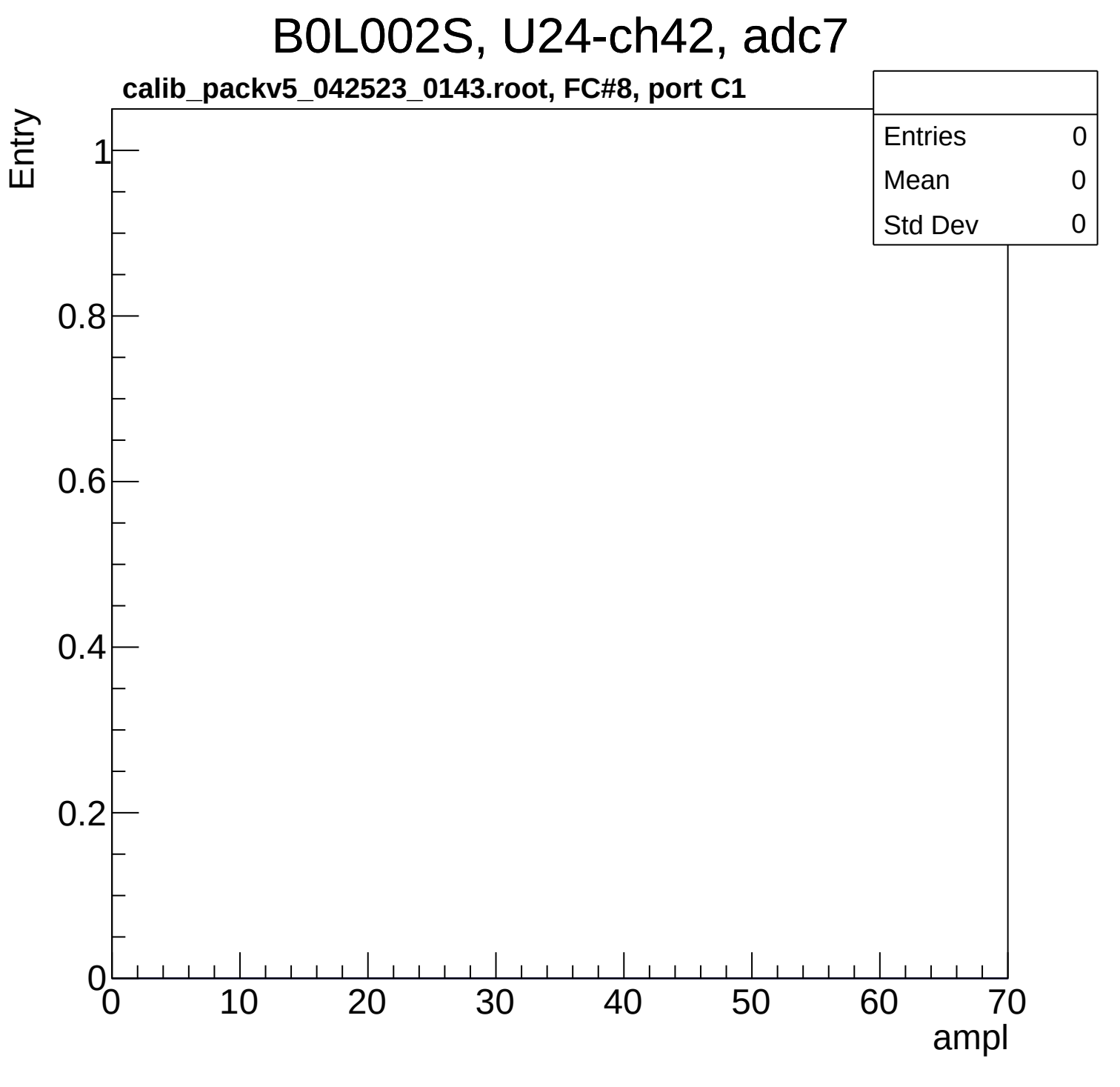
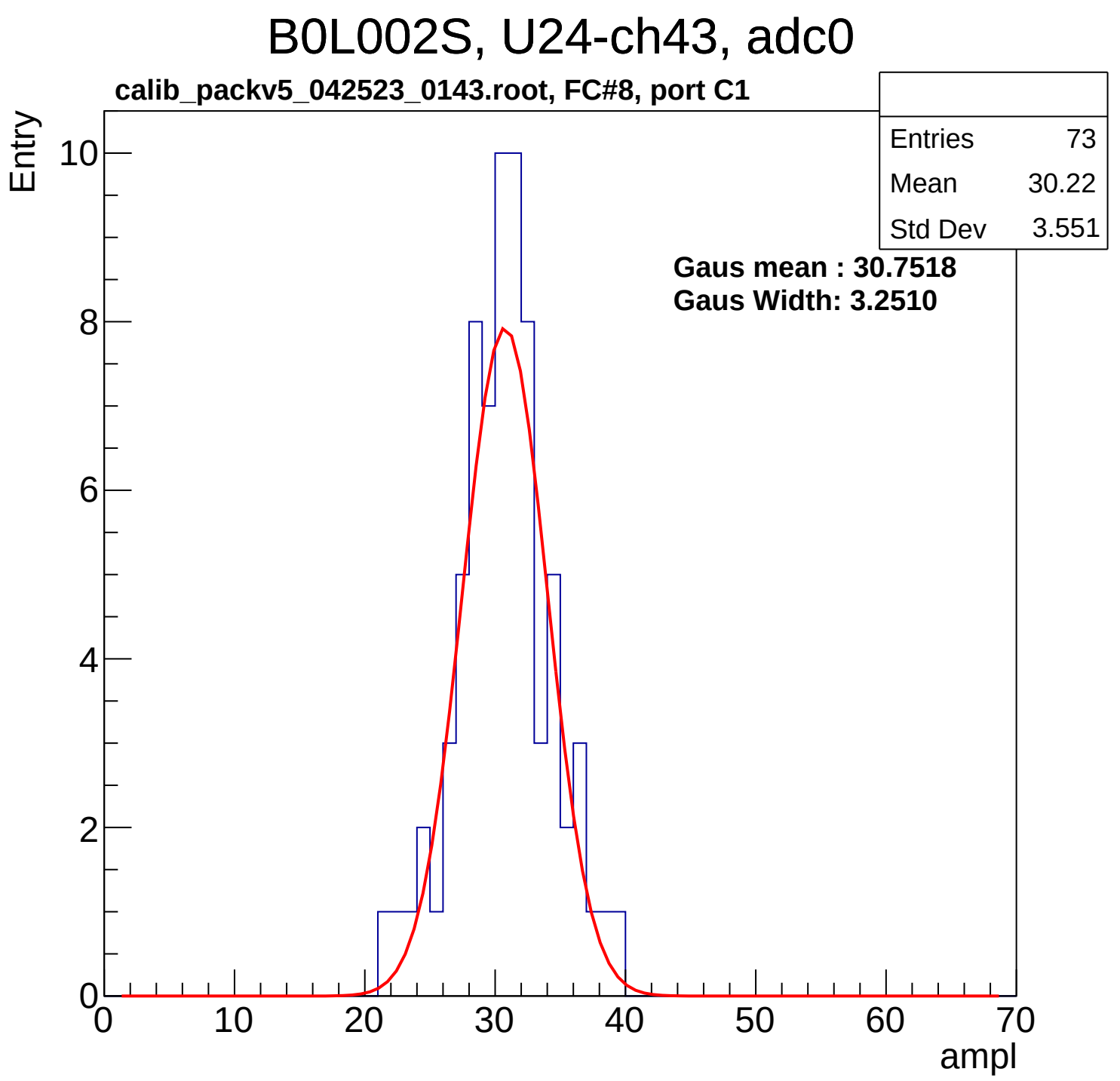
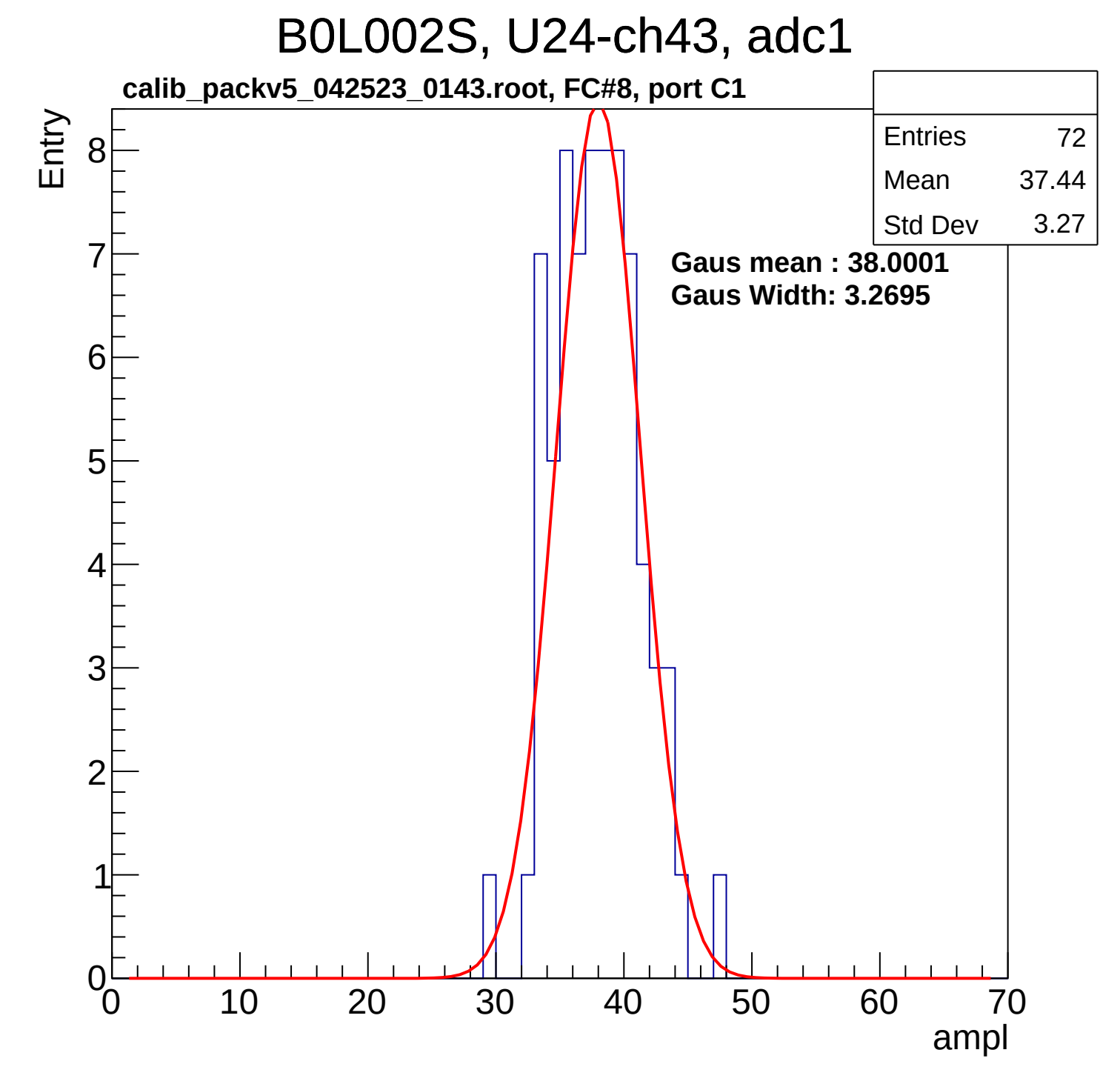

B0L002S, U24-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.22
Std Dev 3.551
Gaus mean : 30.7518
Gaus Width: 3.2510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.44
Std Dev 3.27
Gaus mean : 38.0001
Gaus Width: 3.2695
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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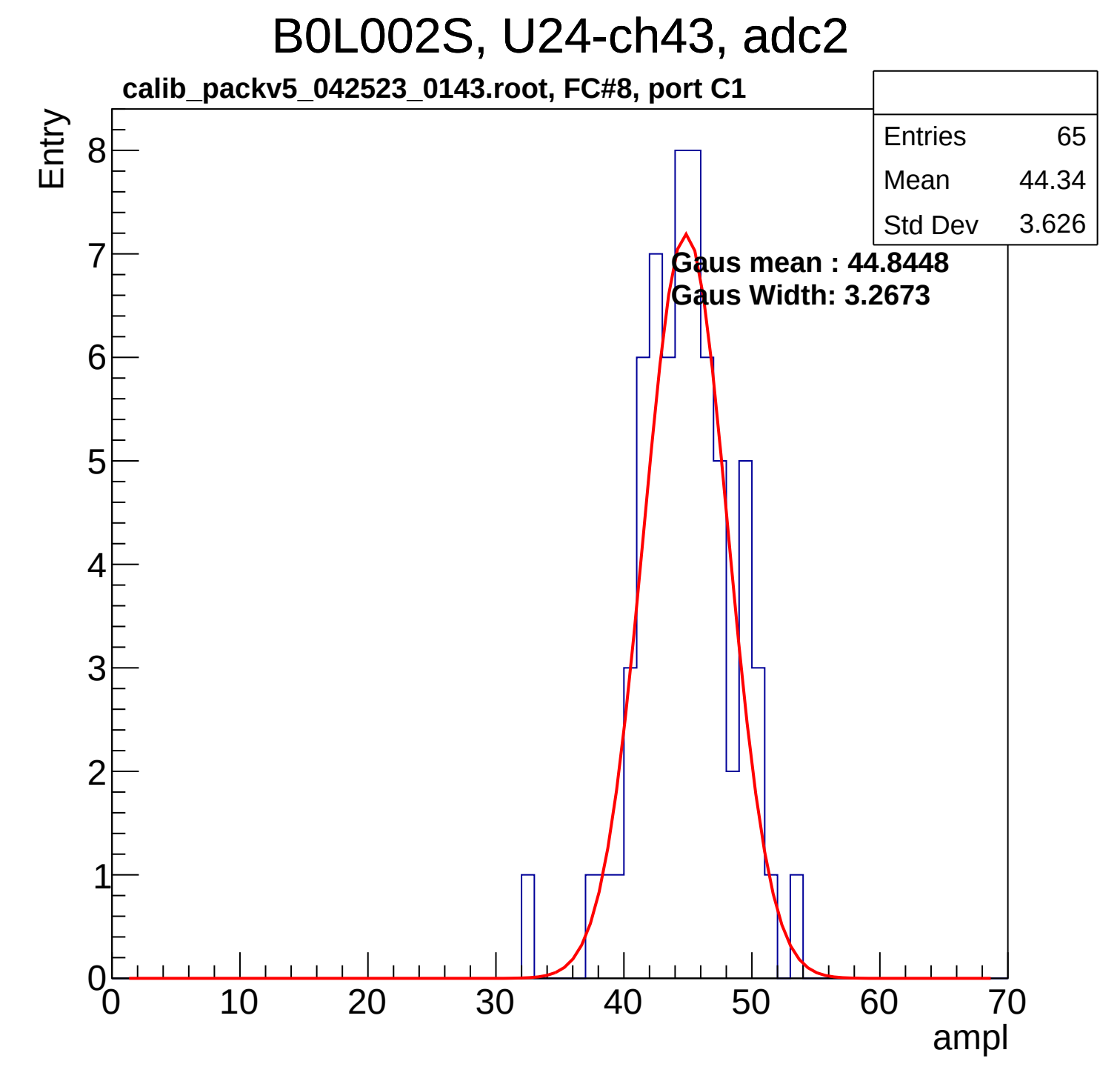
B0L002S, U24-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.34
Std Dev 3.626
Gaus mean : 44.8448
Gaus Width: 3.2673
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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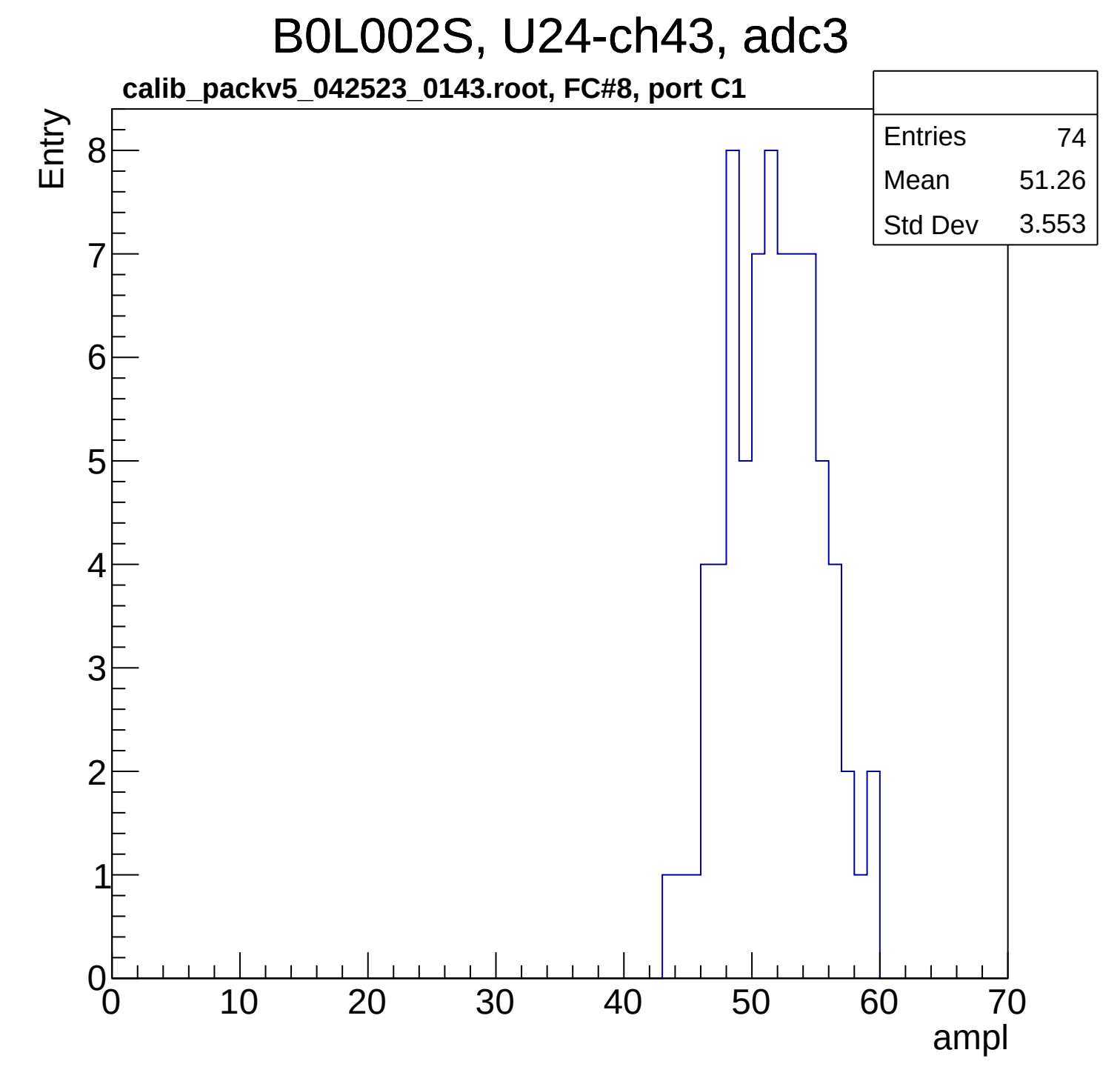
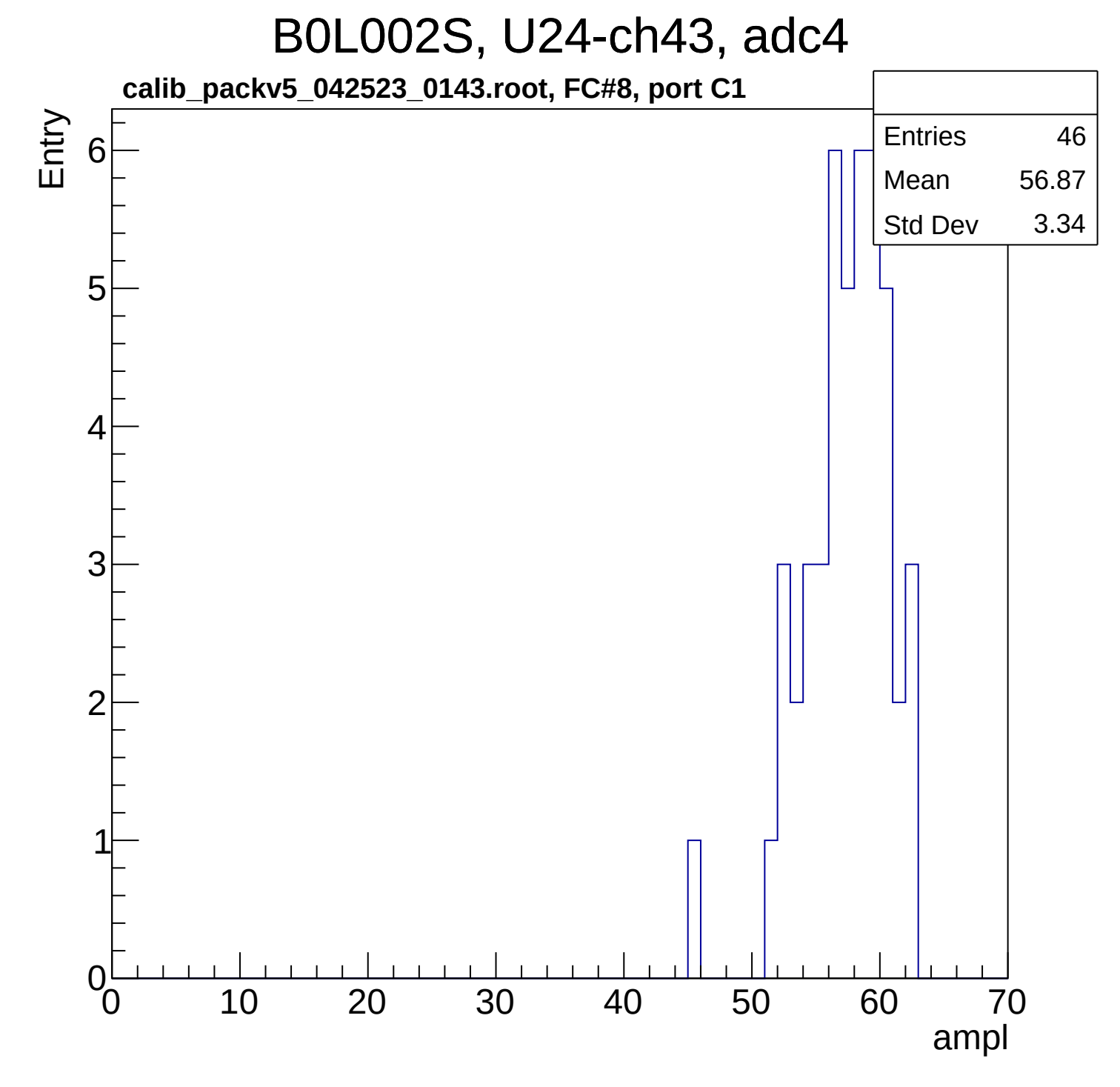
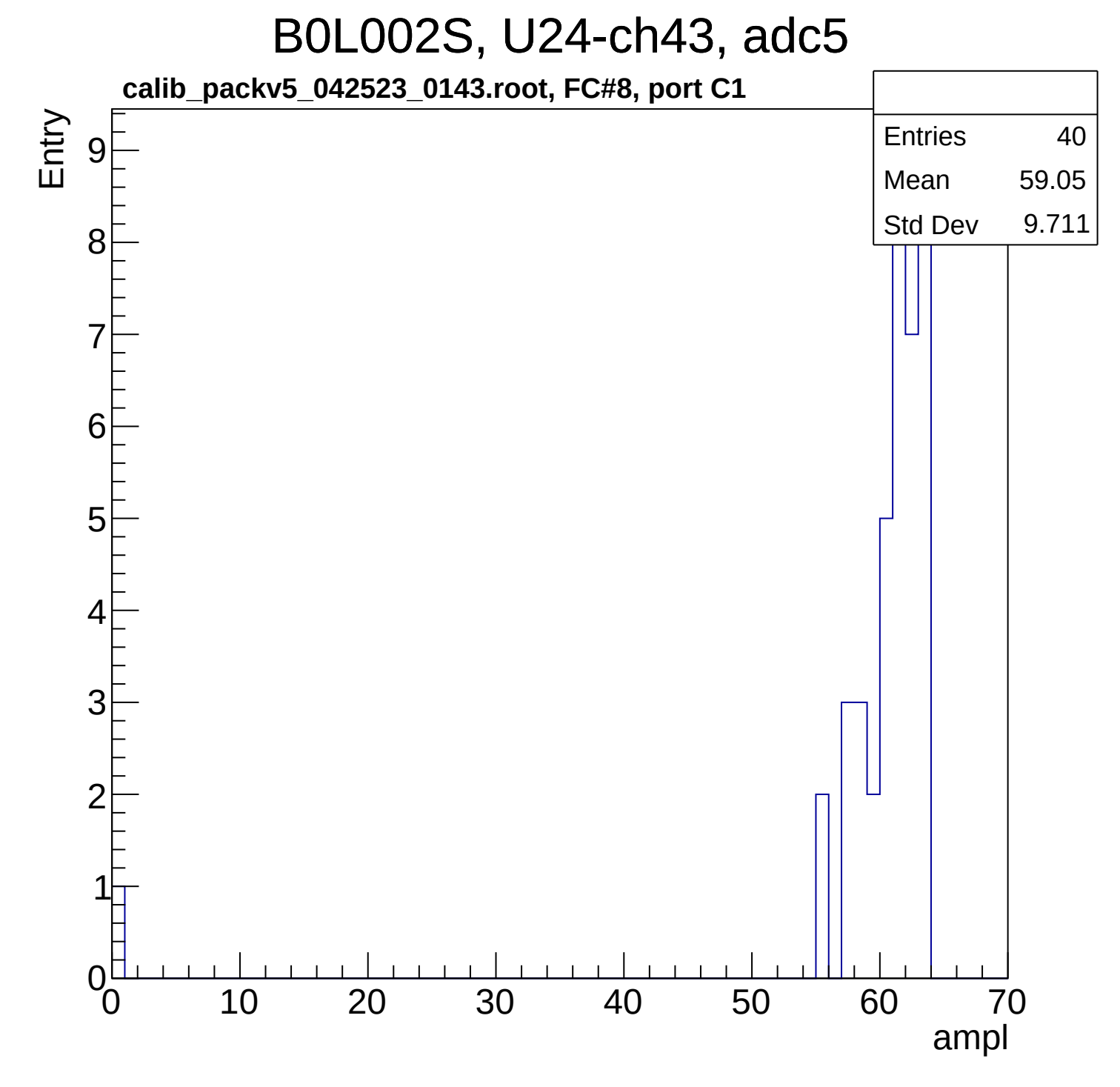
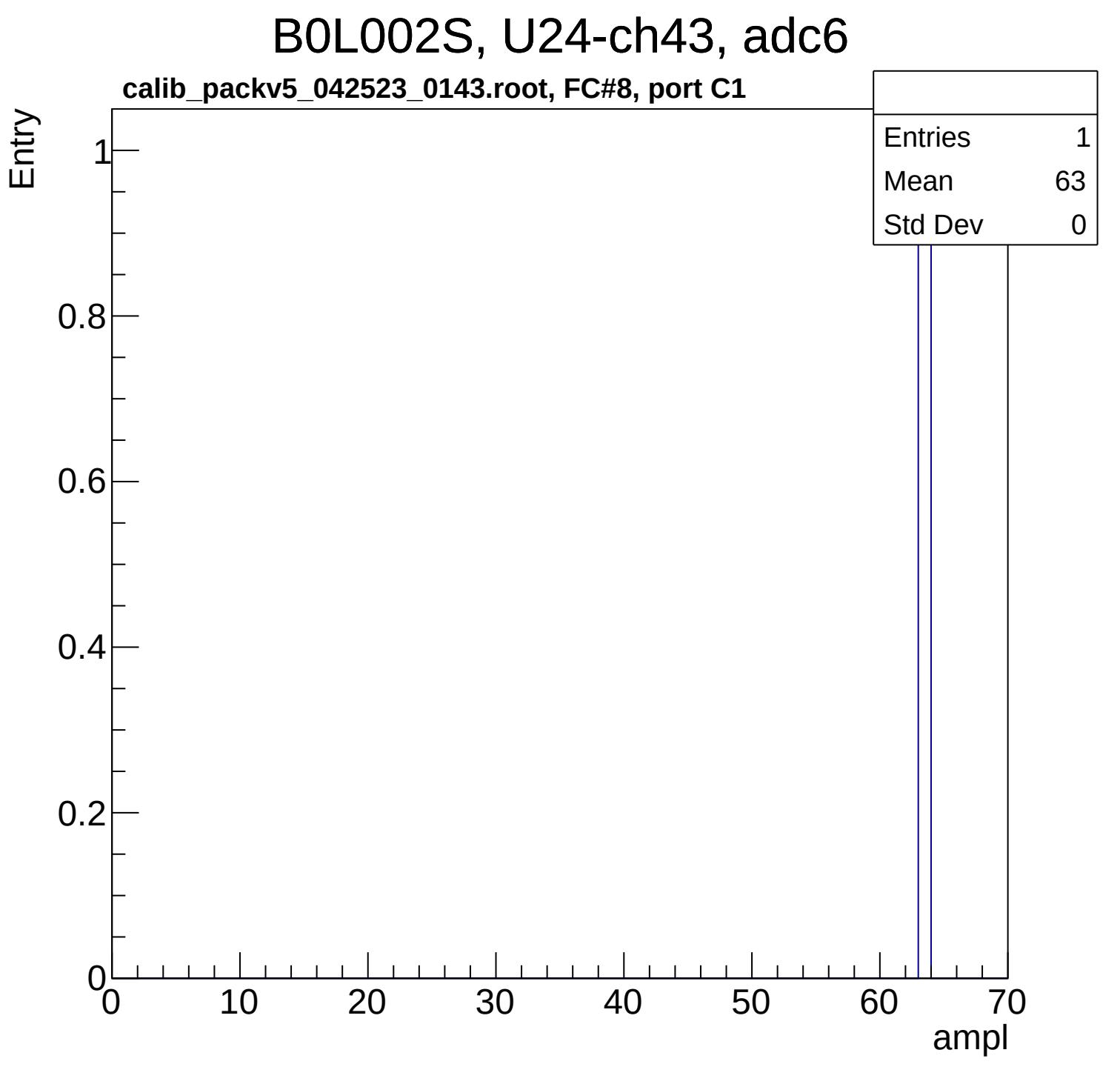
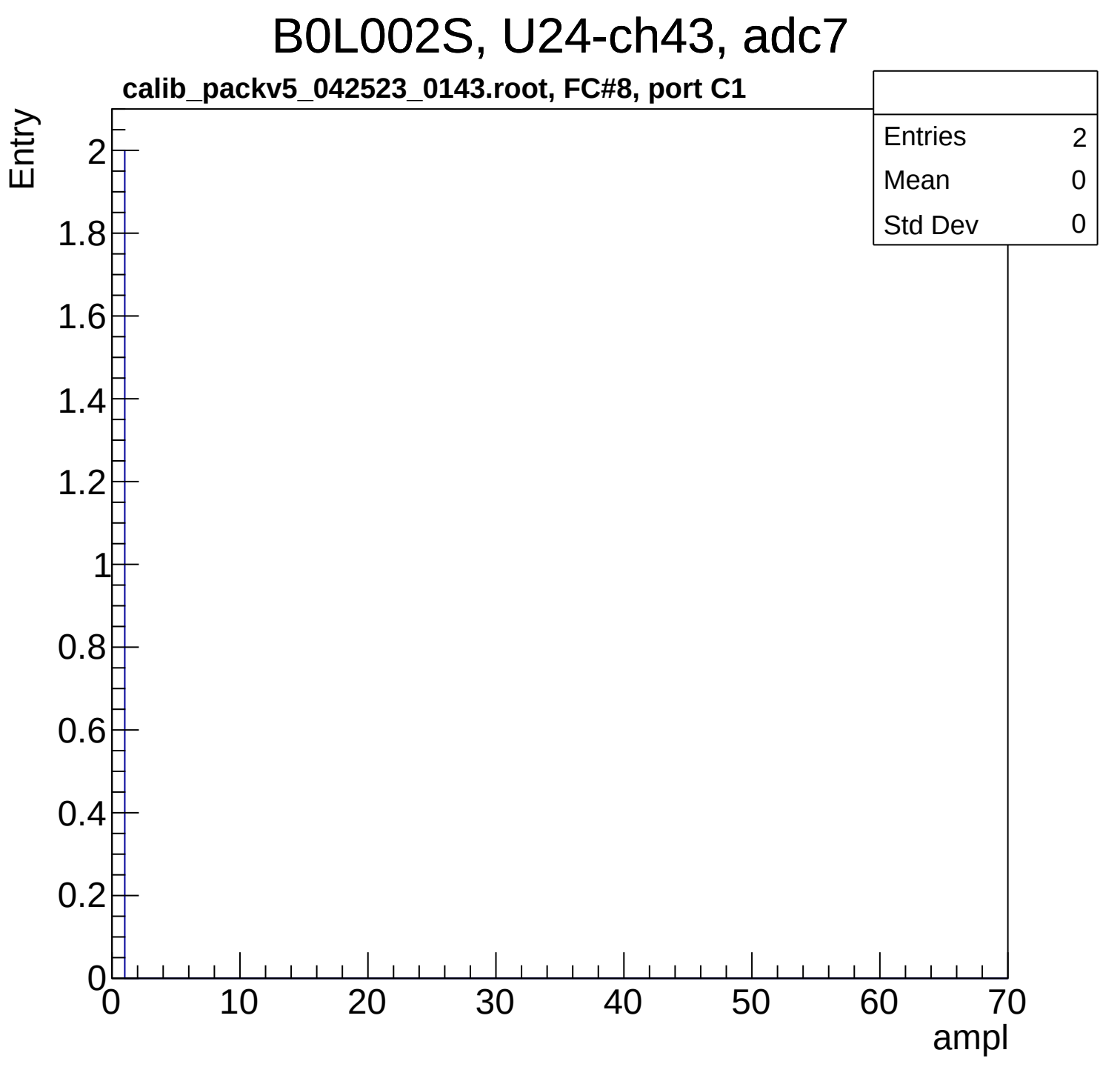
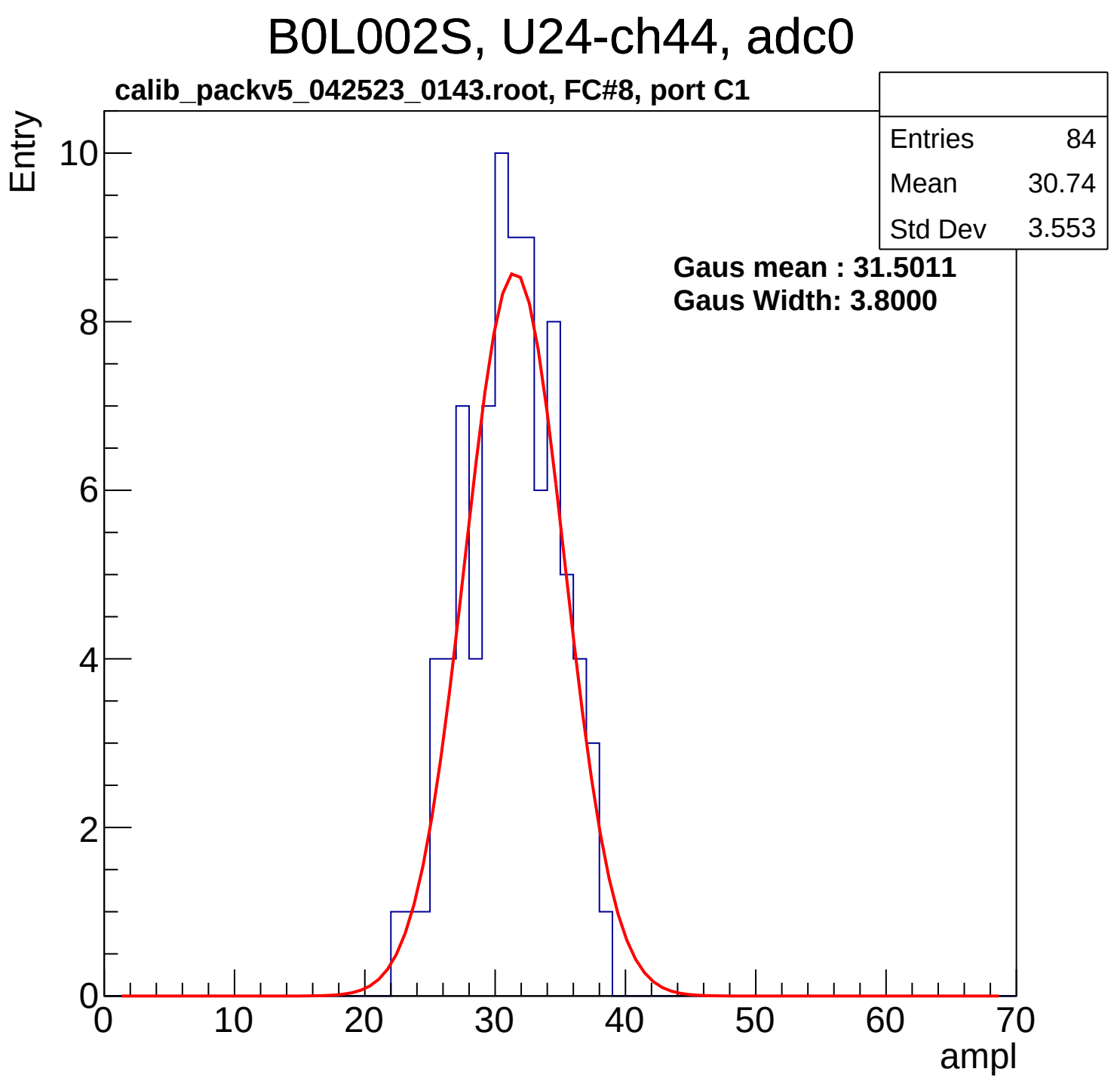

B0L002S, U24-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 51.26
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

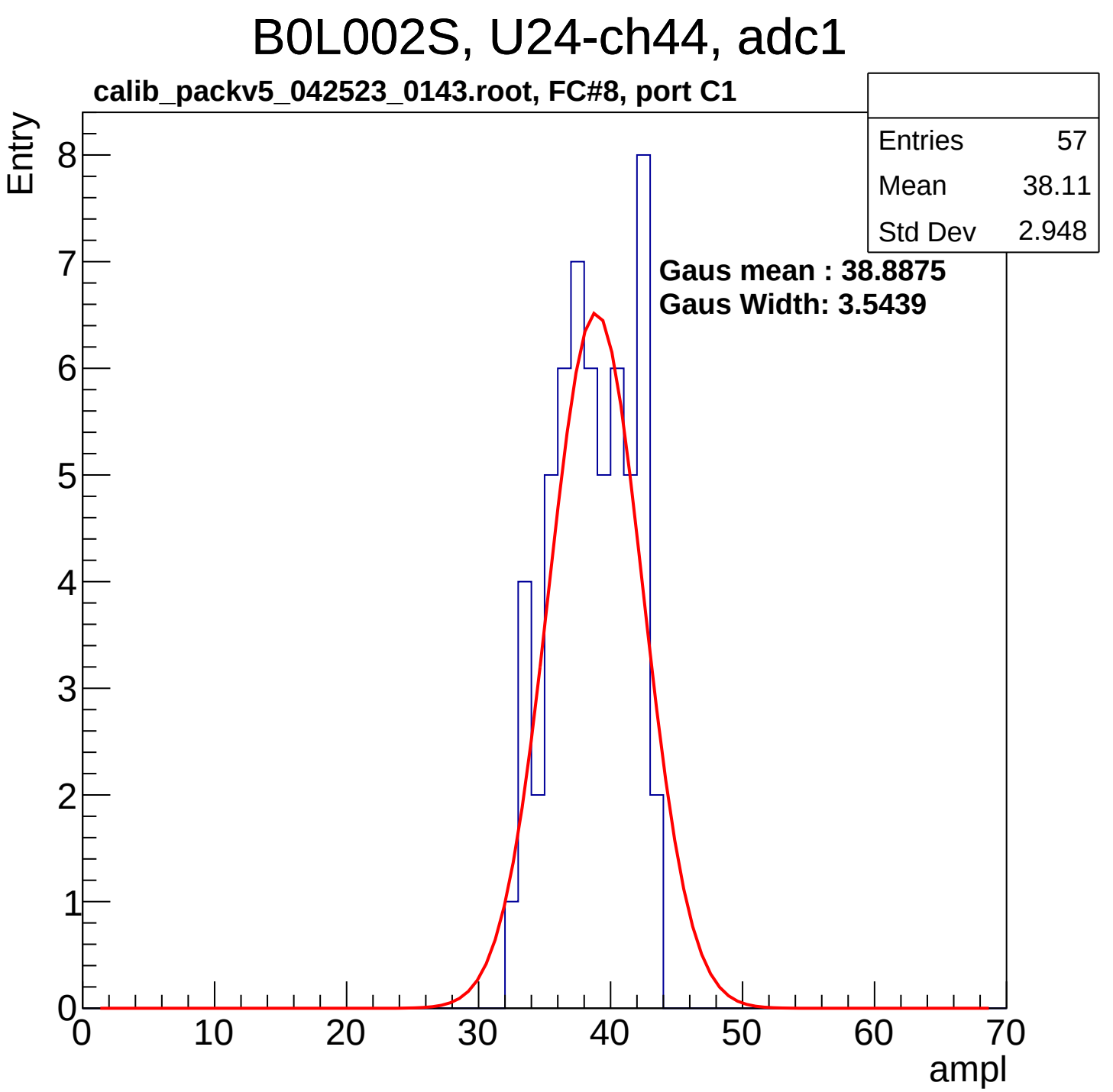

B0L002S, U24-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 56.87
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

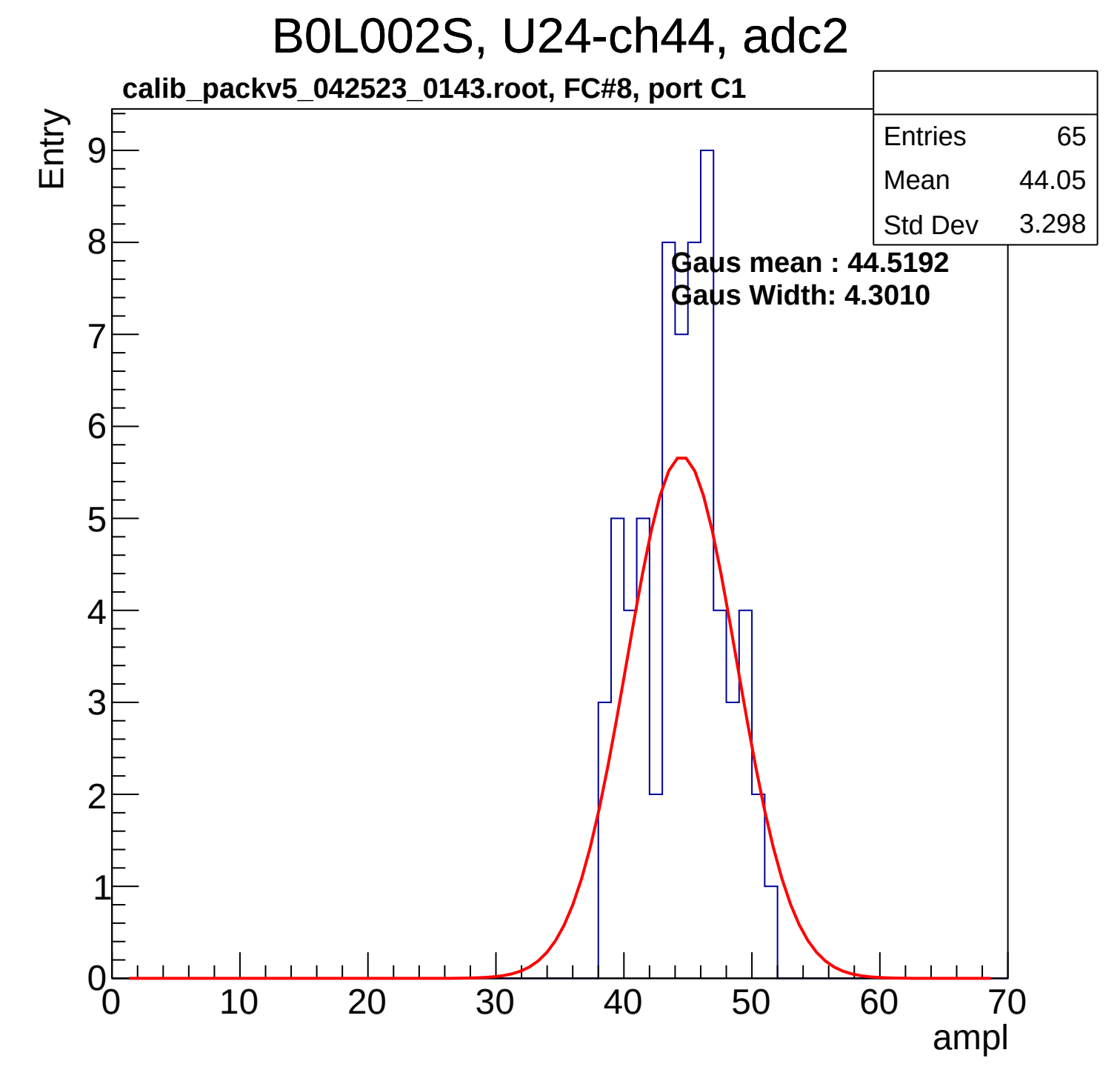

B0L002S, U24-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.05
Std Dev 9.711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

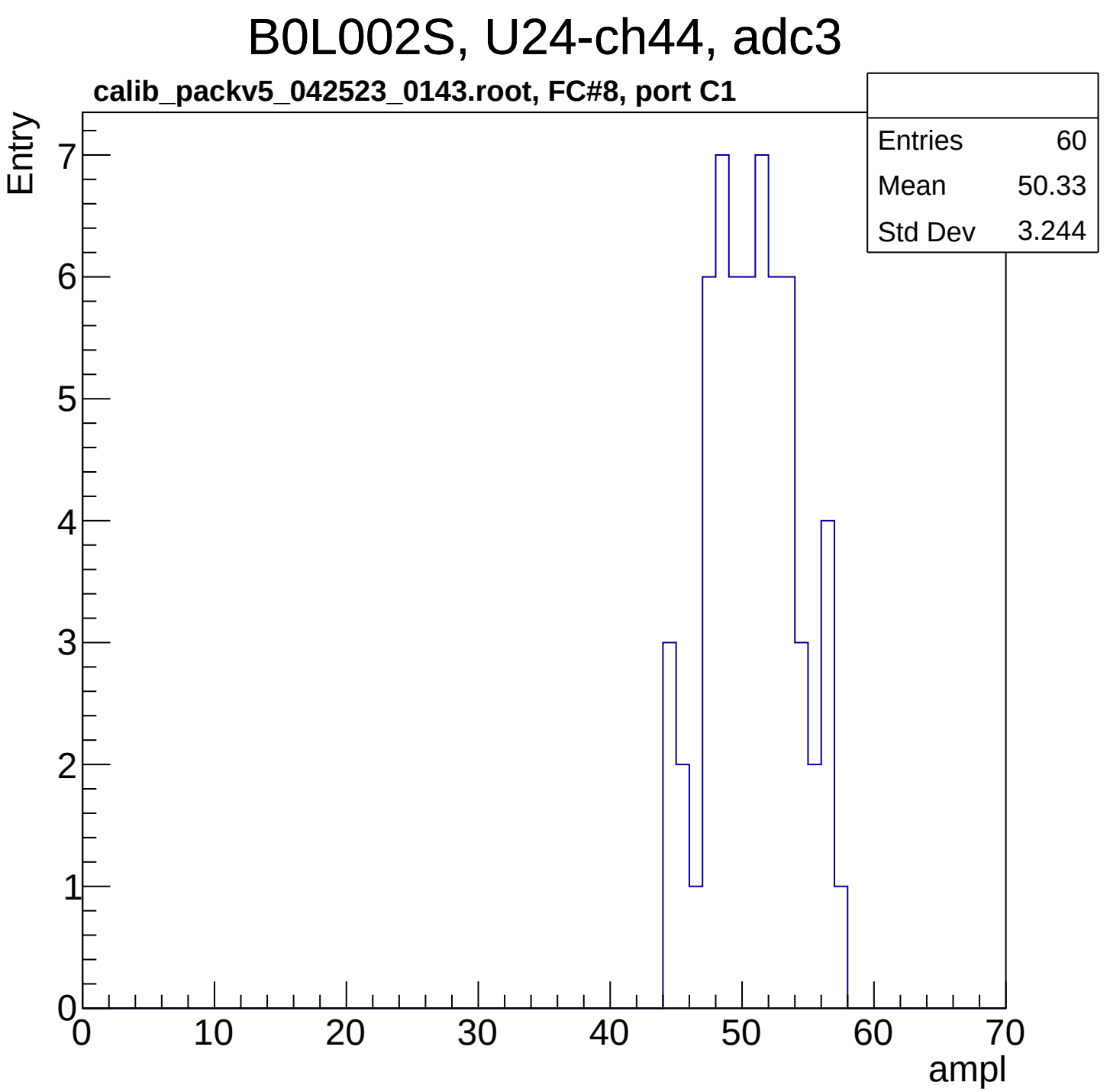

B0L002S, U24-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

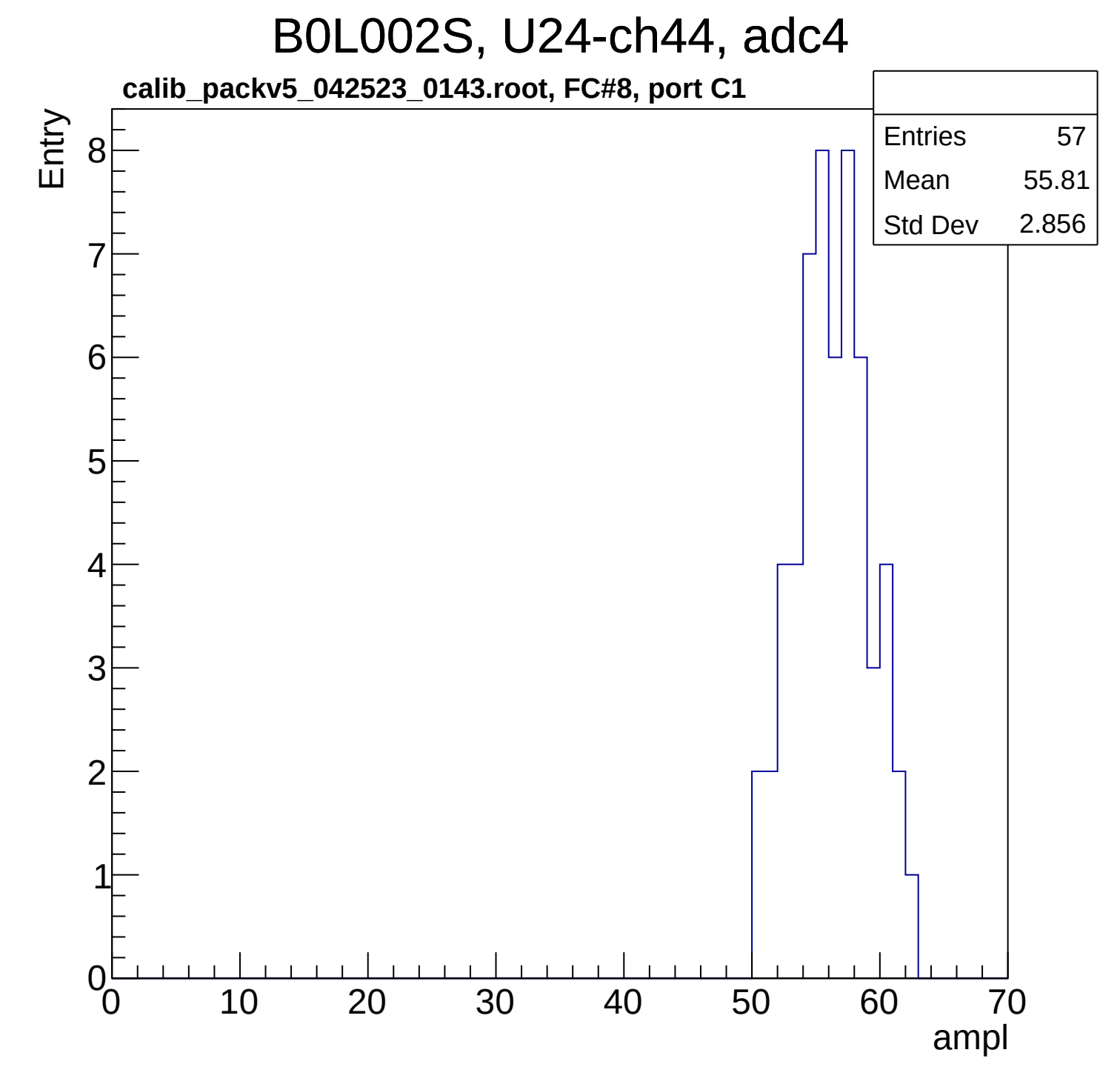

B0L002S, U24-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

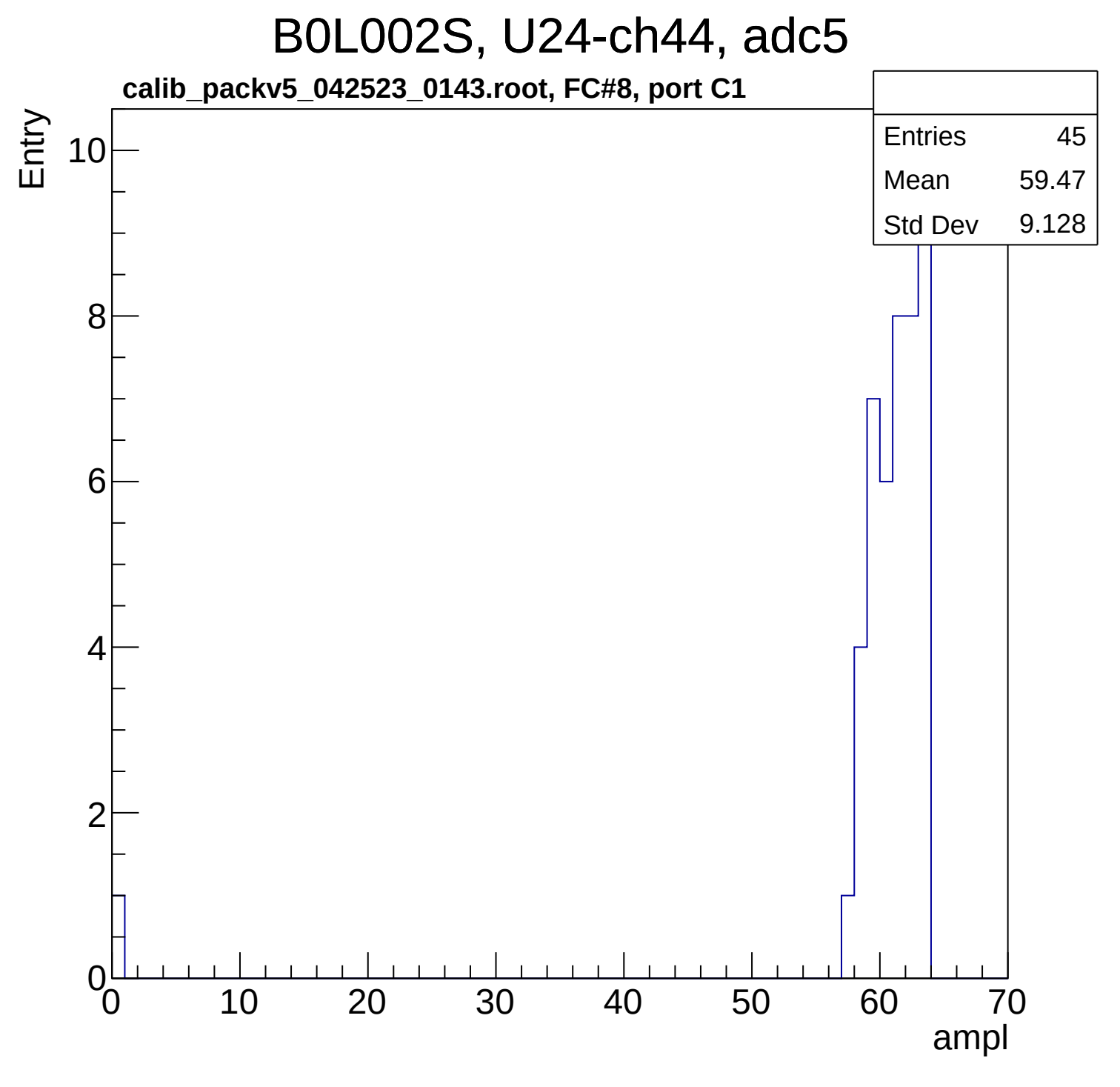

B0L002S, U24-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.74
Std Dev 3.553
Gaus mean : 31.5011
Gaus Width: 3.8000
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

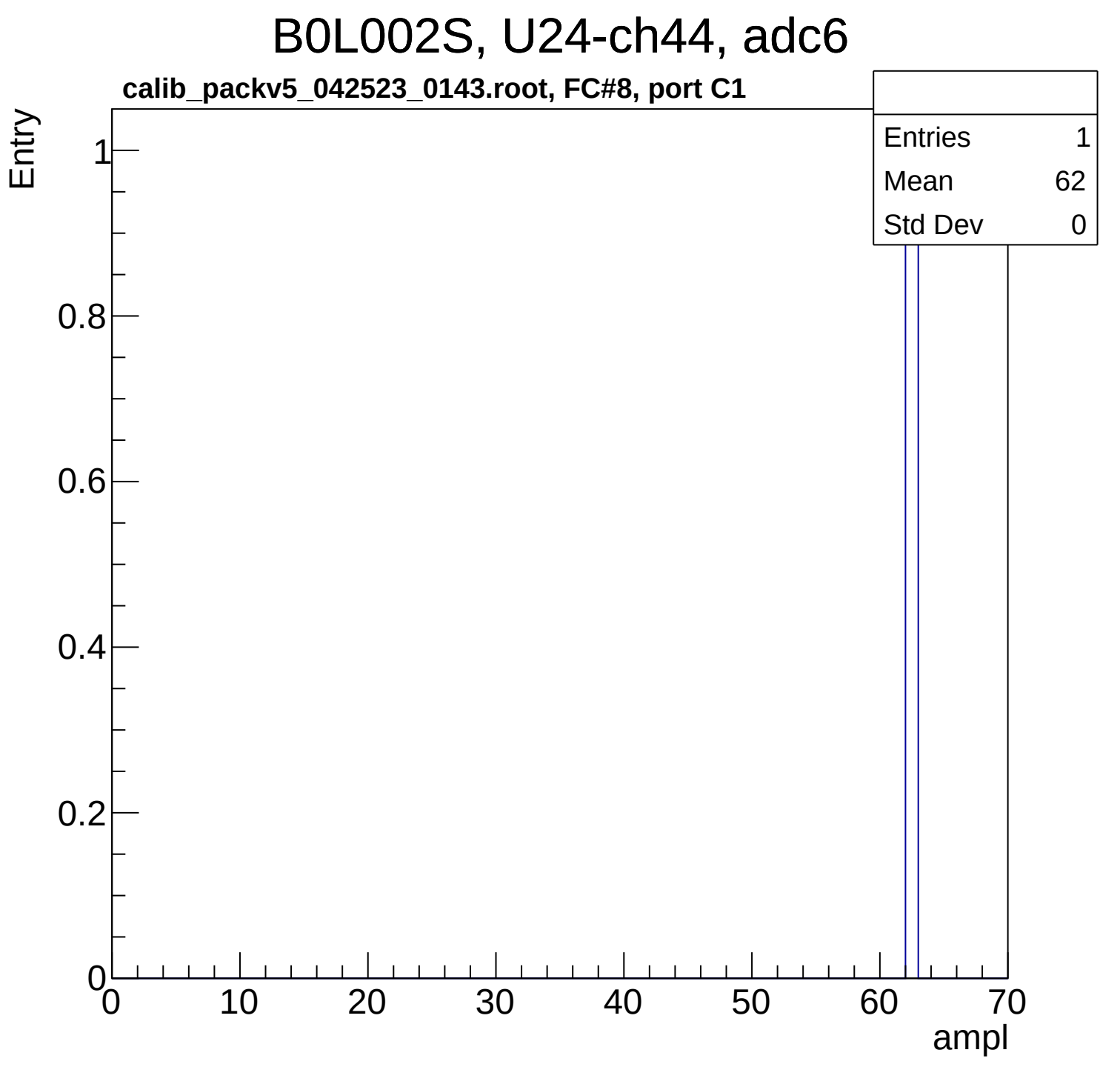

B0L002S, U24-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 38.11
Std Dev 2.948
Gaus mean : 38.8875
Gaus Width: 3.5439
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

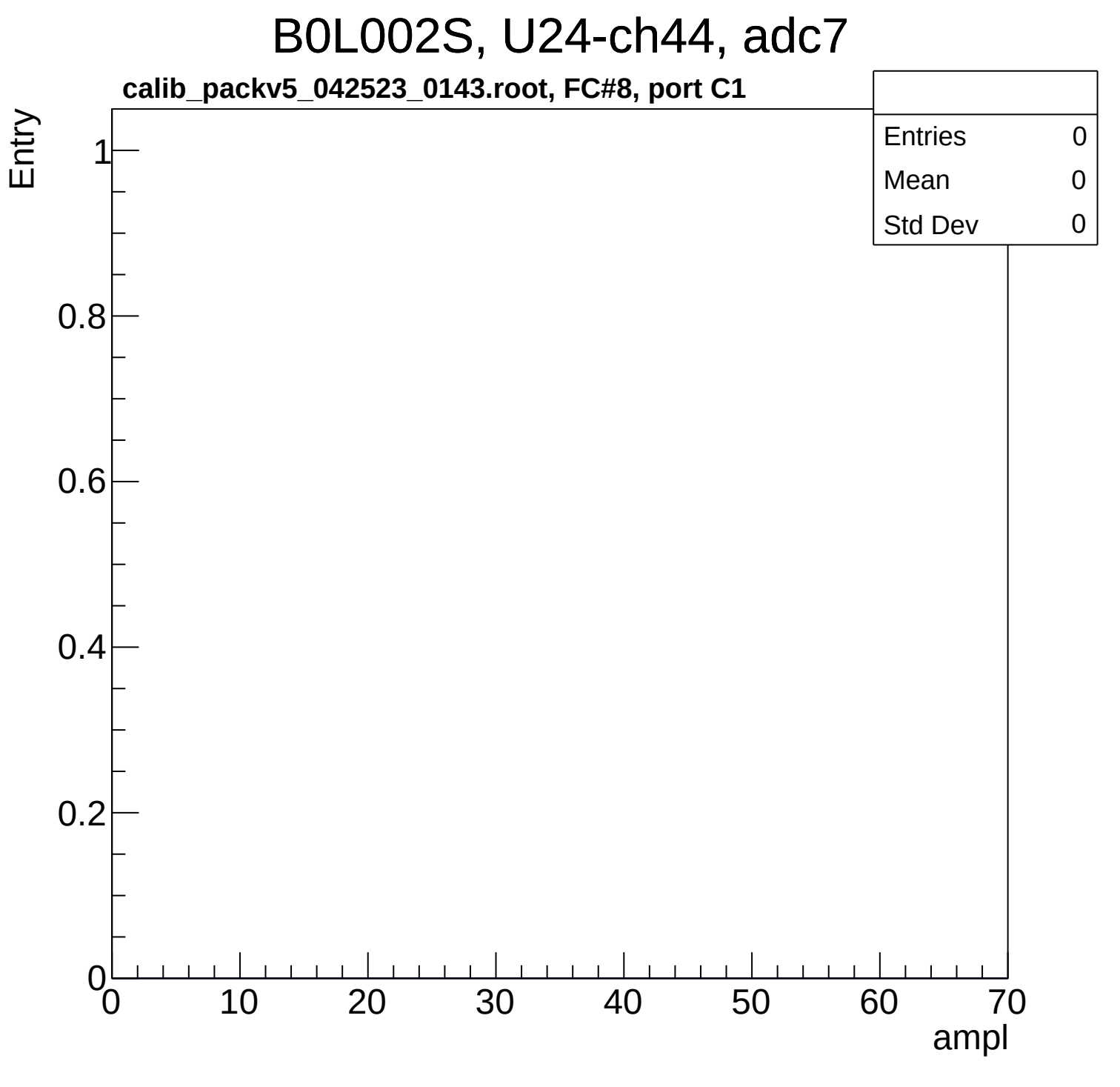

B0L002S, U24-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.05
Std Dev 3.298
Gaus mean : 44.5192
Gaus Width: 4.3010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.33
Std Dev 3.244
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.81
Std Dev 2.856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.47
Std Dev 9.128
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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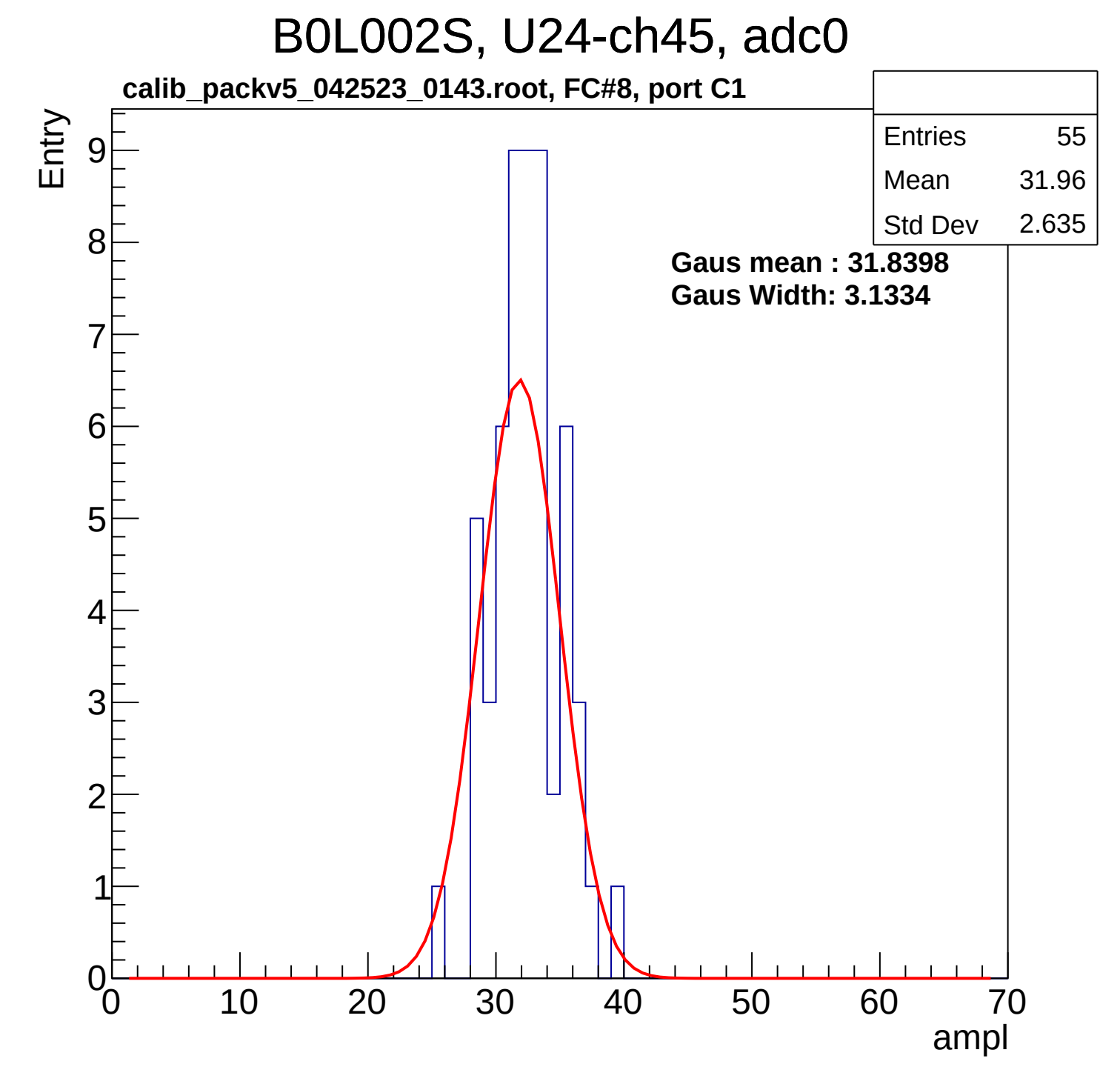
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 31.96
Std Dev 2.635
Gaus mean : 31.8398
Gaus Width: 3.1334
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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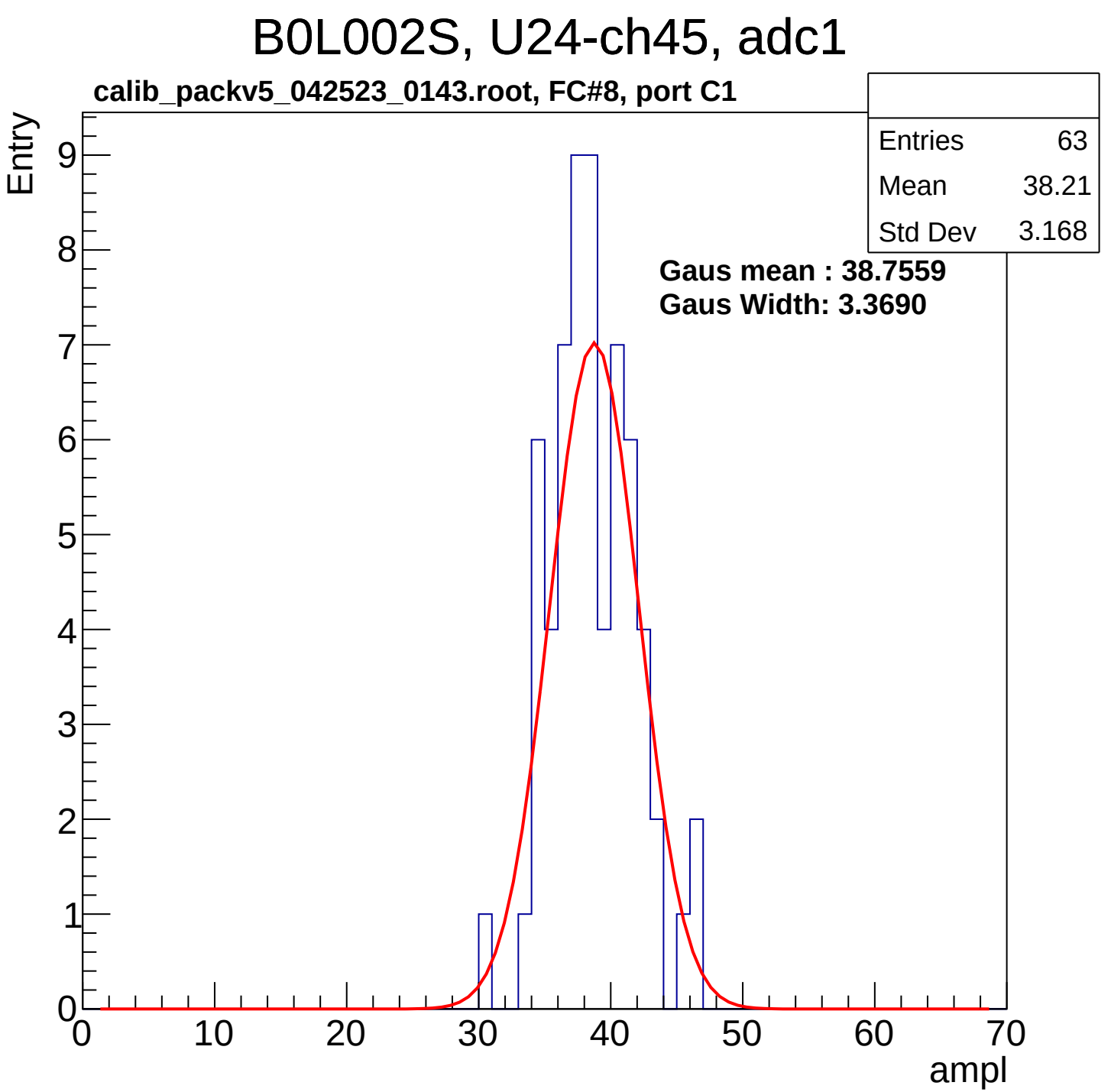
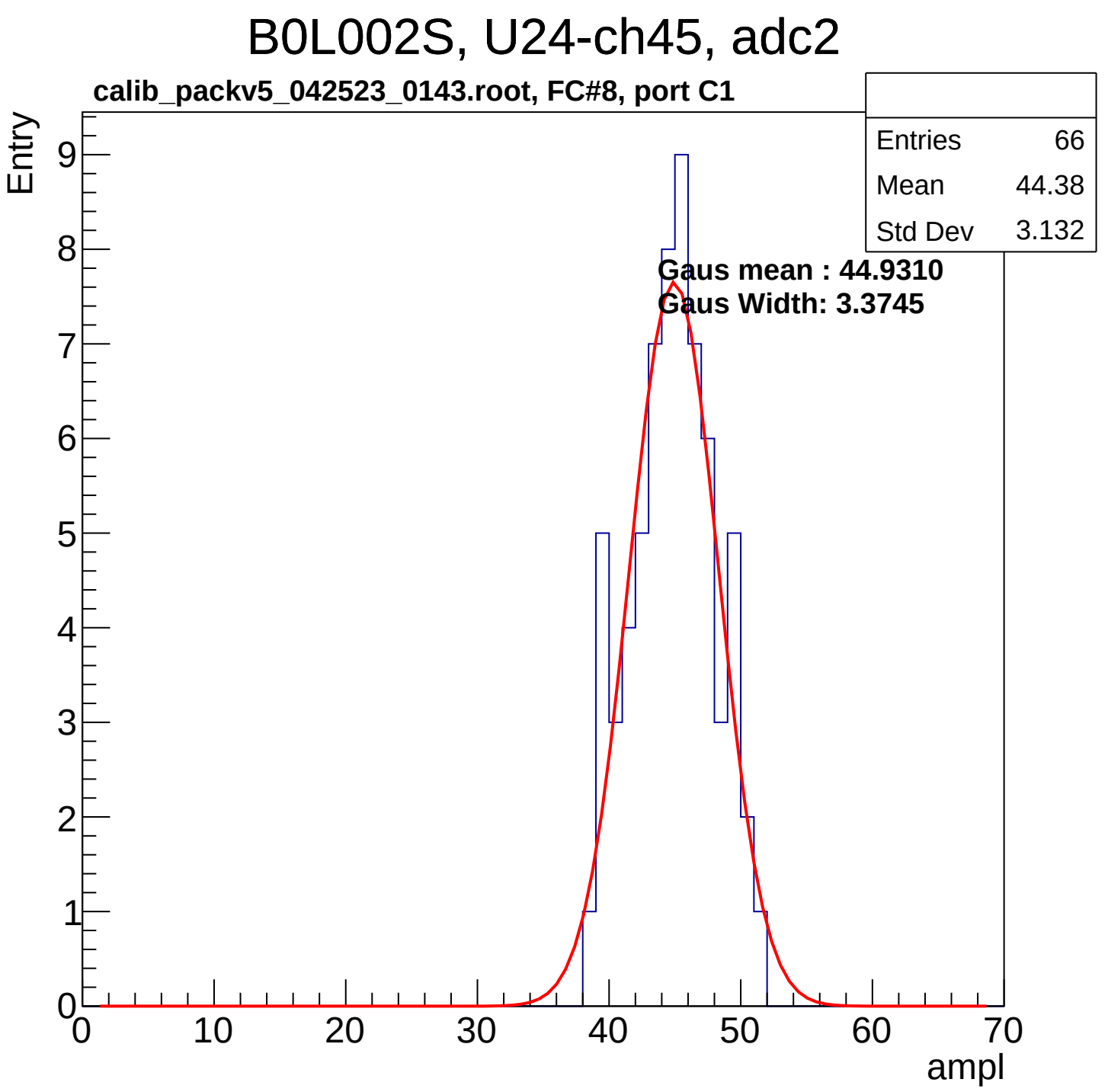

B0L002S, U24-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.21
Std Dev 3.168
Gaus mean : 38.7559
Gaus Width: 3.3690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.38
Std Dev 3.132
Gaus mean : 44.9310
Gaus Width: 3.3745
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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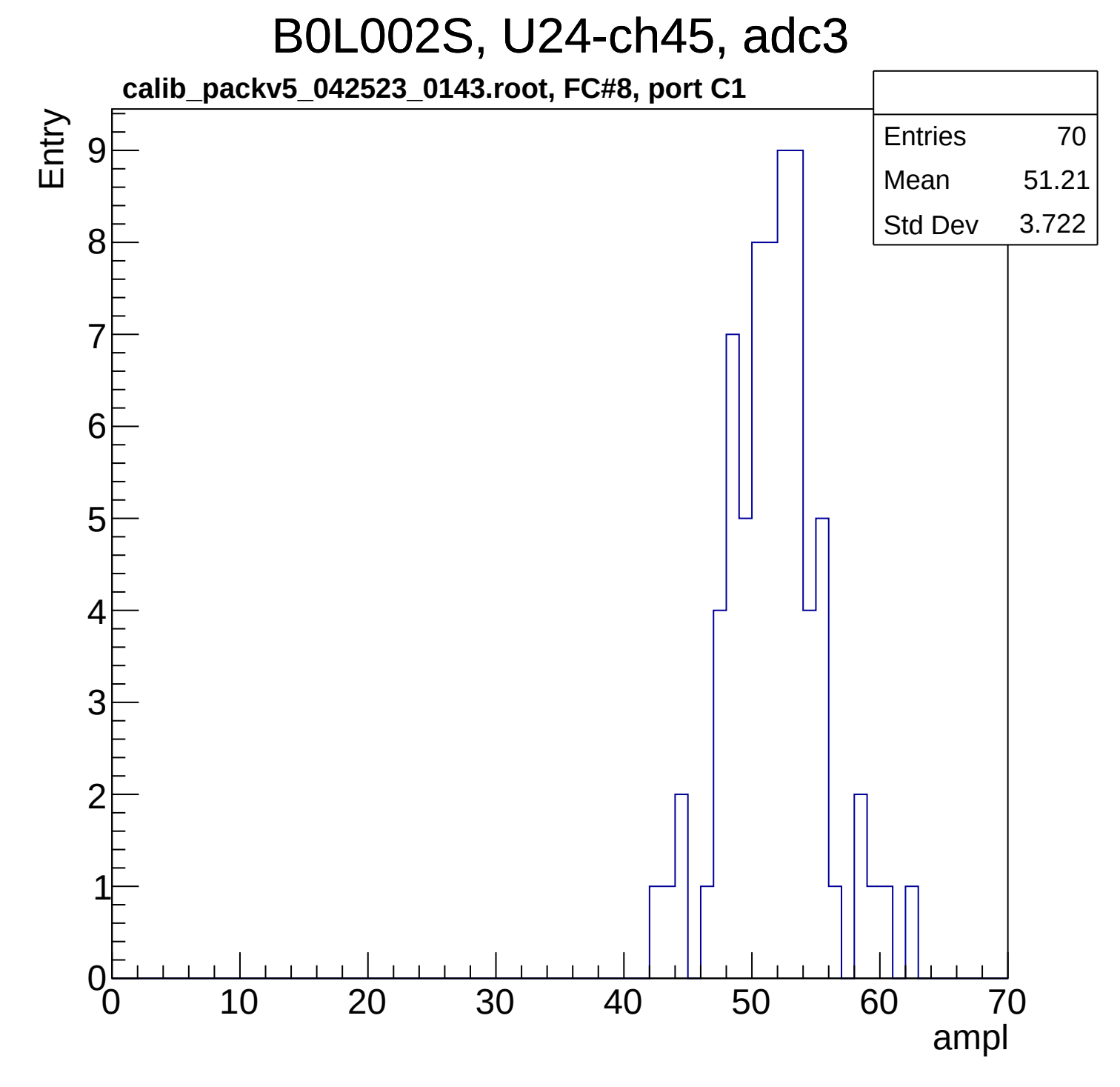
B0L002S, U24-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.21
Std Dev 3.722
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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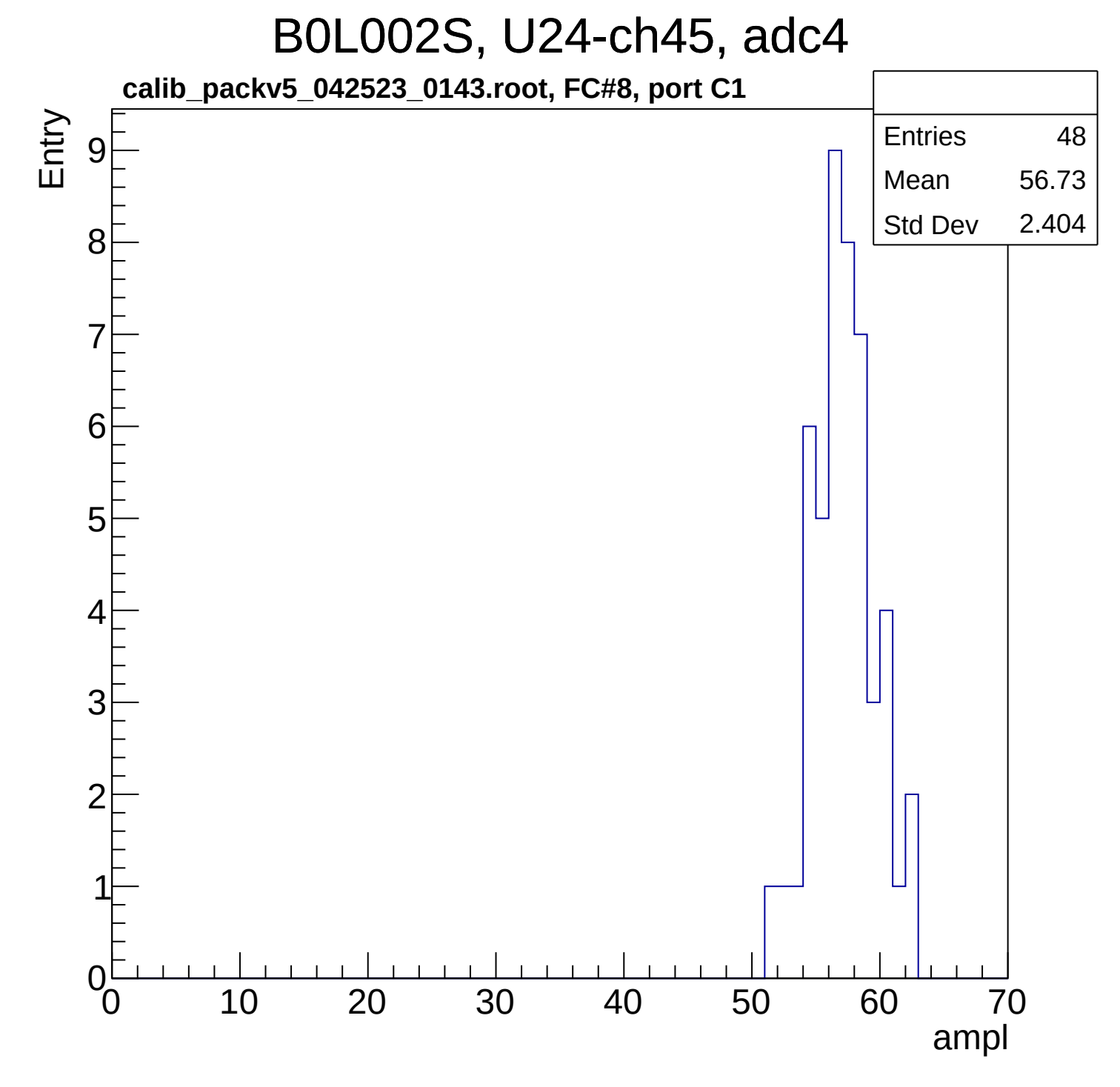
60
70


B0L002S, U24-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 56.73
Std Dev 2.404
Entry
ampl
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6
7
8
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0
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60
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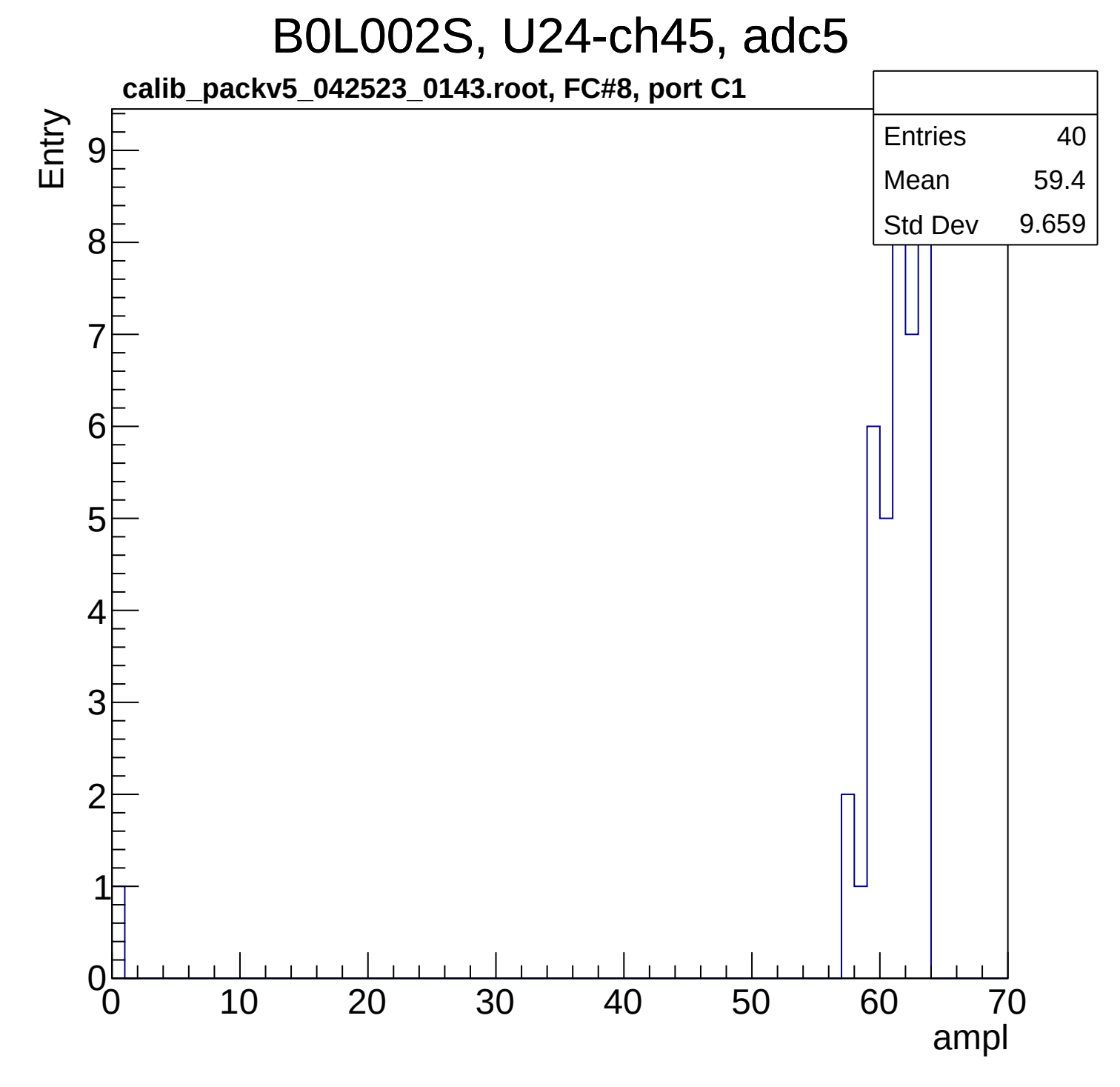
B0L002S, U24-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.4
Std Dev 9.659
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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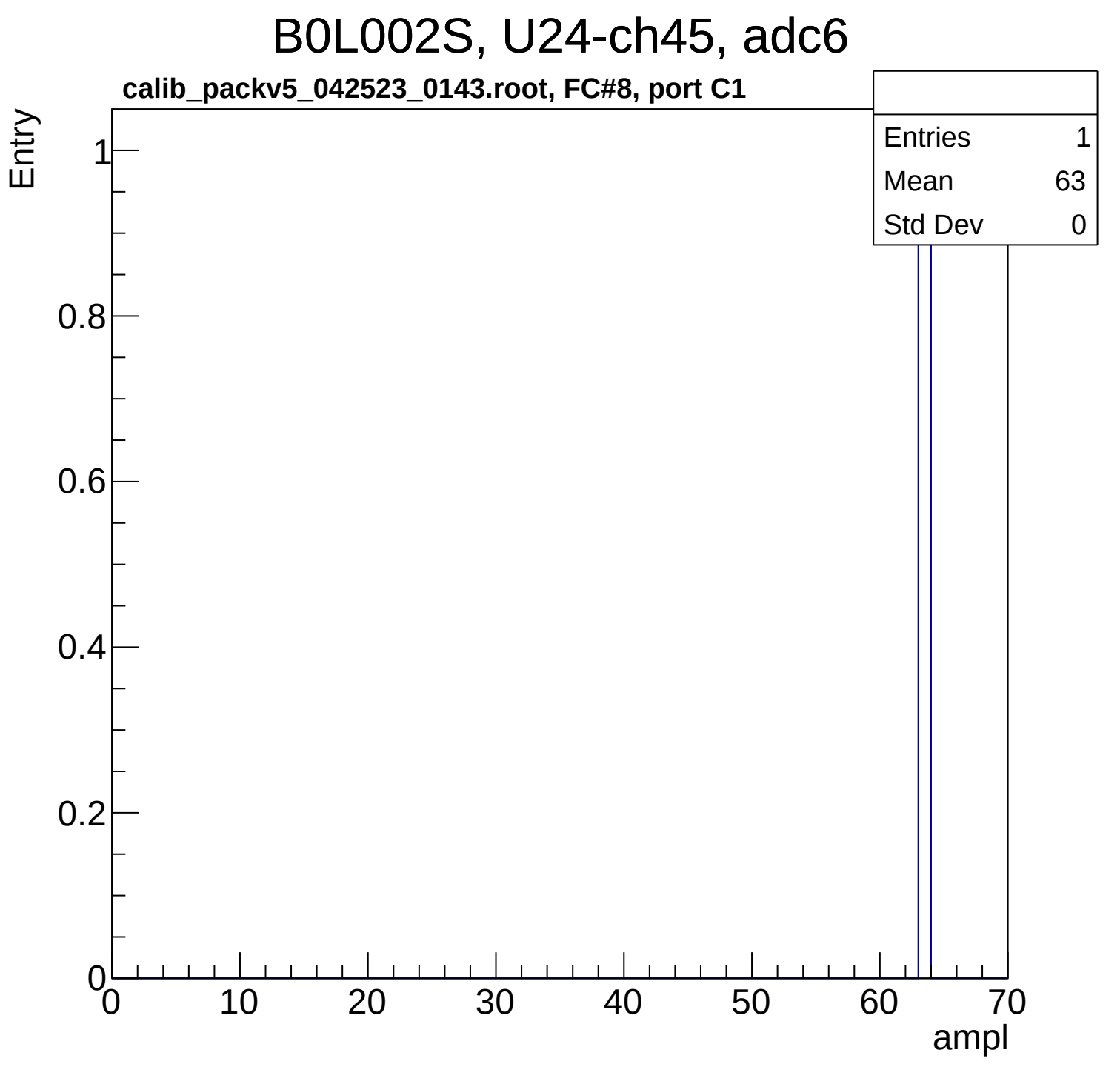
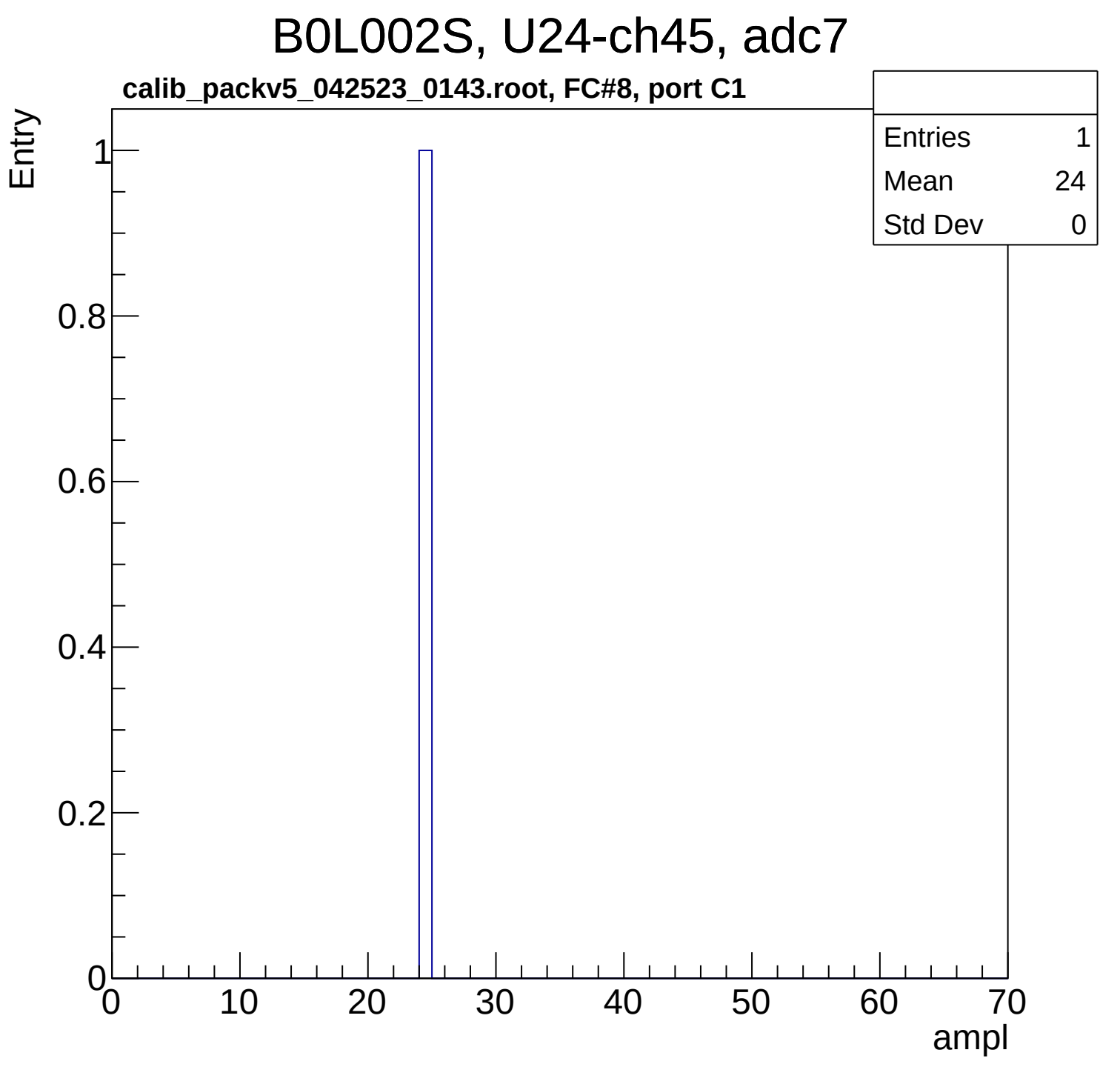

B0L002S, U24-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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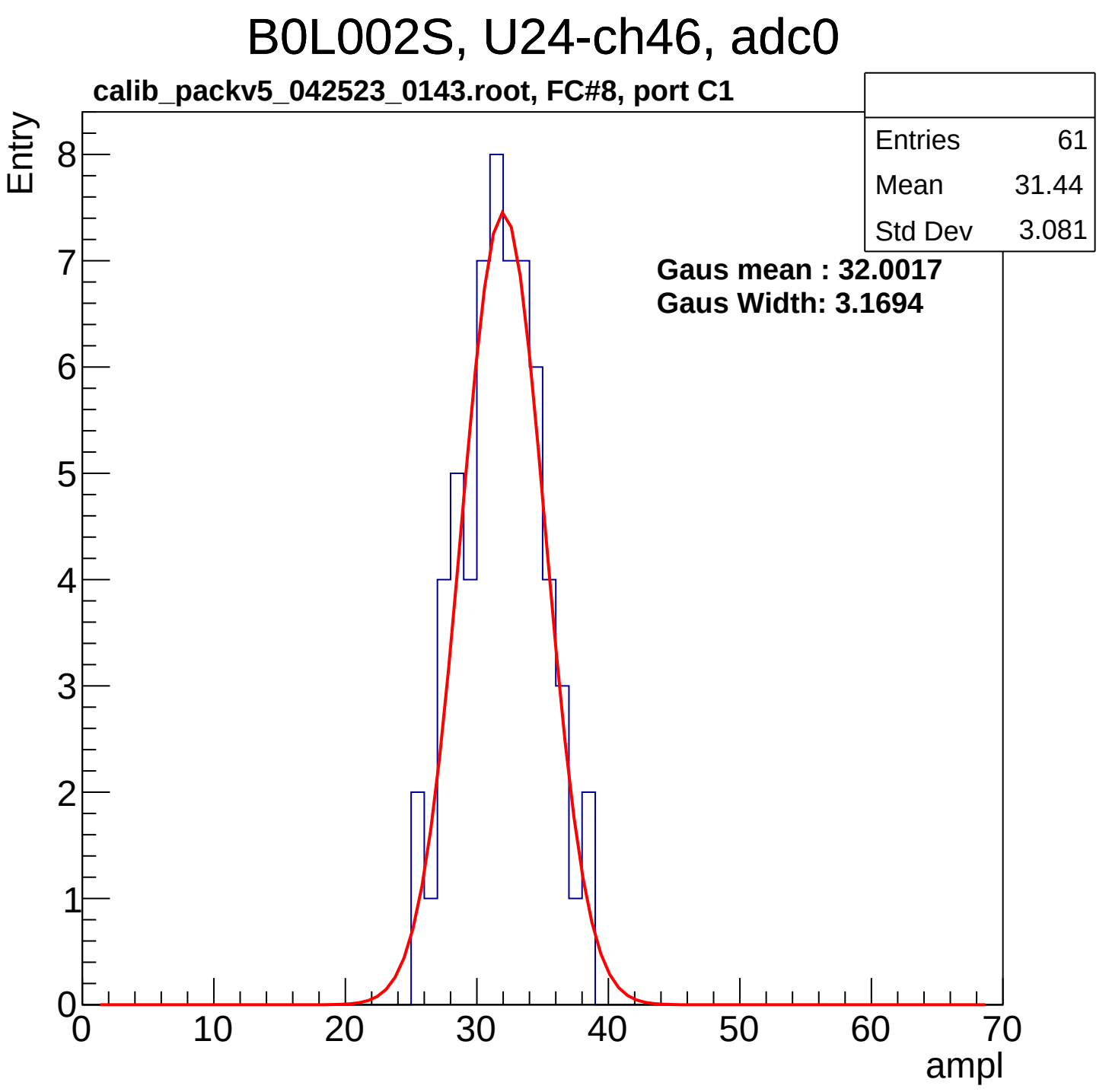
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 31.44
Std Dev 3.081
Gaus mean : 32.0017
Gaus Width: 3.1694
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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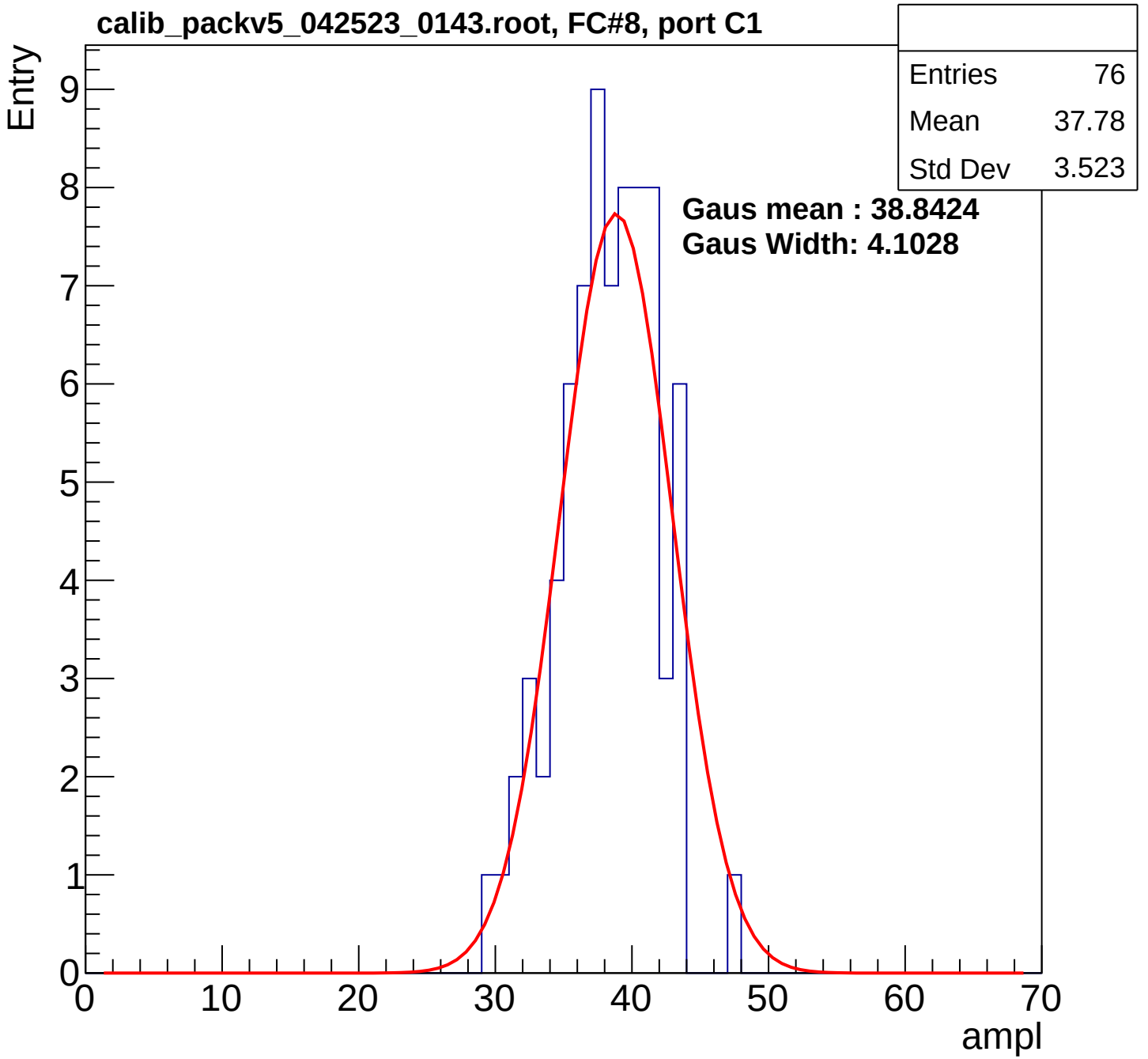
B0L002S, U24-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.78
Std Dev 3.523
Gaus mean : 38.8424
Gaus Width: 4.1028
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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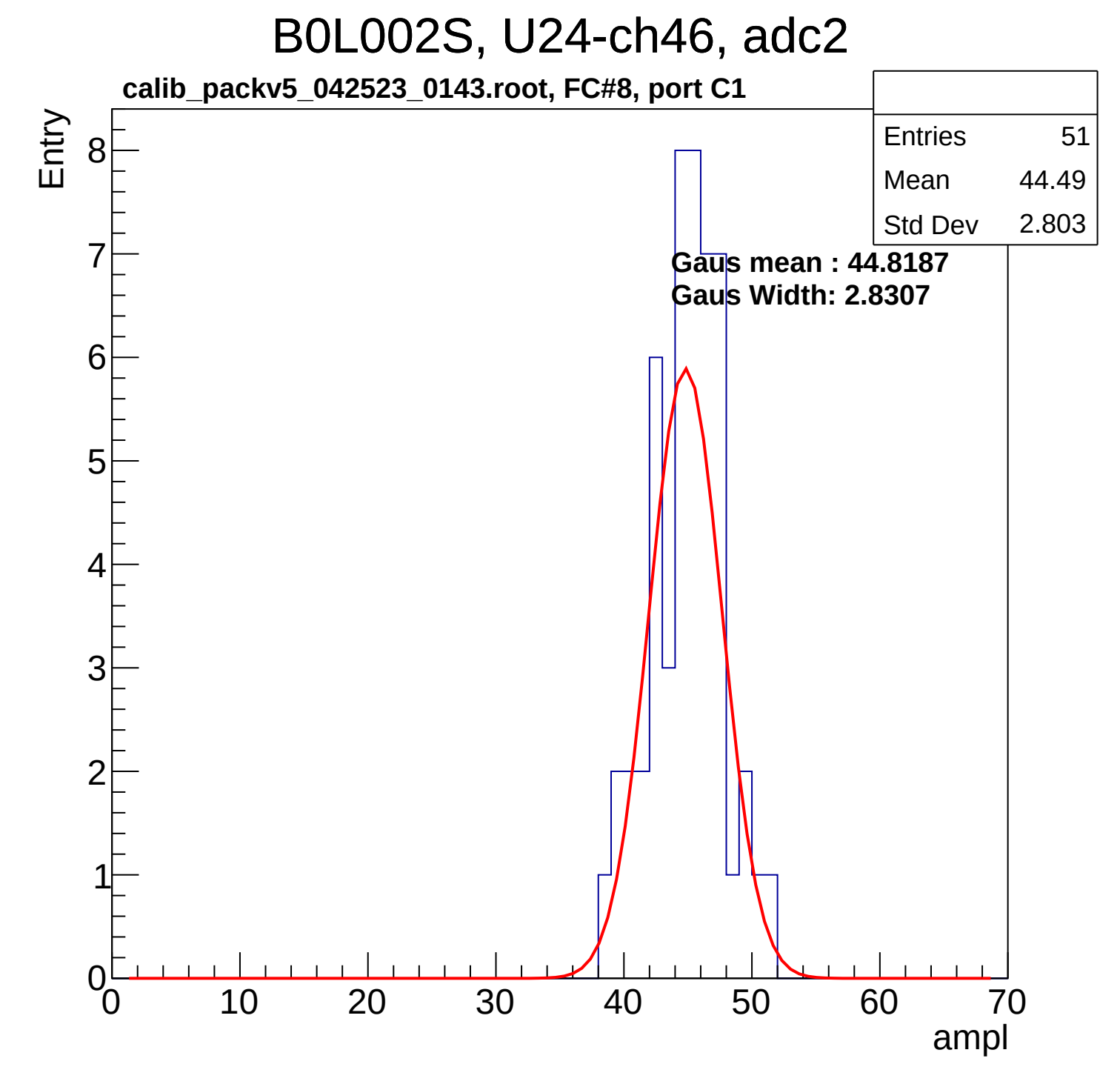
B0L002S, U24-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 44.49
Std Dev 2.803
Gaus mean : 44.8187
Gaus Width: 2.8307
Entry
ampl
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1
2
3
4
5
6
7
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0
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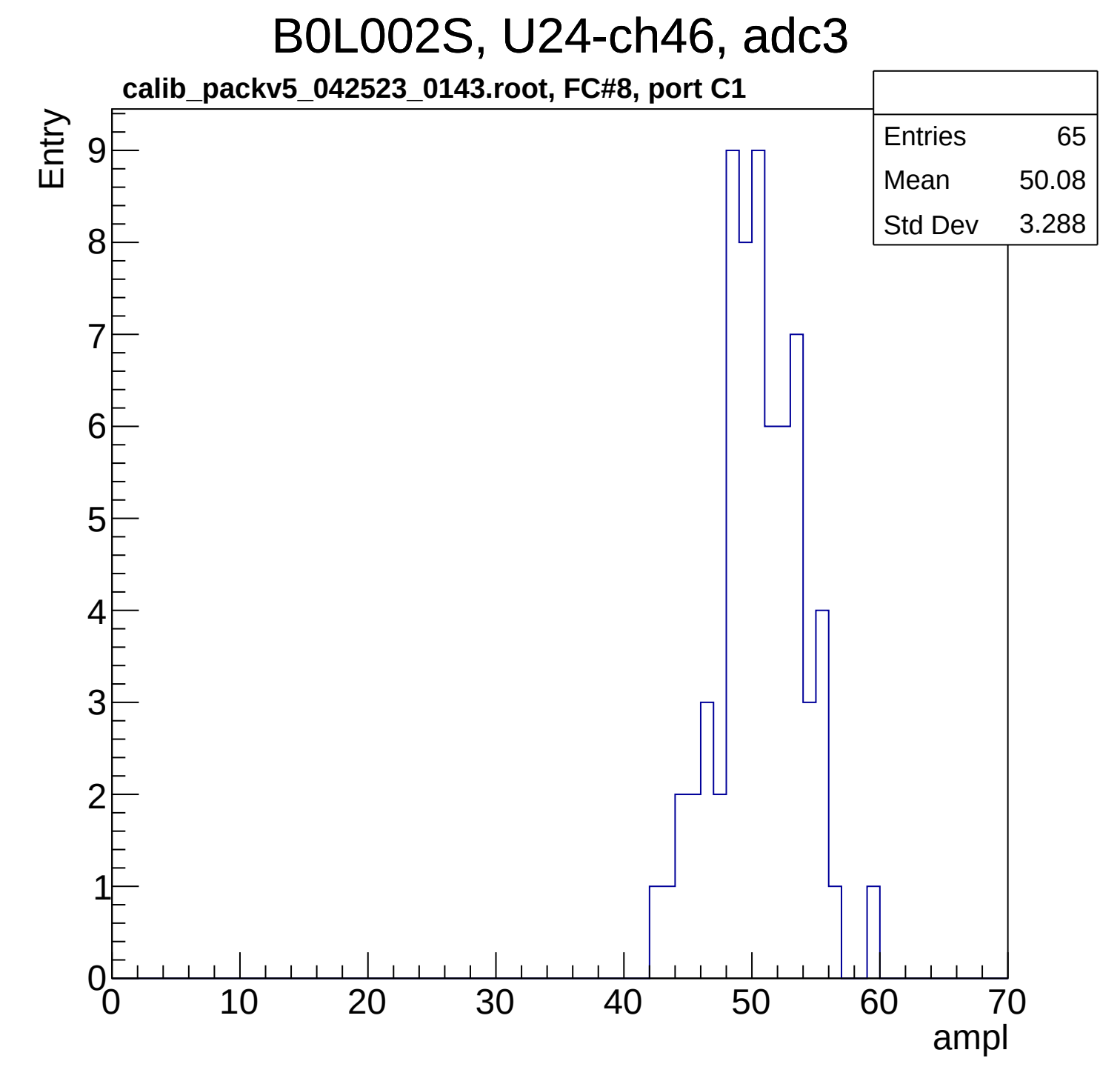
B0L002S, U24-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.08
Std Dev 3.288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
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60
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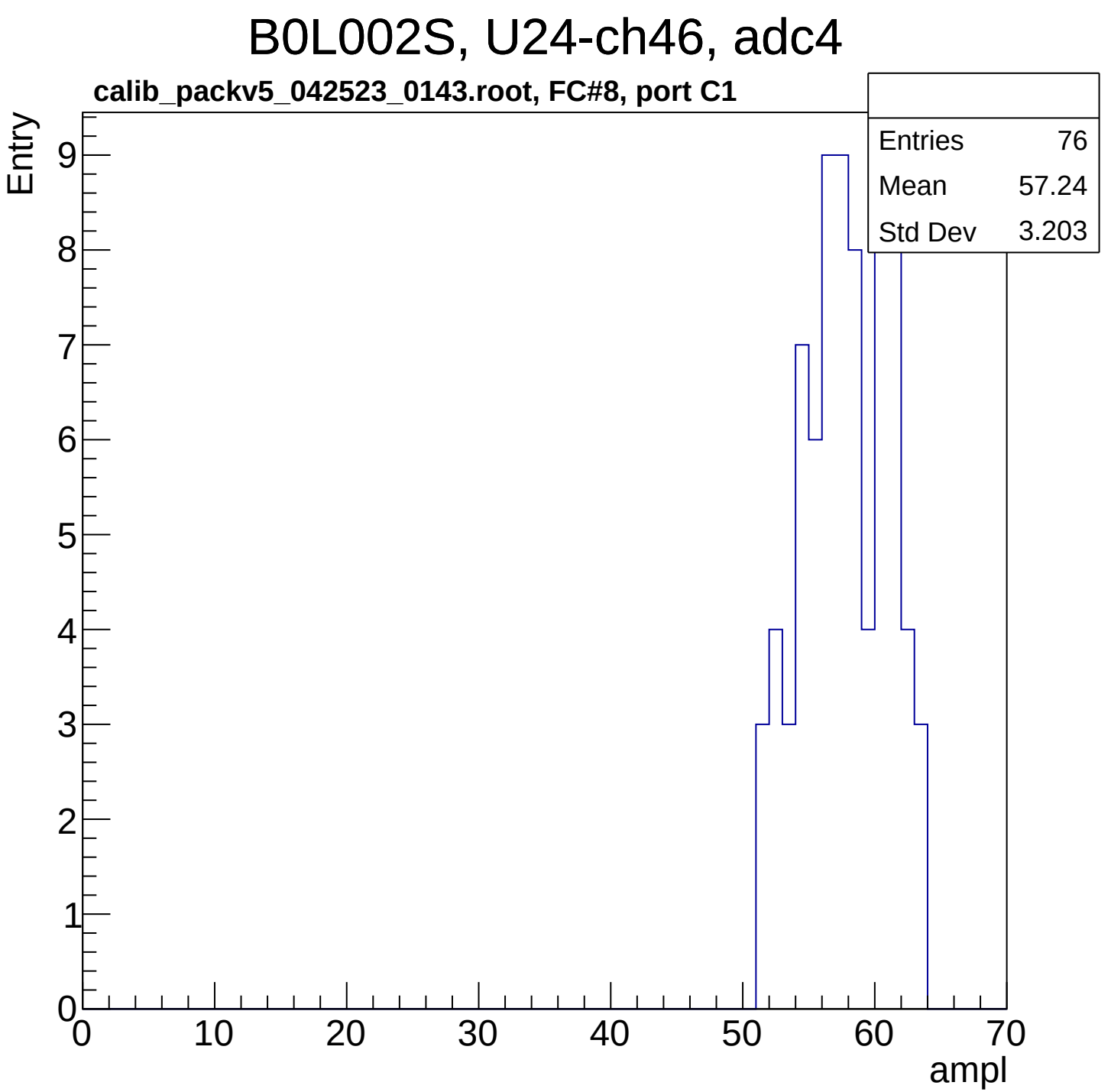
B0L002S, U24-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 57.24
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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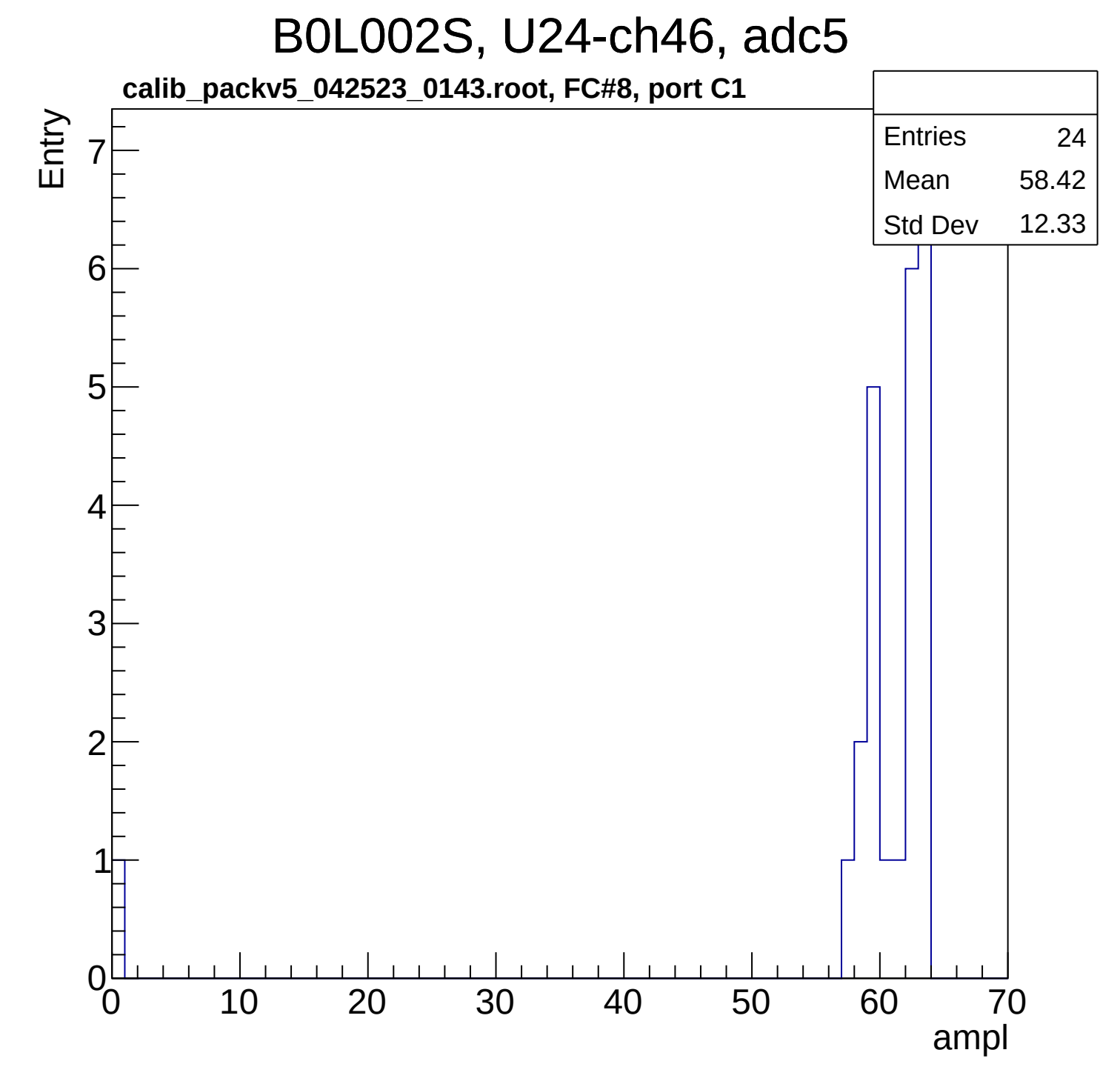
B0L002S, U24-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.42
Std Dev 12.33
Entry
ampl
0
1
2
3
4
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0
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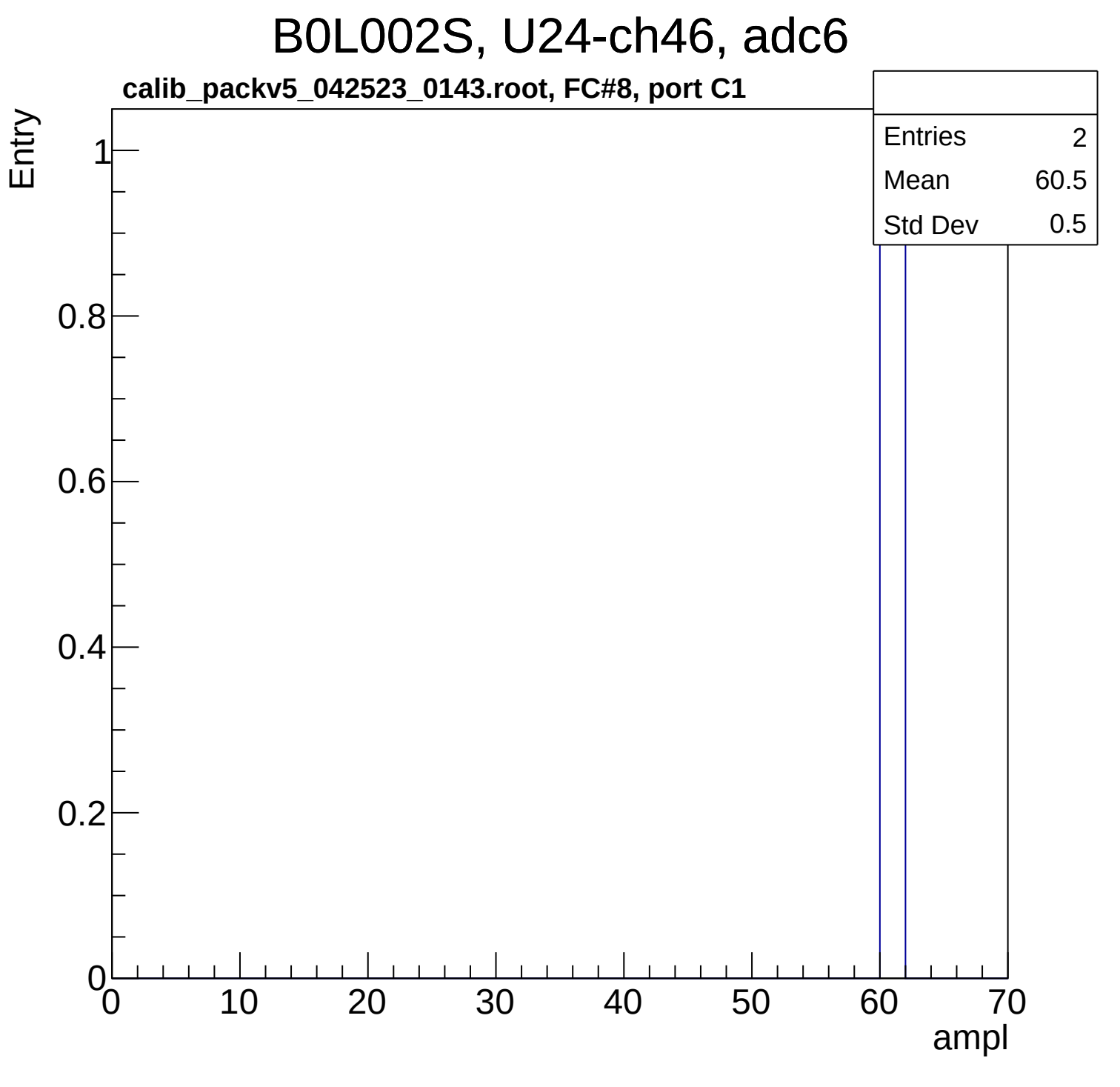
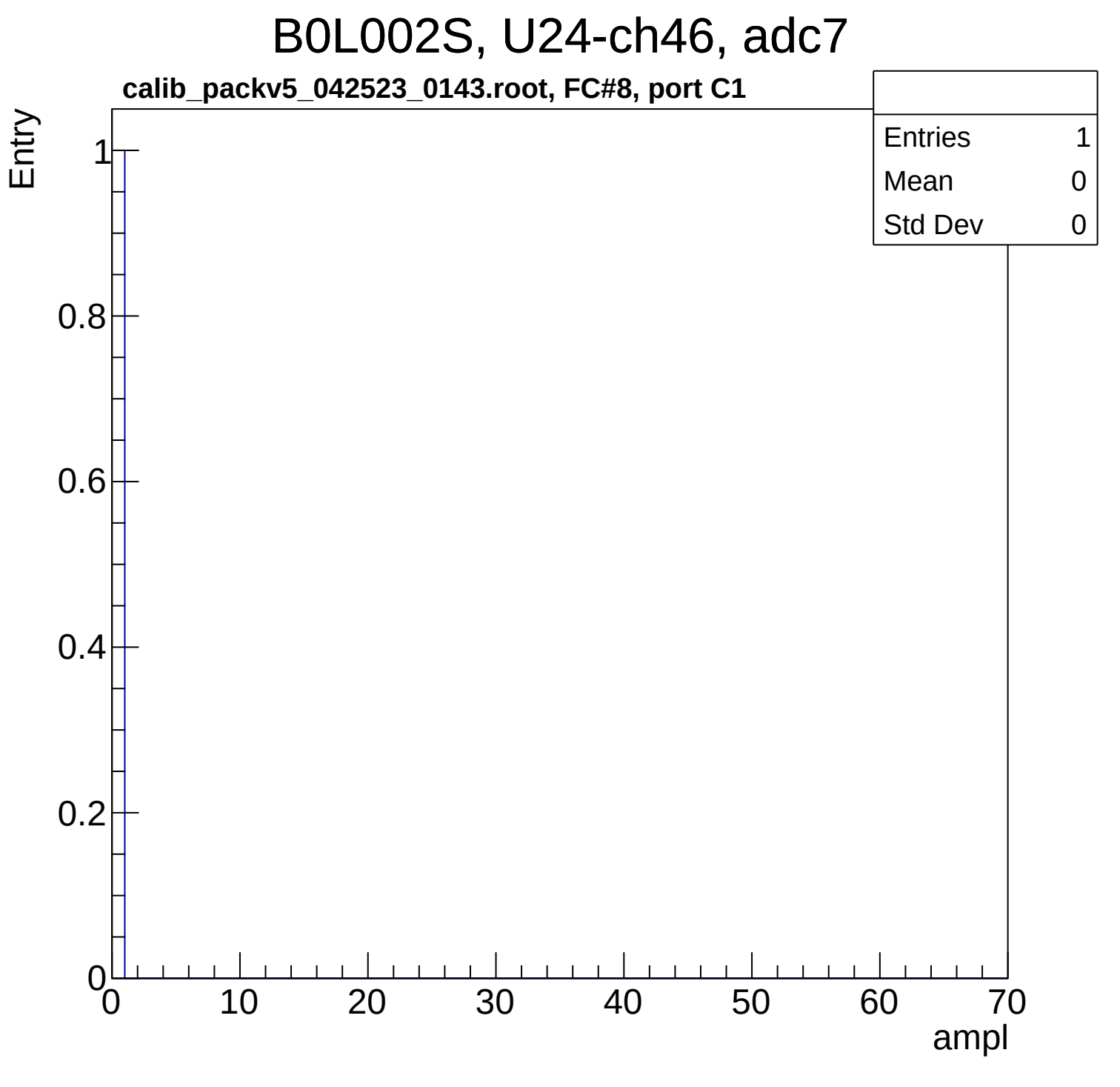
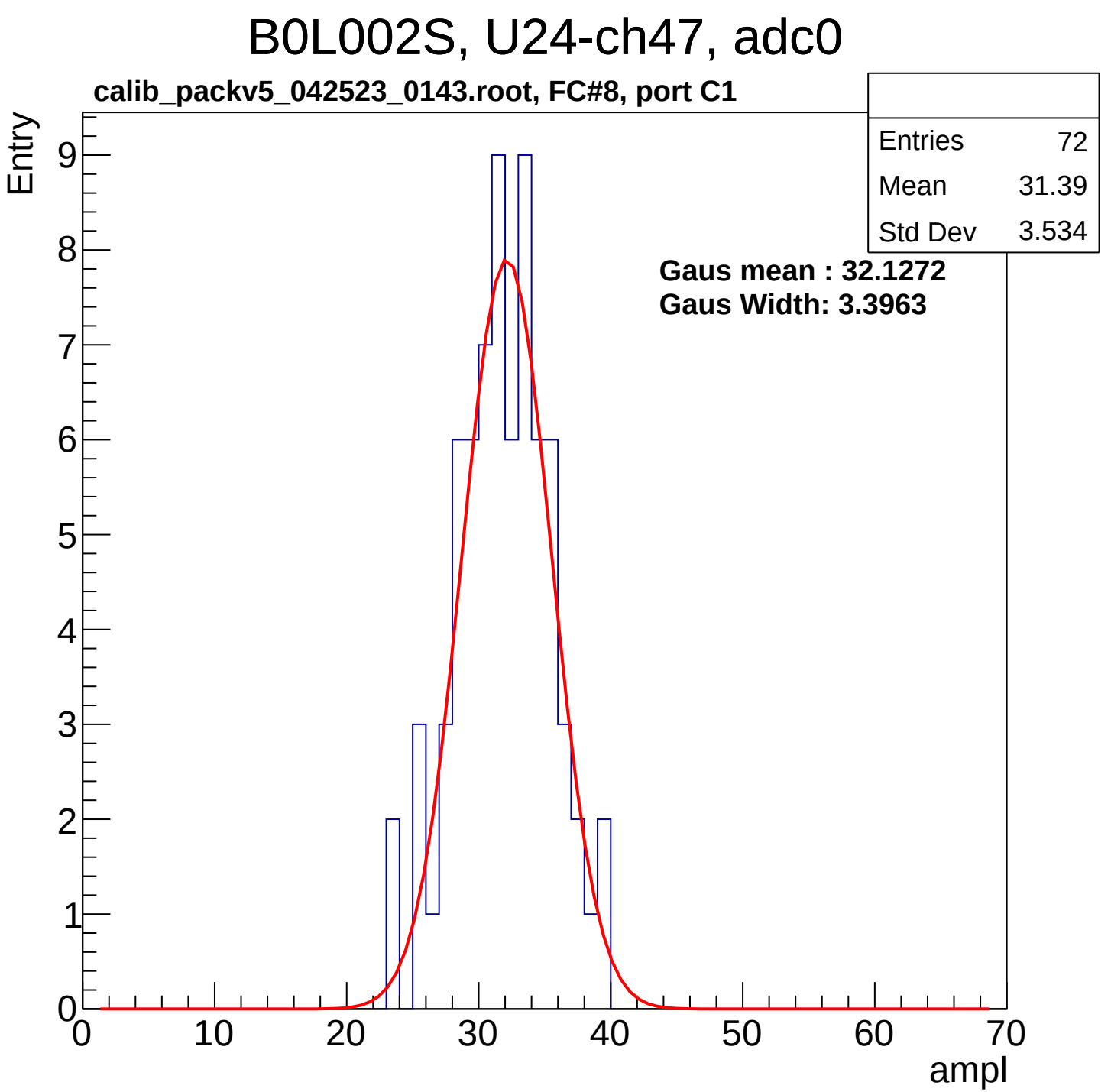

B0L002S, U24-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.39
Std Dev 3.534
Gaus mean : 32.1272
Gaus Width: 3.3963
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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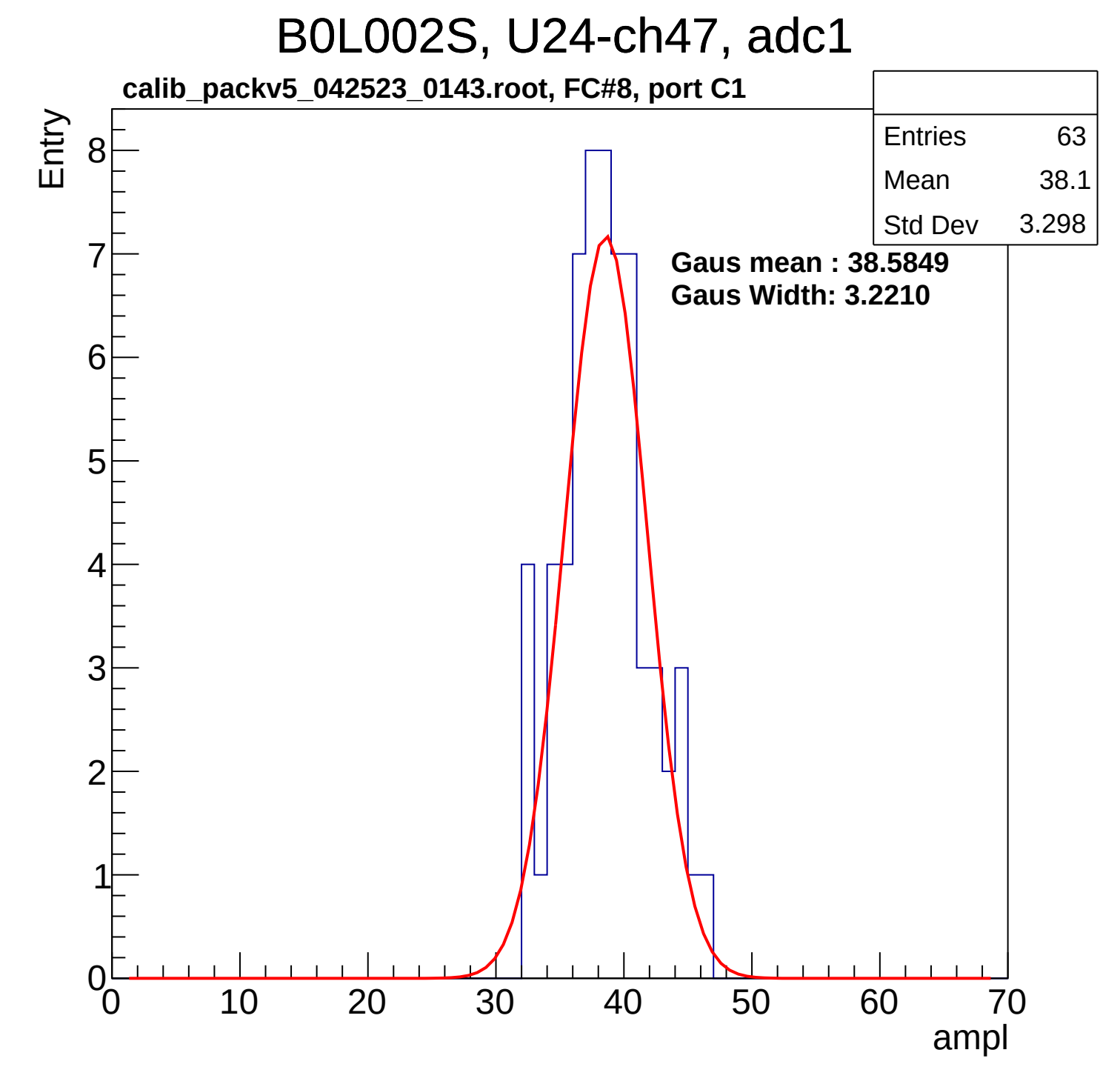
B0L002S, U24-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.1
Std Dev 3.298
Gaus mean : 38.5849
Gaus Width: 3.2210
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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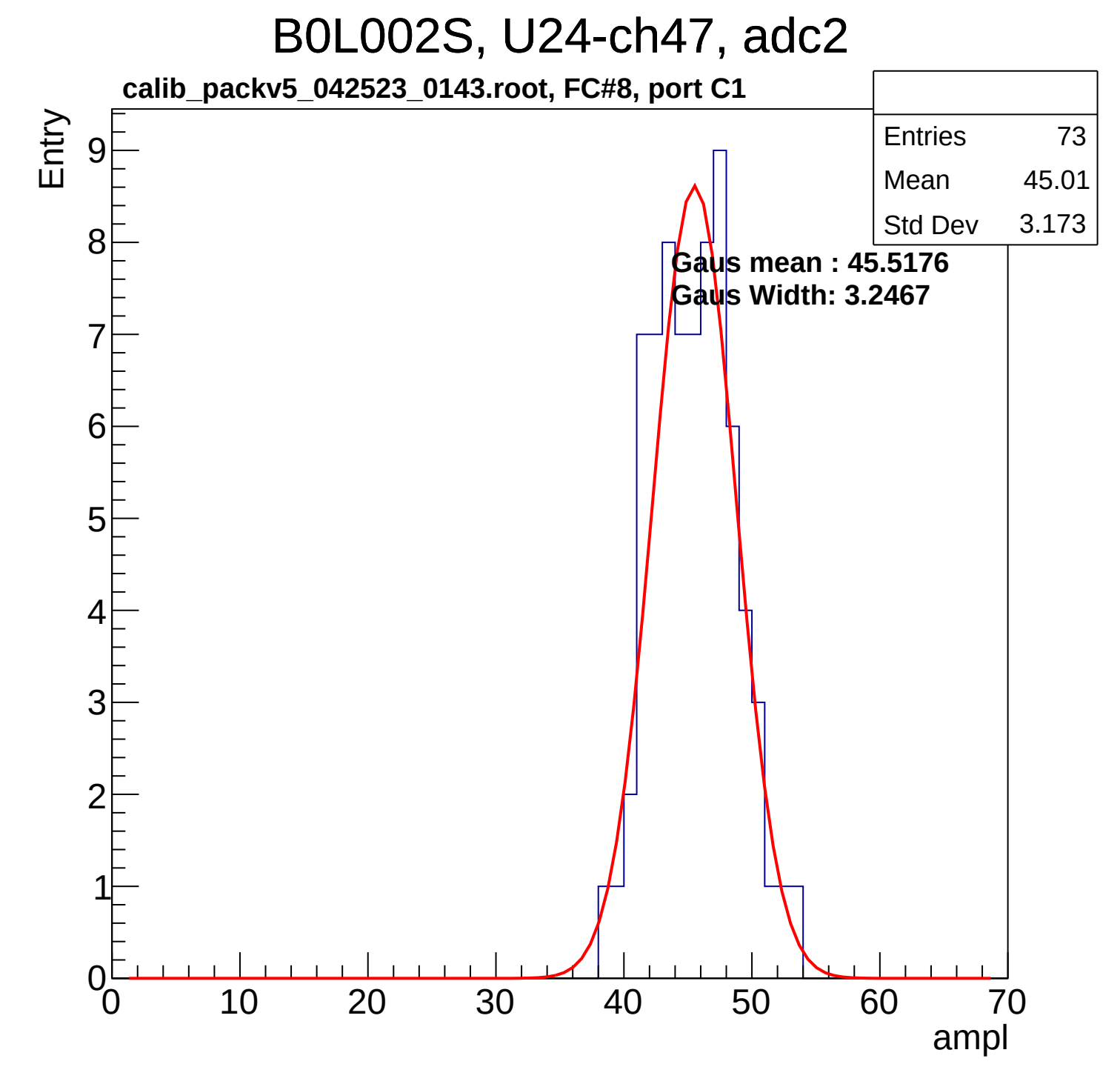
B0L002S, U24-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 45.01
Std Dev 3.173
Gaus mean : 45.5176
Gaus Width: 3.2467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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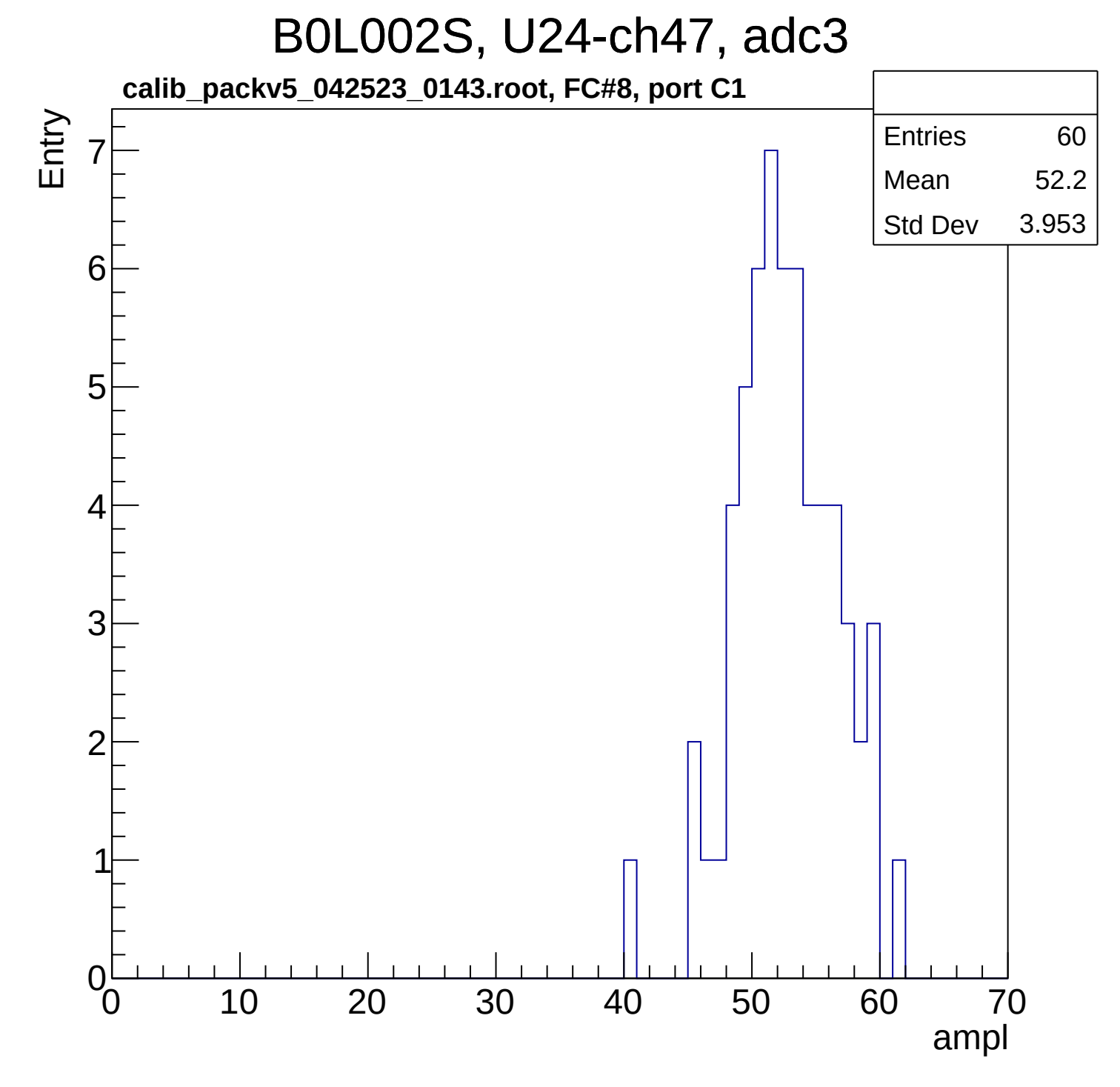
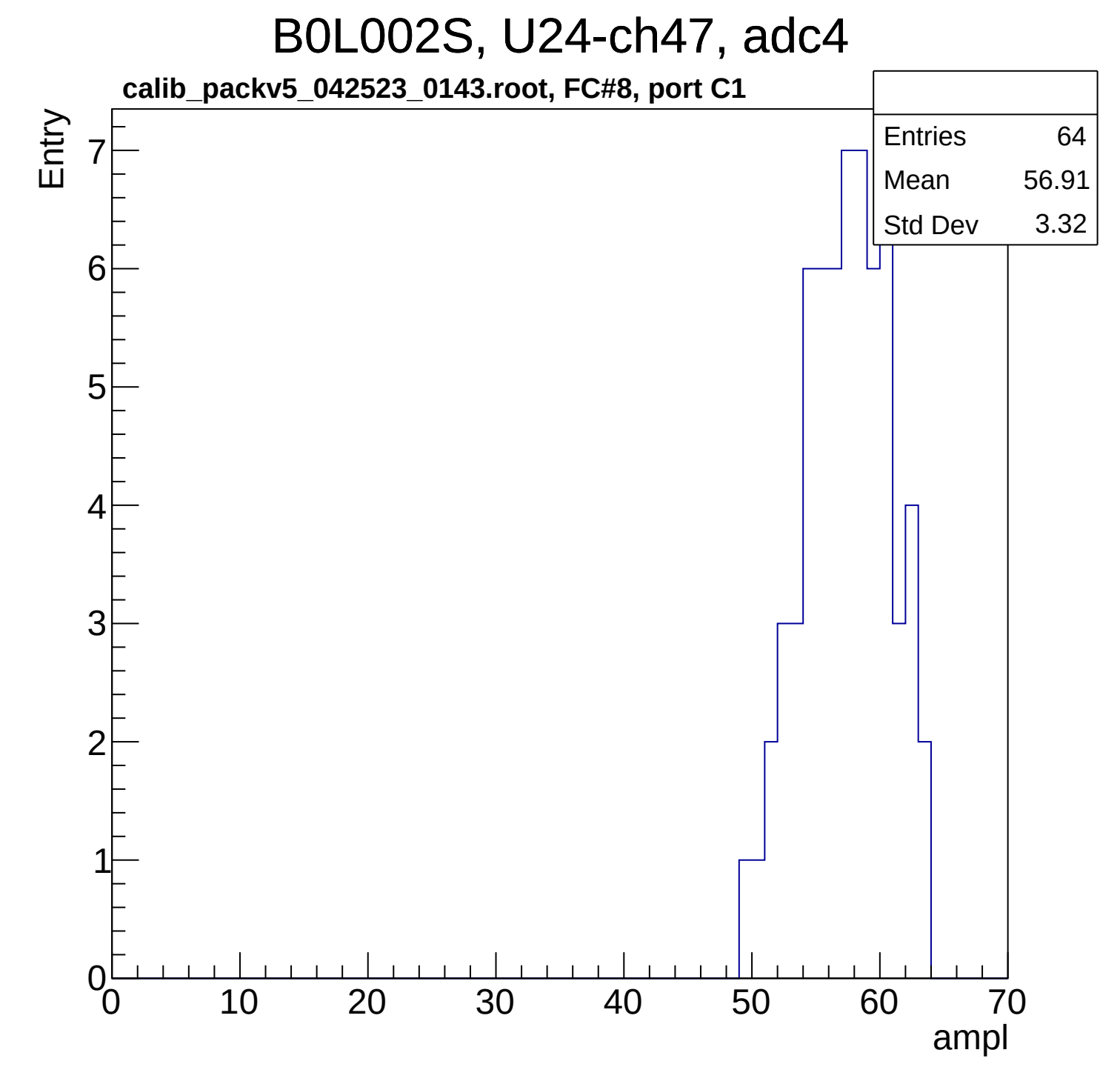
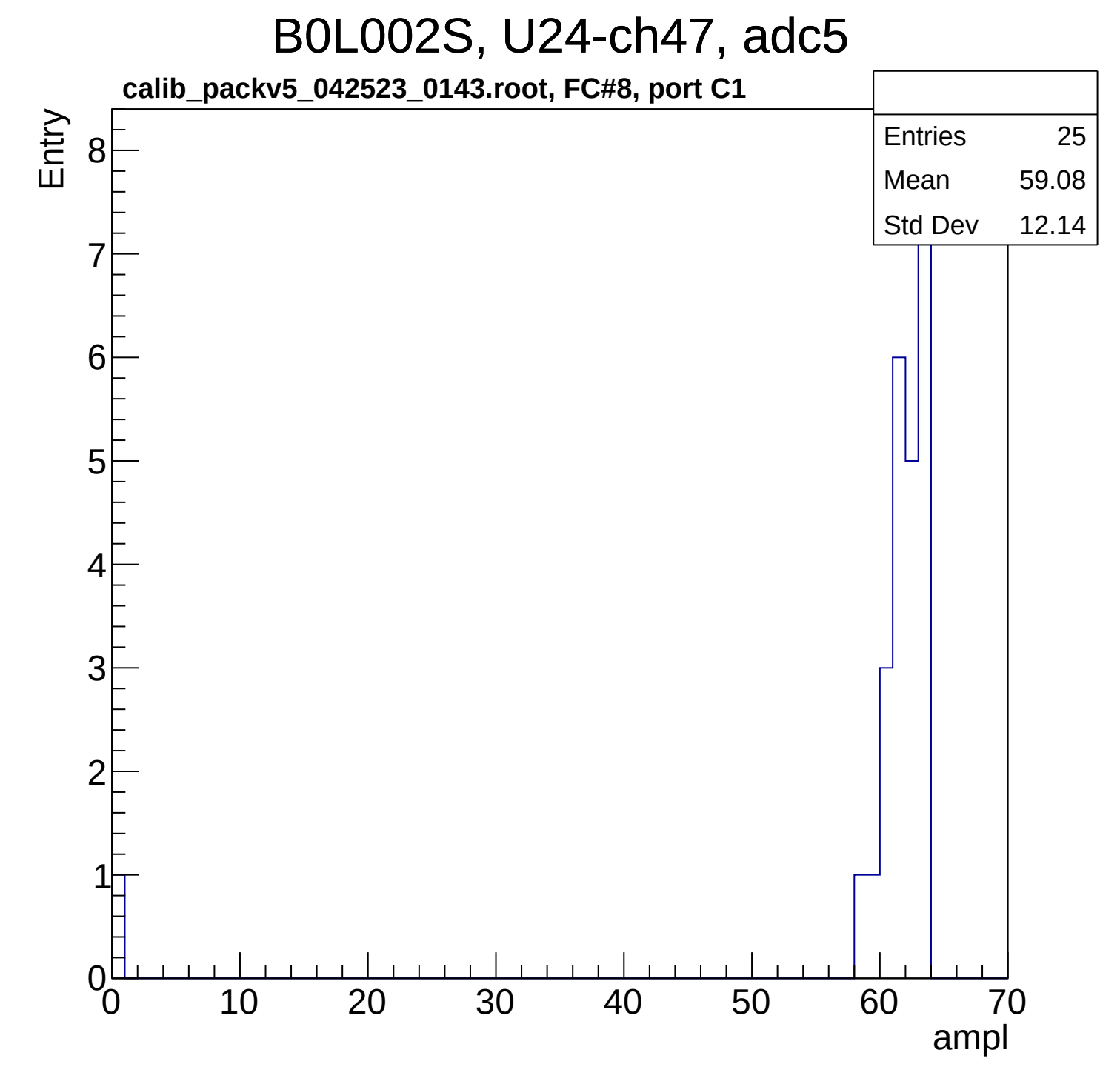
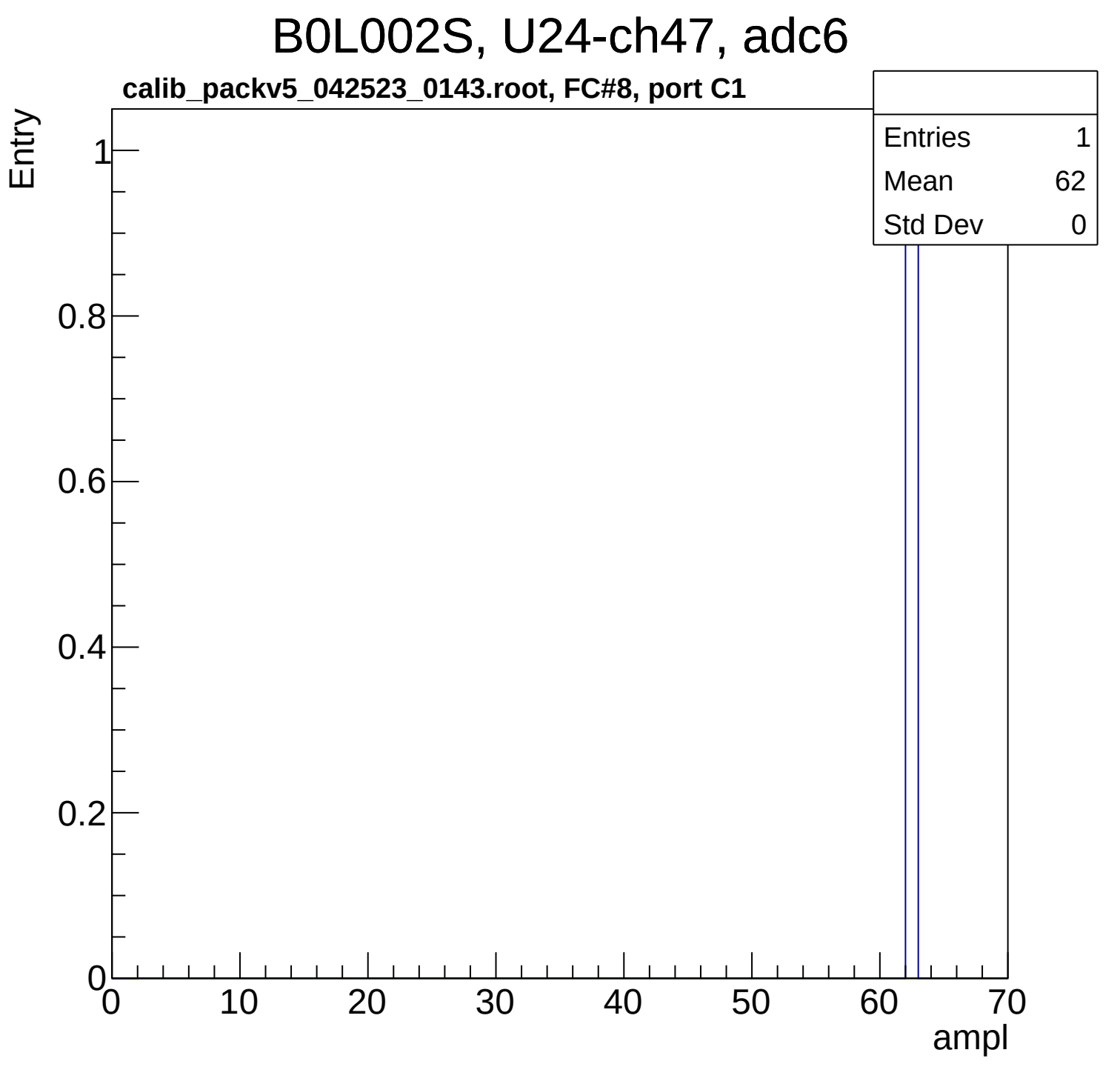
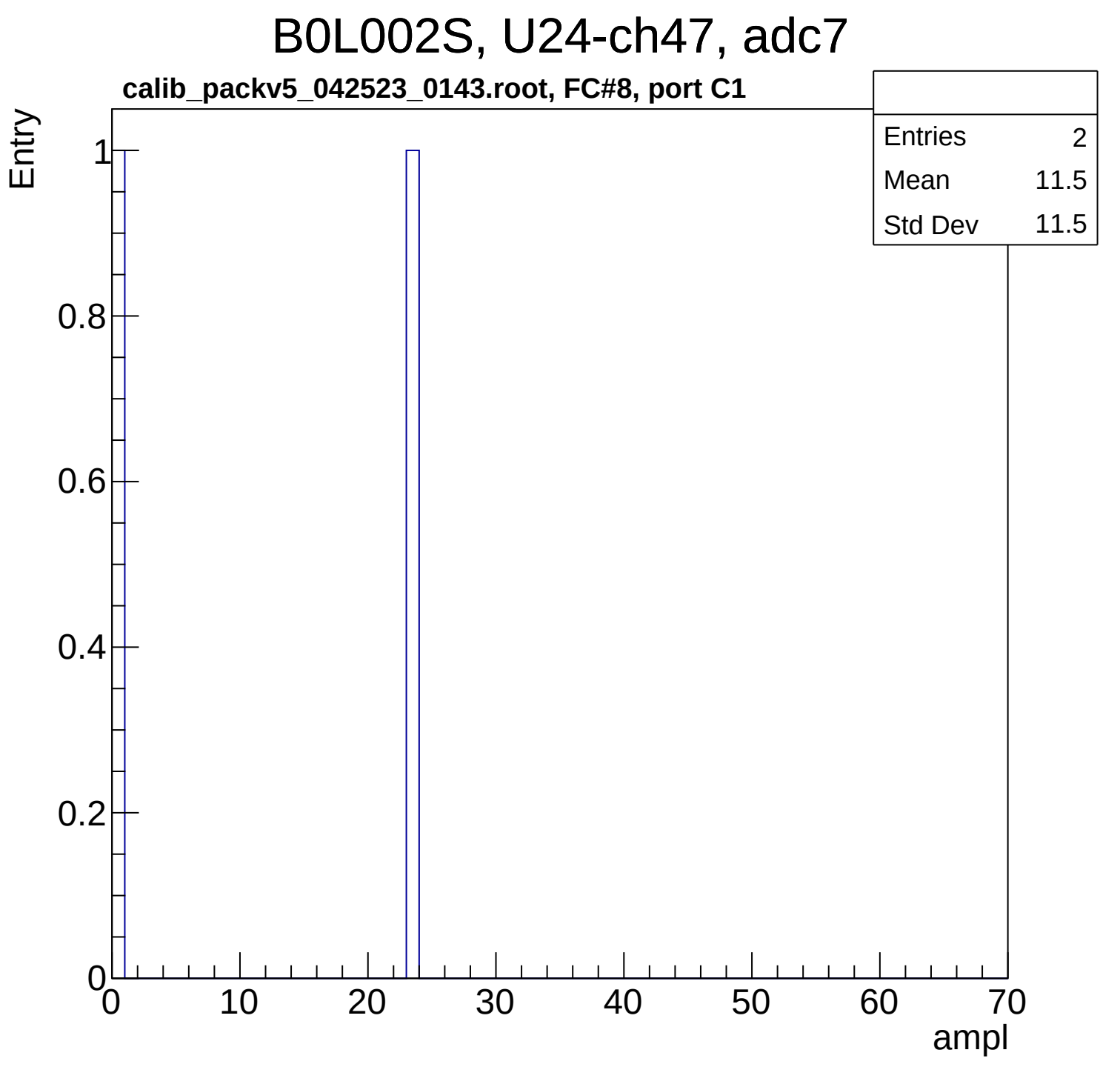
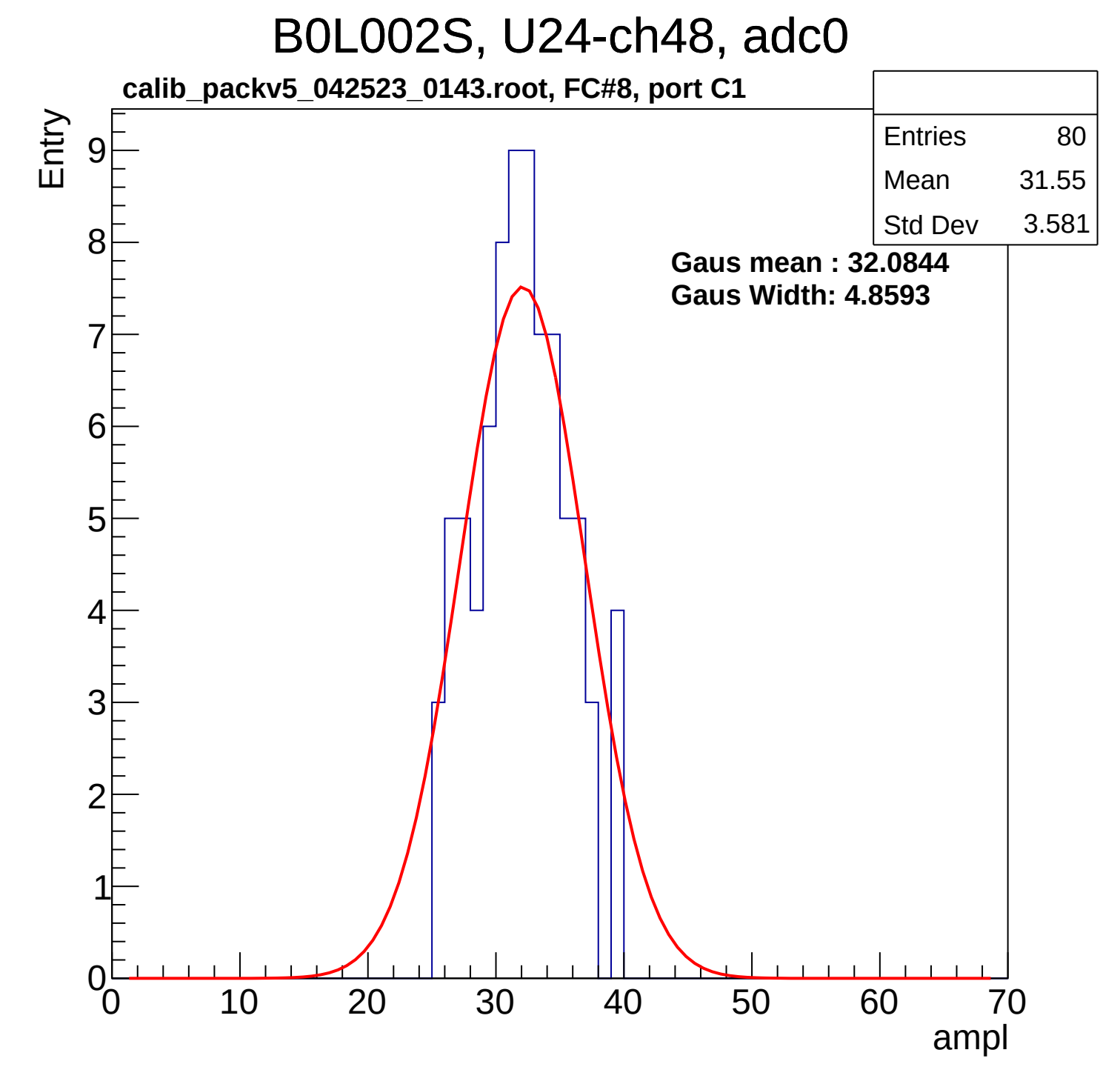

B0L002S, U24-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.2
Std Dev 3.953
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

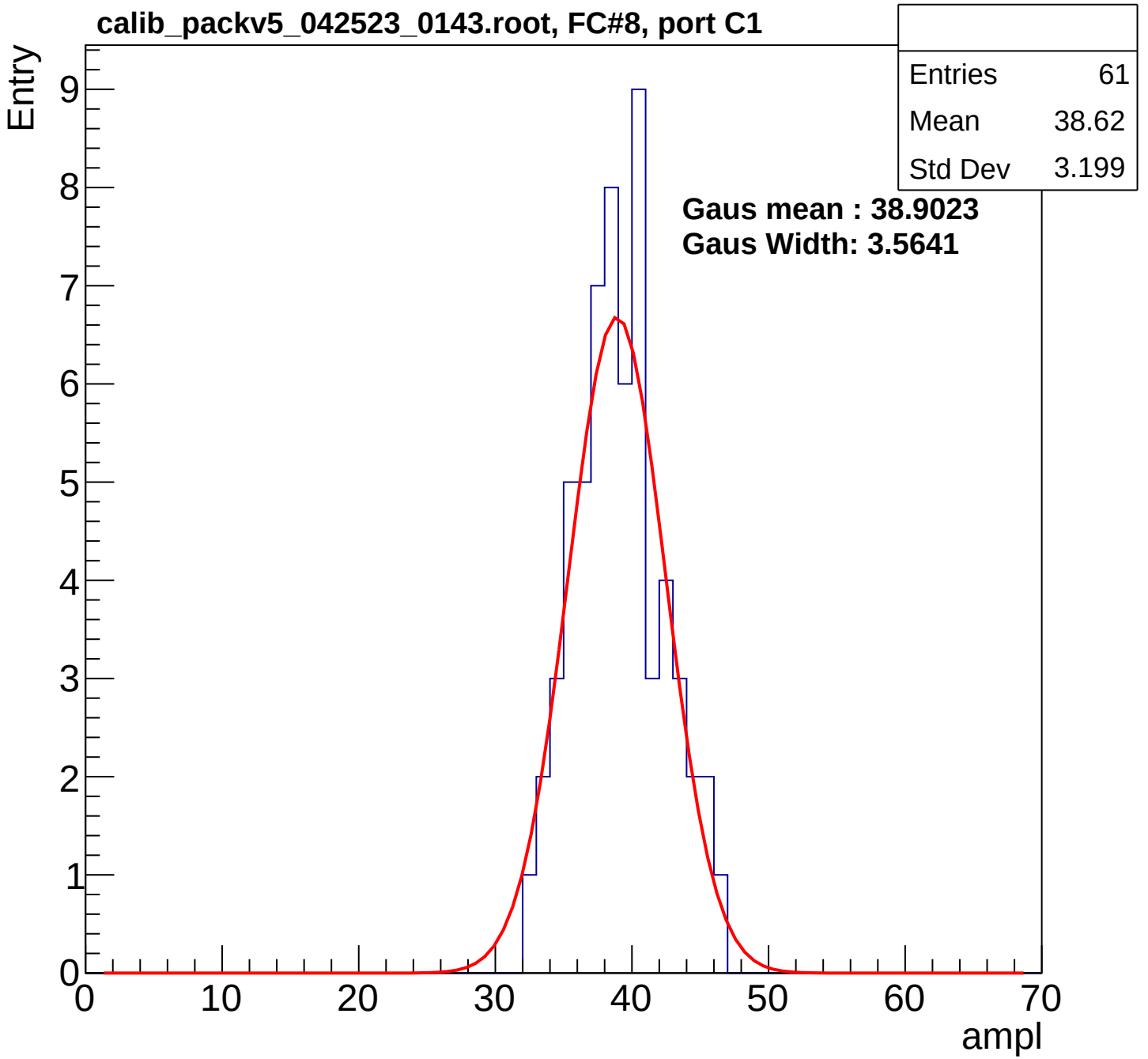

B0L002S, U24-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.91
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

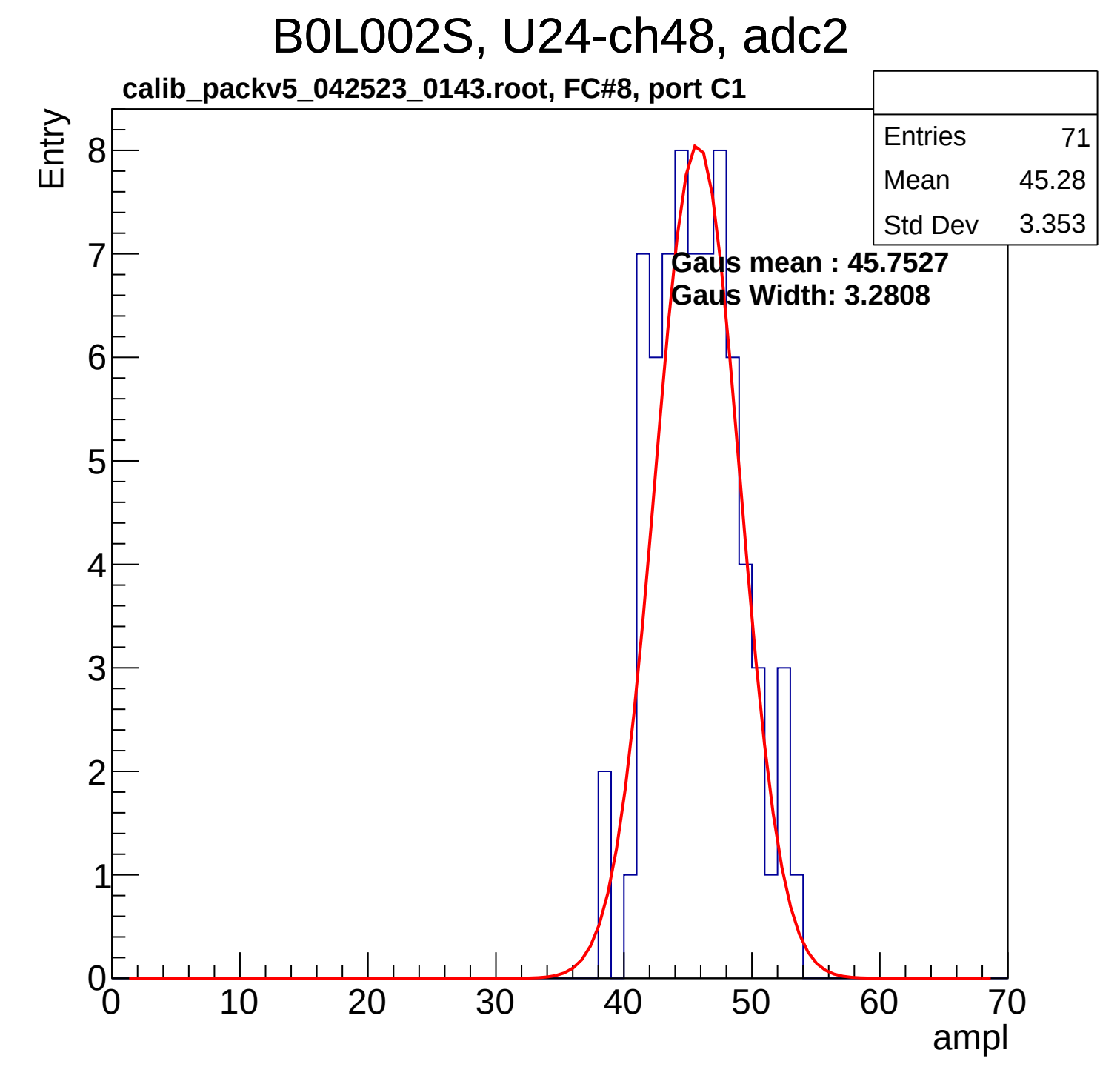

B0L002S, U24-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 59.08
Std Dev 12.14
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

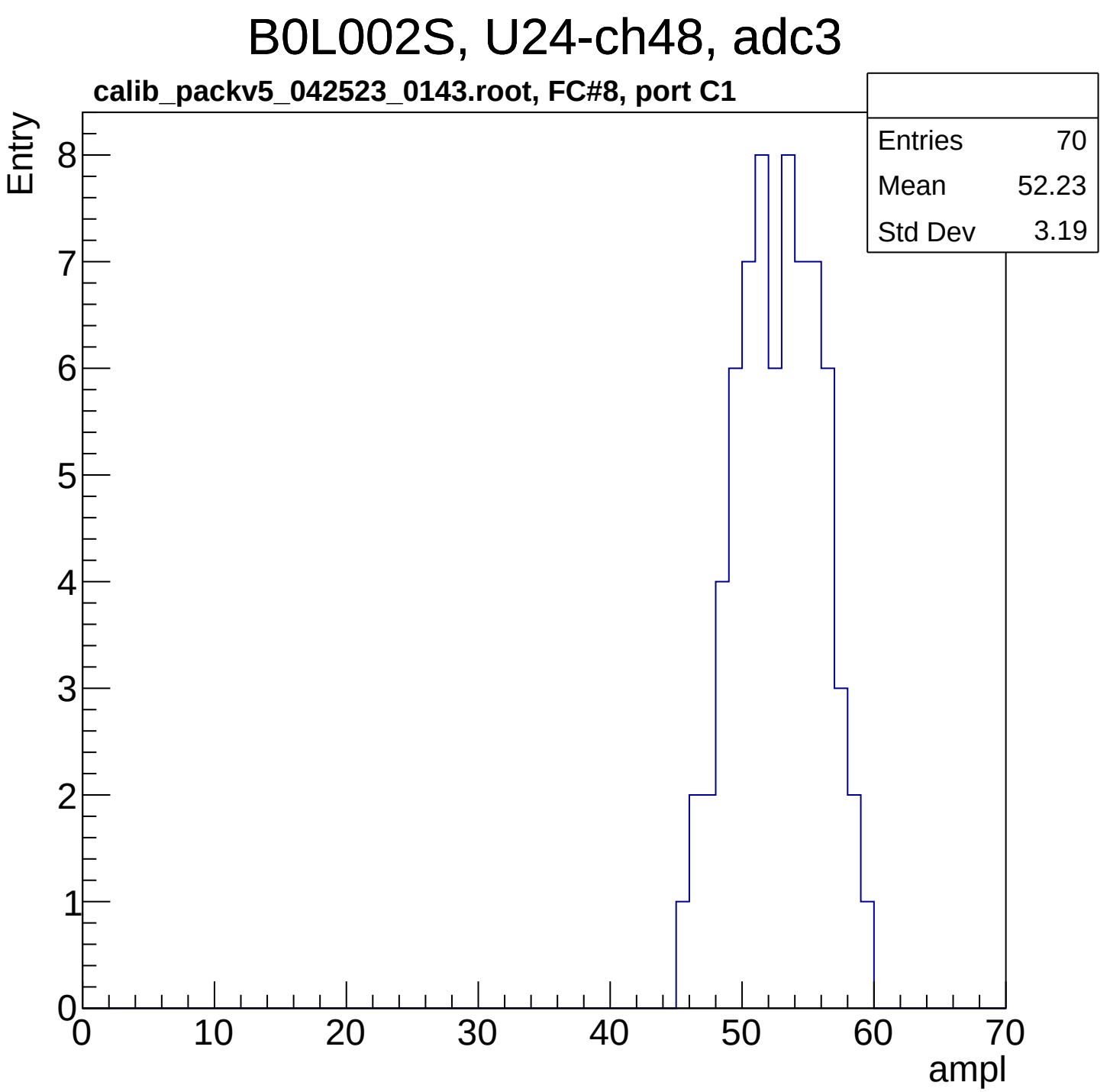

B0L002S, U24-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

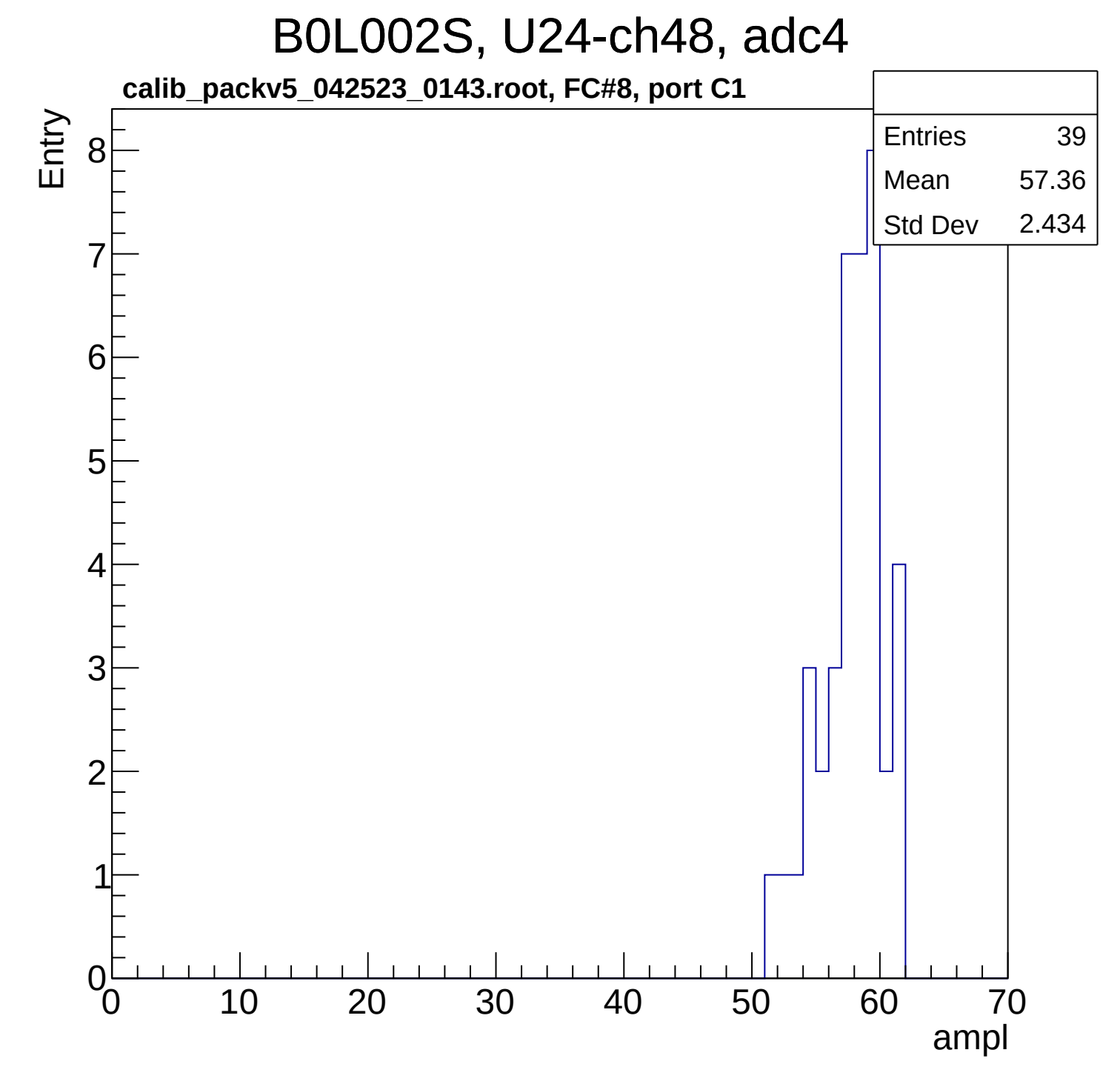

B0L002S, U24-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

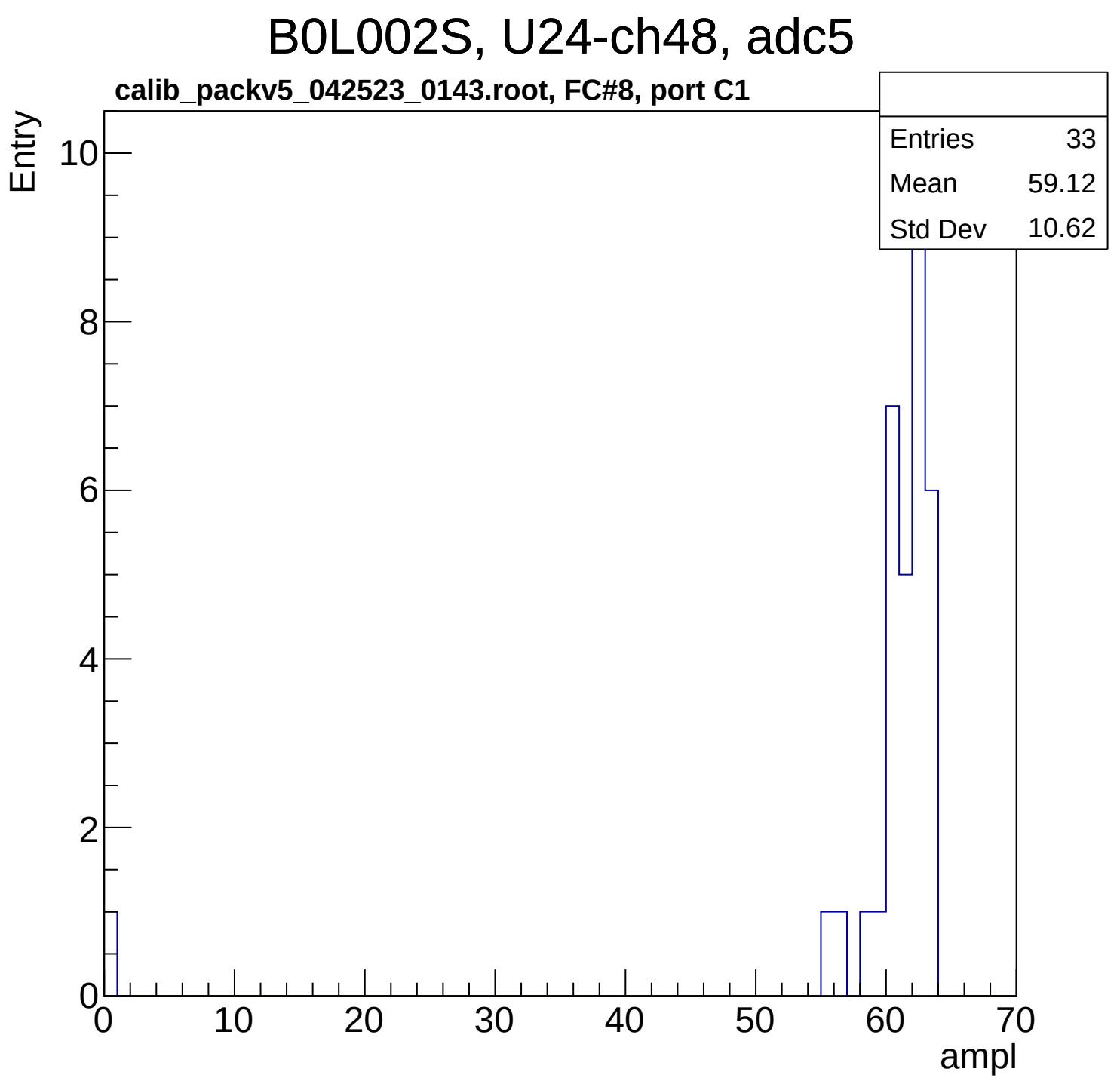

B0L002S, U24-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 31.55
Std Dev 3.581
Gaus mean : 32.0844
Gaus Width: 4.8593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

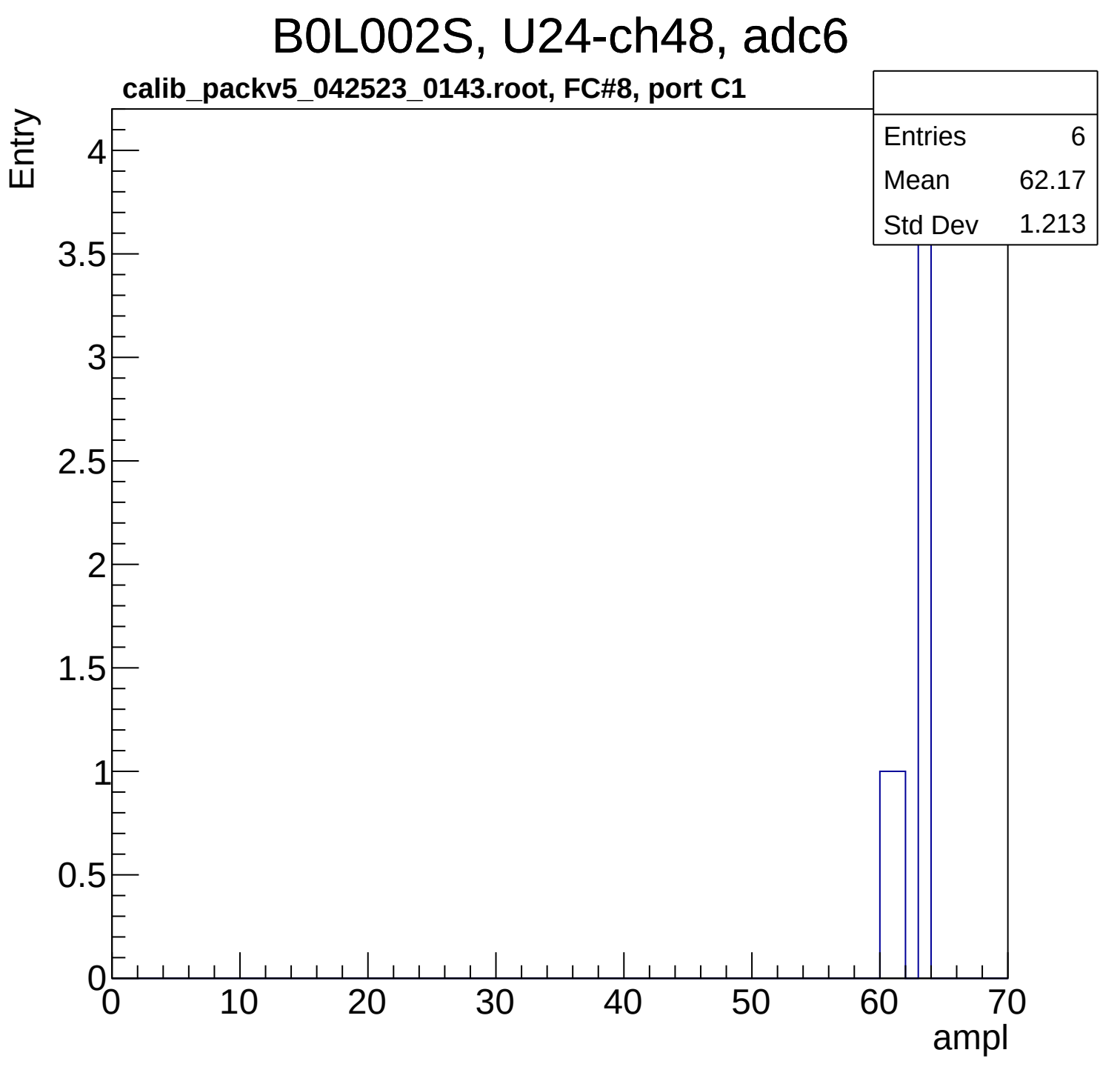

B0L002S, U24-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.62
Std Dev 3.199
Gaus mean : 38.9023
Gaus Width: 3.5641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

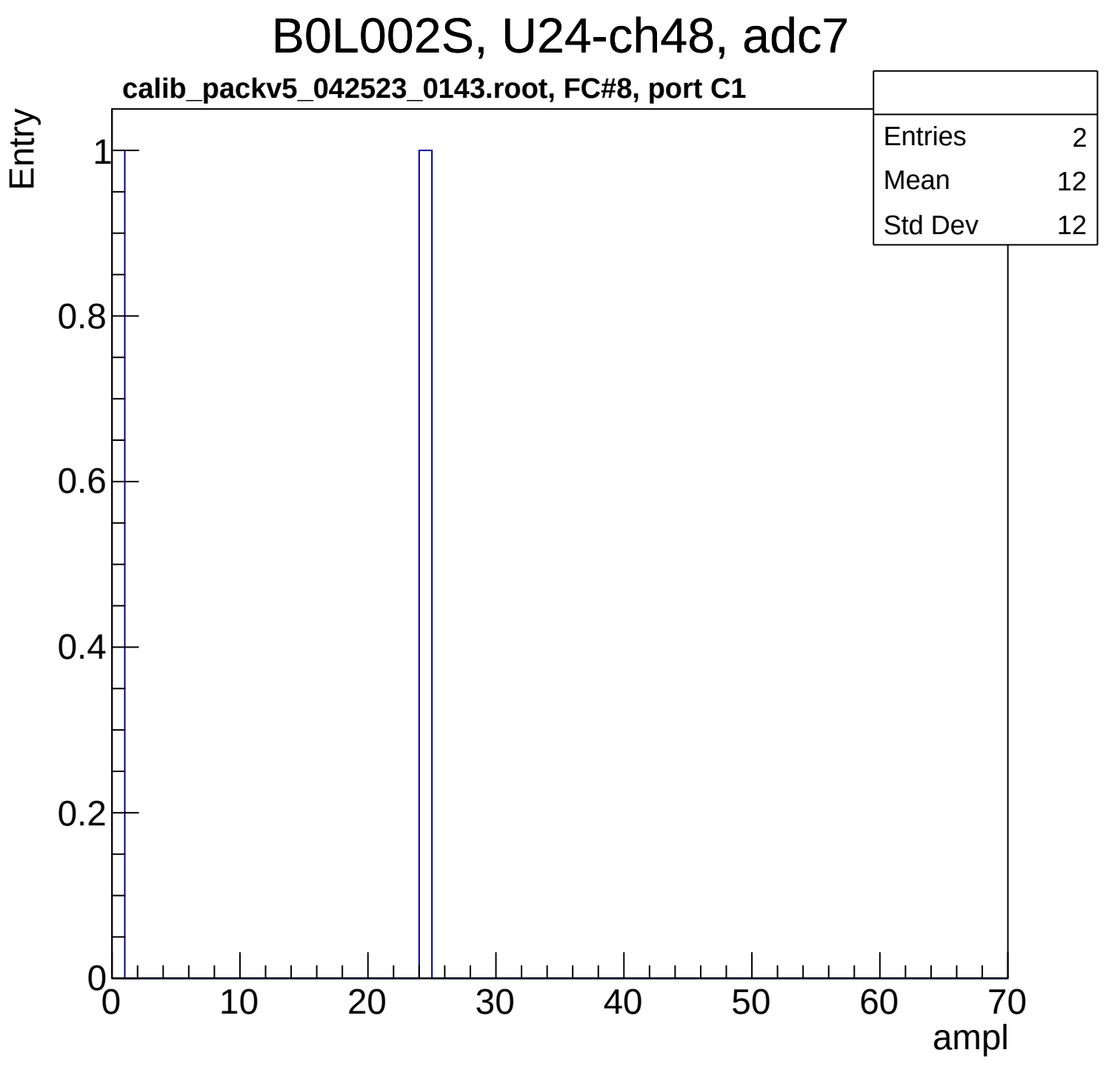

B0L002S, U24-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 45.28
Std Dev 3.353
Gaus mean : 45.7527
Gaus Width: 3.2808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

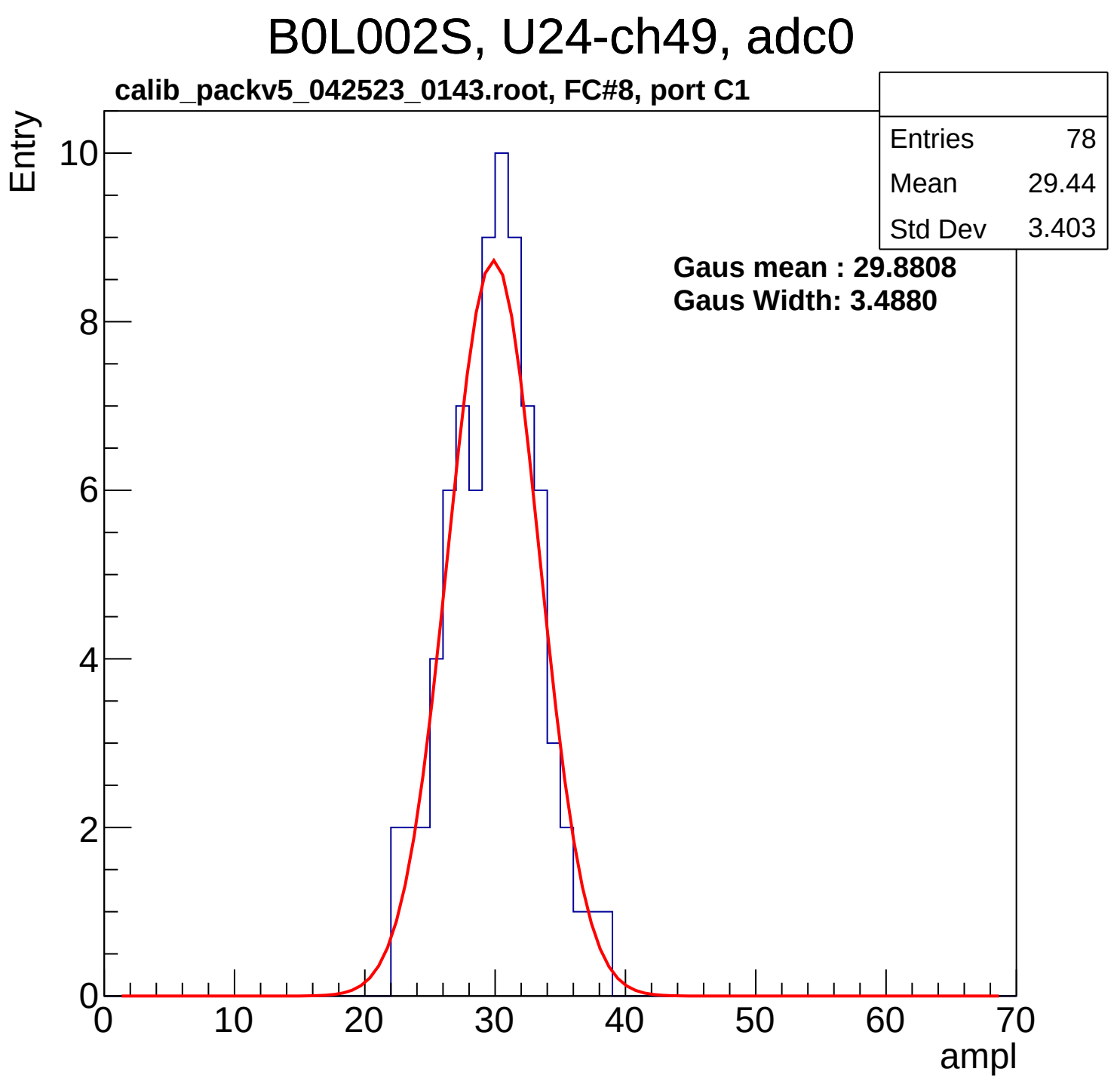

B0L002S, U24-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 52.23
Std Dev 3.19
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

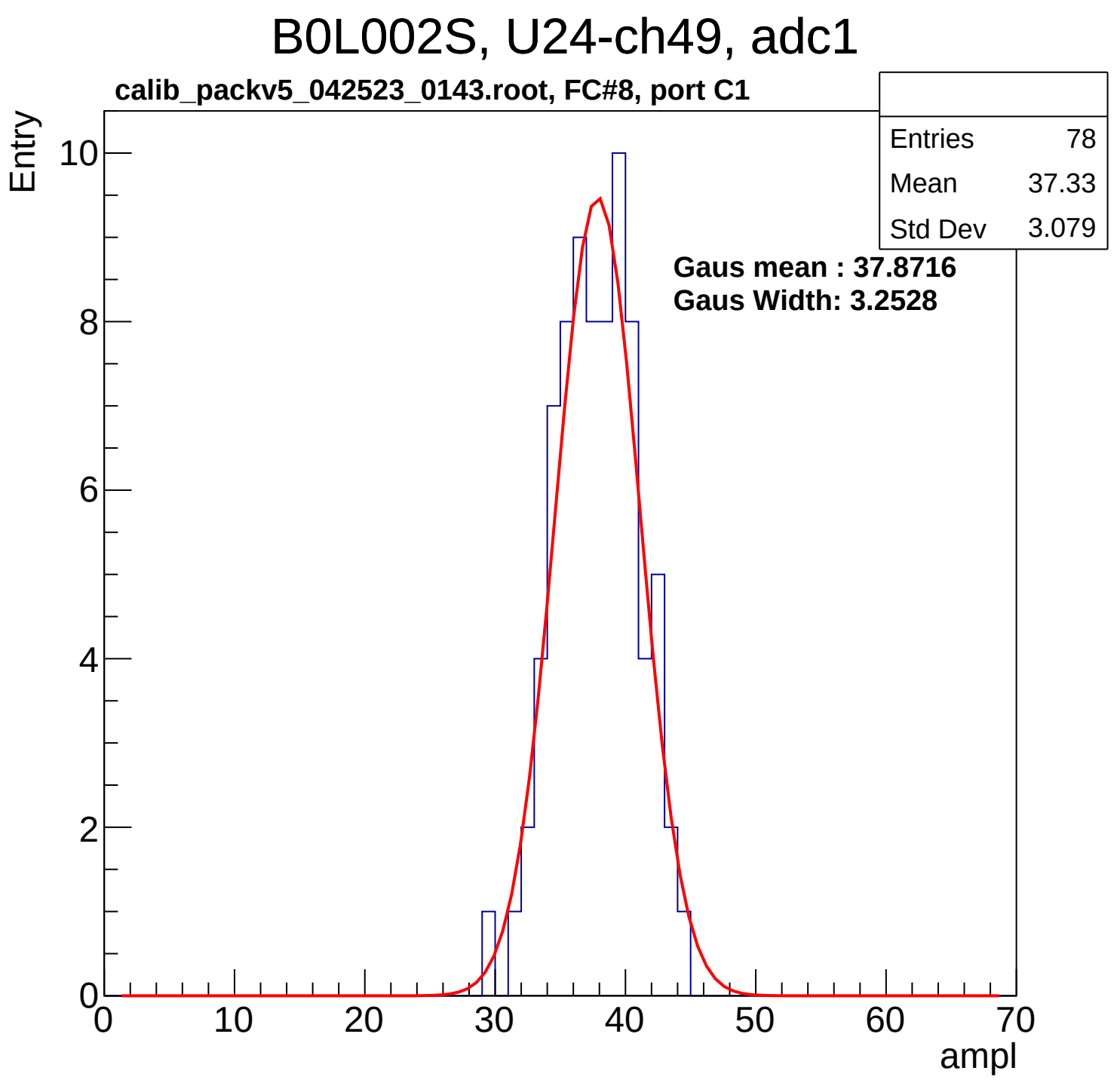

B0L002S, U24-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 57.36
Std Dev 2.434
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

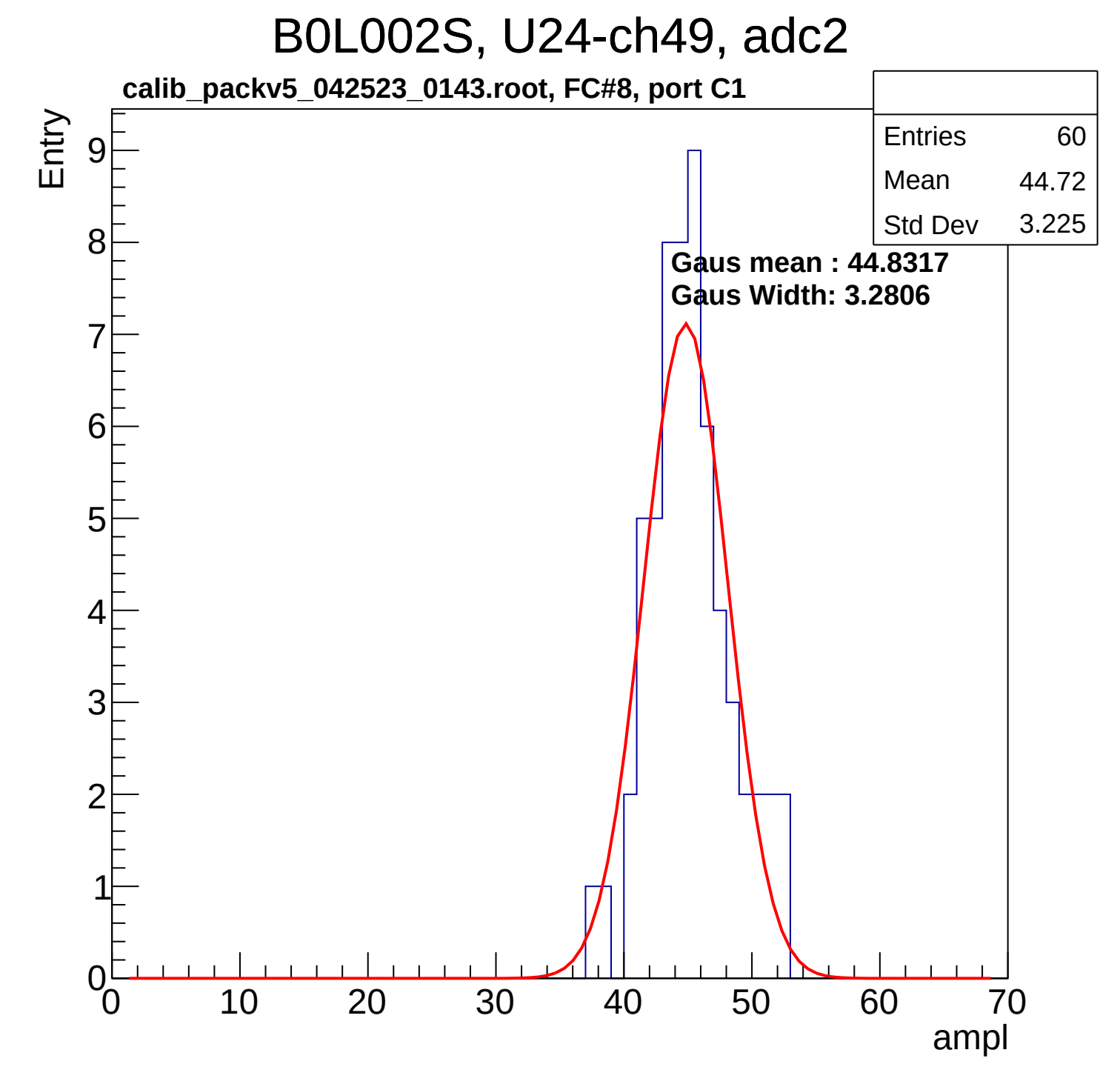

B0L002S, U24-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.12
Std Dev 10.62
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

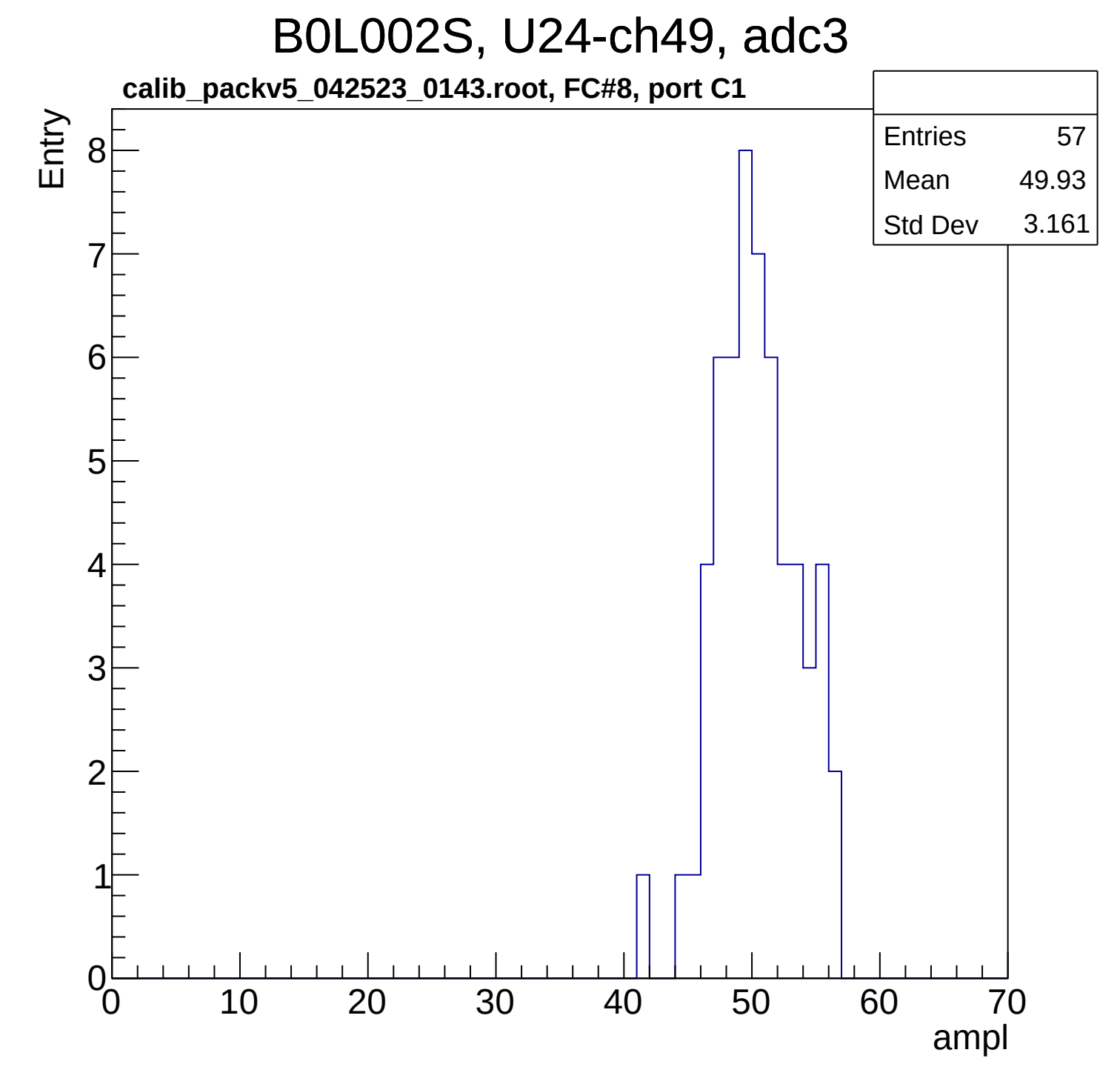

B0L002S, U24-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U24-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 29.44
Std Dev 3.403
Gaus mean : 29.8808
Gaus Width: 3.4880
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.33
Std Dev 3.079
Gaus mean : 37.8716
Gaus Width: 3.2528
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.72
Std Dev 3.225
Gaus mean : 44.8317
Gaus Width: 3.2806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 49.93
Std Dev 3.161
Entry
ampl
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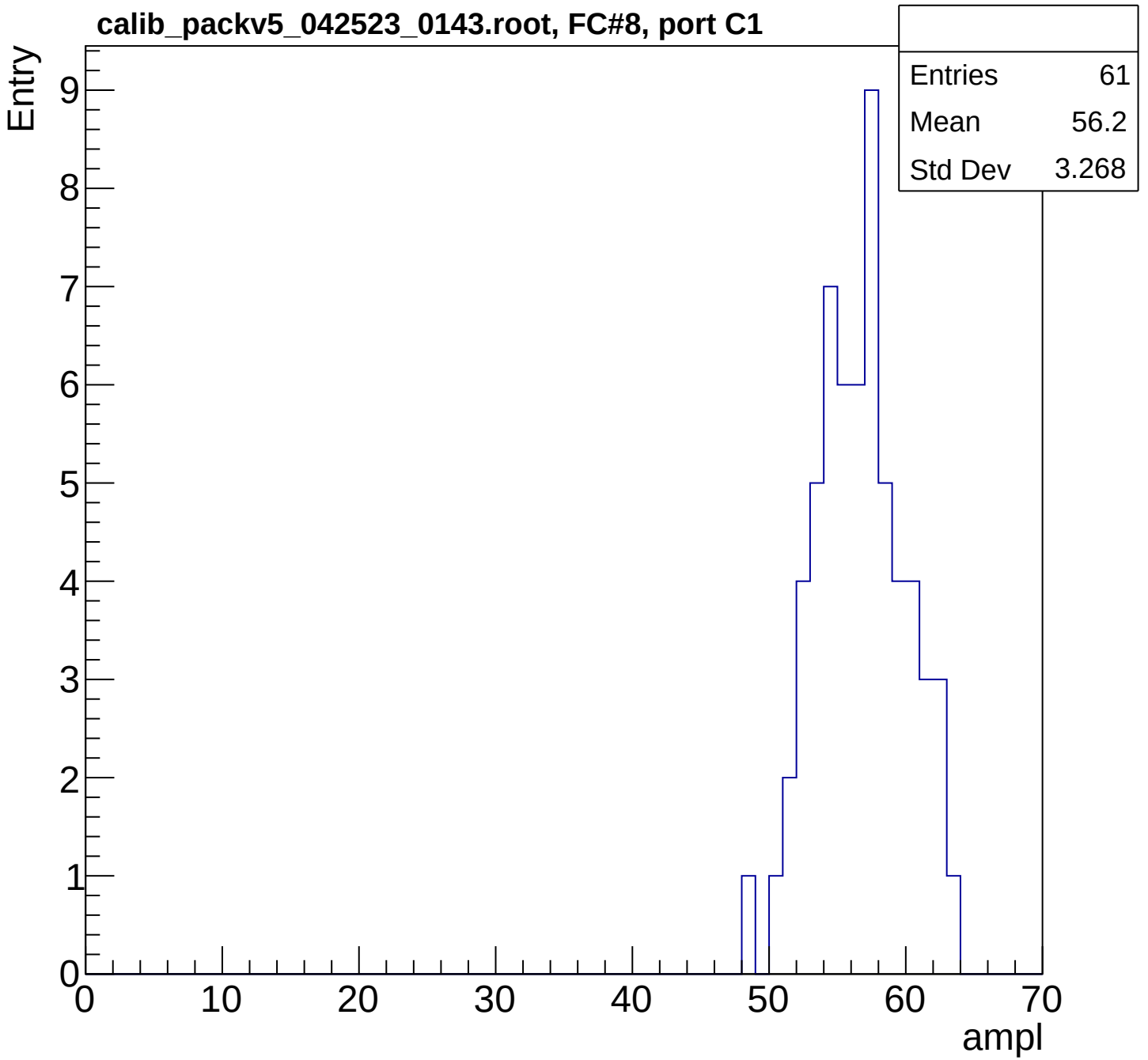
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.2
Std Dev 3.268
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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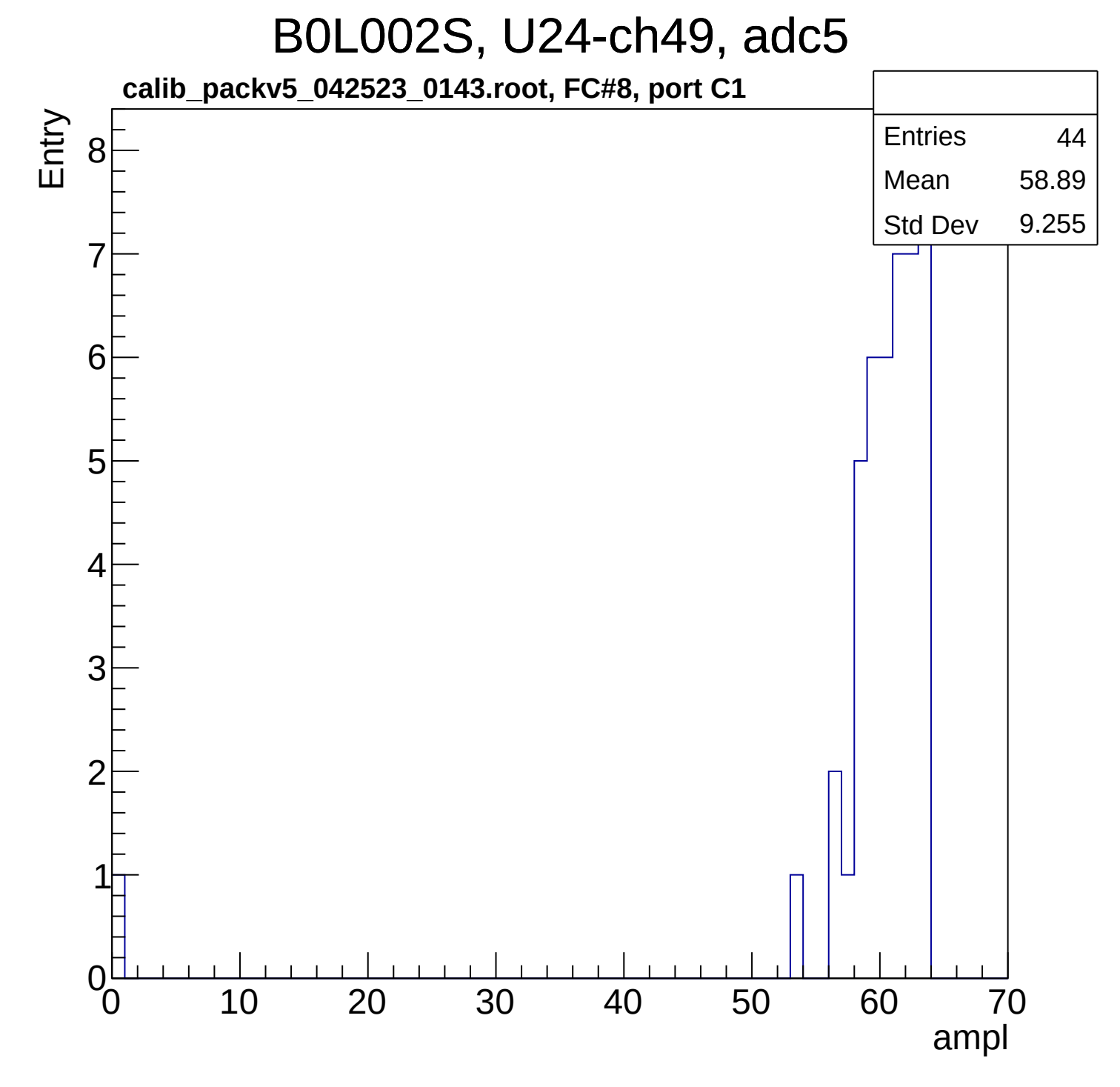
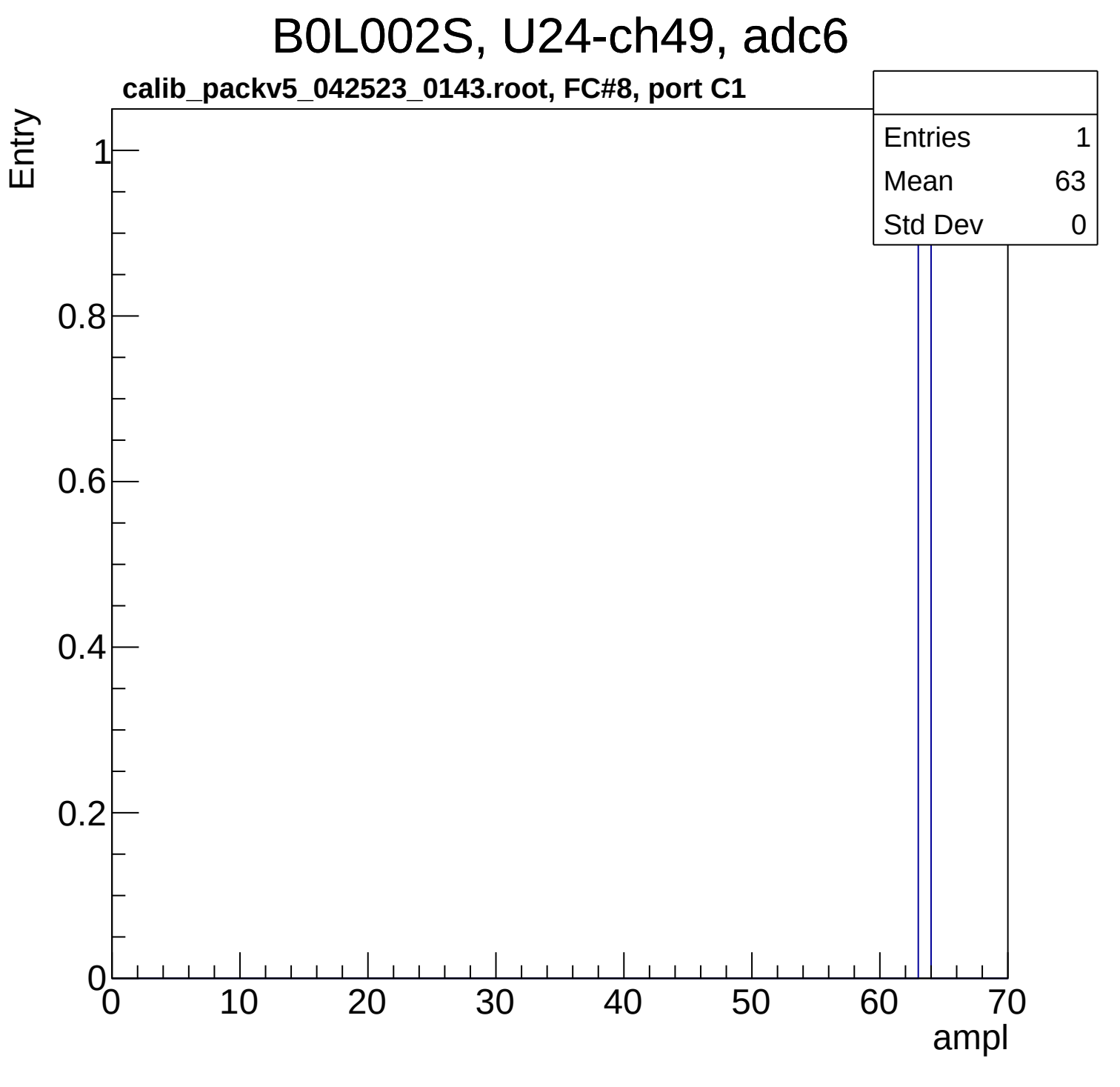
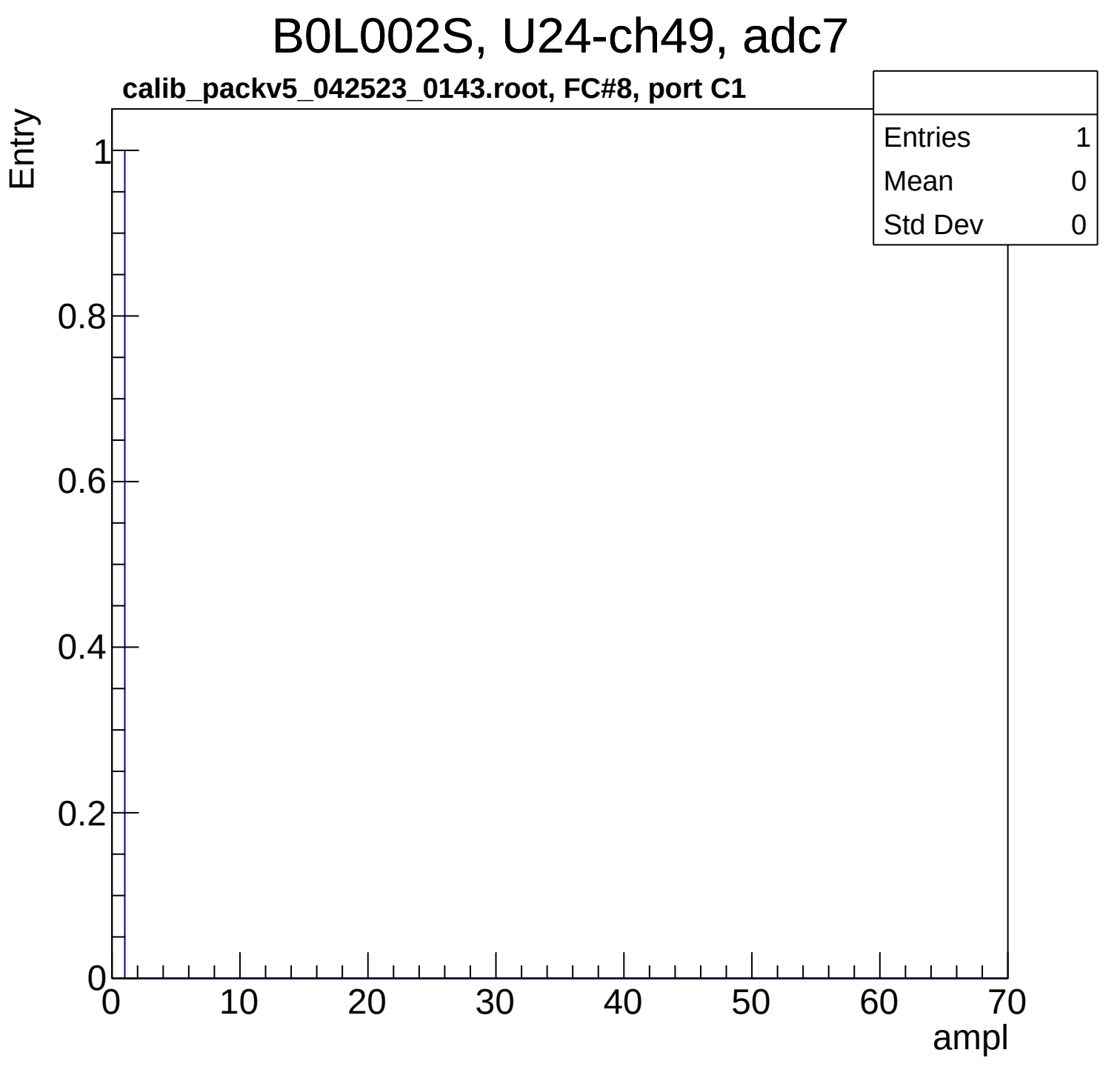
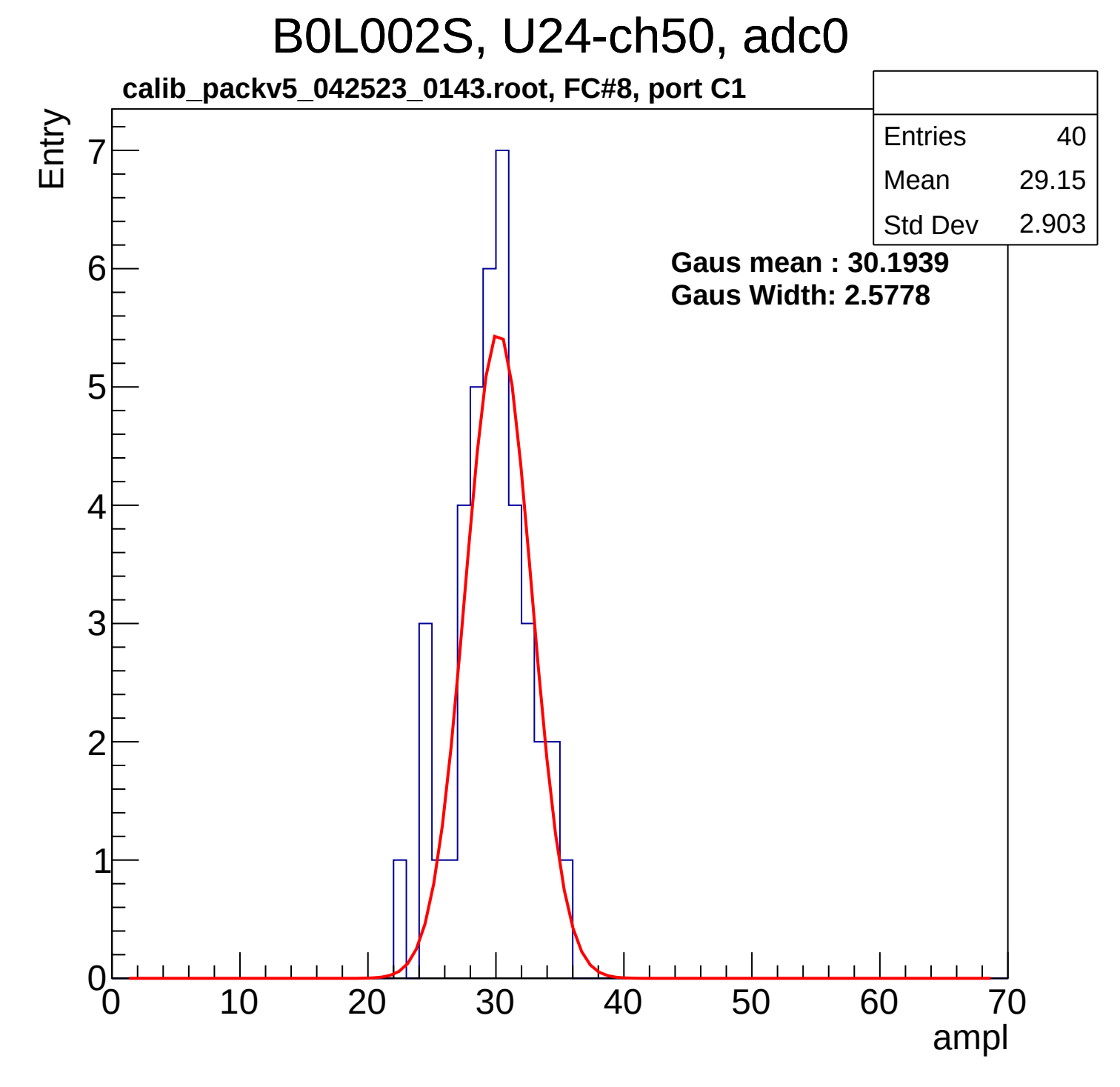
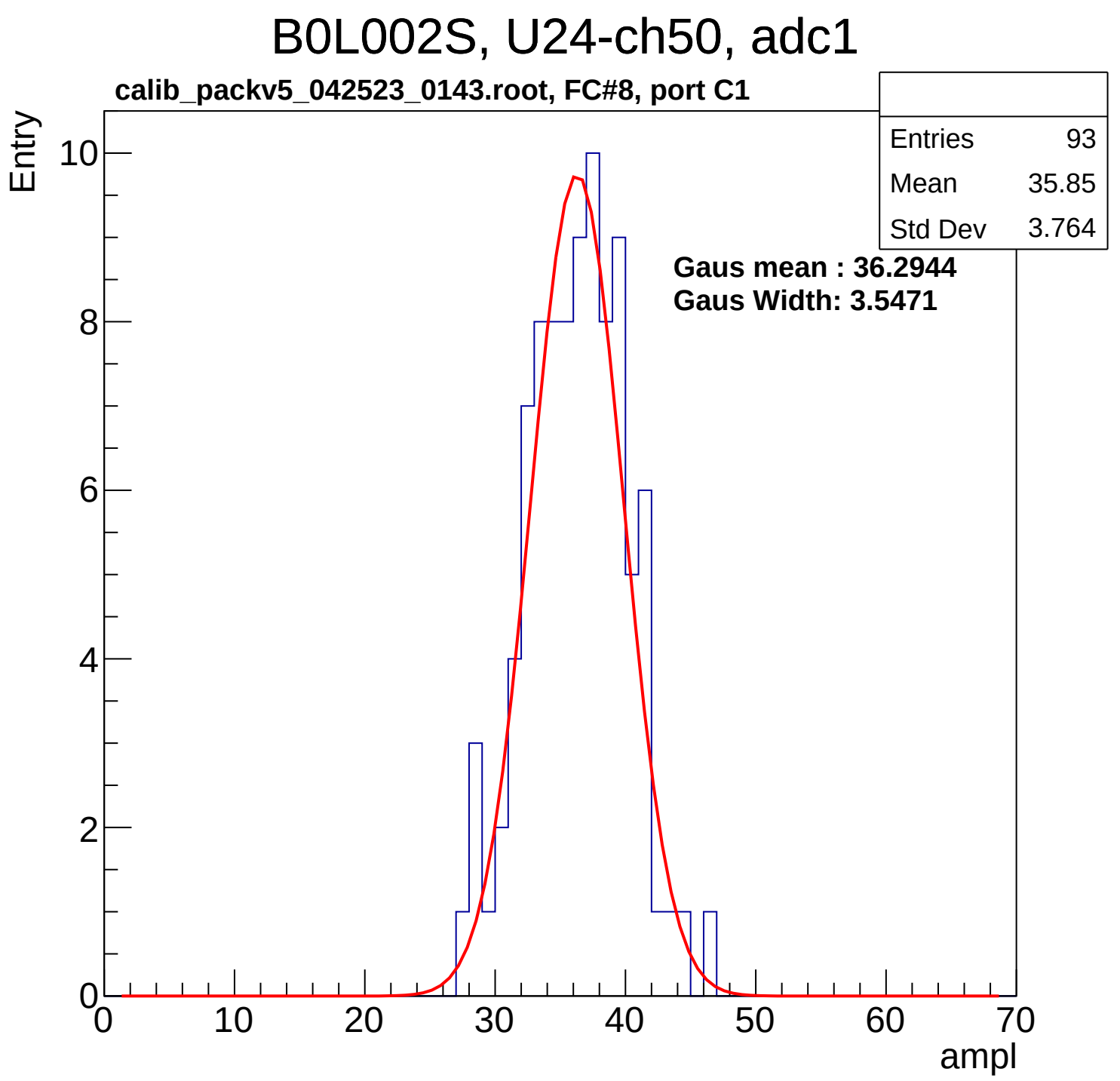
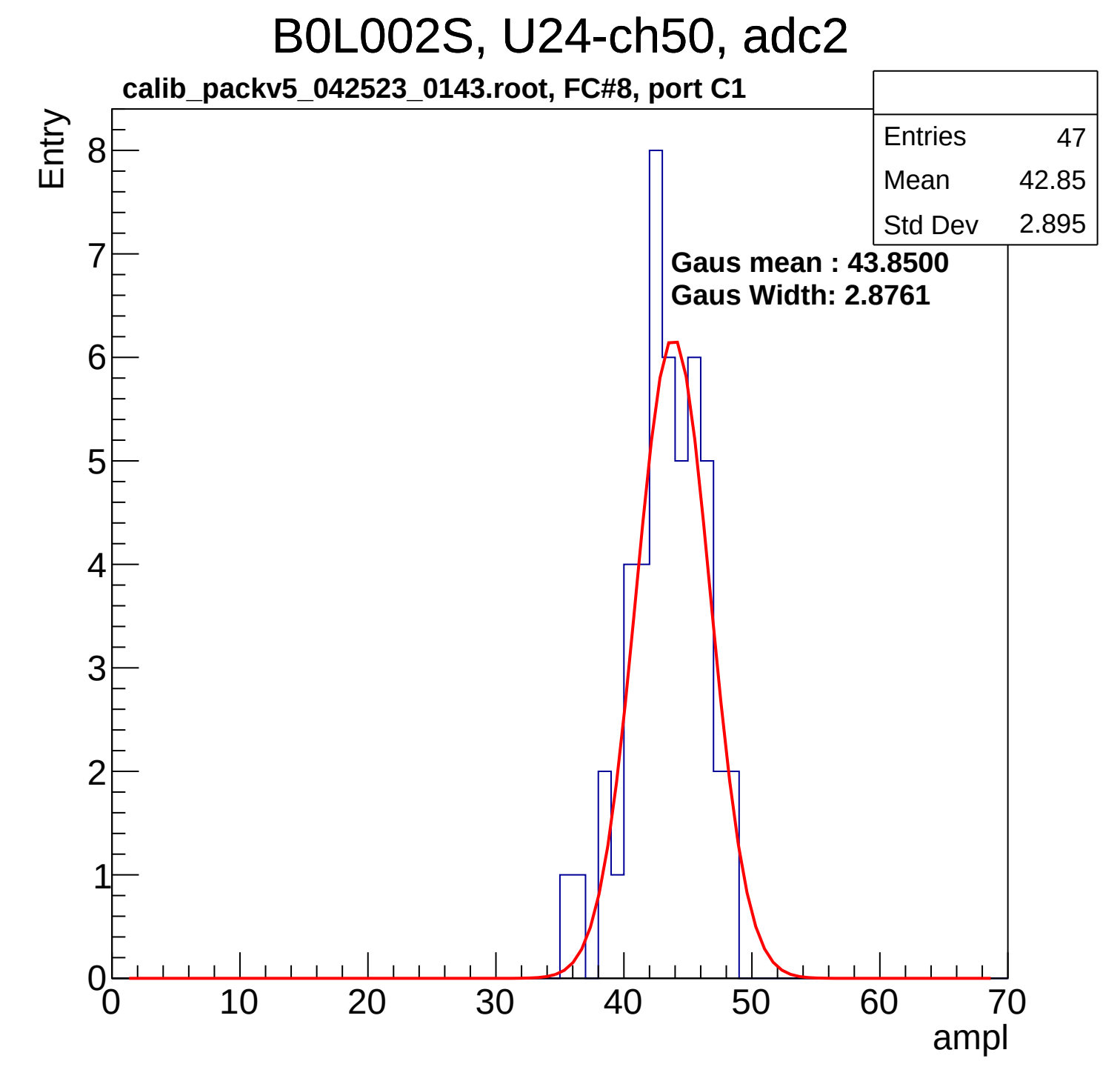

B0L002S, U24-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.89
Std Dev 9.255
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

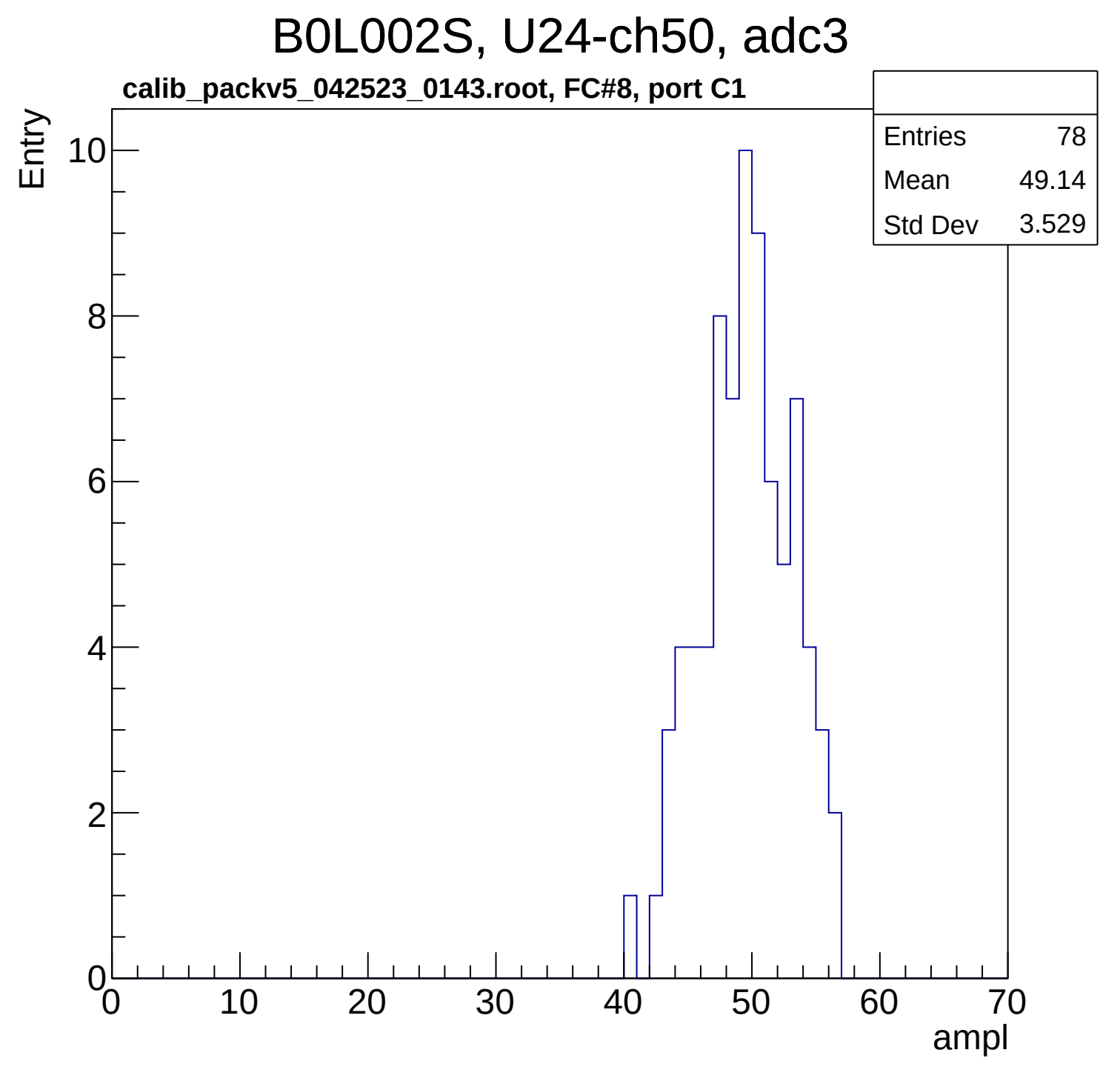

B0L002S, U24-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 29.15
Std Dev 2.903
Gaus mean : 30.1939
Gaus Width: 2.5778
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 93
Mean 35.85
Std Dev 3.764
Gaus mean : 36.2944
Gaus Width: 3.5471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 42.85
Std Dev 2.895
Gaus mean : 43.8500
Gaus Width: 2.8761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 49.14
Std Dev 3.529
Entry
ampl
0
2
4
6
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0
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40
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60
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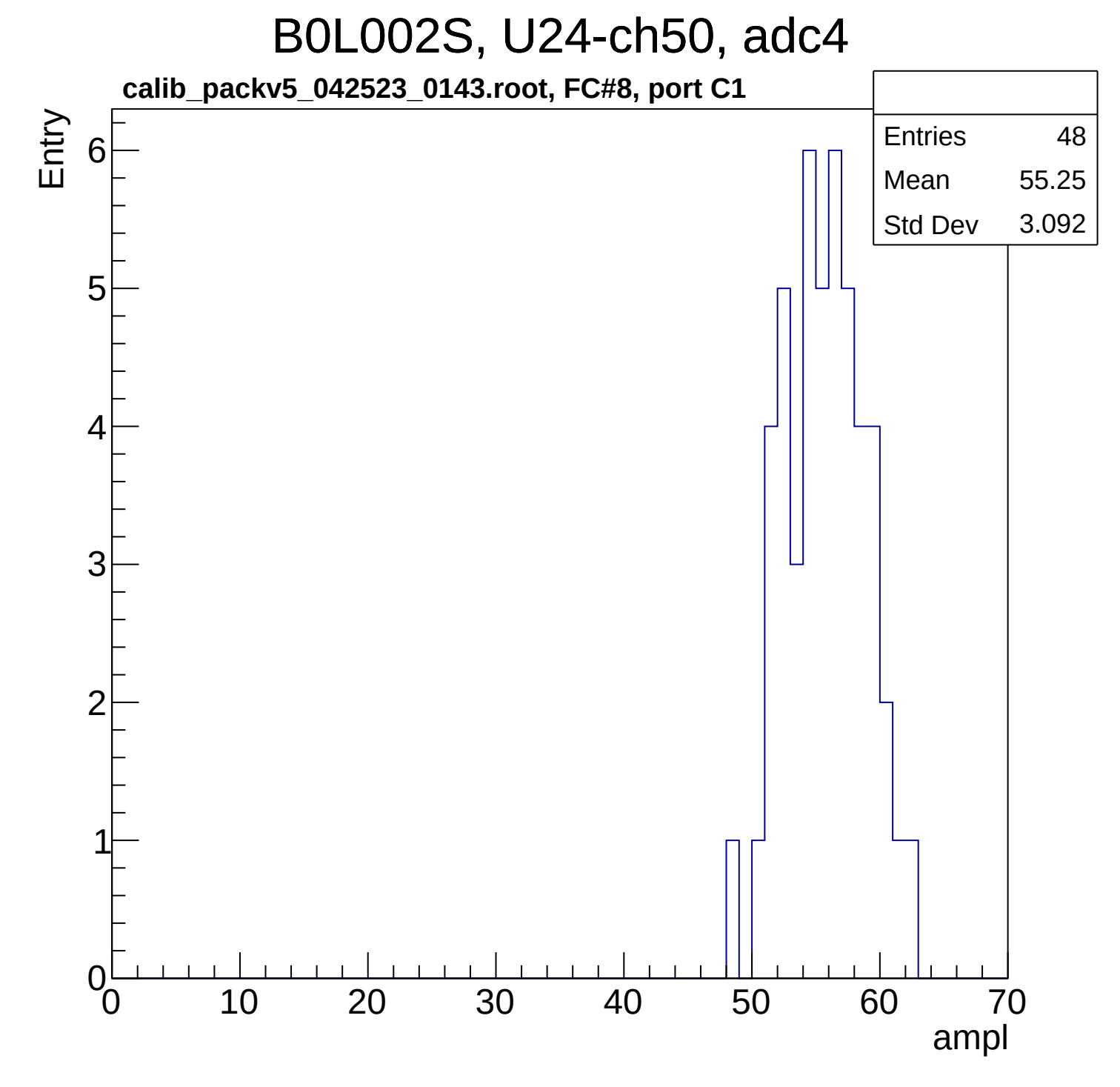
B0L002S, U24-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 55.25
Std Dev 3.092
Entry
ampl
0
1
2
3
4
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6
0
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60
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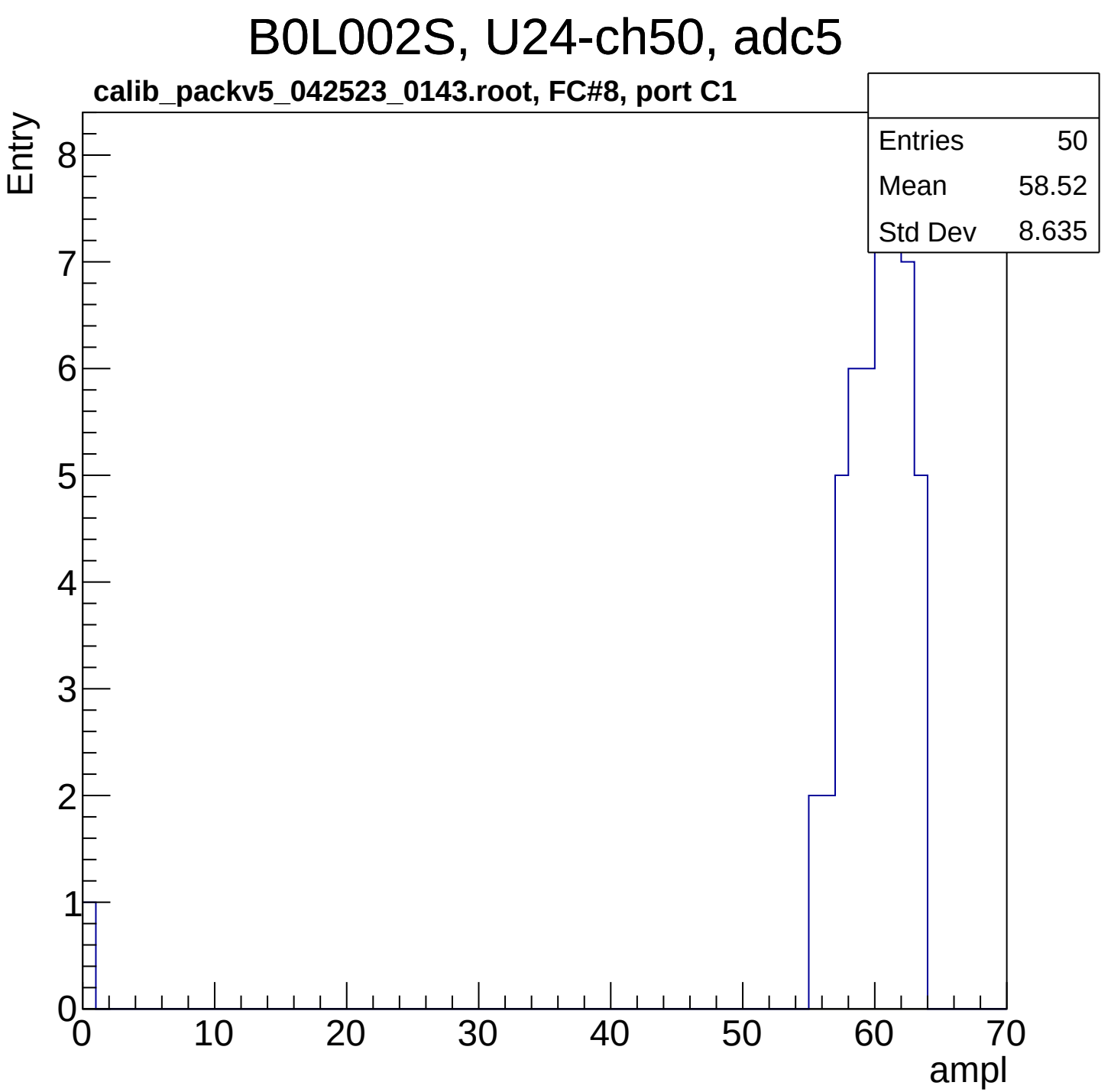
B0L002S, U24-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.52
Std Dev 8.635
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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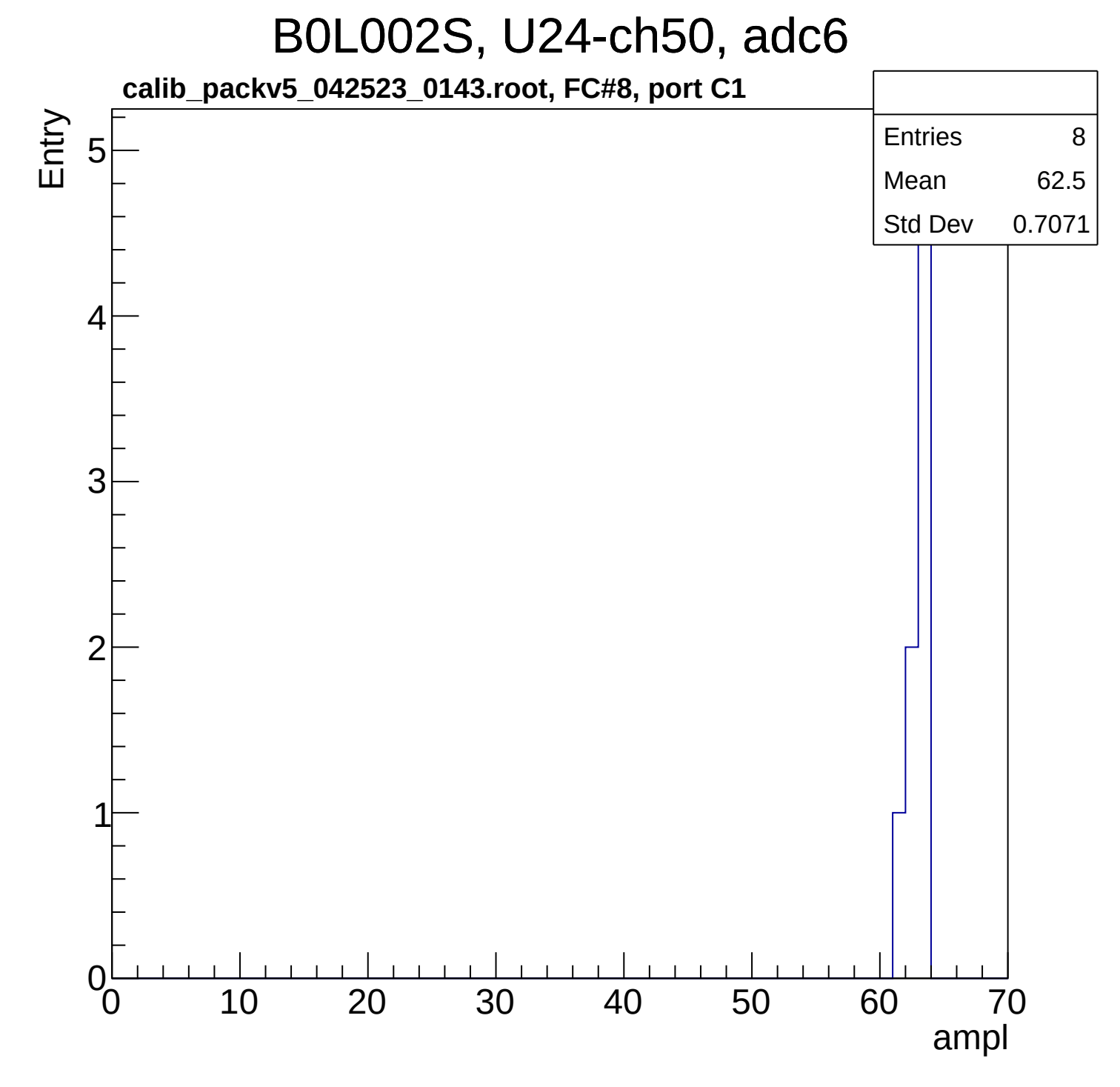
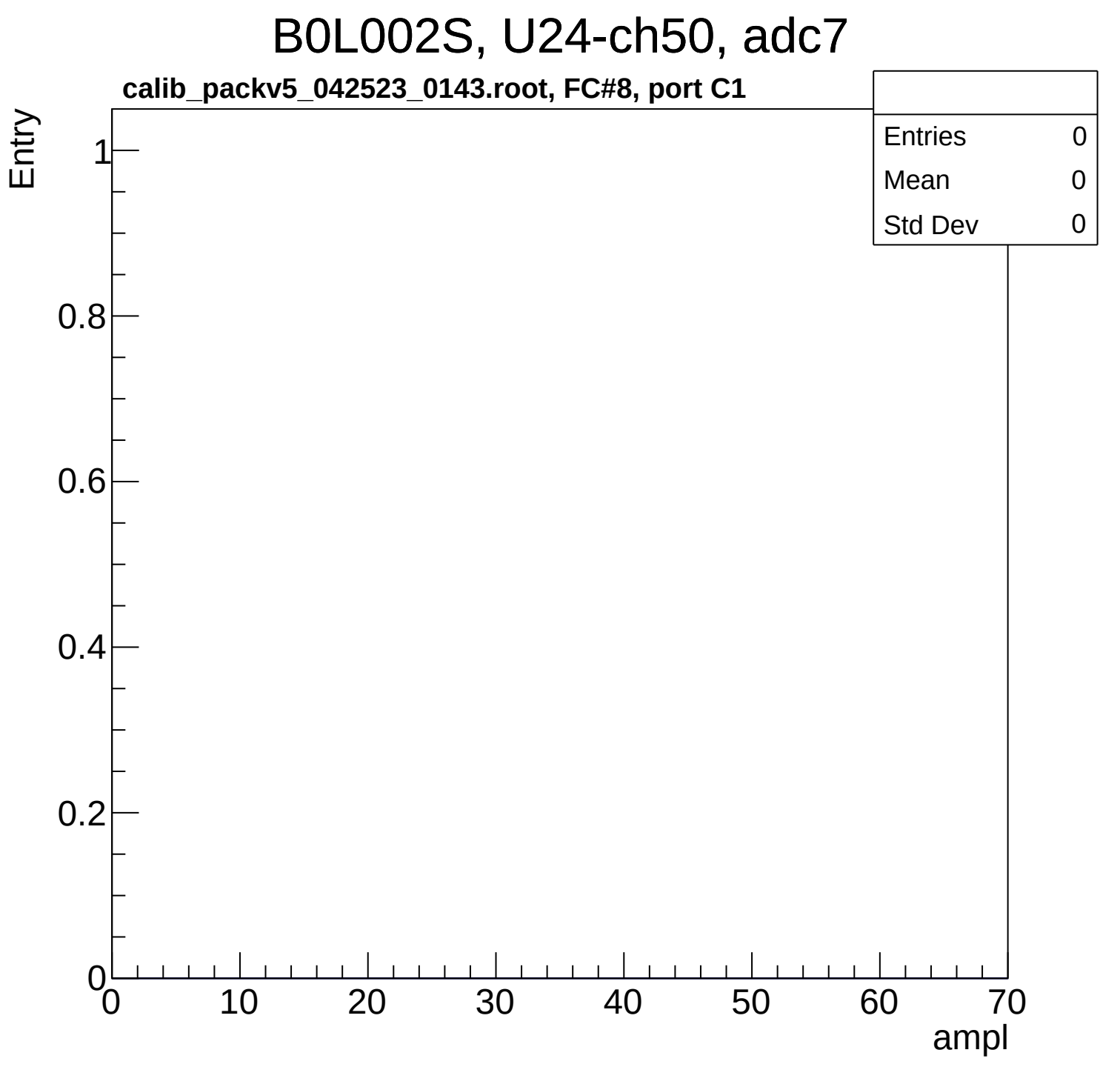
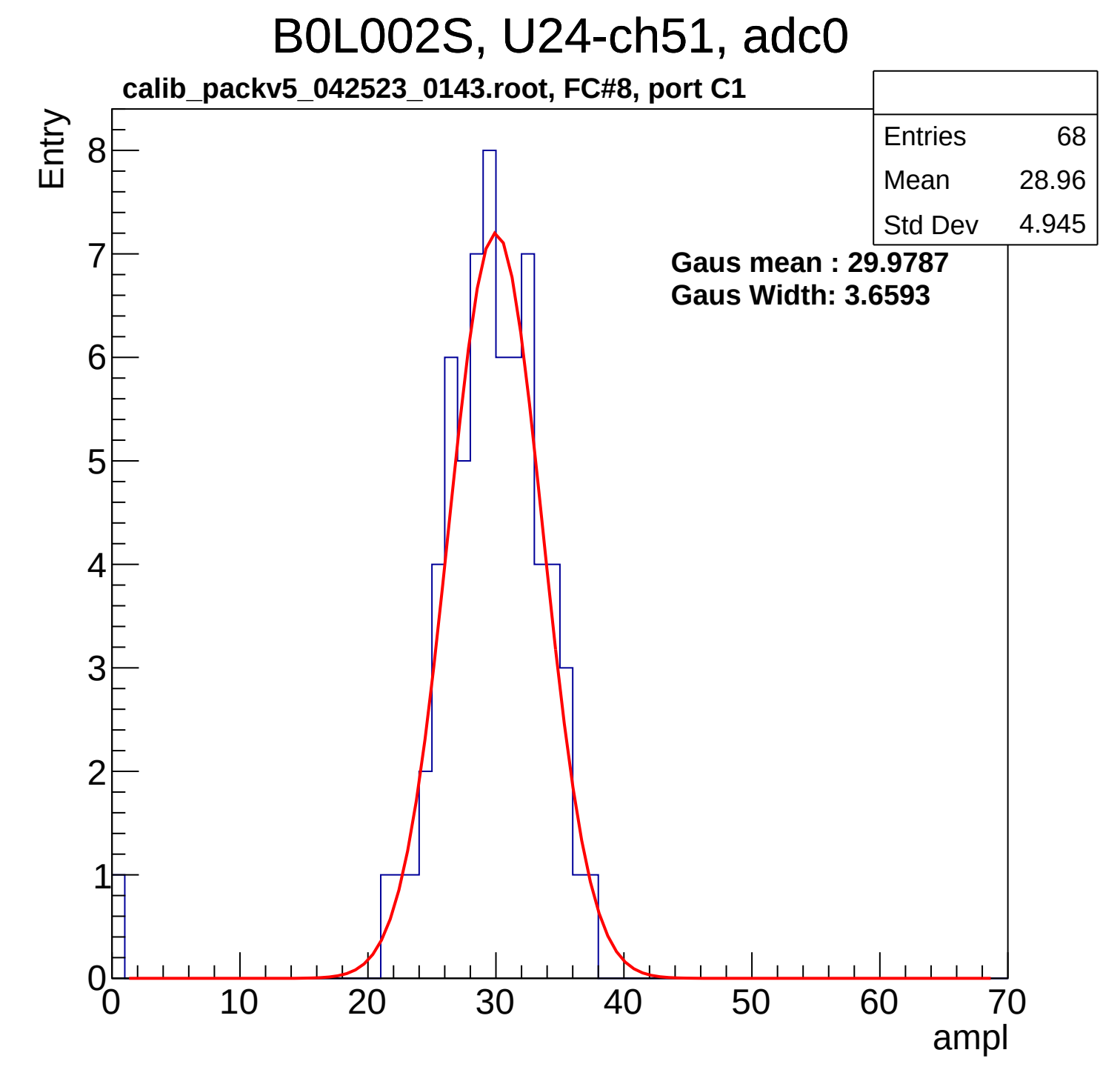
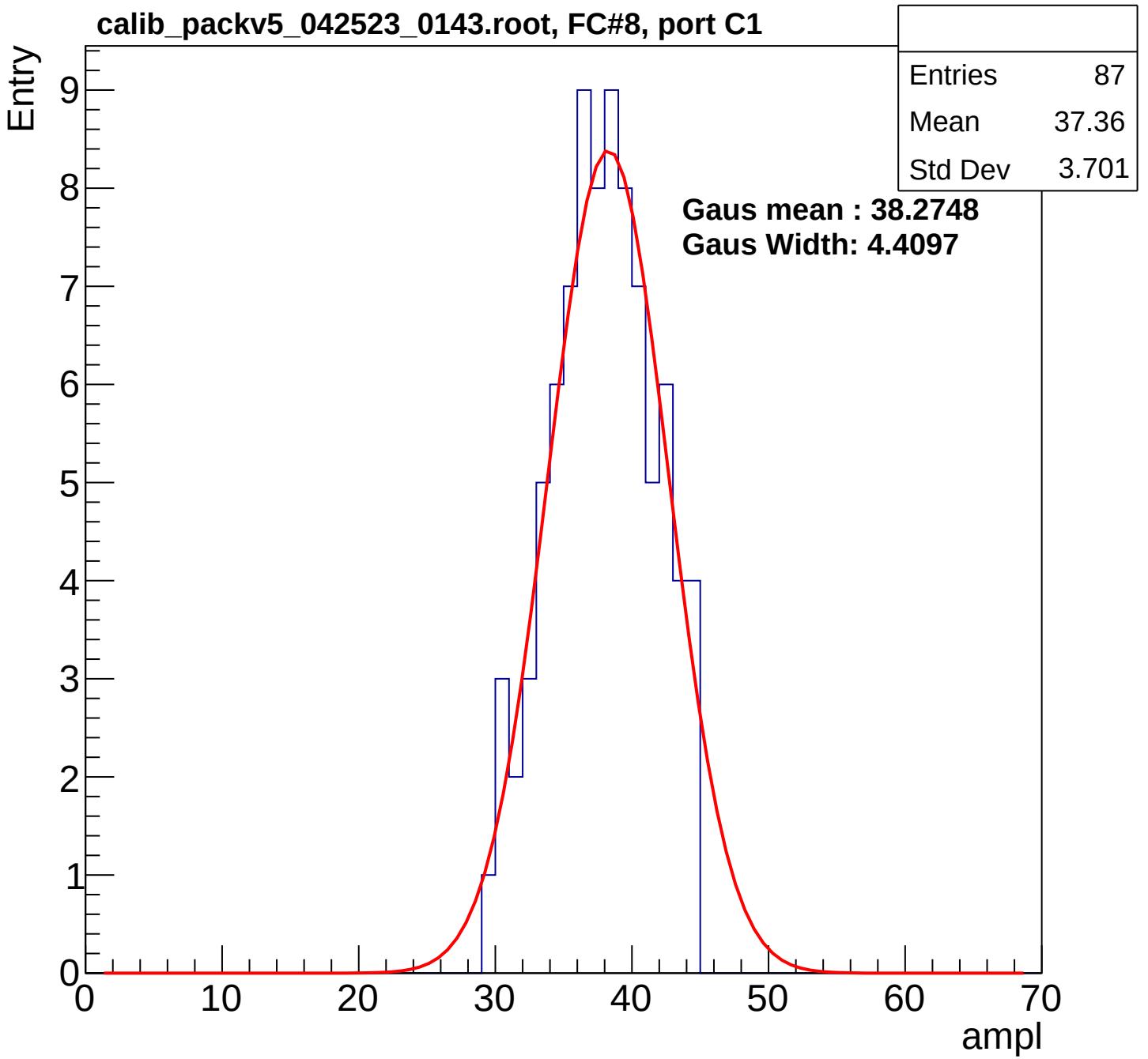

B0L002S, U24-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 28.96
Std Dev 4.945
Gaus mean : 29.9787
Gaus Width: 3.6593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 37.36
Std Dev 3.701
Gaus mean : 38.2748
Gaus Width: 4.4097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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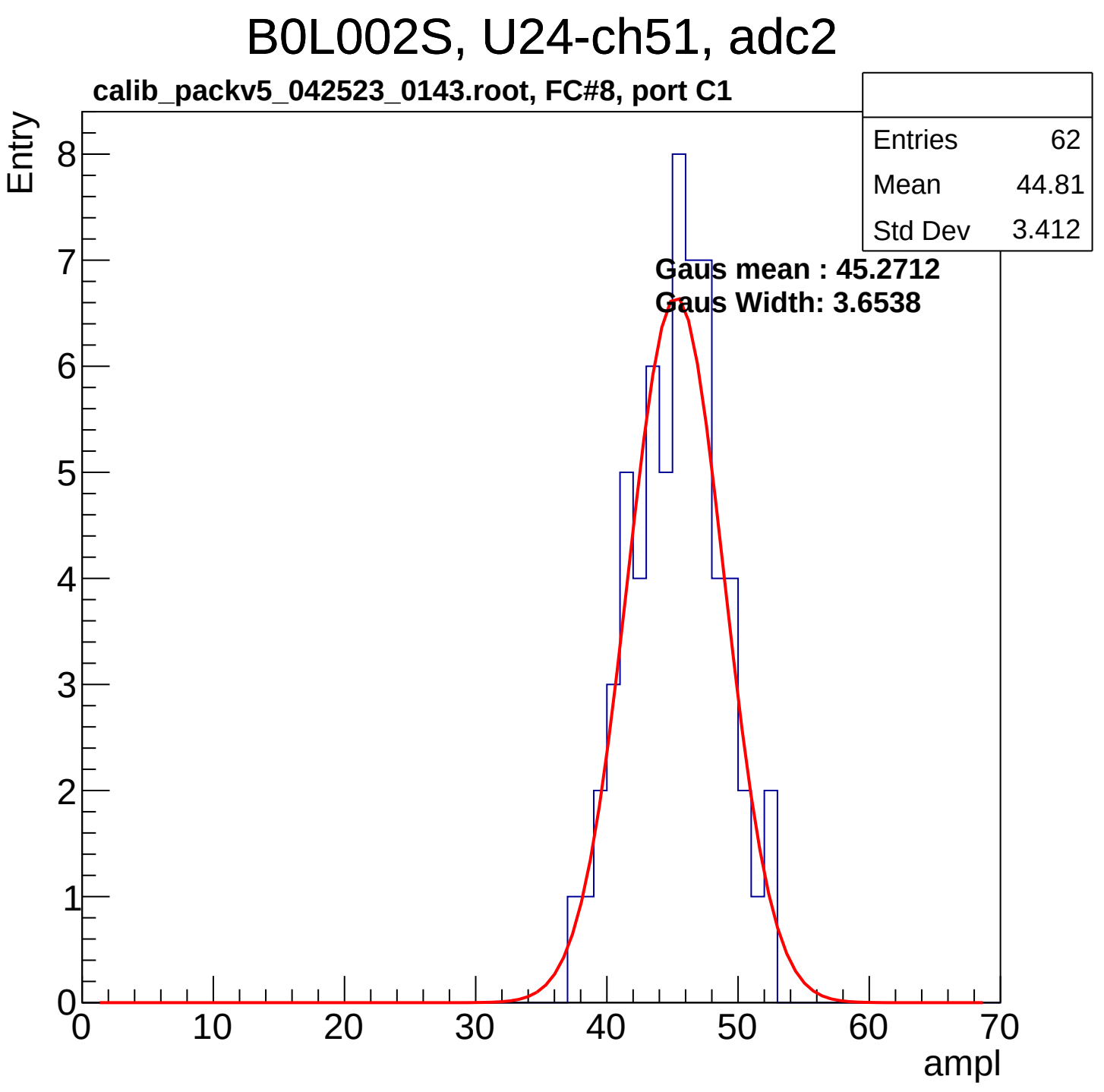
B0L002S, U24-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.81
Std Dev 3.412
Gaus mean : 45.2712
Gaus Width: 3.6538
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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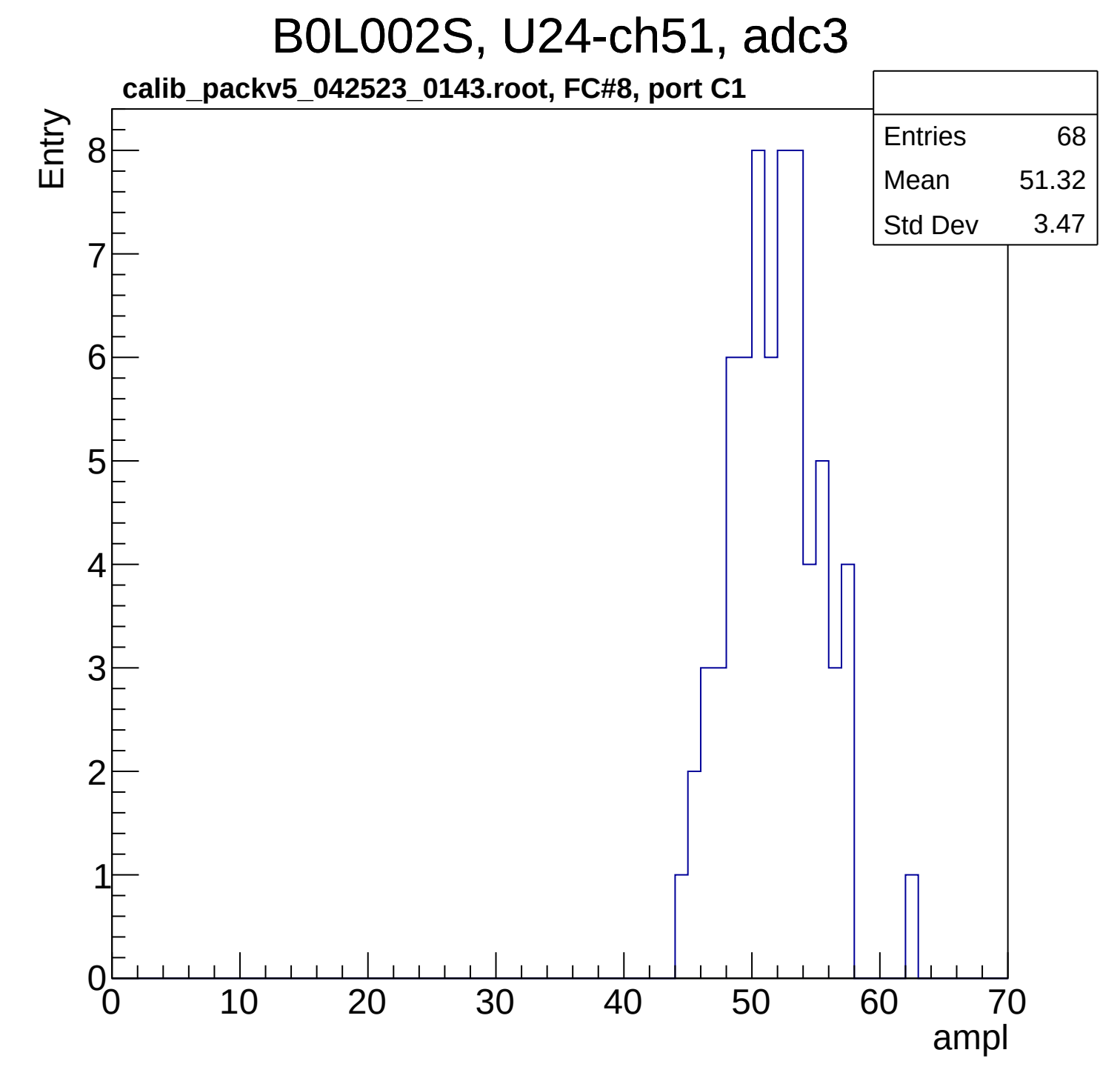
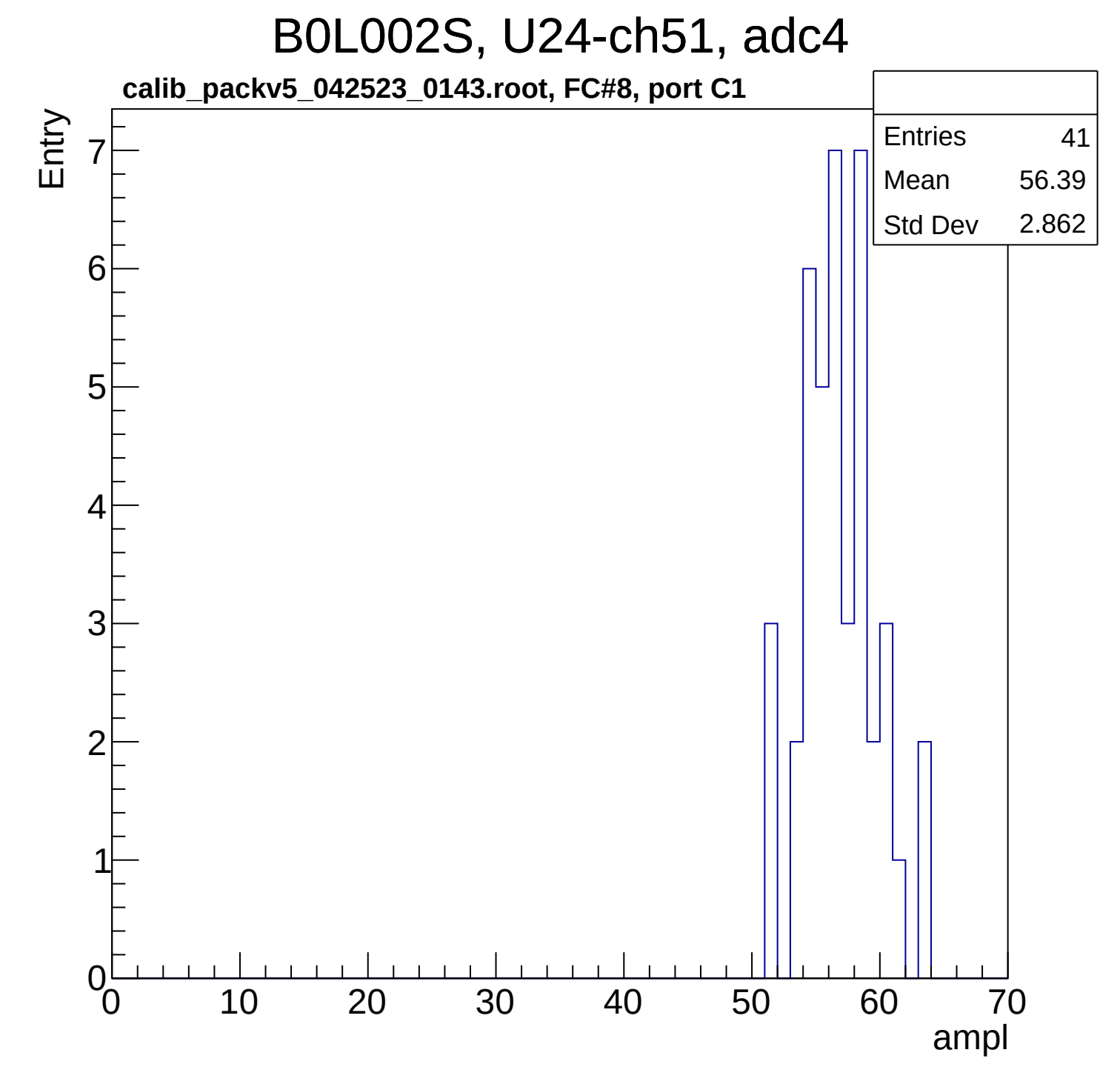

B0L002S, U24-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.32
Std Dev 3.47
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 56.39
Std Dev 2.862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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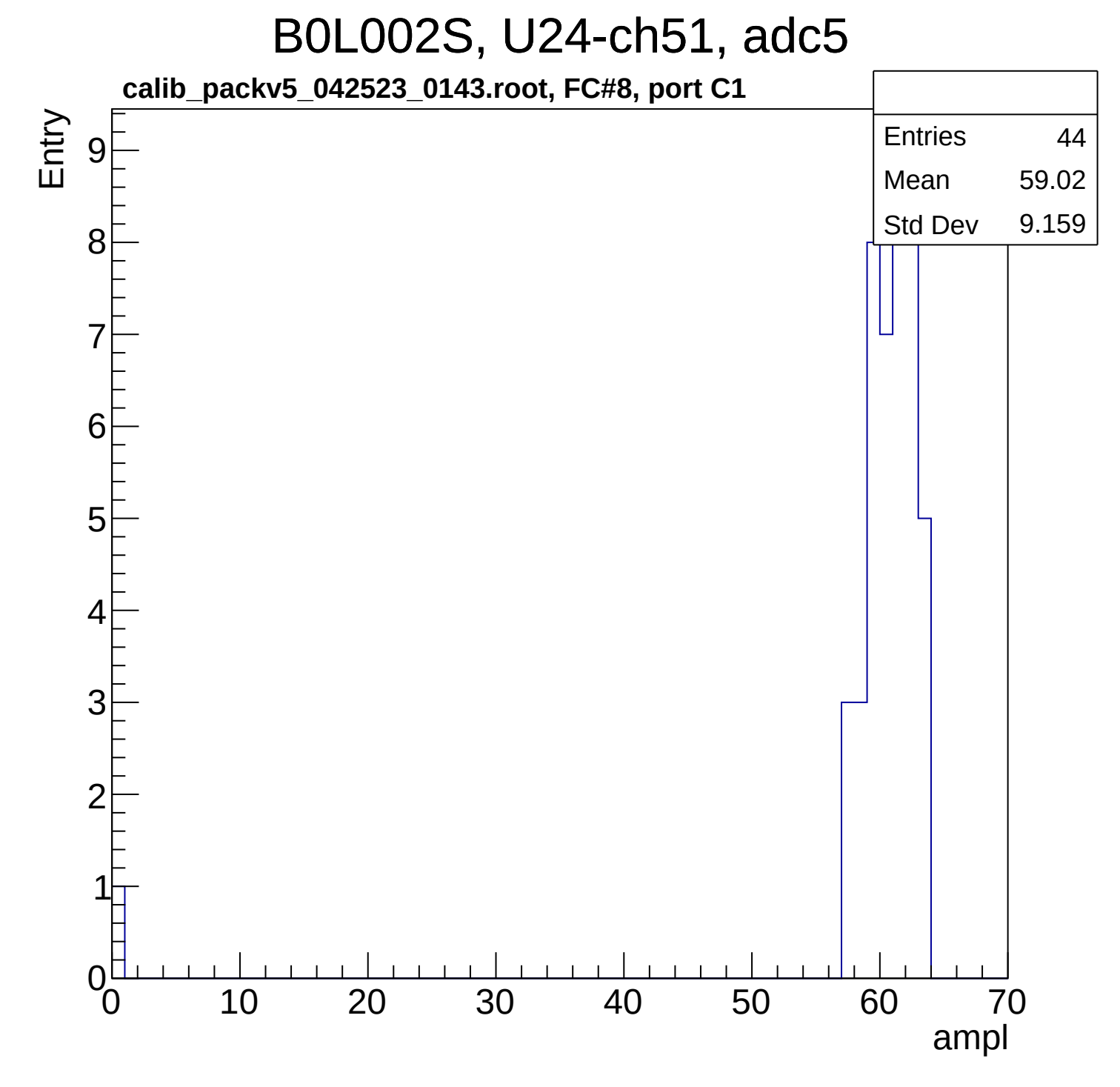
B0L002S, U24-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.02
Std Dev 9.159
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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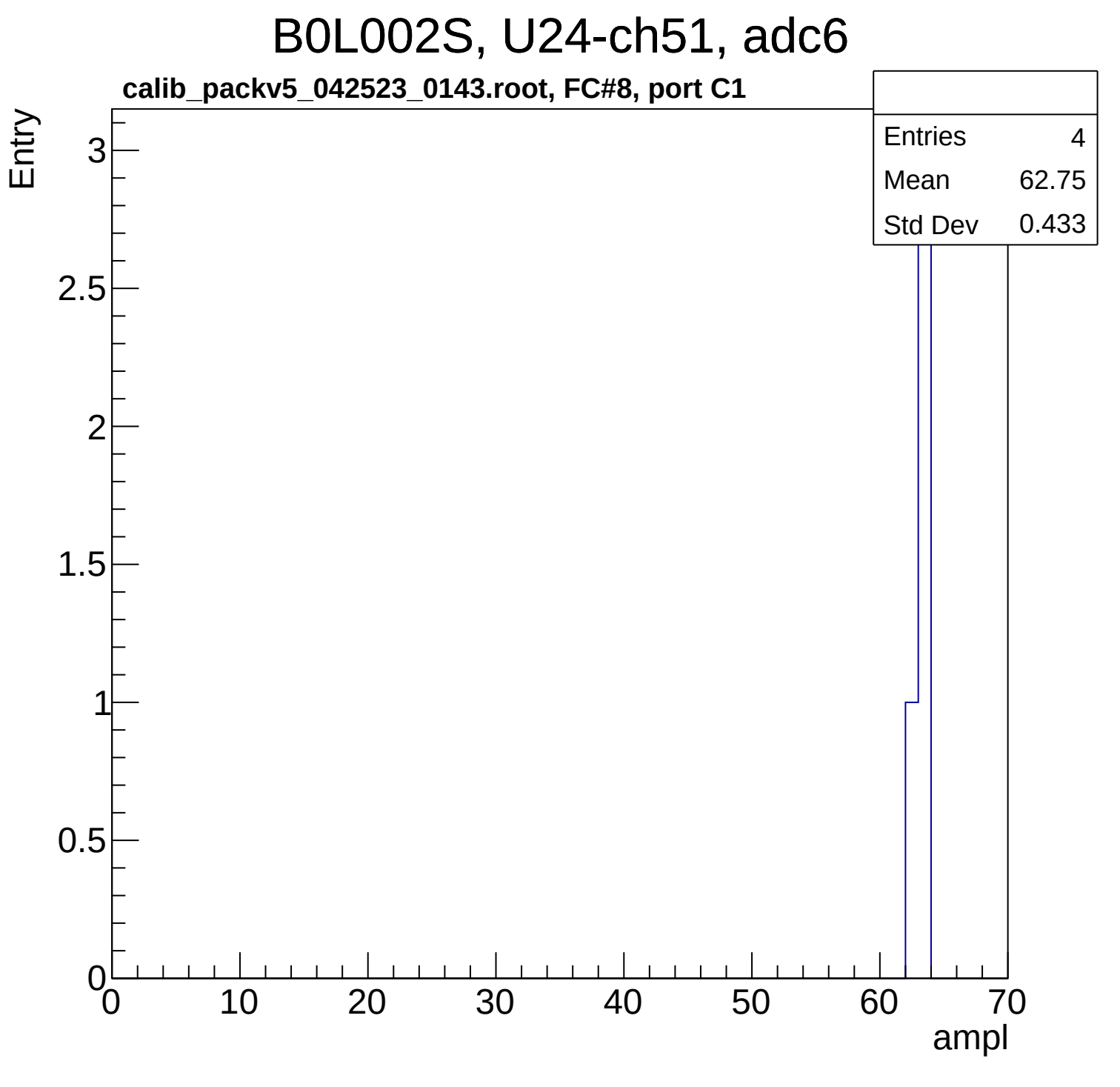
B0L002S, U24-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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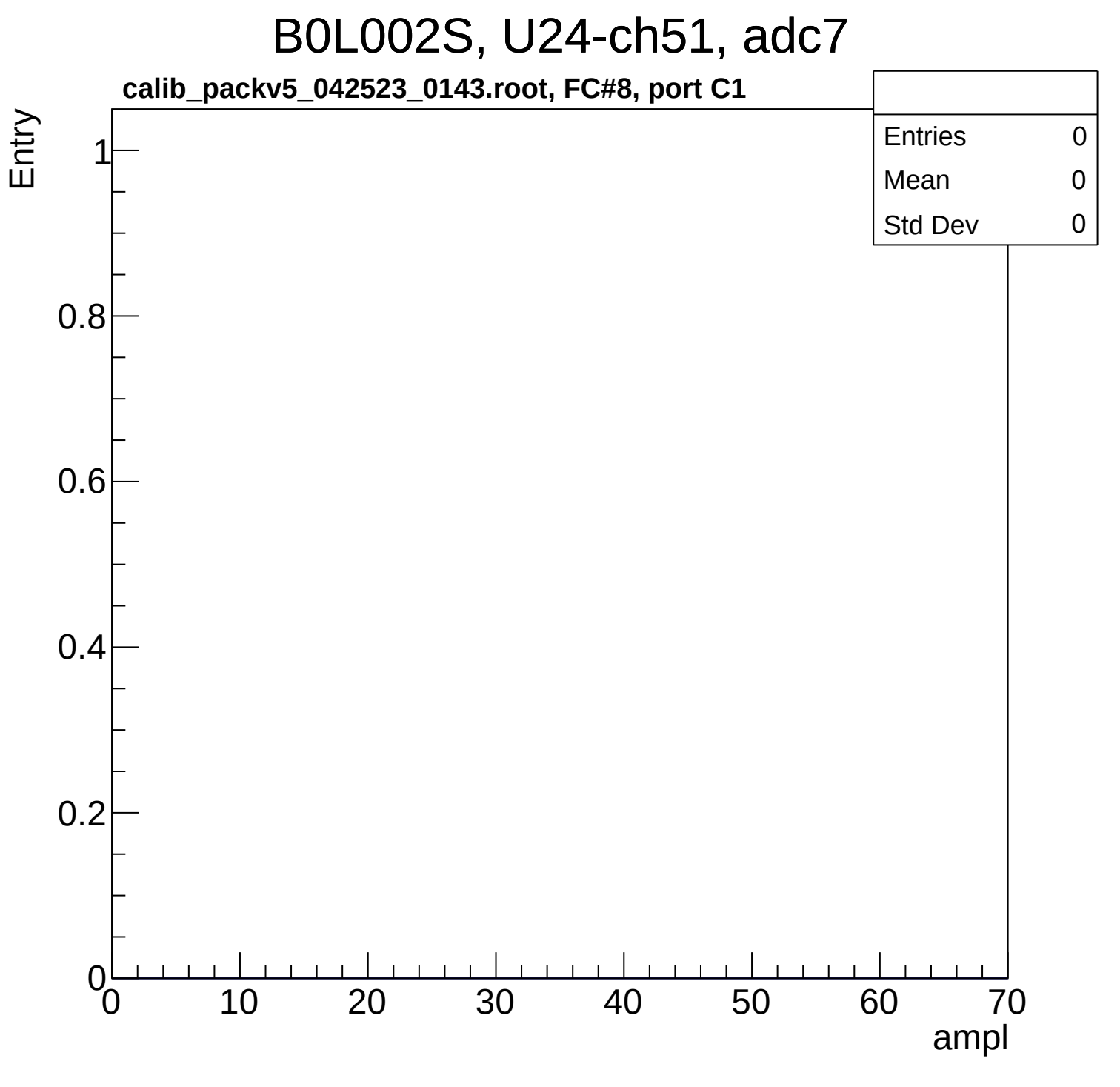
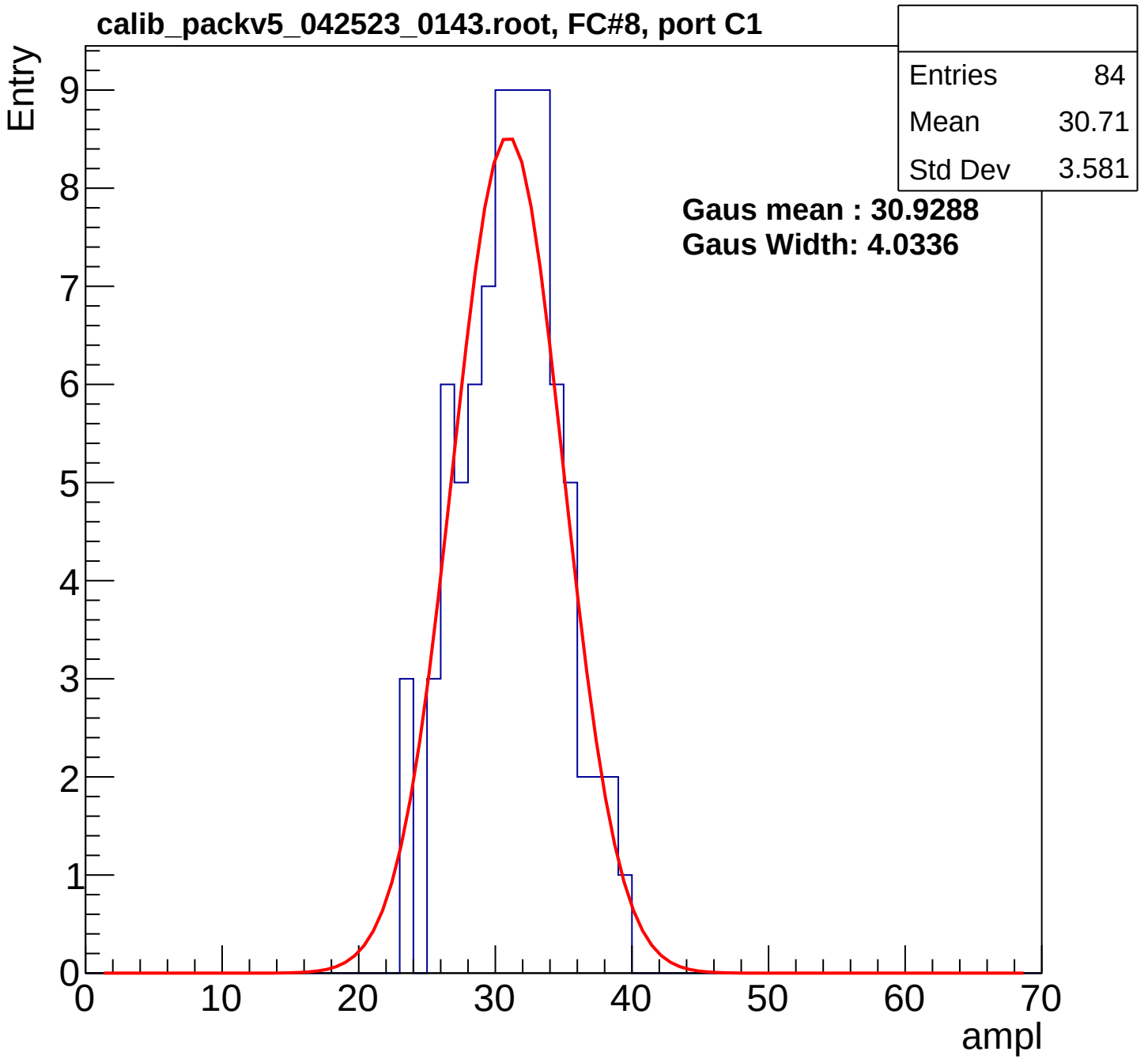
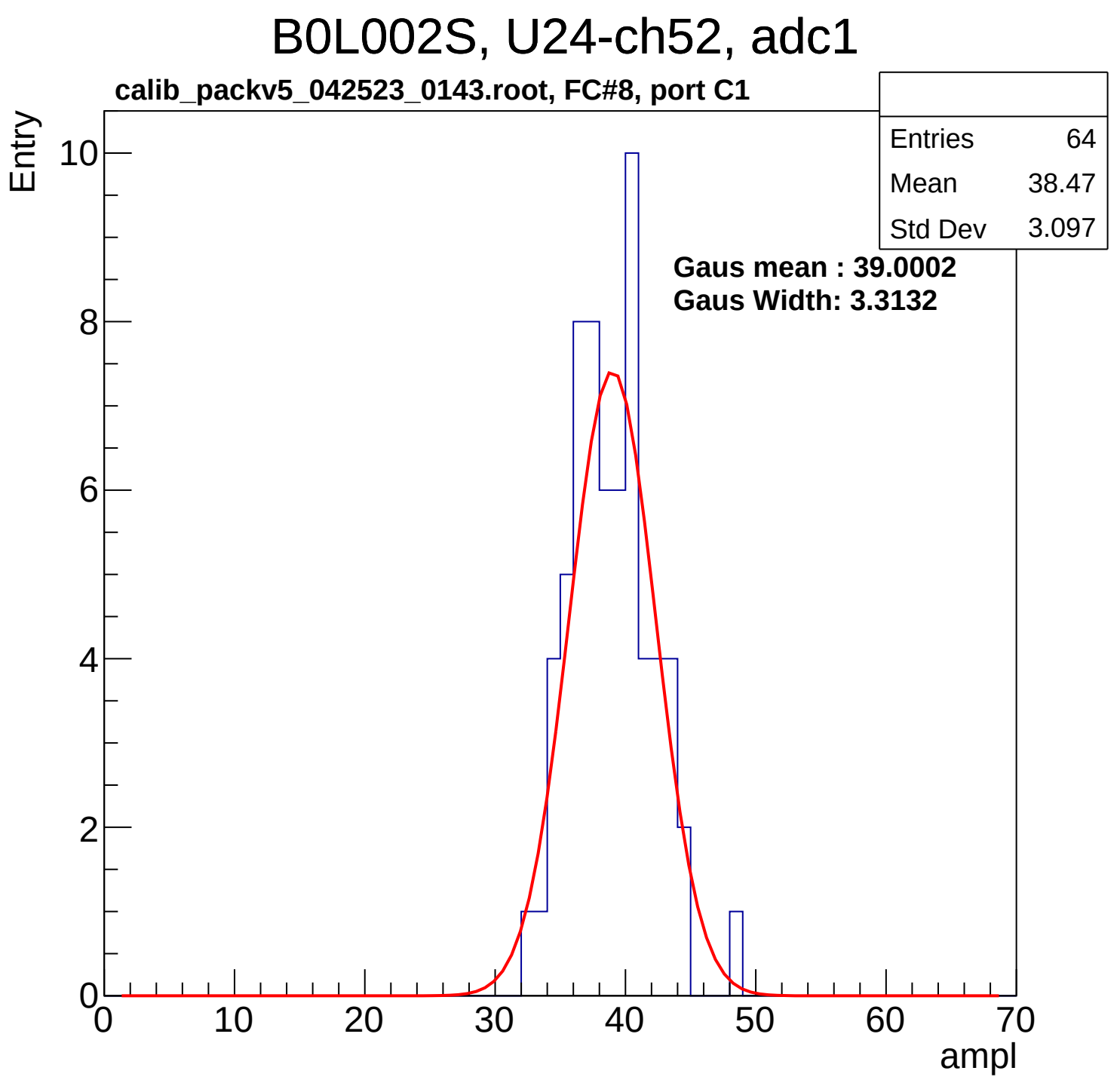
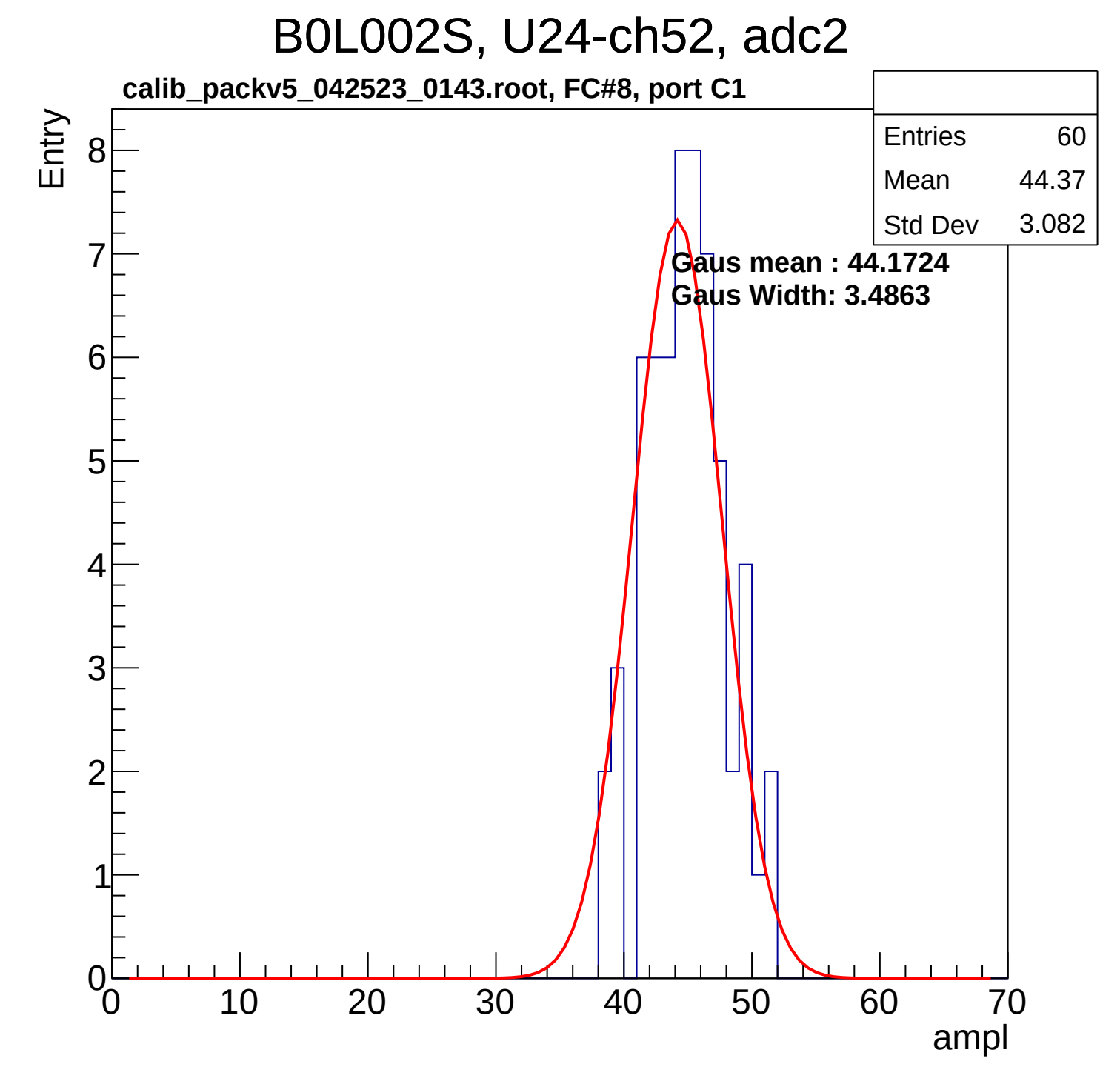
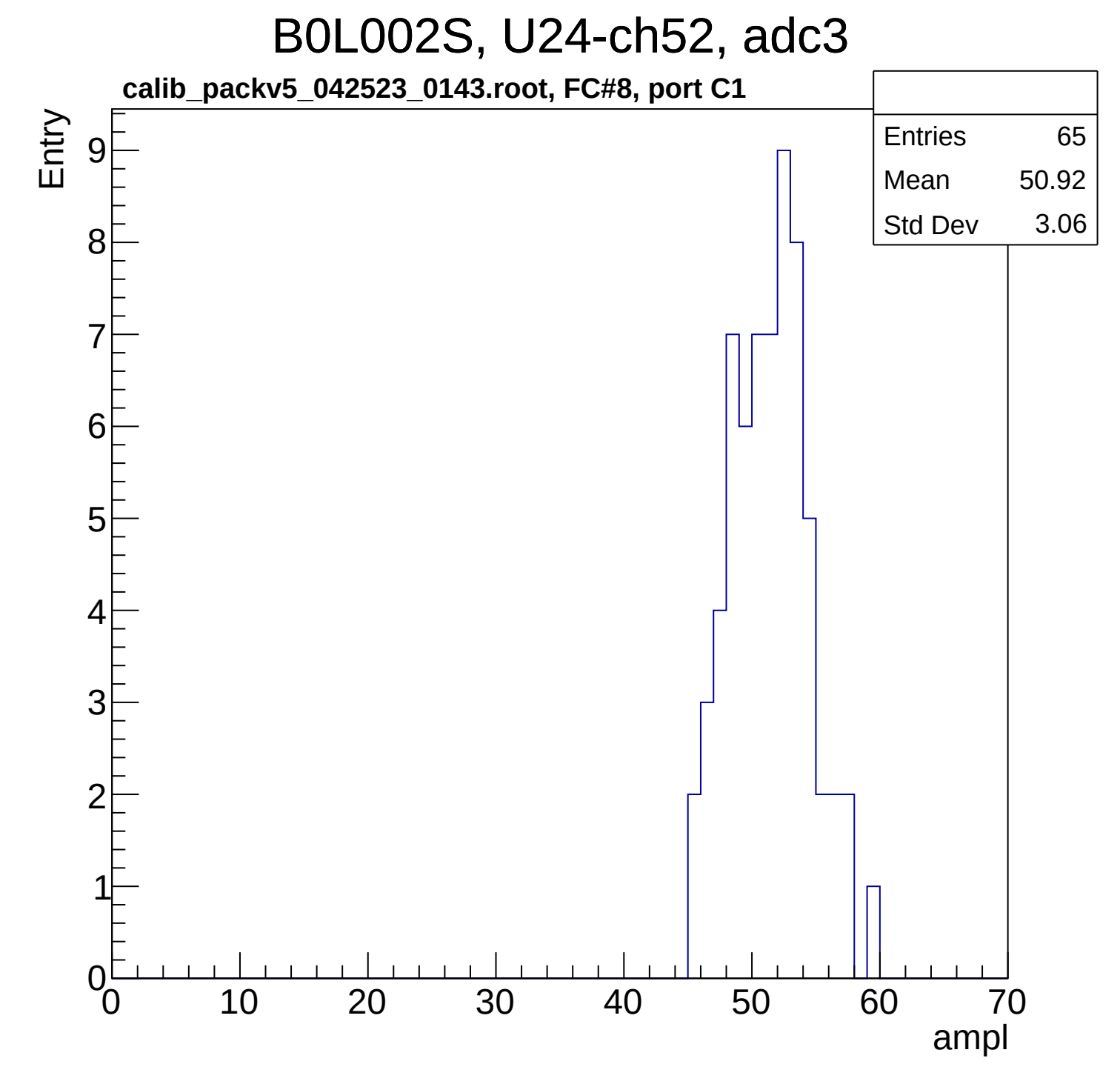
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.71
Std Dev 3.581
Gaus mean : 30.9288
Gaus Width: 4.0336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.47
Std Dev 3.097
Gaus mean : 39.0002
Gaus Width: 3.3132
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.37
Std Dev 3.082
Gaus mean : 44.1724
Gaus Width: 3.4863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.92
Std Dev 3.06
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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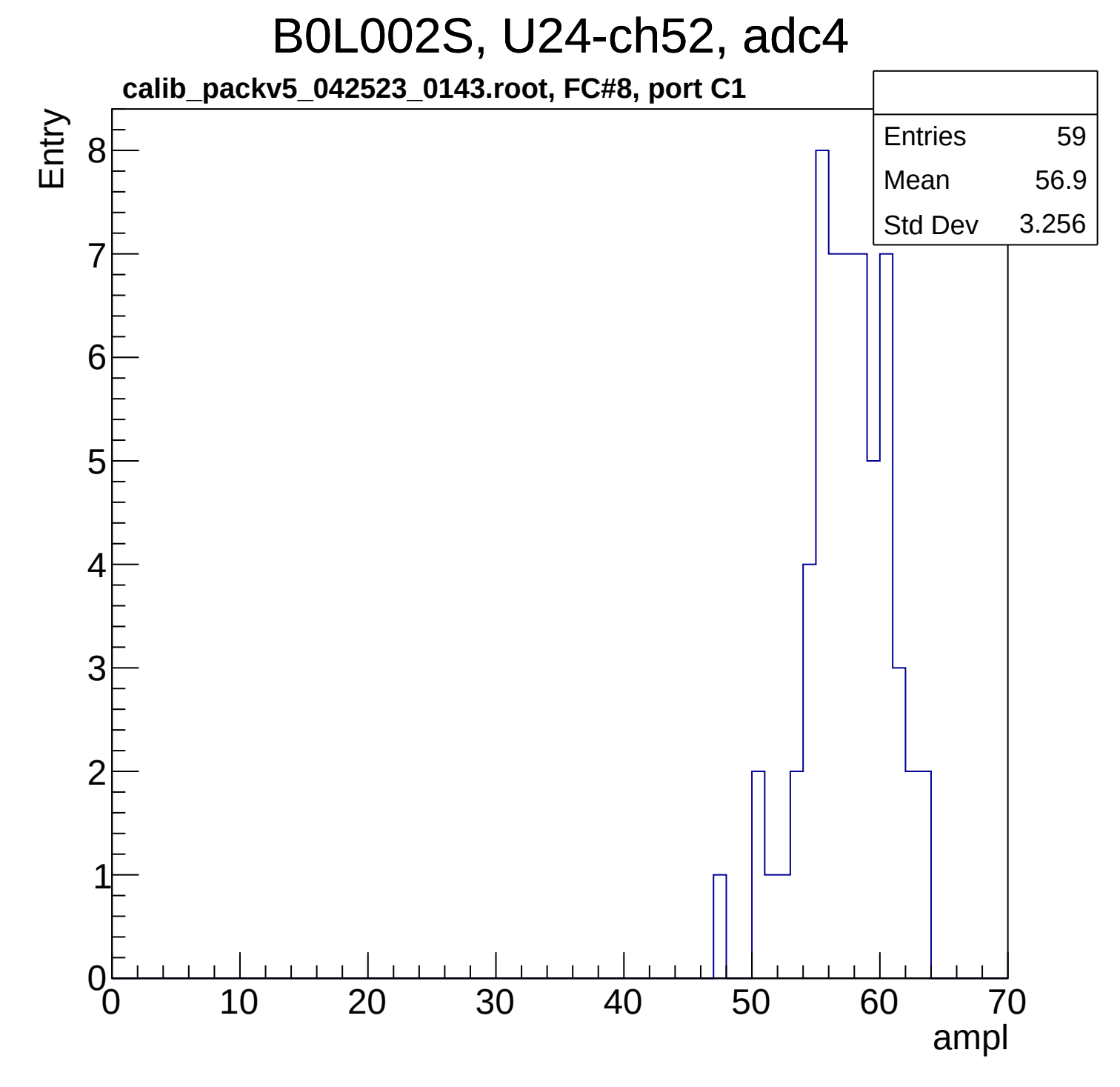
B0L002S, U24-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.9
Std Dev 3.256
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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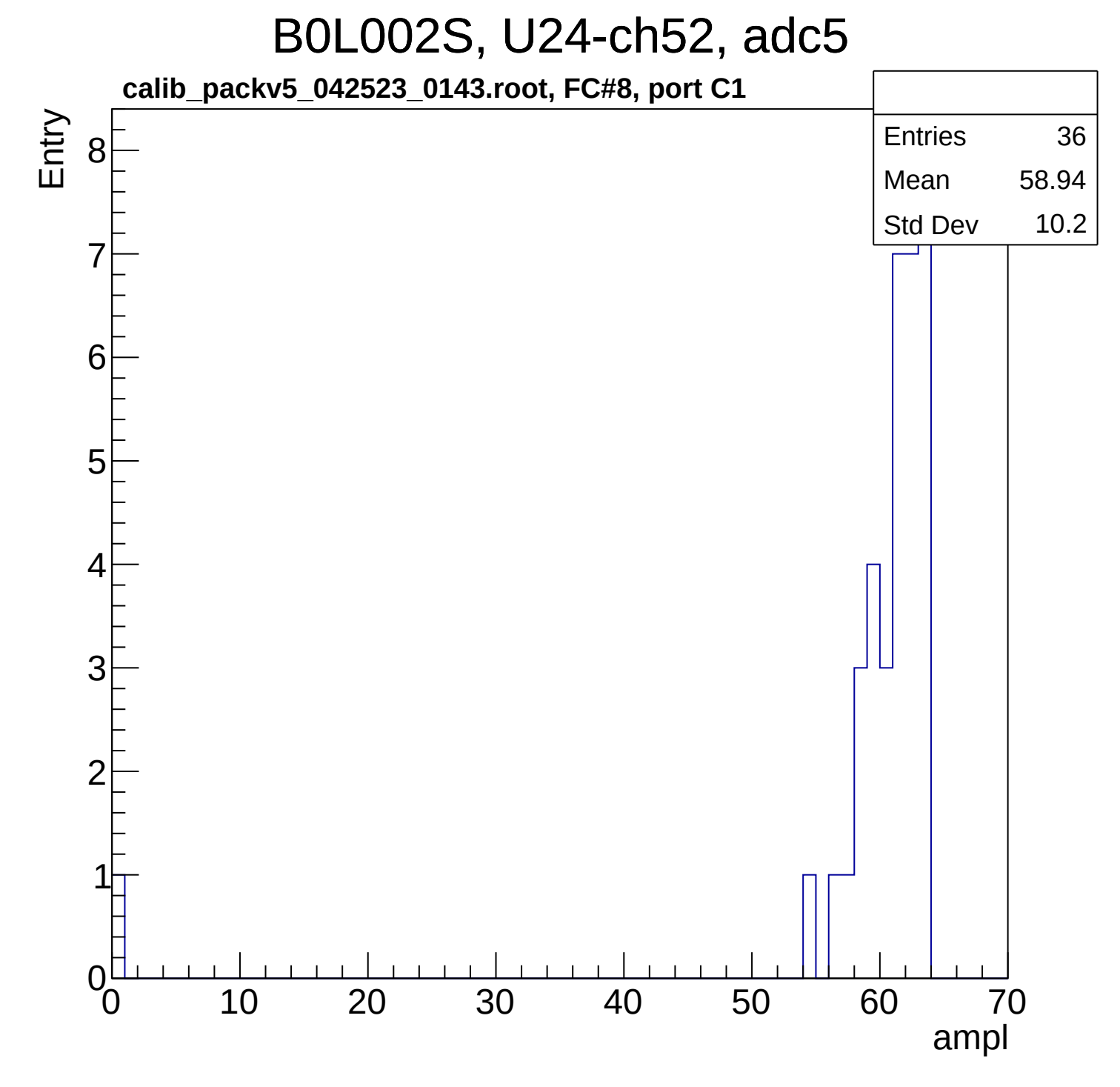
40
50
60
70


B0L002S, U24-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.94
Std Dev 10.2
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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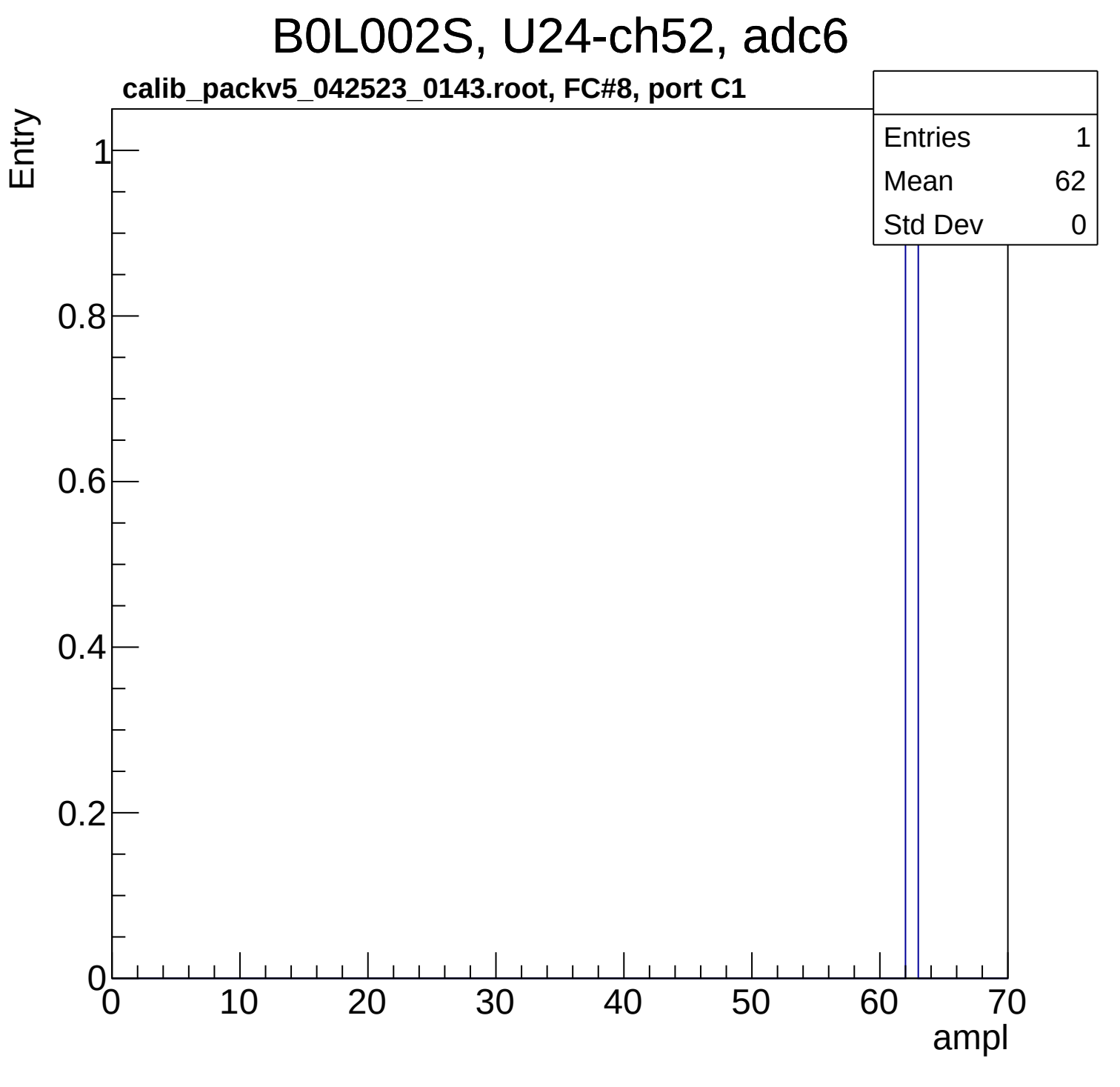
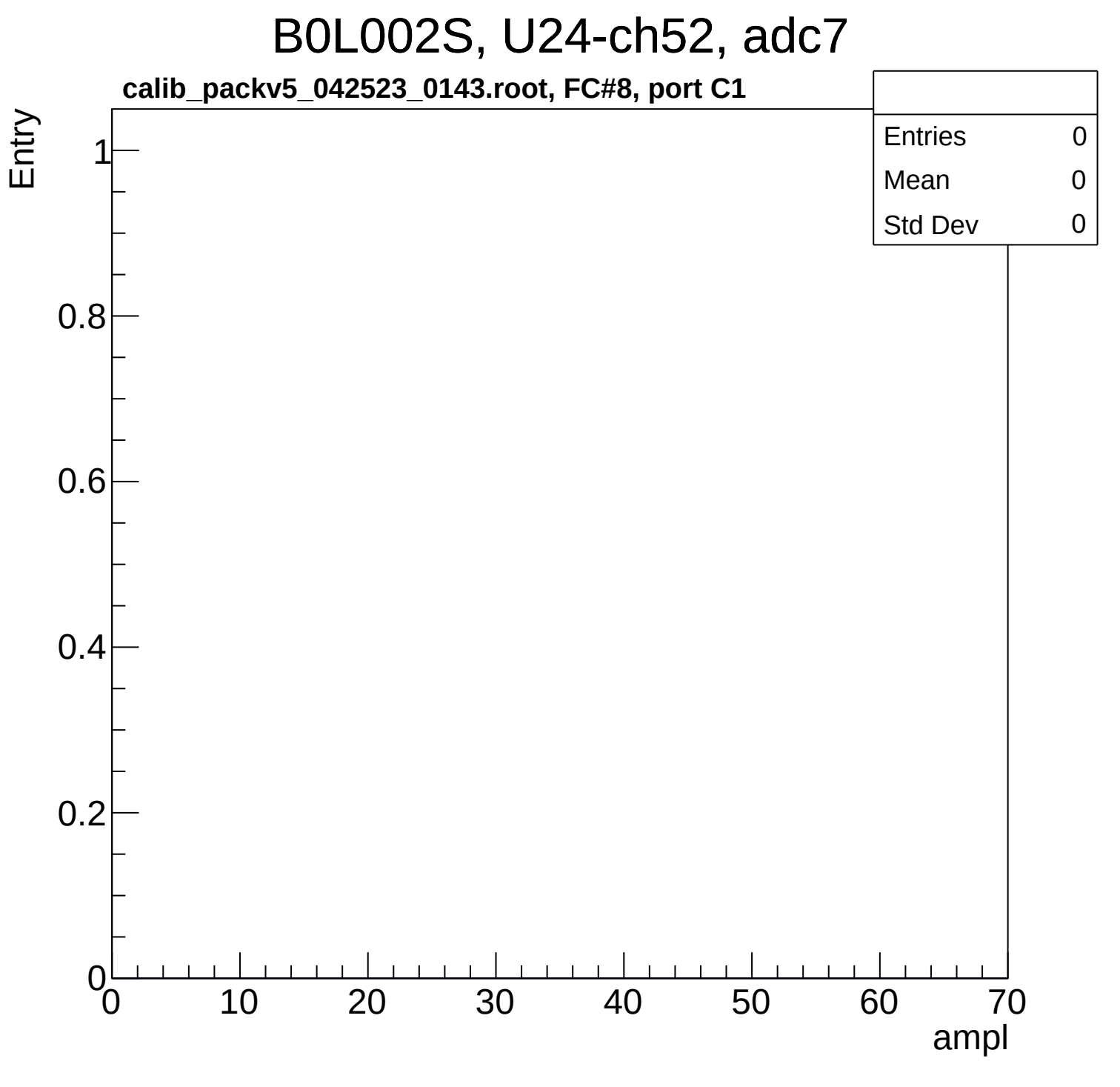
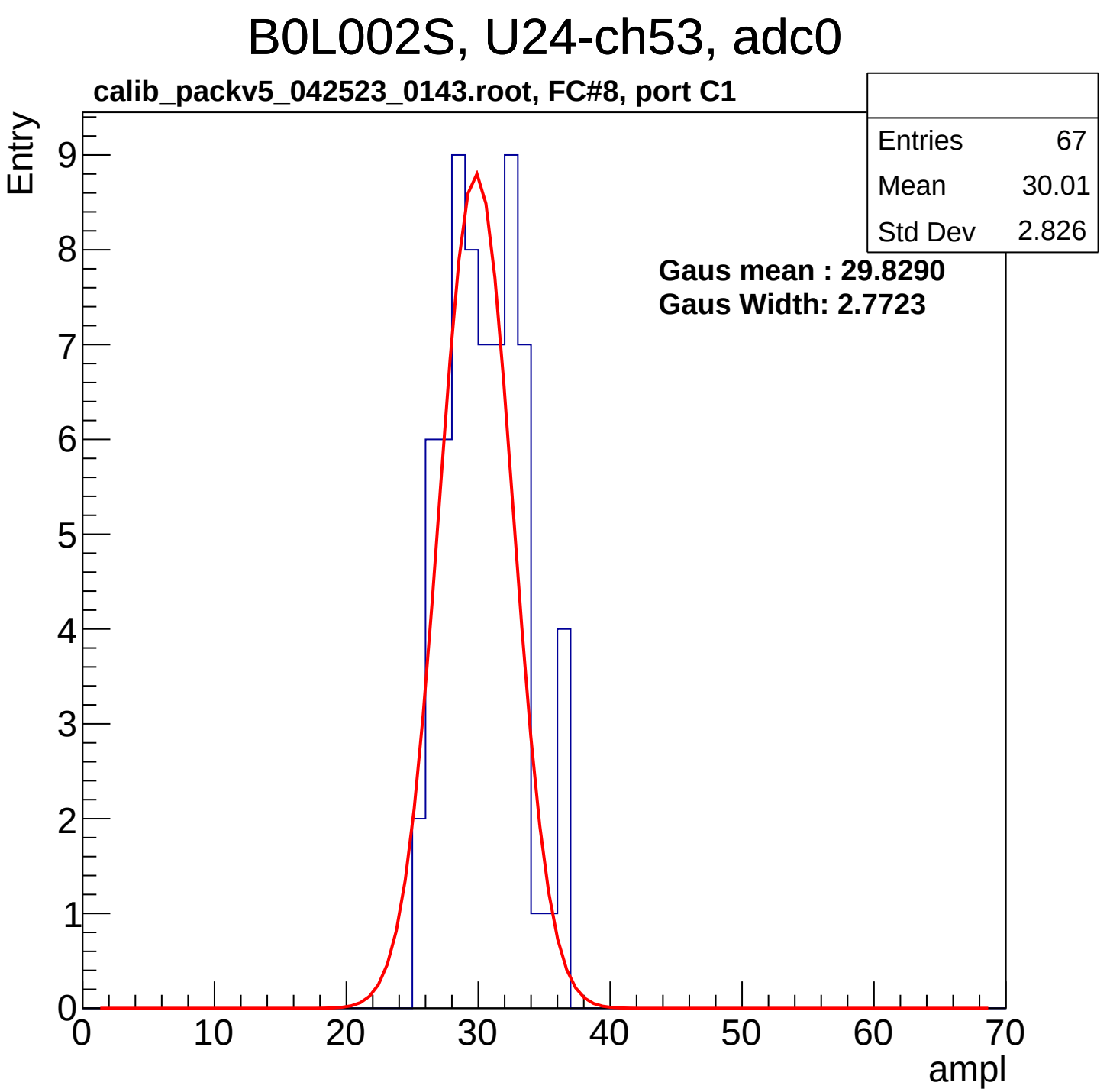

B0L002S, U24-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.01
Std Dev 2.826
Gaus mean : 29.8290
Gaus Width: 2.7723
Entry
ampl
0
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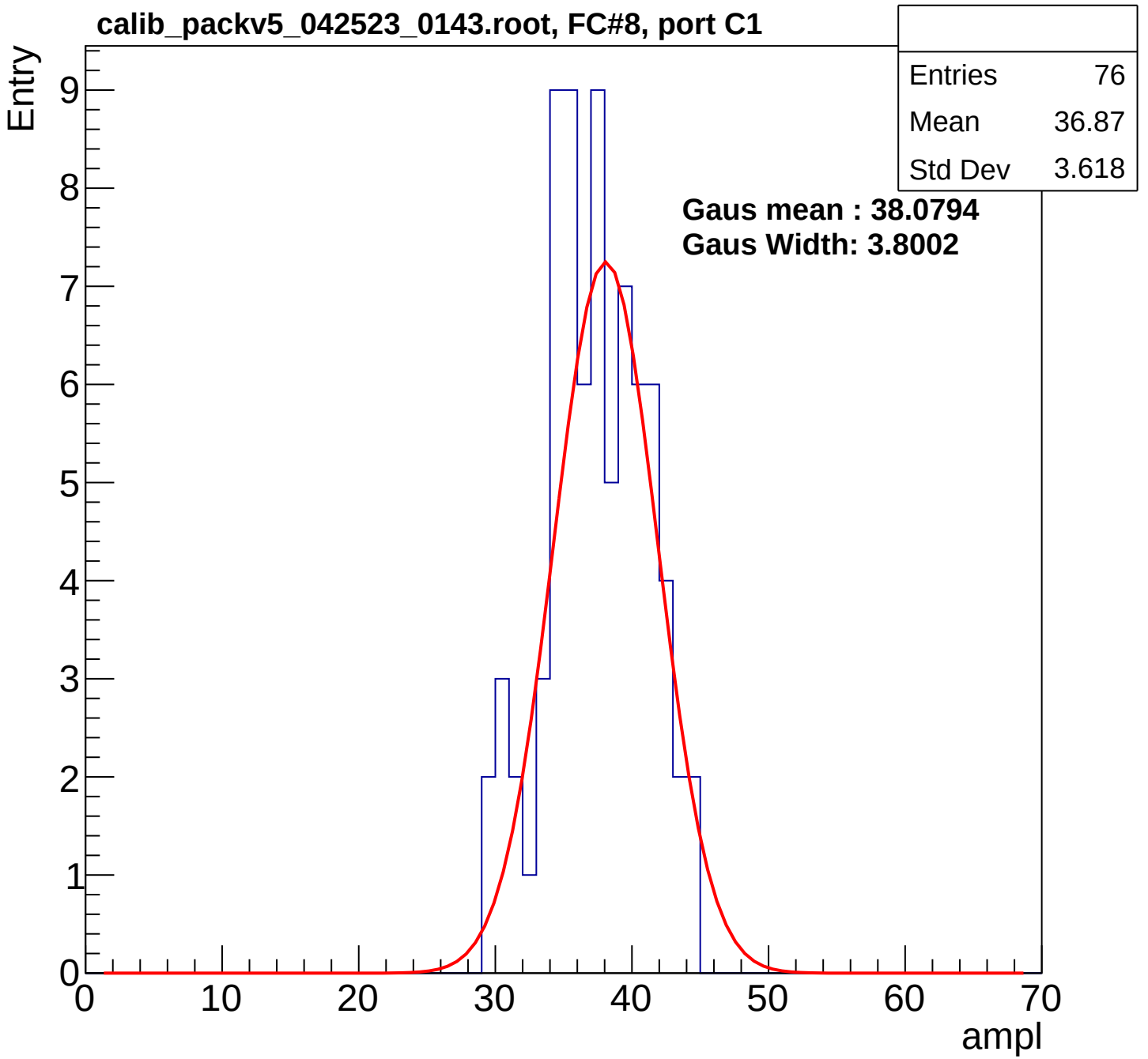
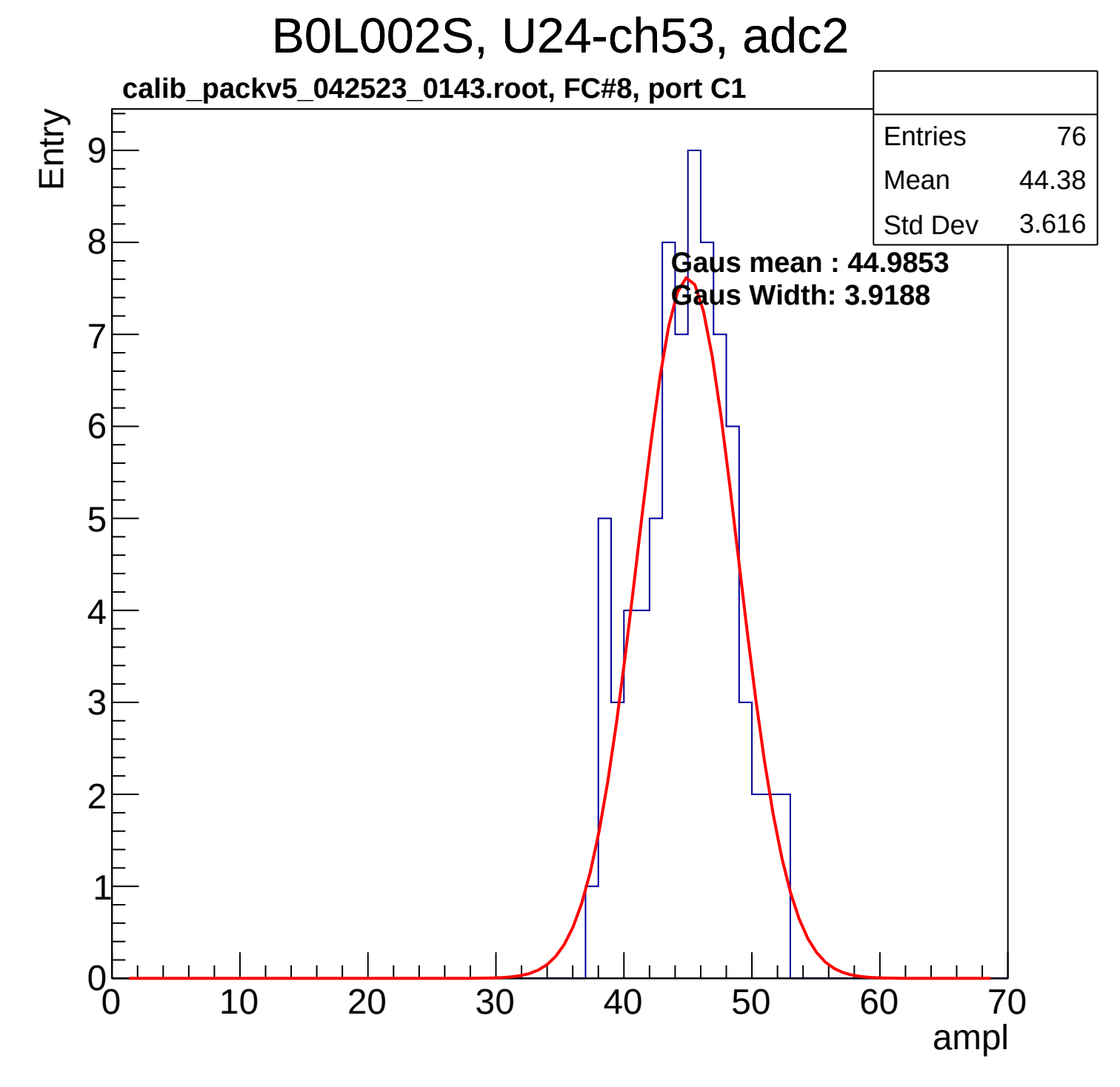
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.87
Std Dev 3.618
Gaus mean : 38.0794
Gaus Width: 3.8002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 44.38
Std Dev 3.616
Gaus mean : 44.9853
Gaus Width: 3.9188
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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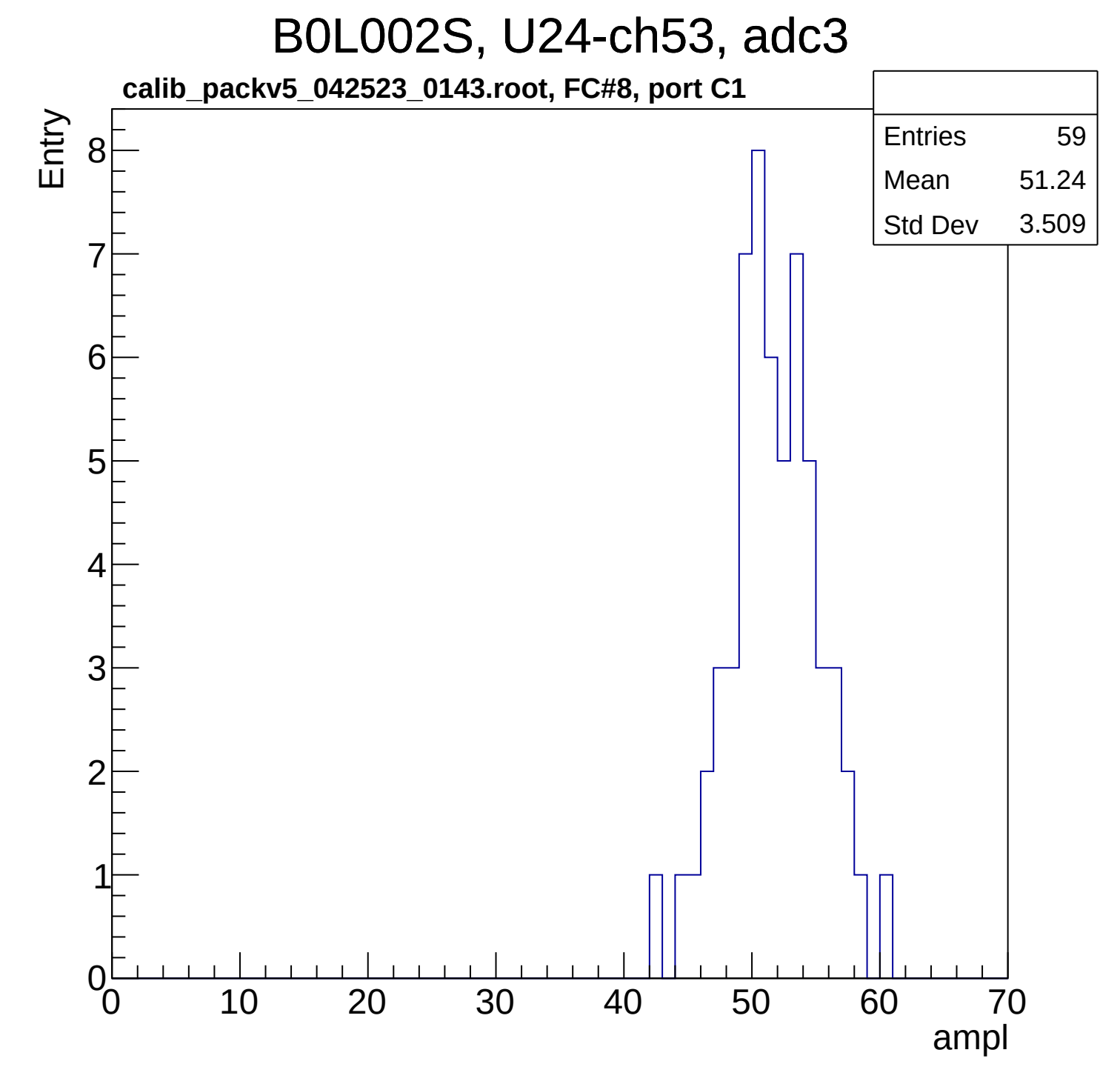
40
50
60
70


B0L002S, U24-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.24
Std Dev 3.509
Entry
ampl
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2
3
4
5
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0
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20
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40
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60
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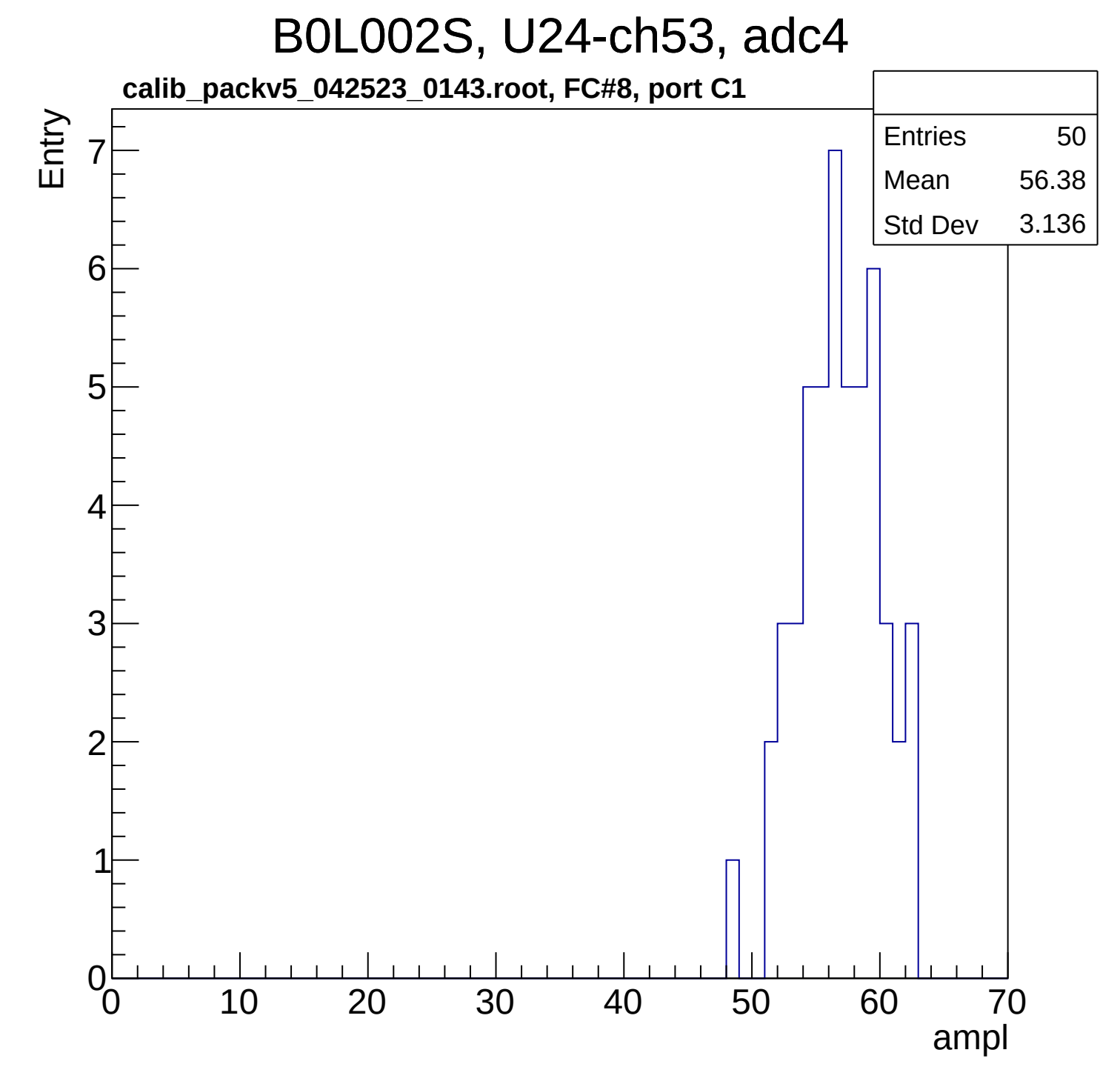
B0L002S, U24-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.38
Std Dev 3.136
Entry
ampl
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2
3
4
5
6
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0
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50
60
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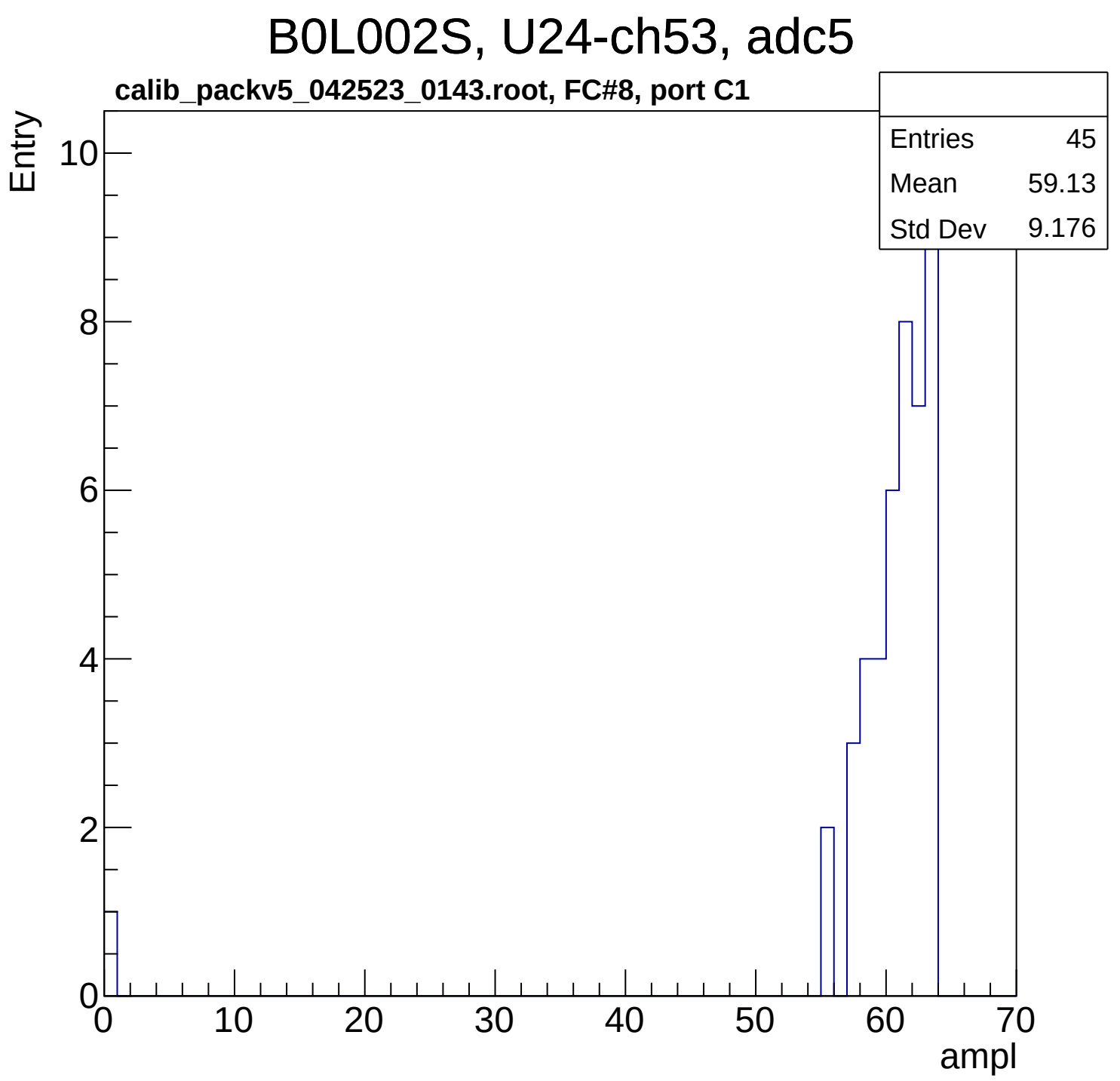
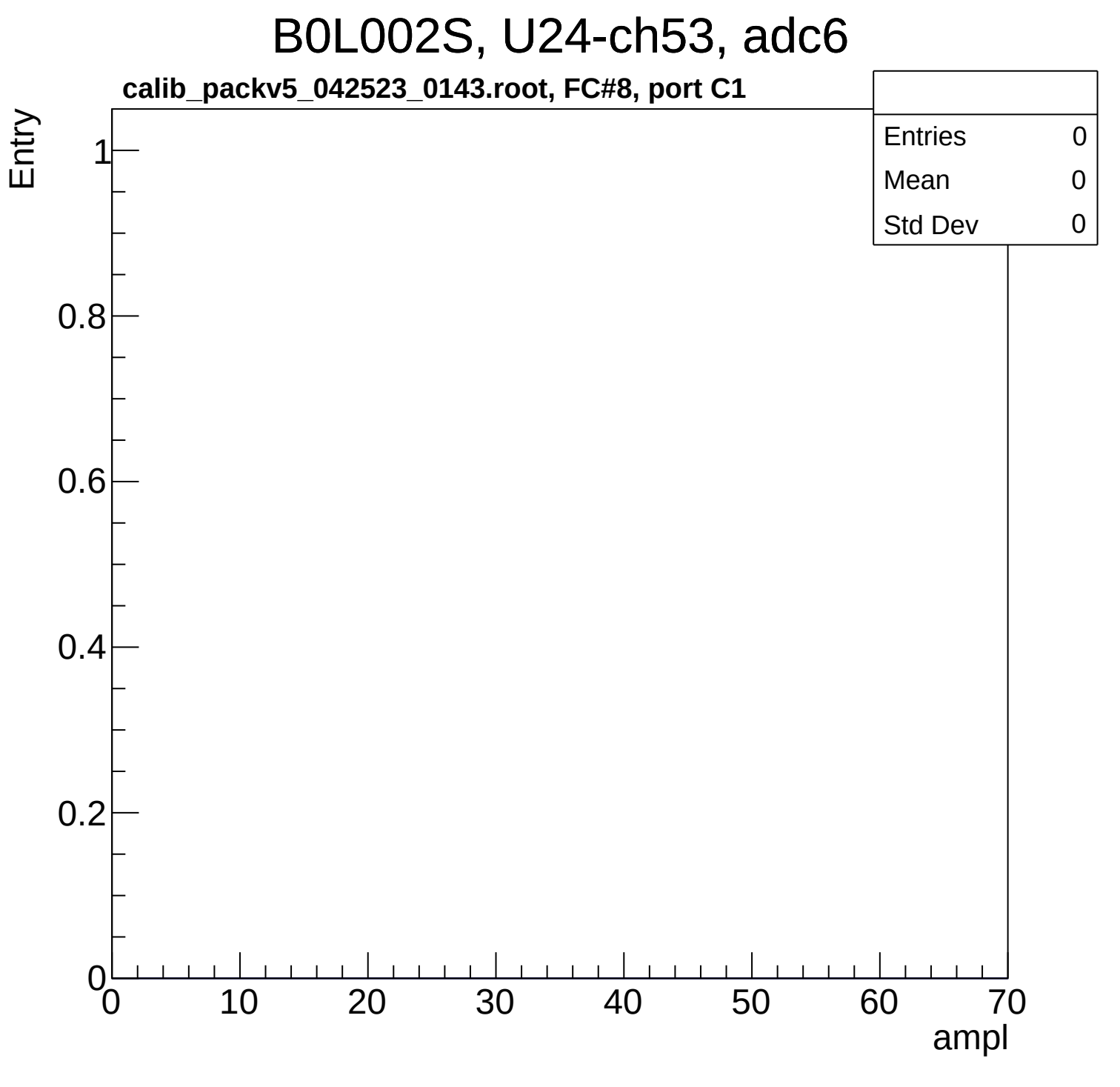

B0L002S, U24-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 59.13
Std Dev 9.176
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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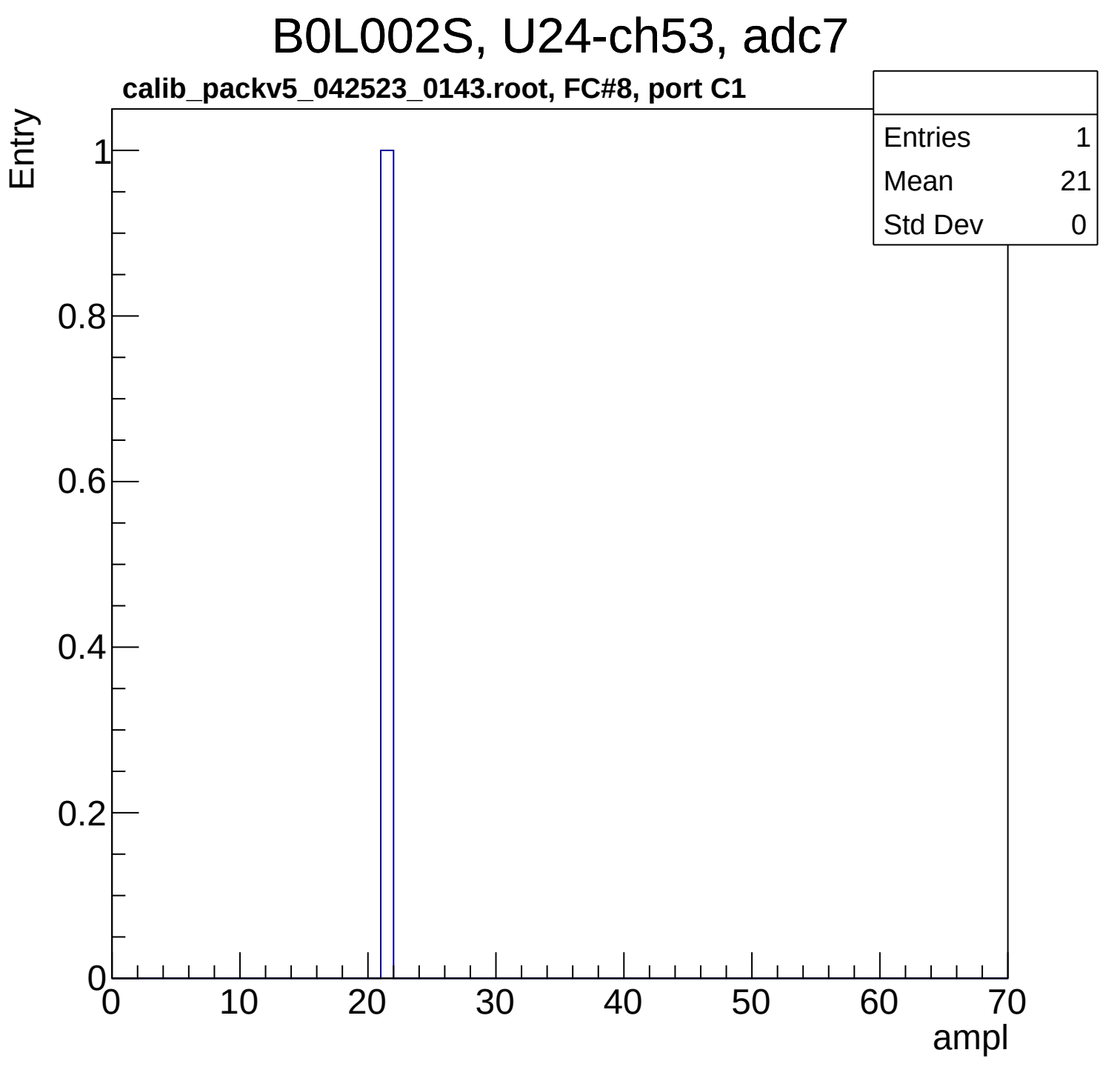
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
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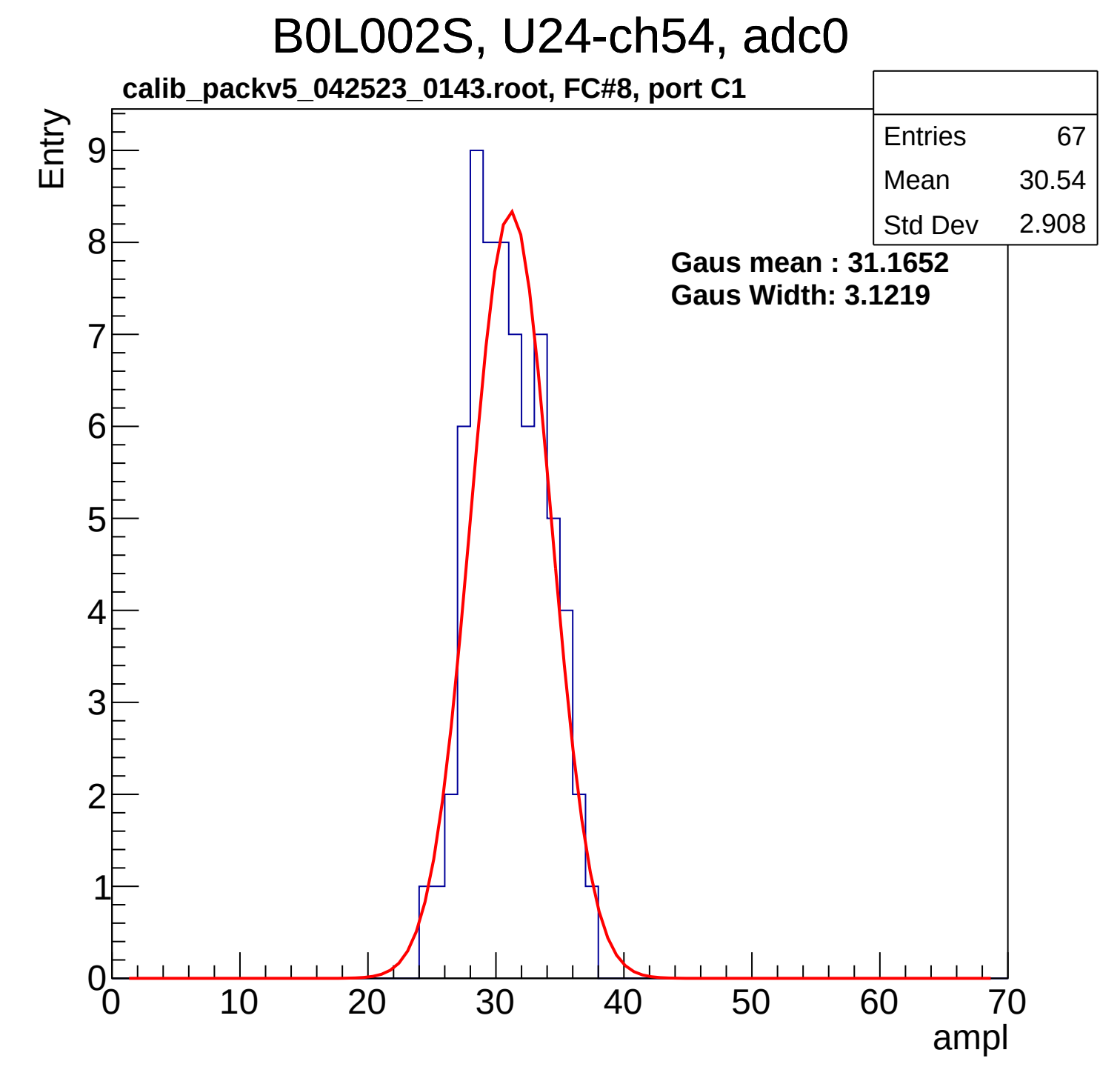
50
60
70
ampl


B0L002S, U24-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.54
Std Dev 2.908
Gaus mean : 31.1652
Gaus Width: 3.1219
Entry
ampl
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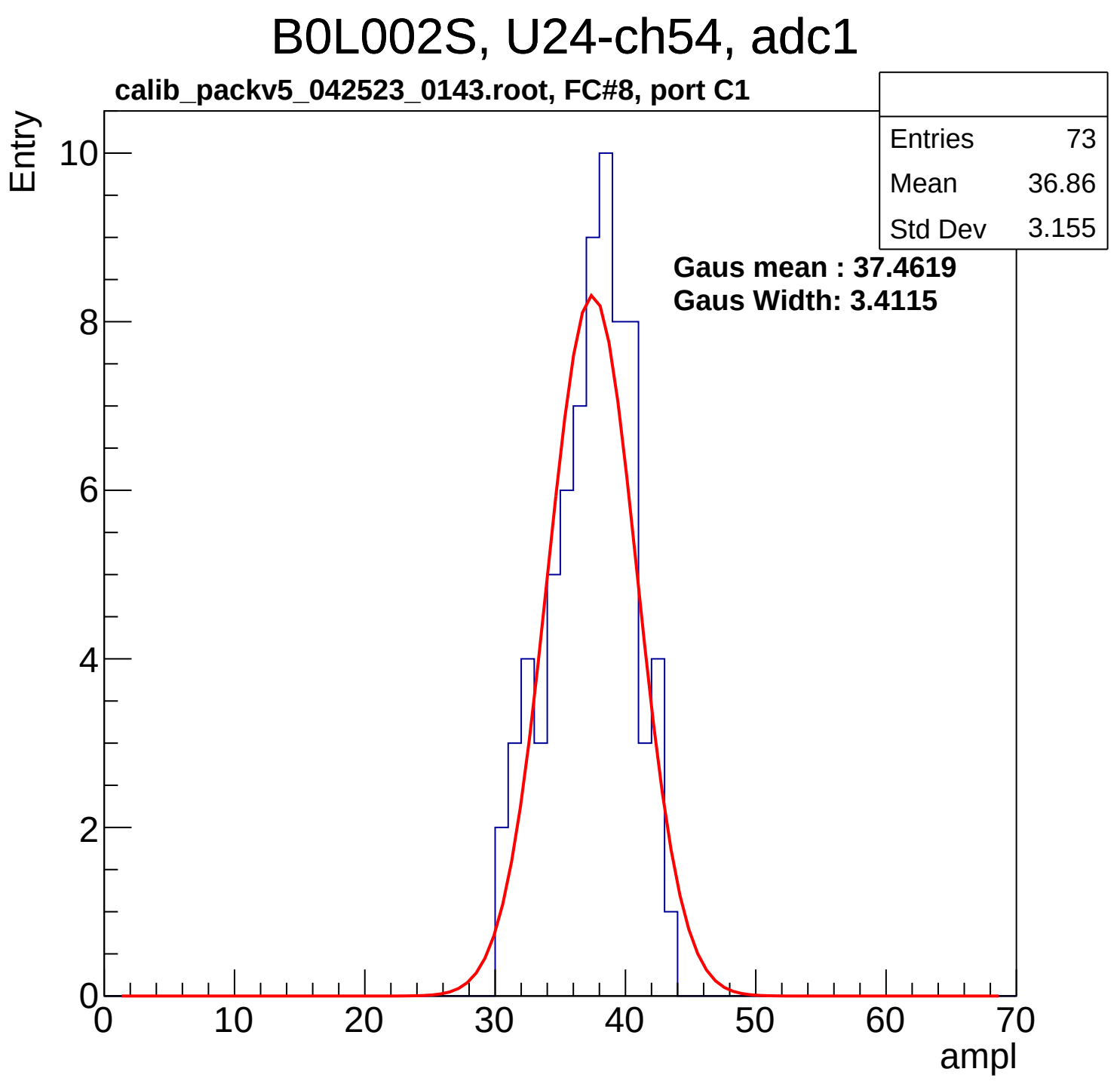
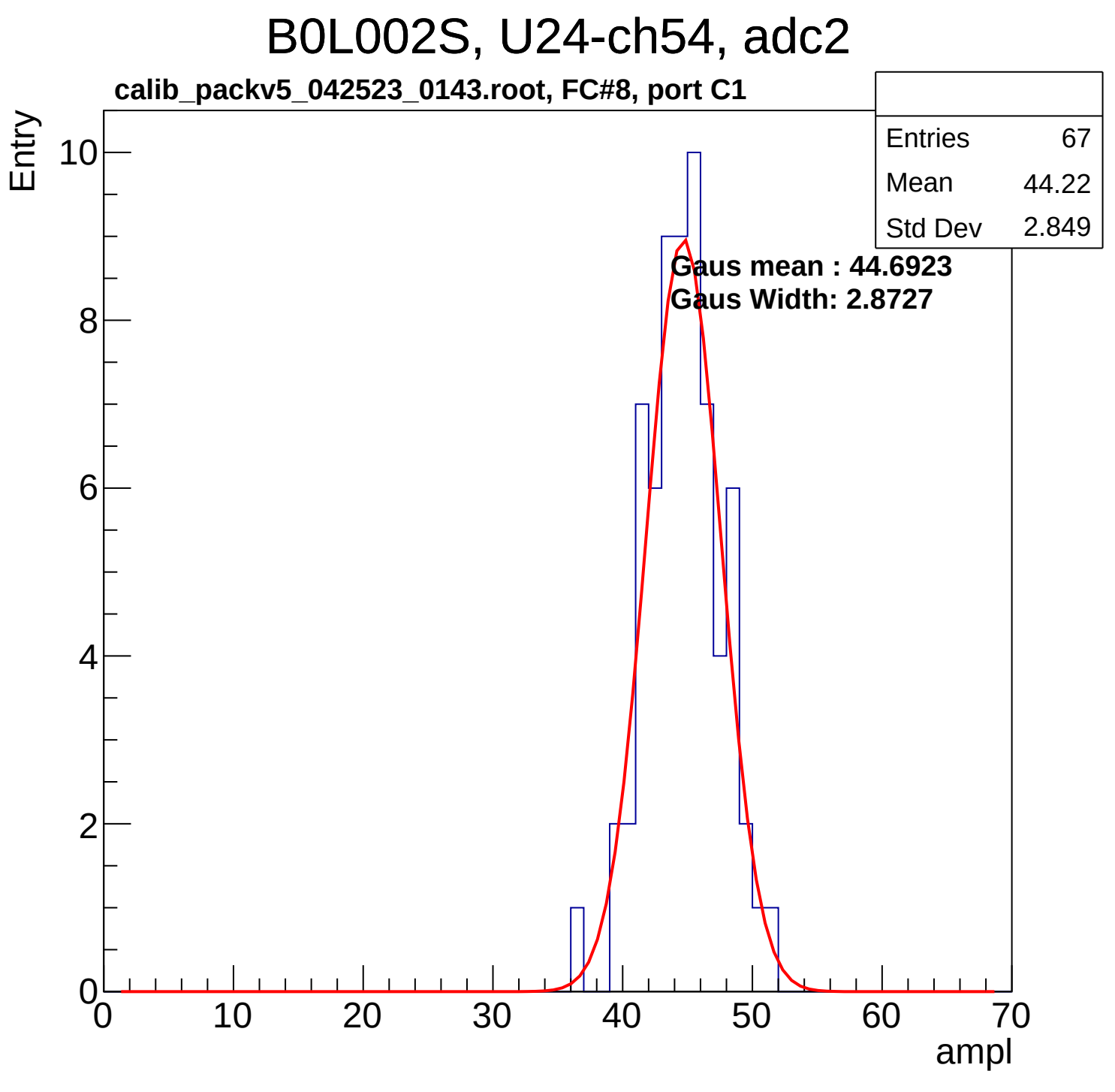
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 36.86
Std Dev 3.155
Gaus mean : 37.4619
Gaus Width: 3.4115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.22
Std Dev 2.849
Gaus mean : 44.6923
Gaus Width: 2.8727
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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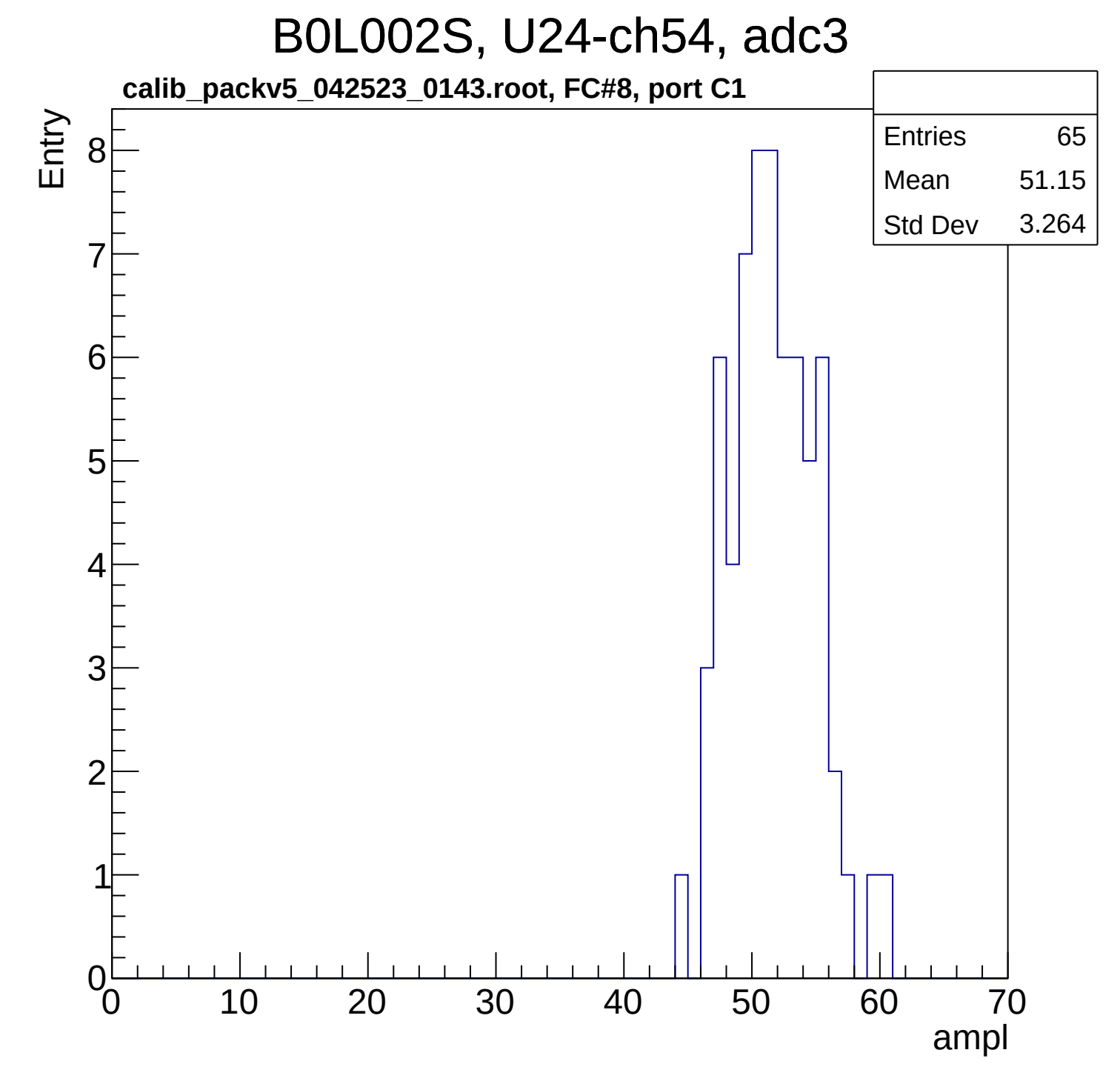
B0L002S, U24-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.15
Std Dev 3.264
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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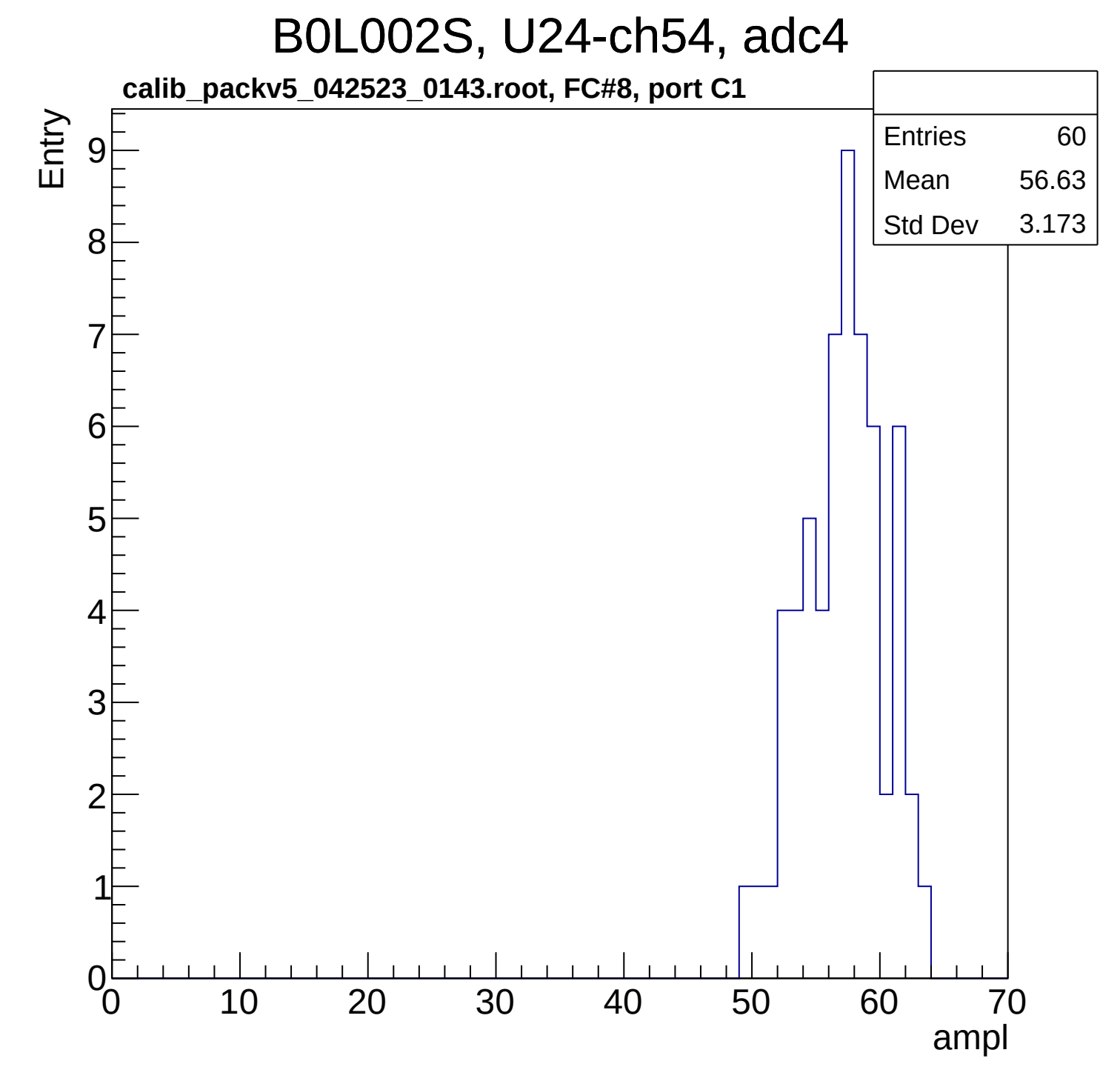
B0L002S, U24-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.63
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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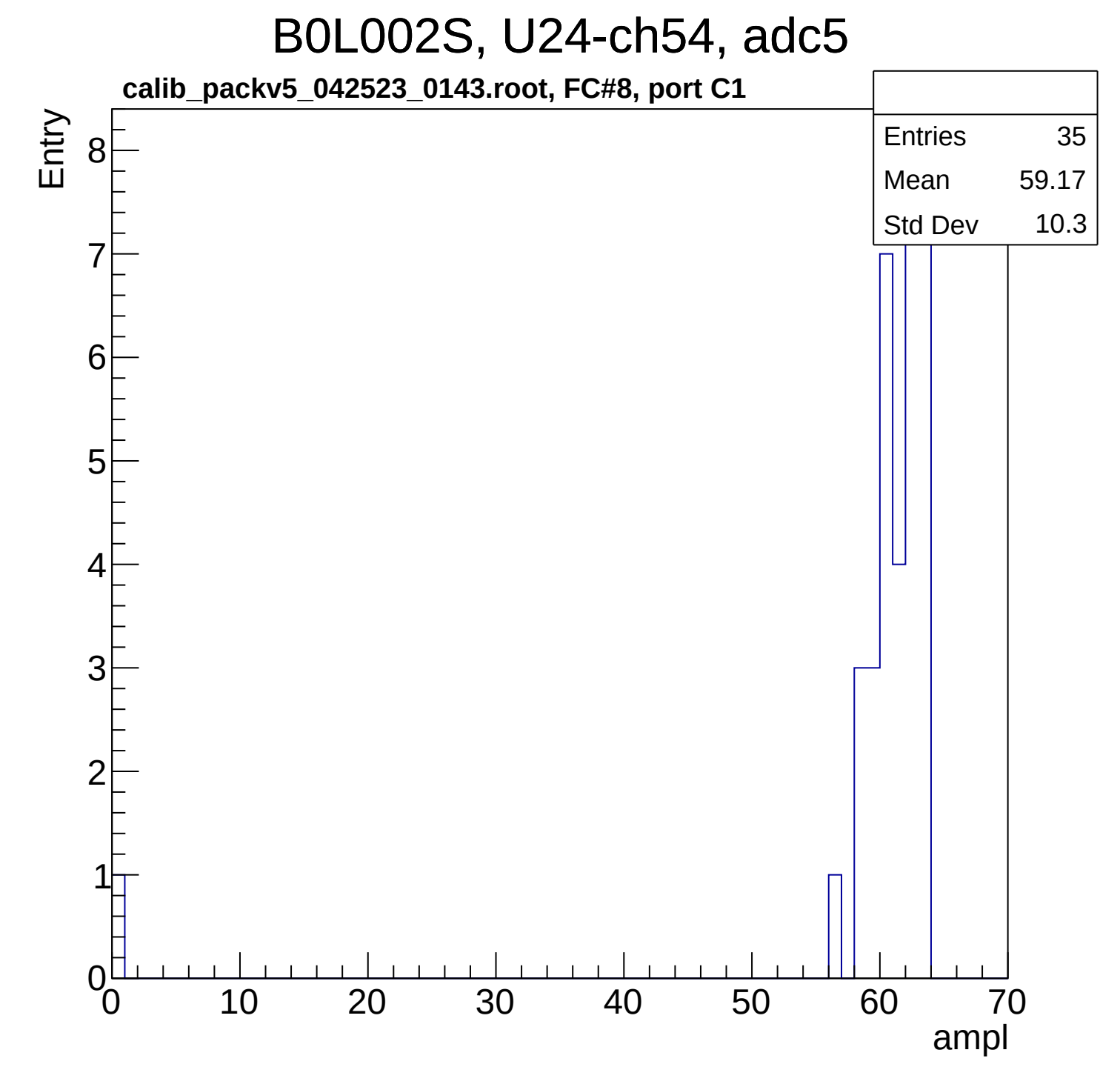
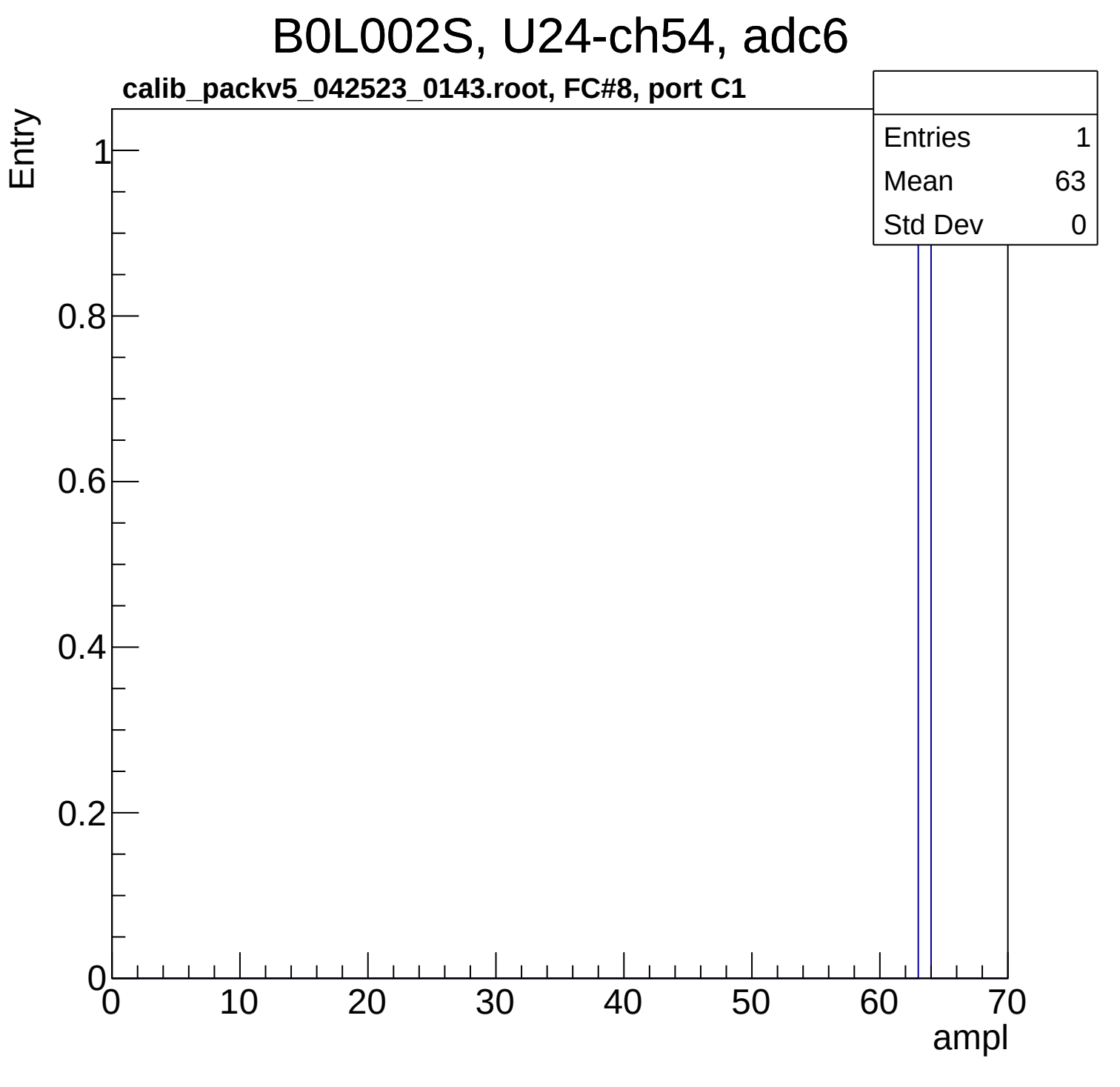
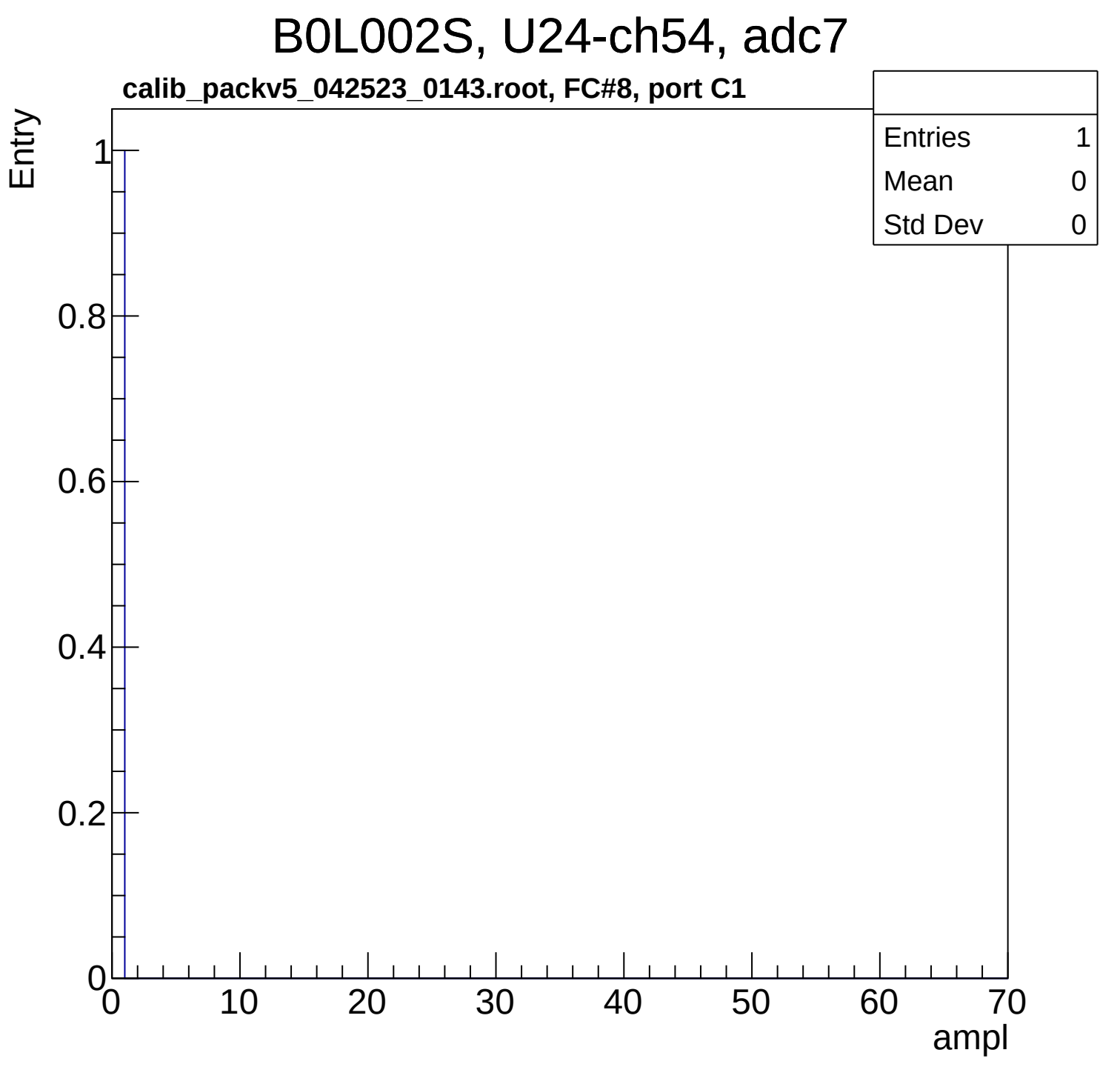
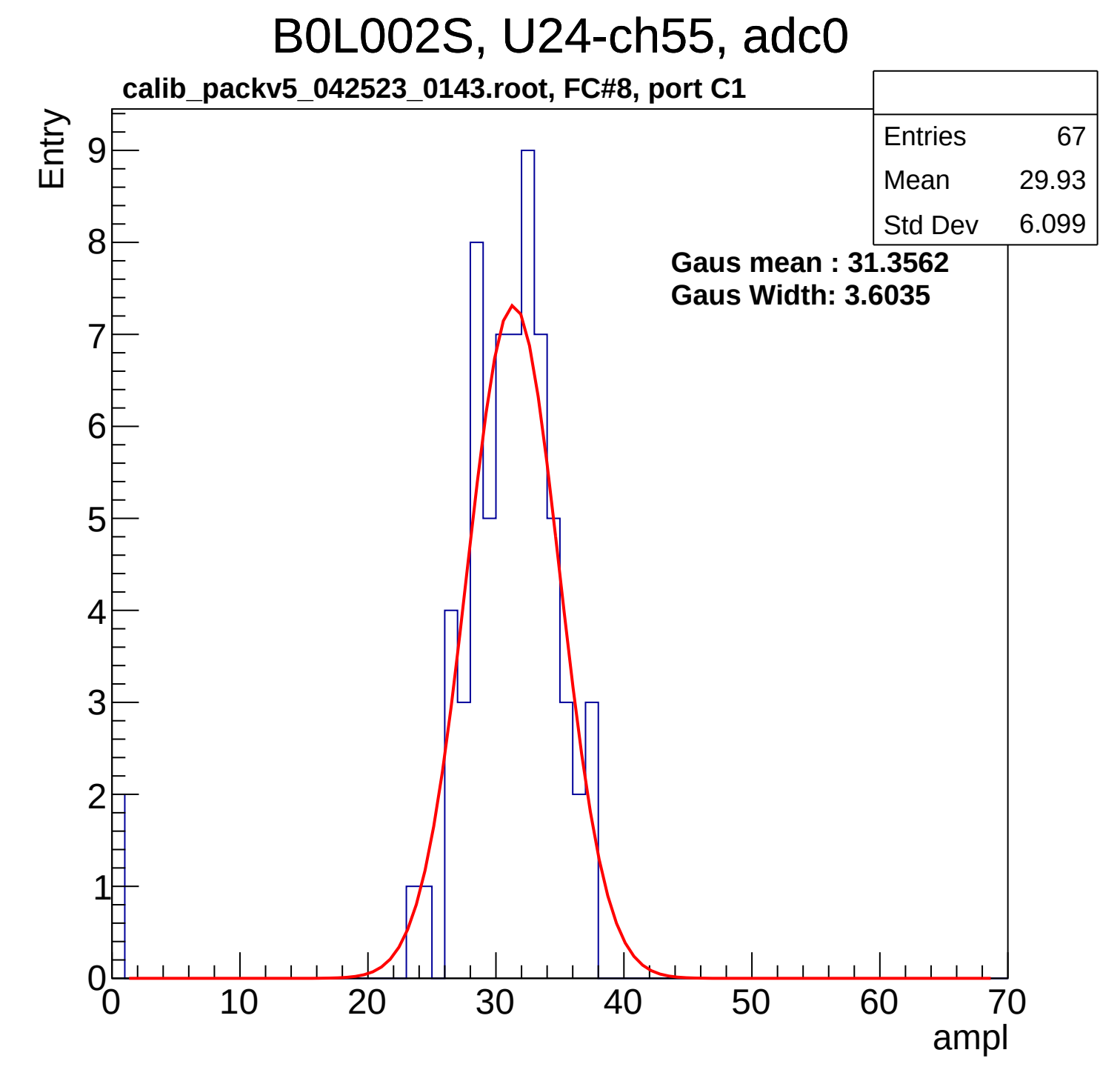
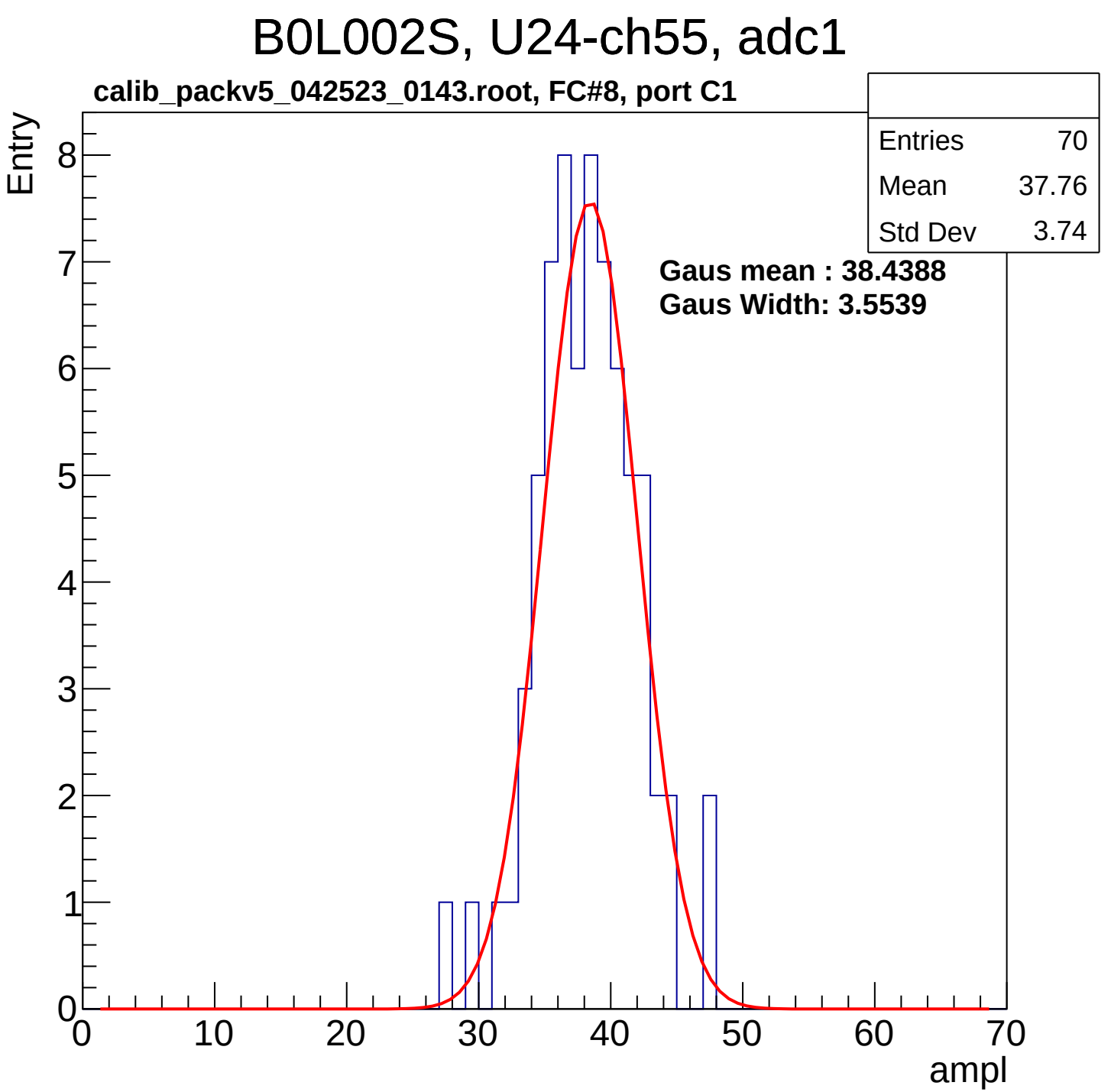
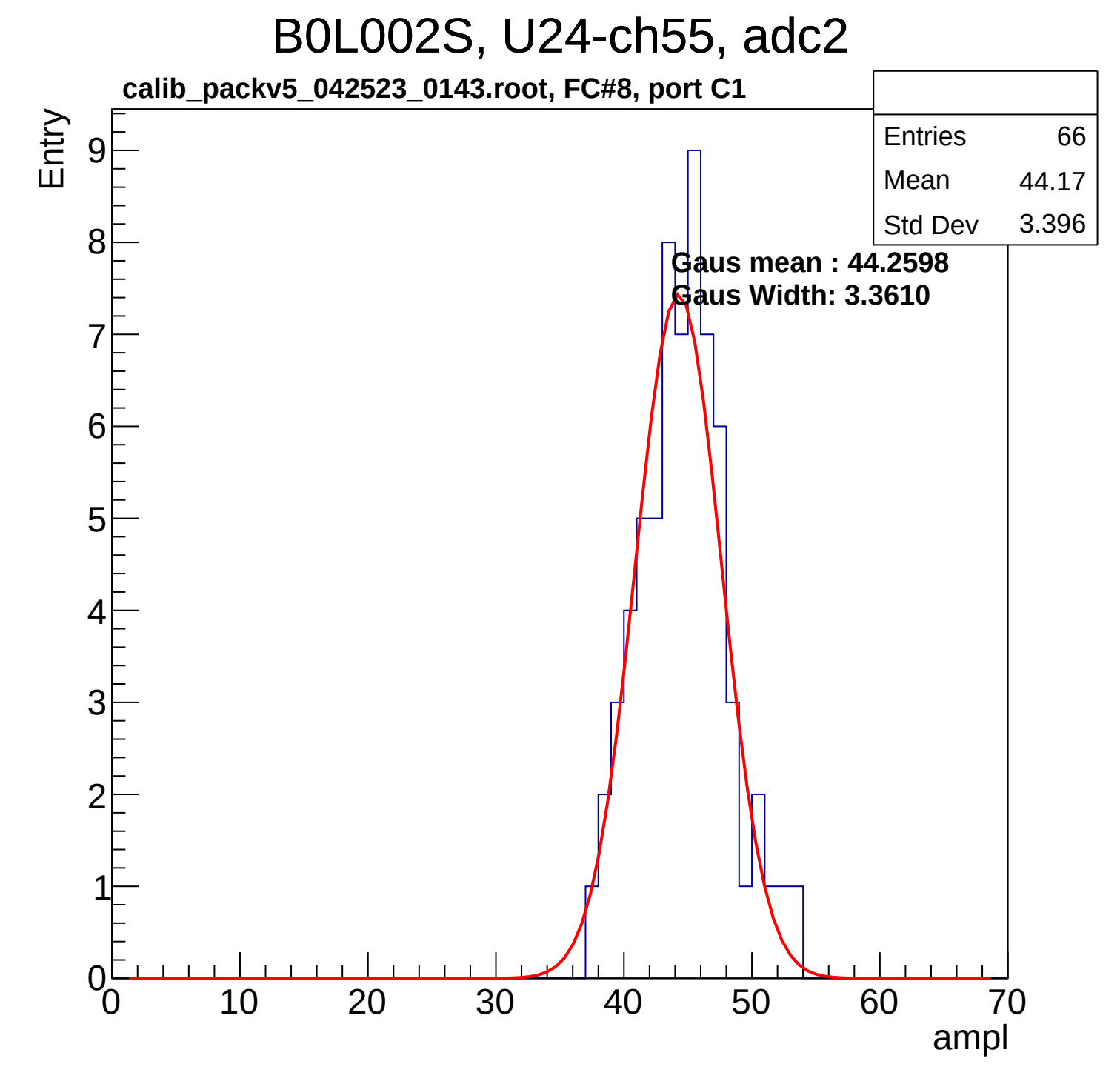

B0L002S, U24-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.17
Std Dev 10.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

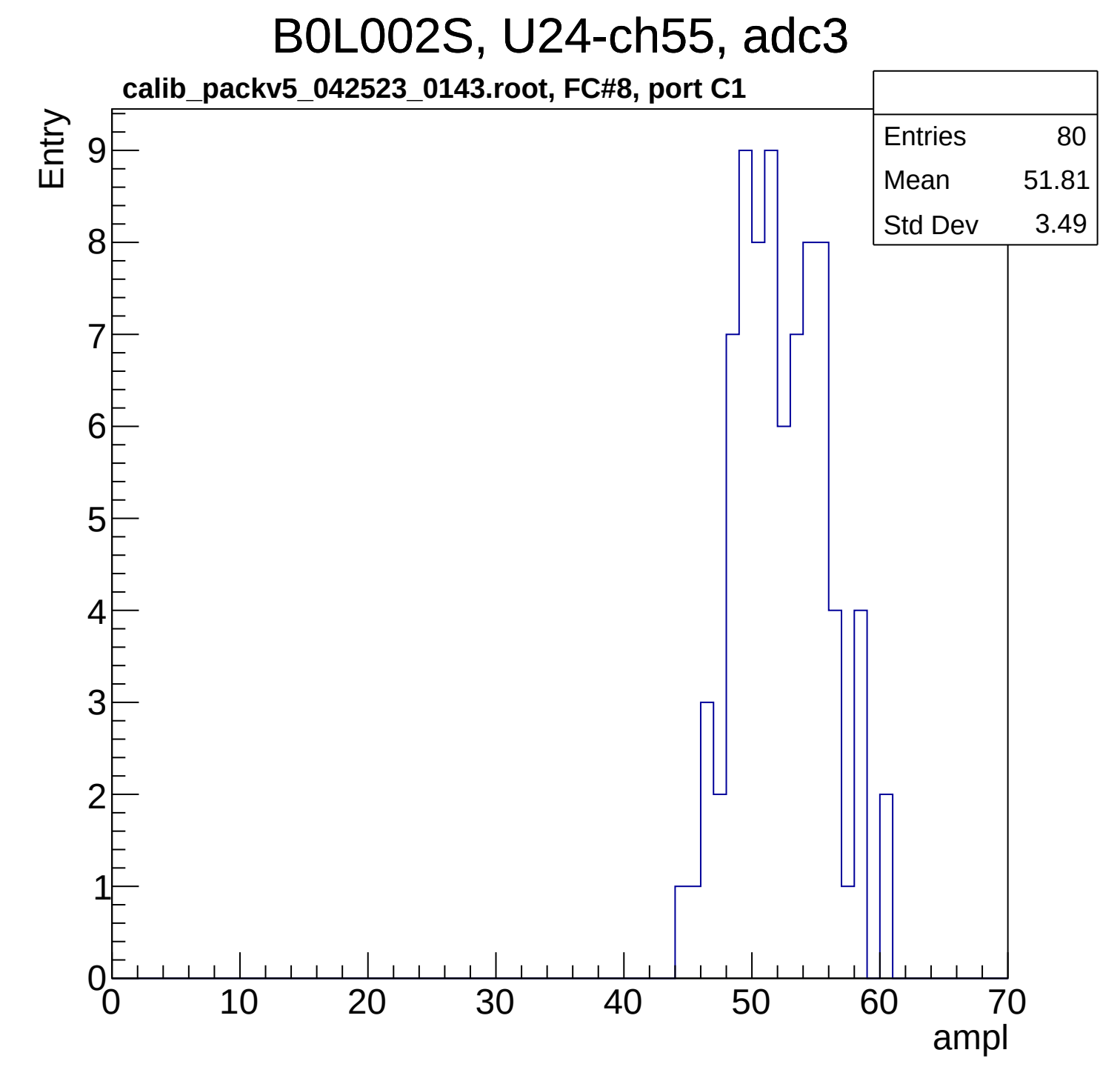

B0L002S, U24-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

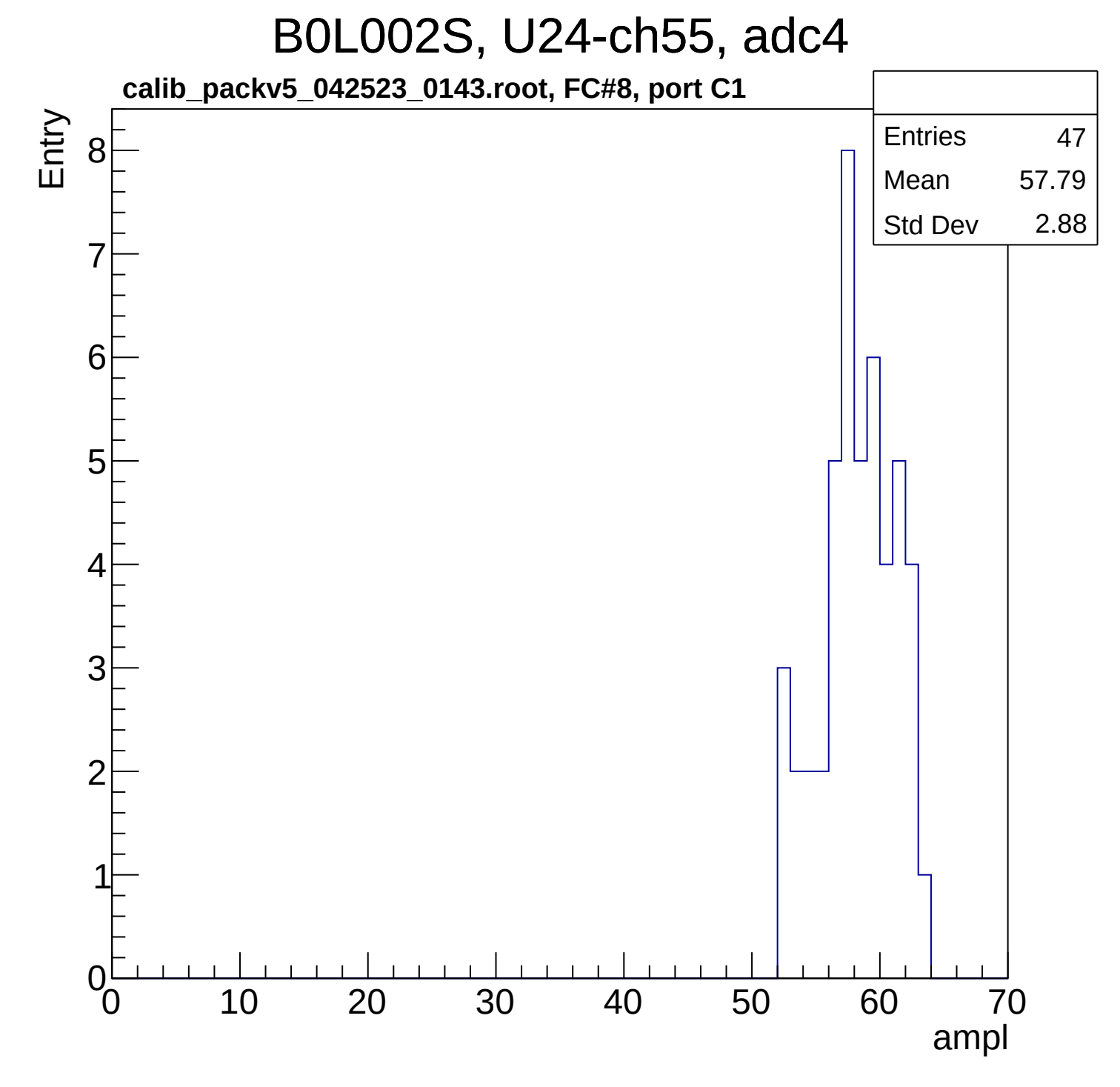

B0L002S, U24-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

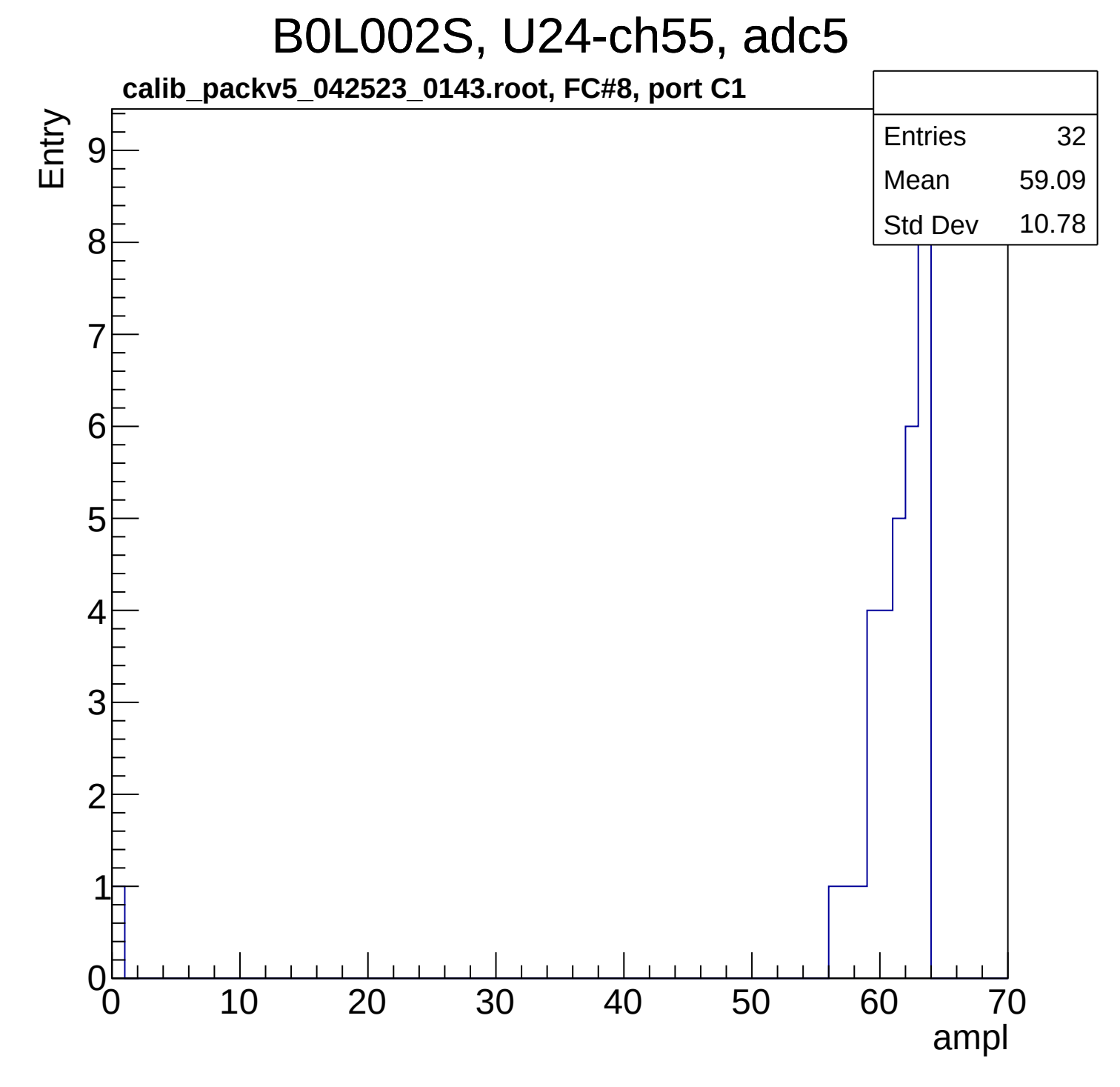

B0L002S, U24-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.93
Std Dev 6.099
Gaus mean : 31.3562
Gaus Width: 3.6035
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

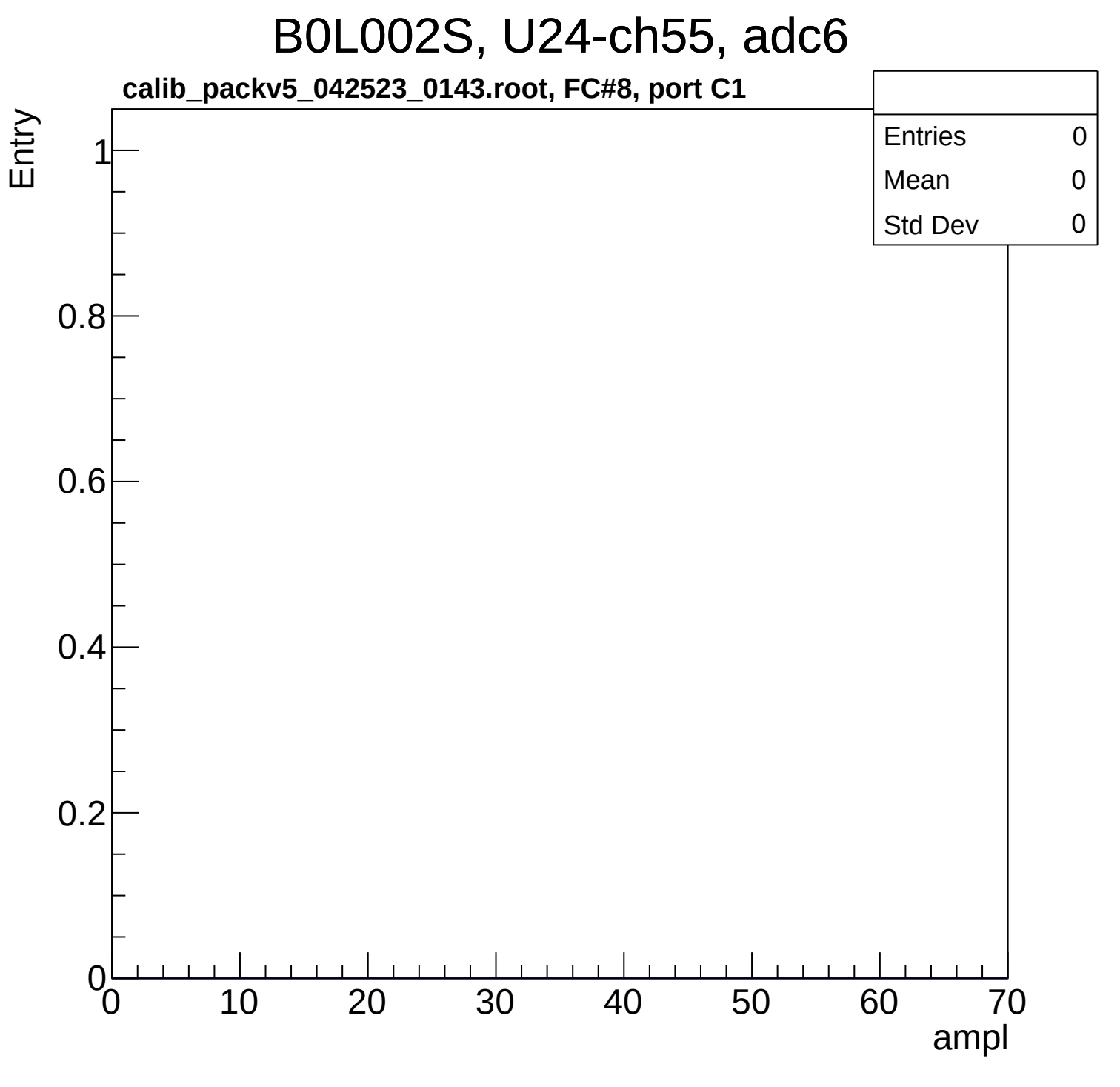

B0L002S, U24-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.76
Std Dev 3.74
Gaus mean : 38.4388
Gaus Width: 3.5539
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

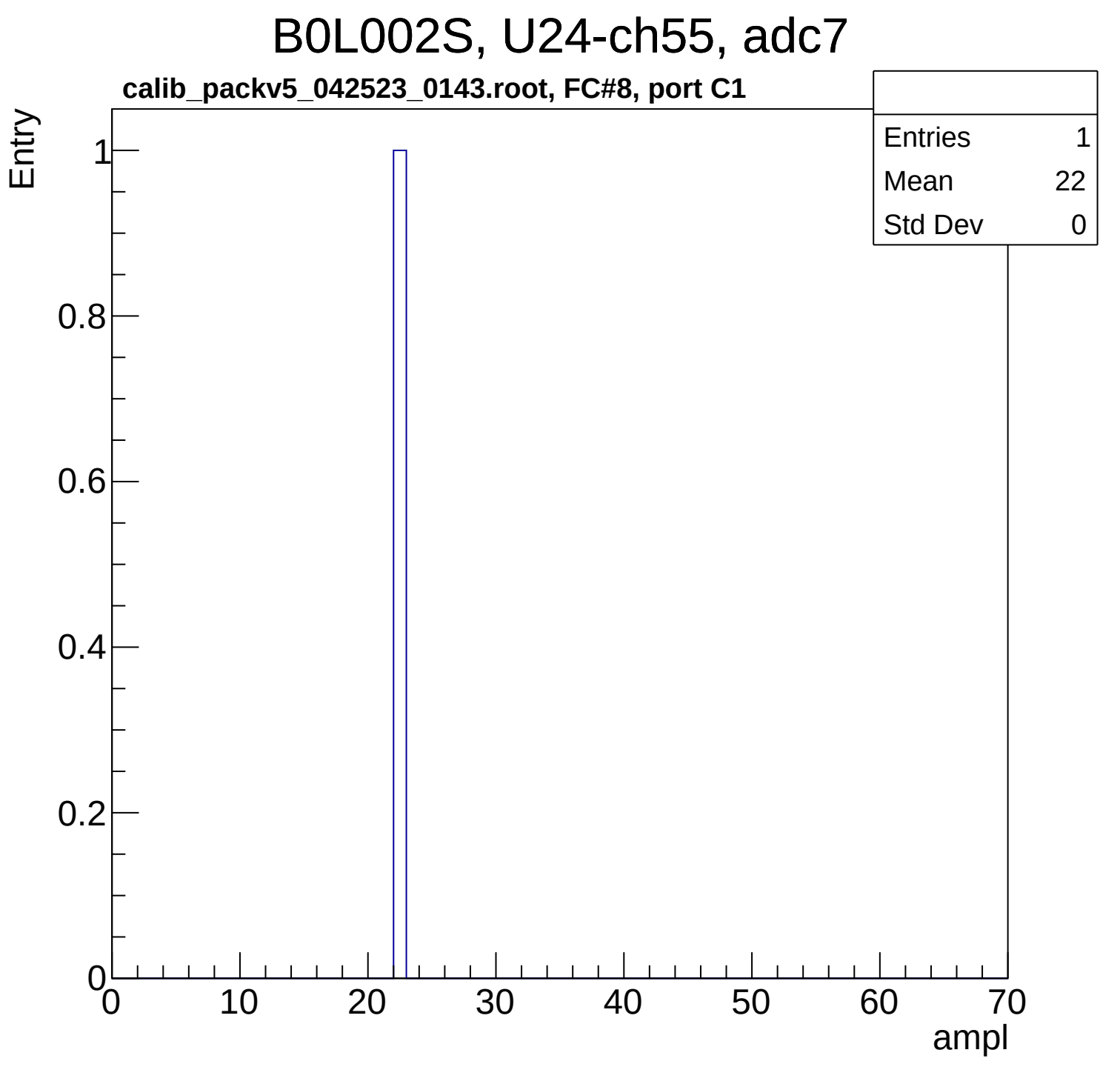

B0L002S, U24-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.17
Std Dev 3.396
Gaus mean : 44.2598
Gaus Width: 3.3610
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

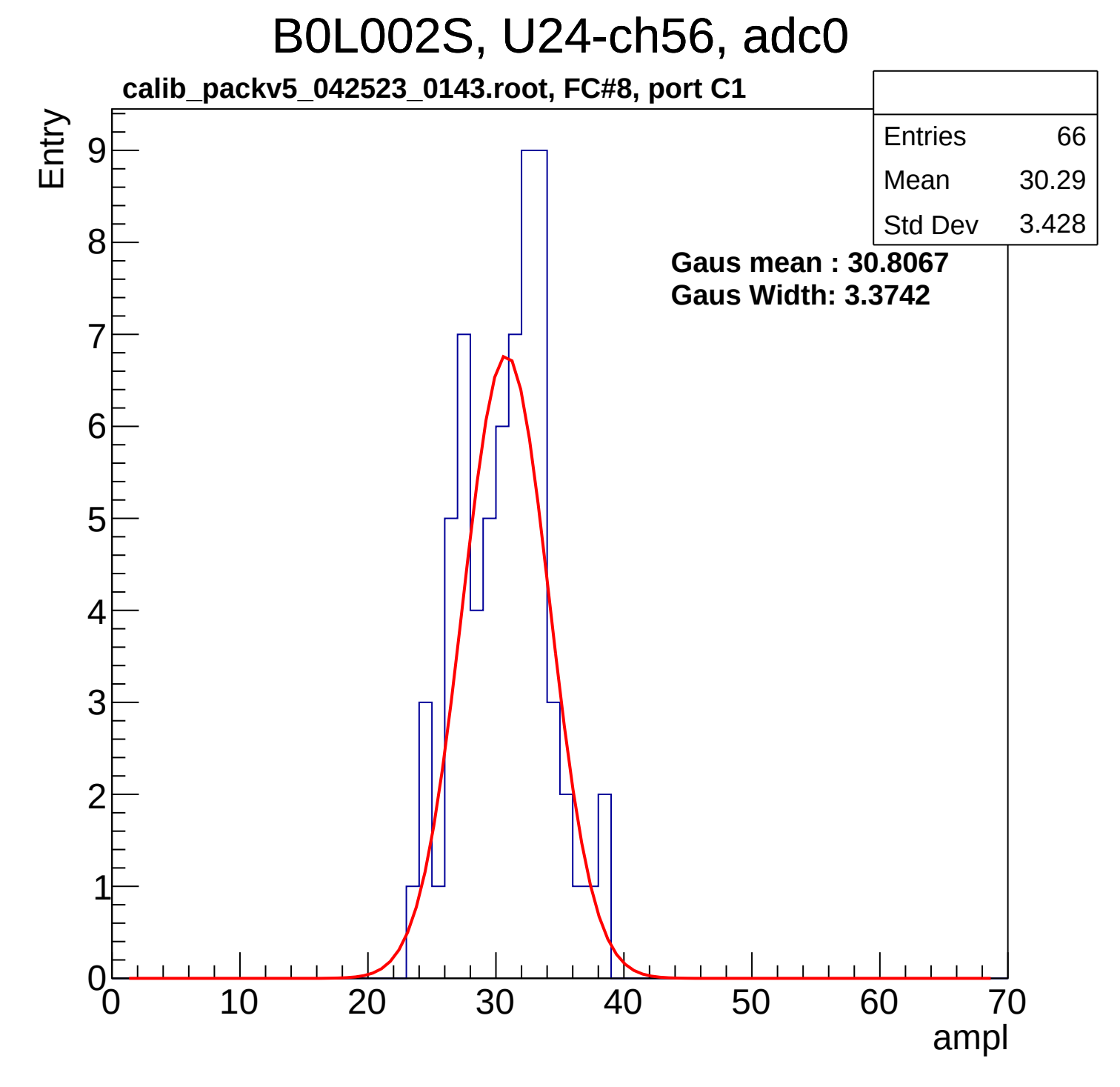

B0L002S, U24-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 51.81
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

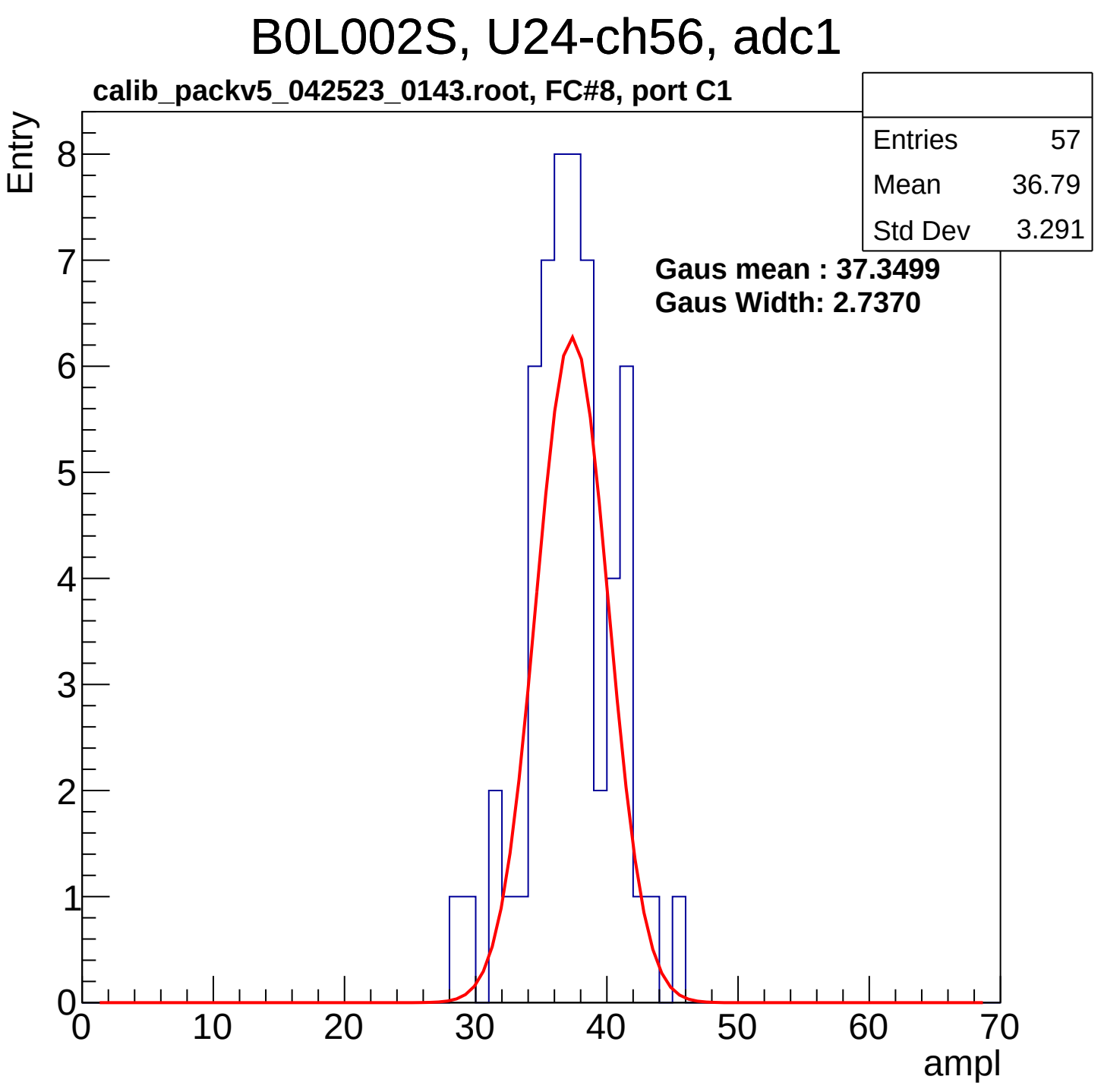

B0L002S, U24-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.79
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

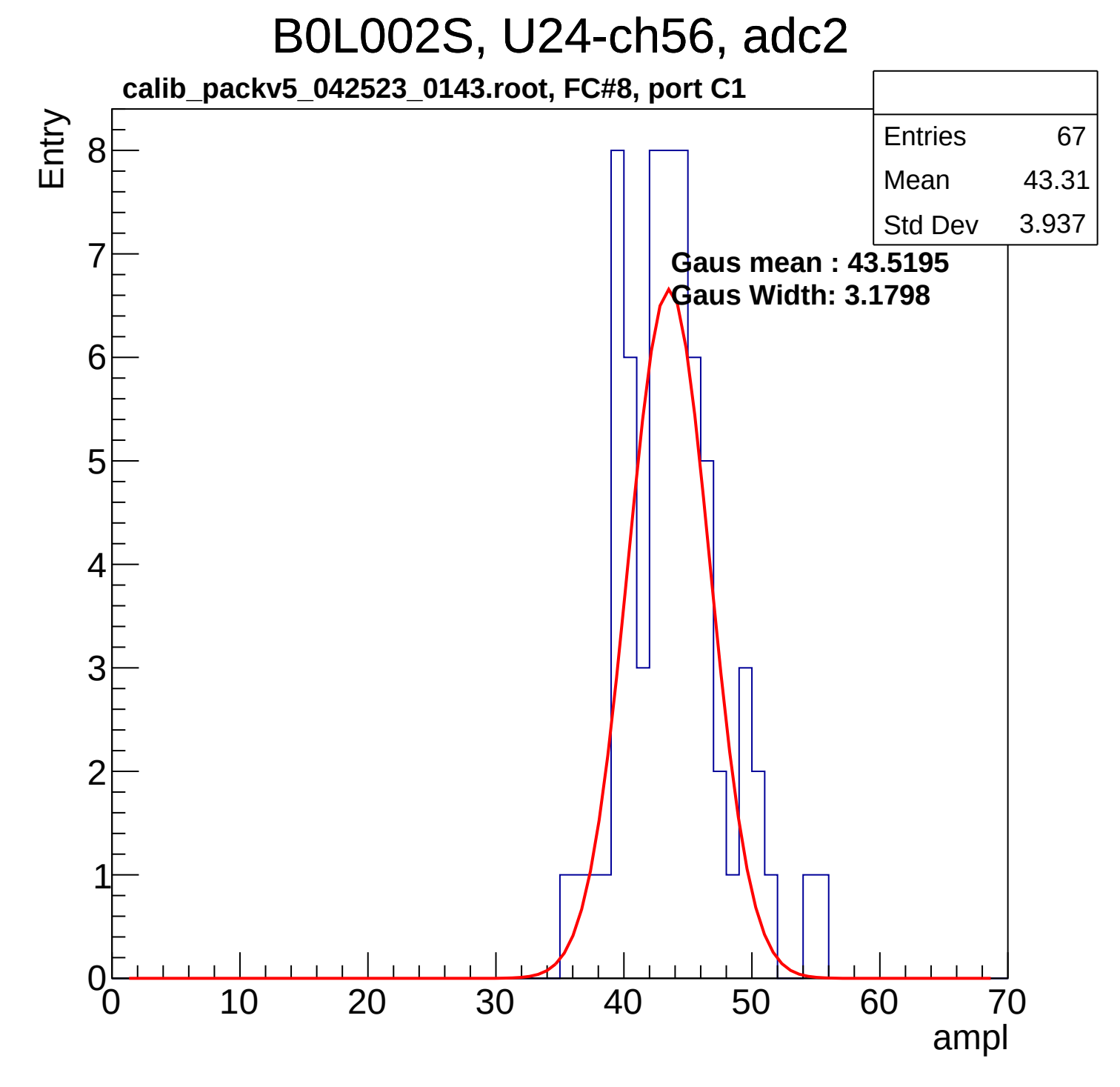

B0L002S, U24-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.09
Std Dev 10.78
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

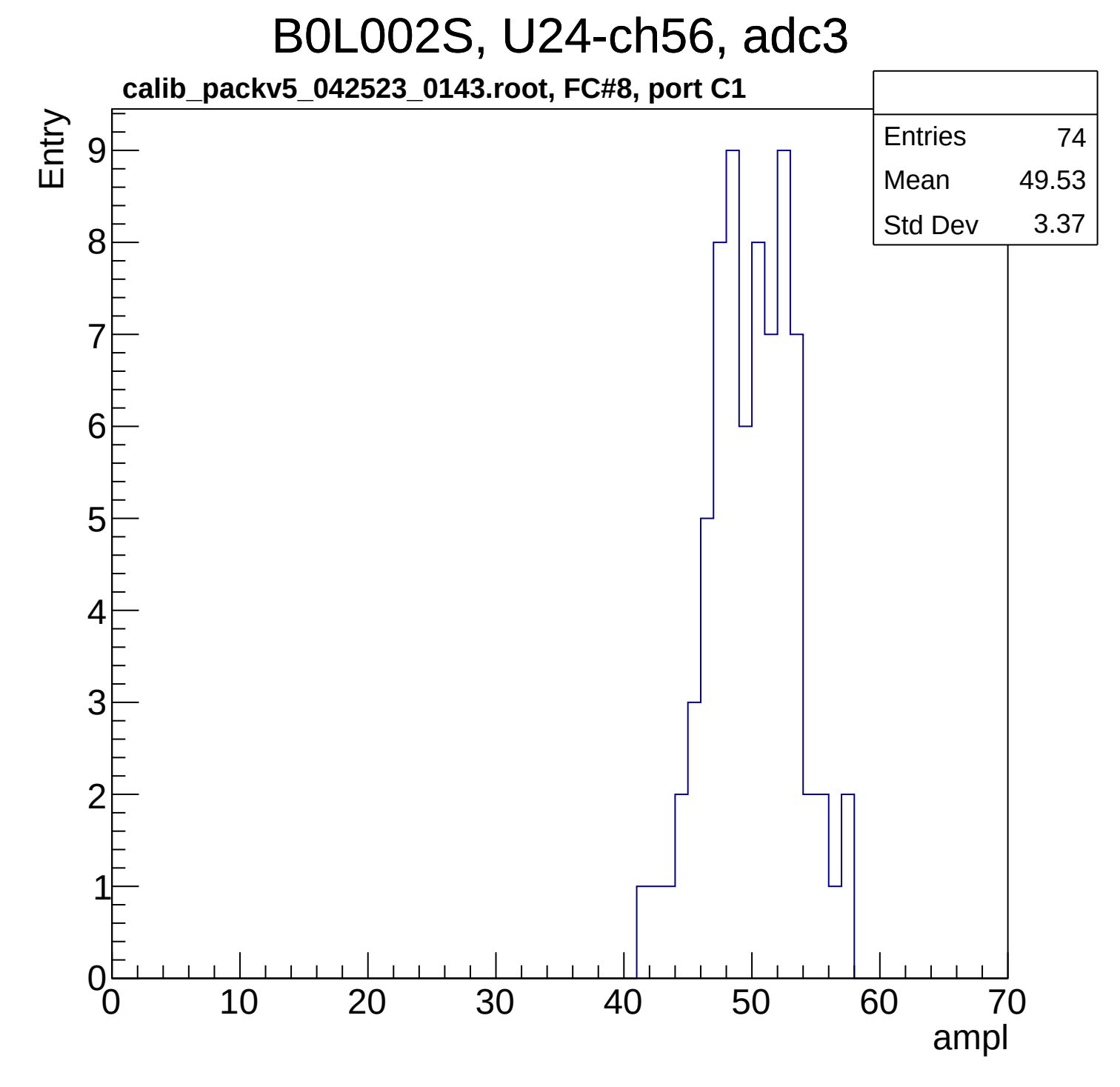

B0L002S, U24-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

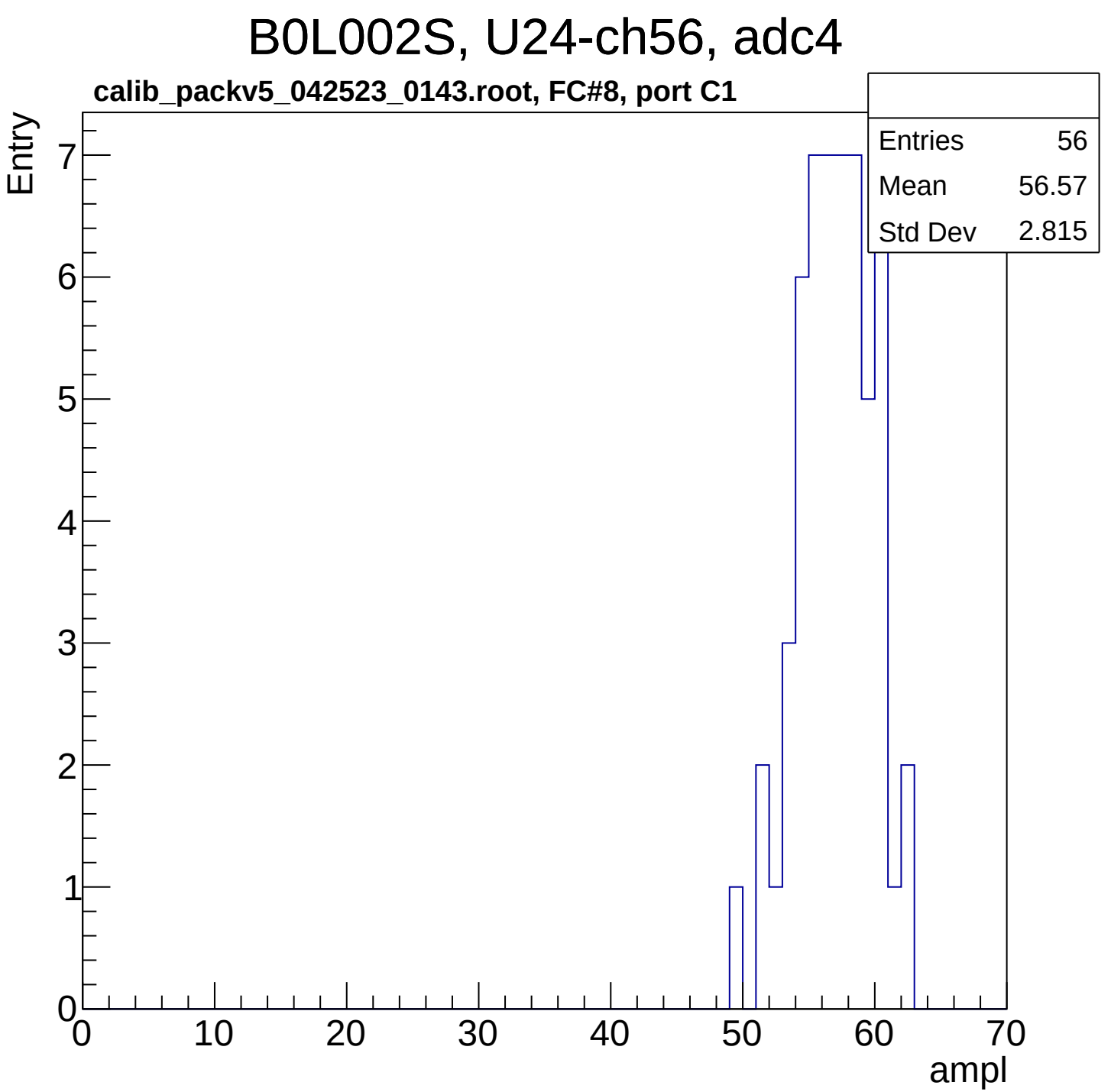

B0L002S, U24-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

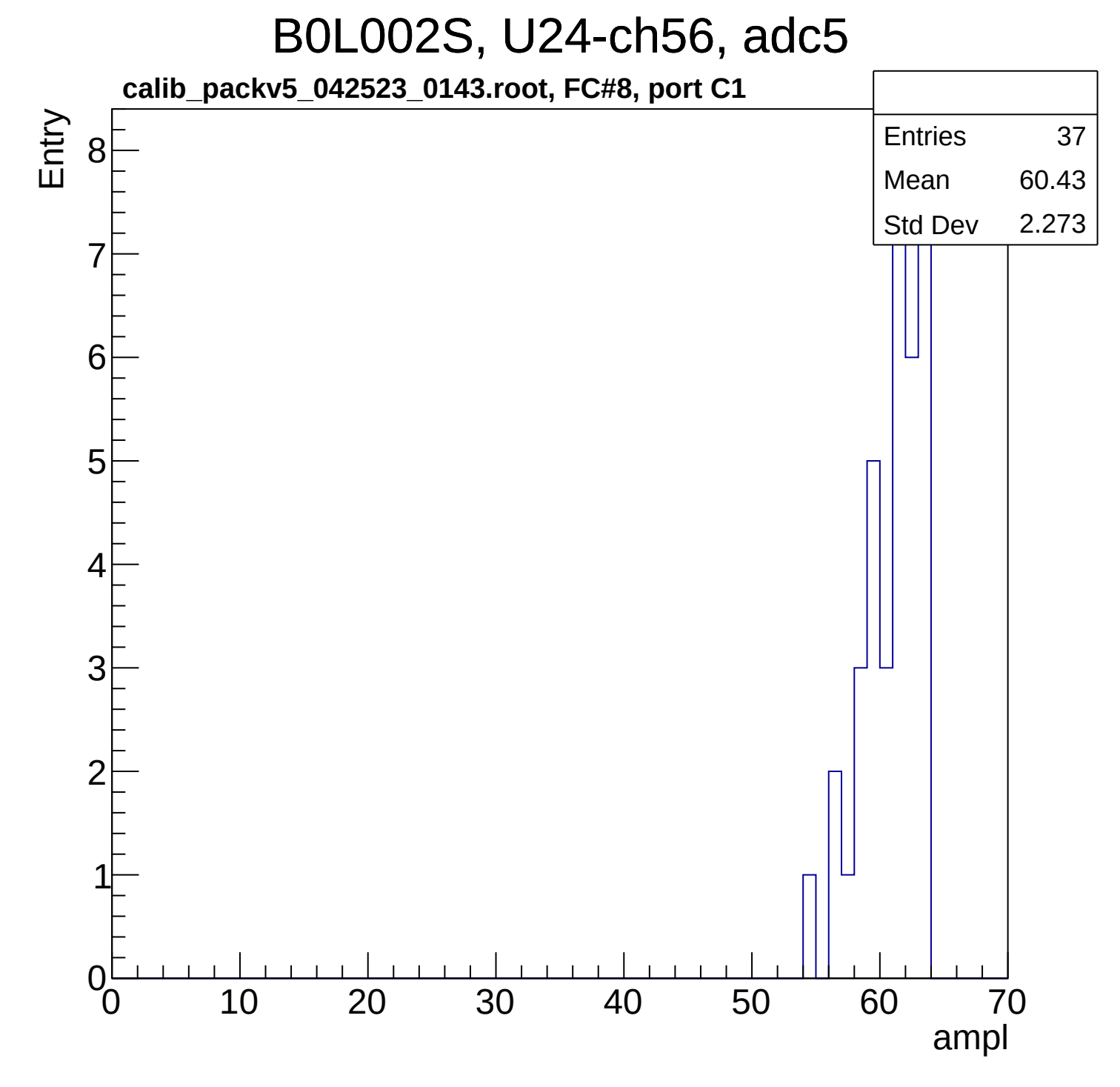

B0L002S, U24-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.29
Std Dev 3.428
Gaus mean : 30.8067
Gaus Width: 3.3742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 36.79
Std Dev 3.291
Gaus mean : 37.3499
Gaus Width: 2.7370
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.31
Std Dev 3.937
Gaus mean : 43.5195
Gaus Width: 3.1798
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 49.53
Std Dev 3.37
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.57
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 60.43
Std Dev 2.273
Entry
ampl
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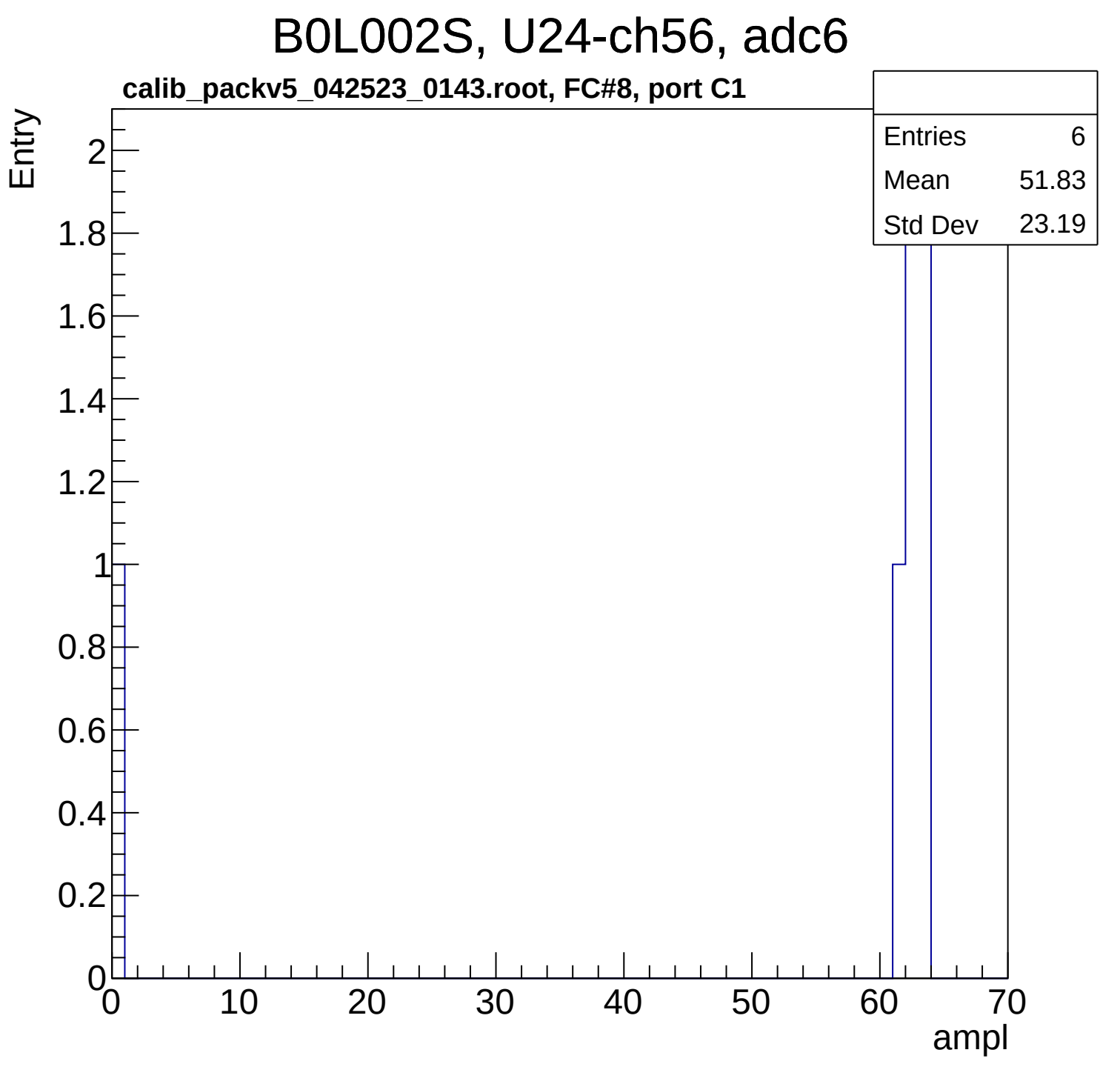
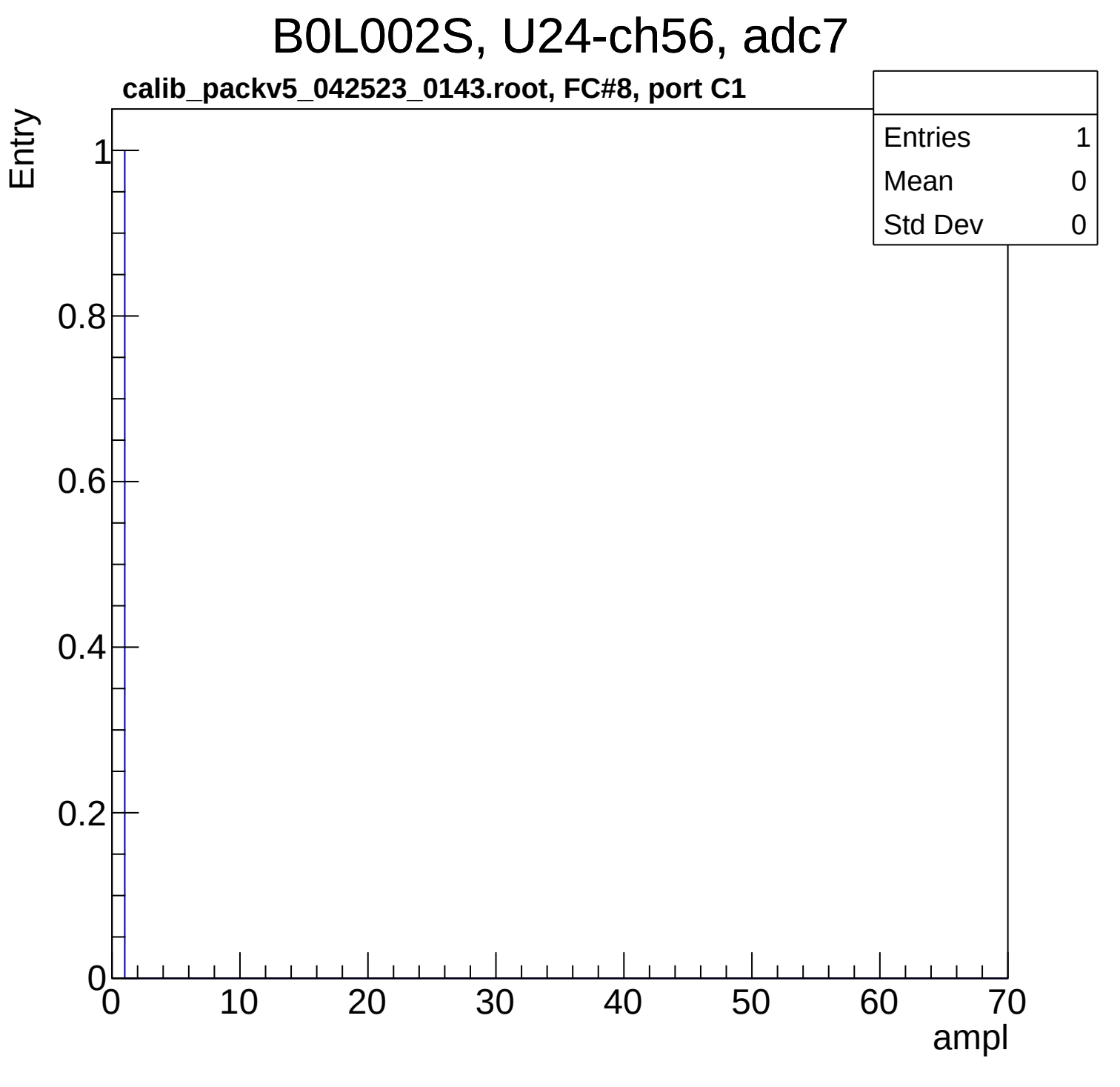
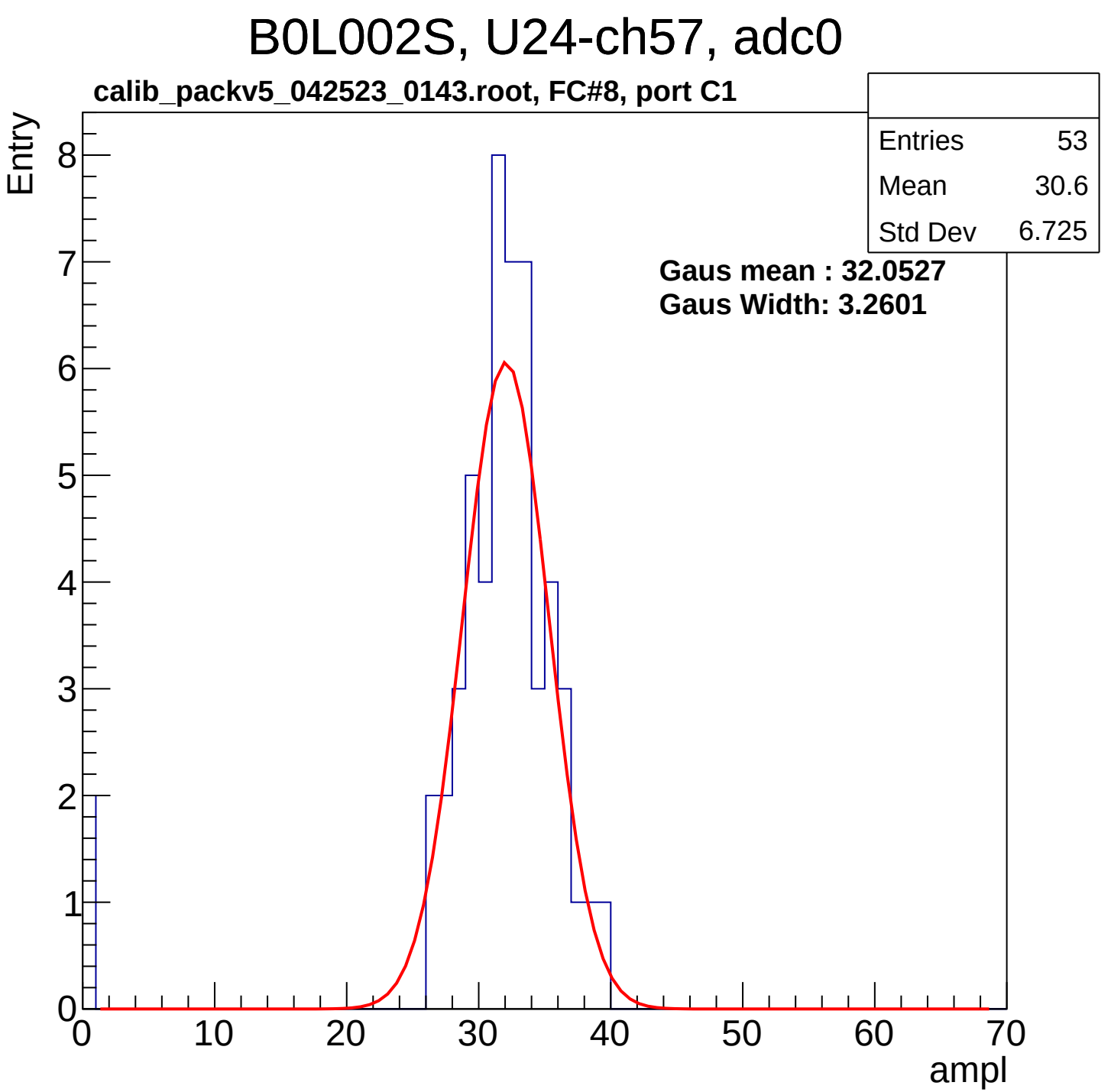
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 30.6
Std Dev 6.725
Gaus mean : 32.0527
Gaus Width: 3.2601
Entry
ampl
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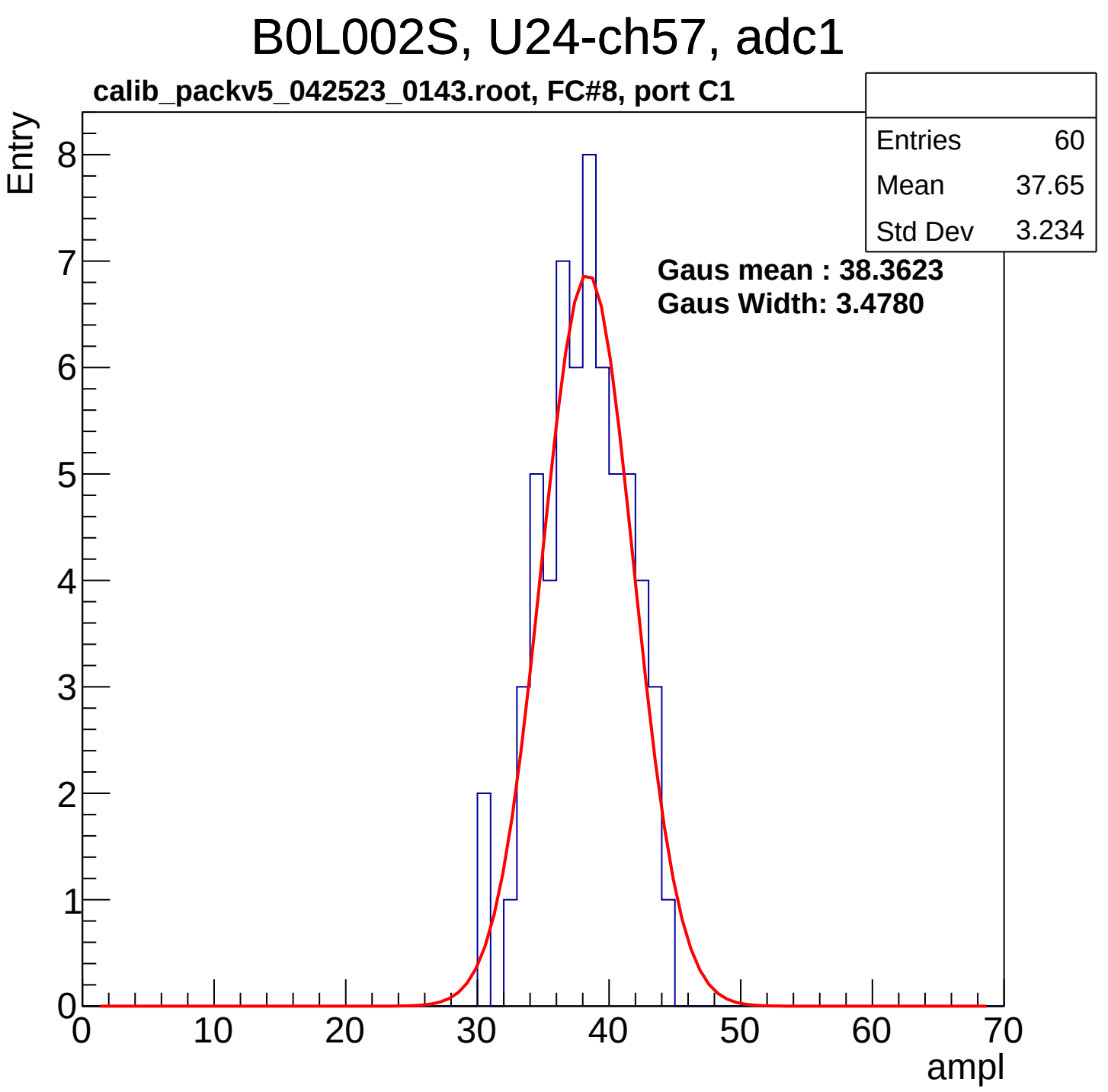
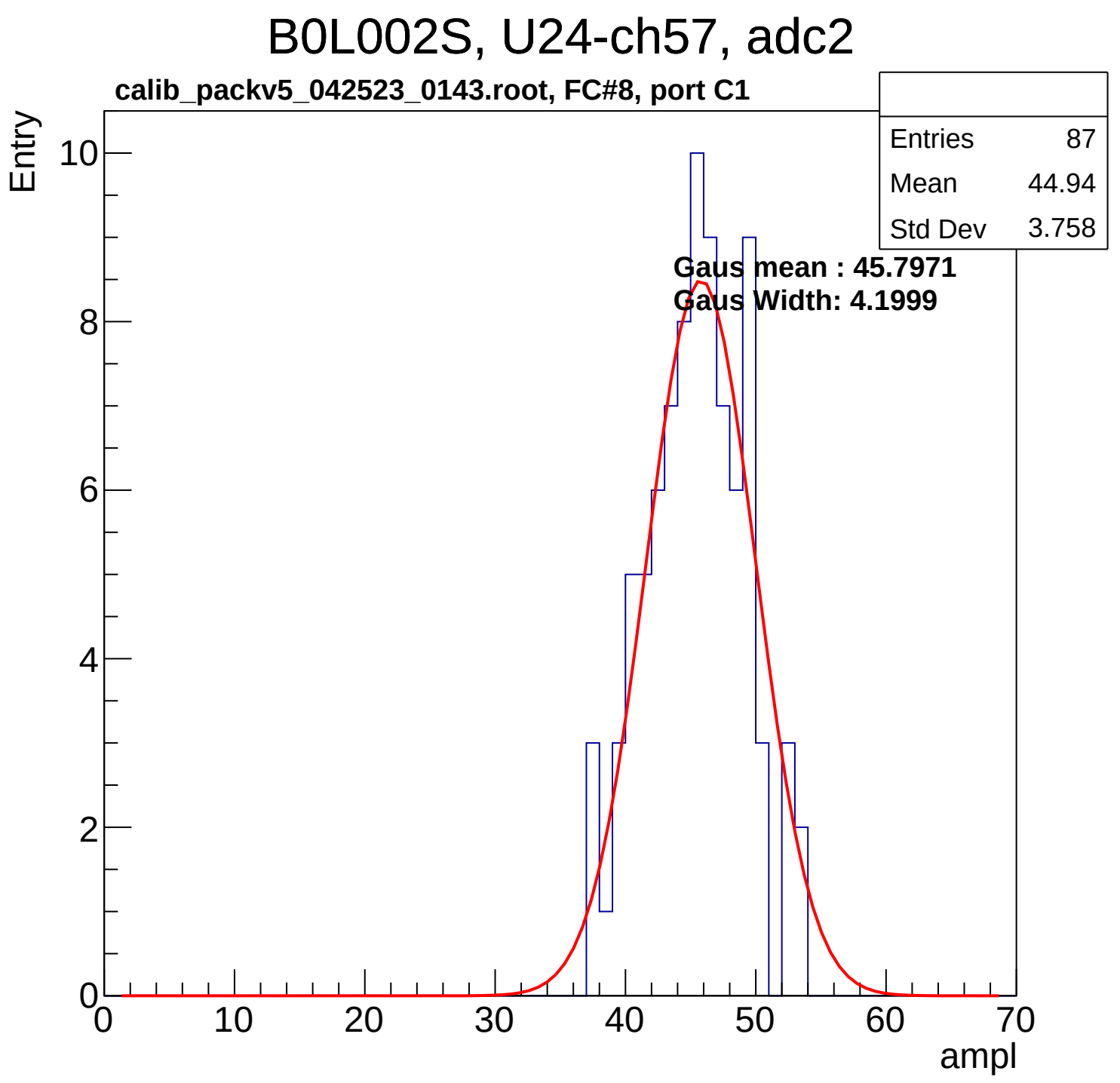
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.65
Std Dev 3.234
Gaus mean : 38.3623
Gaus Width: 3.4780
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 44.94
Std Dev 3.758
Gaus mean : 45.7971
Gaus Width: 4.1999
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
50
60
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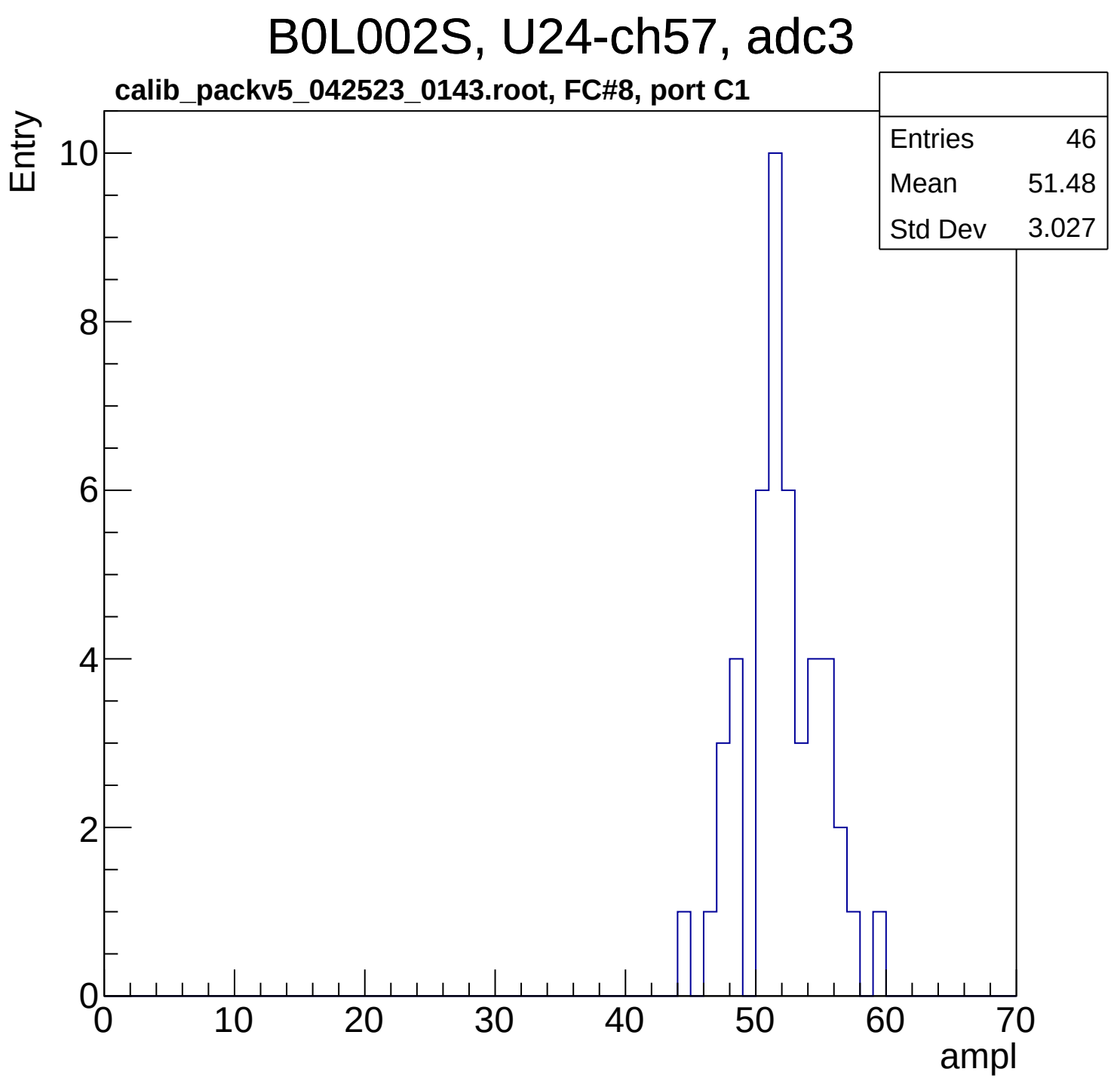
B0L002S, U24-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 51.48
Std Dev 3.027
Entry
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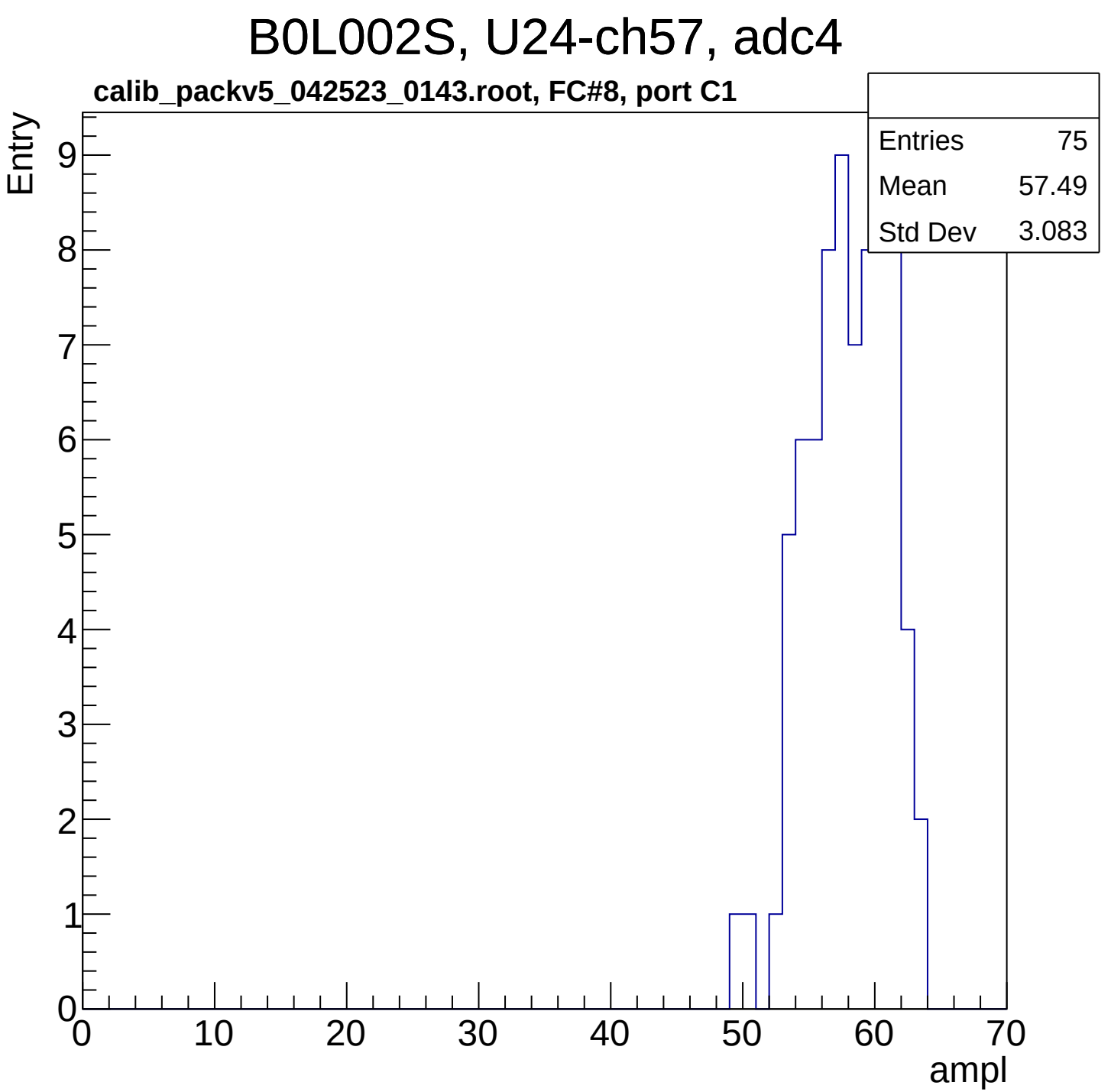
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 57.49
Std Dev 3.083
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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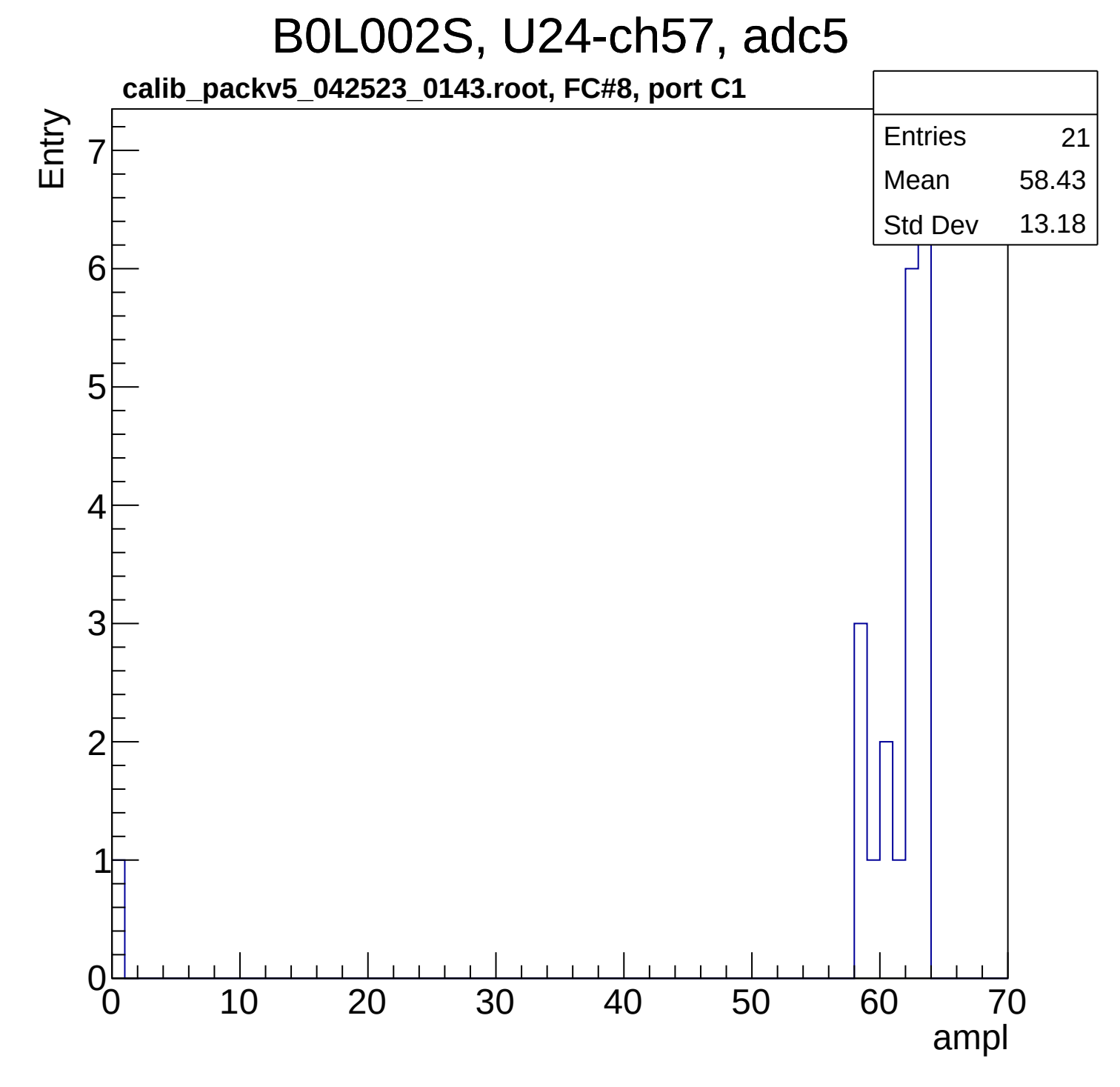
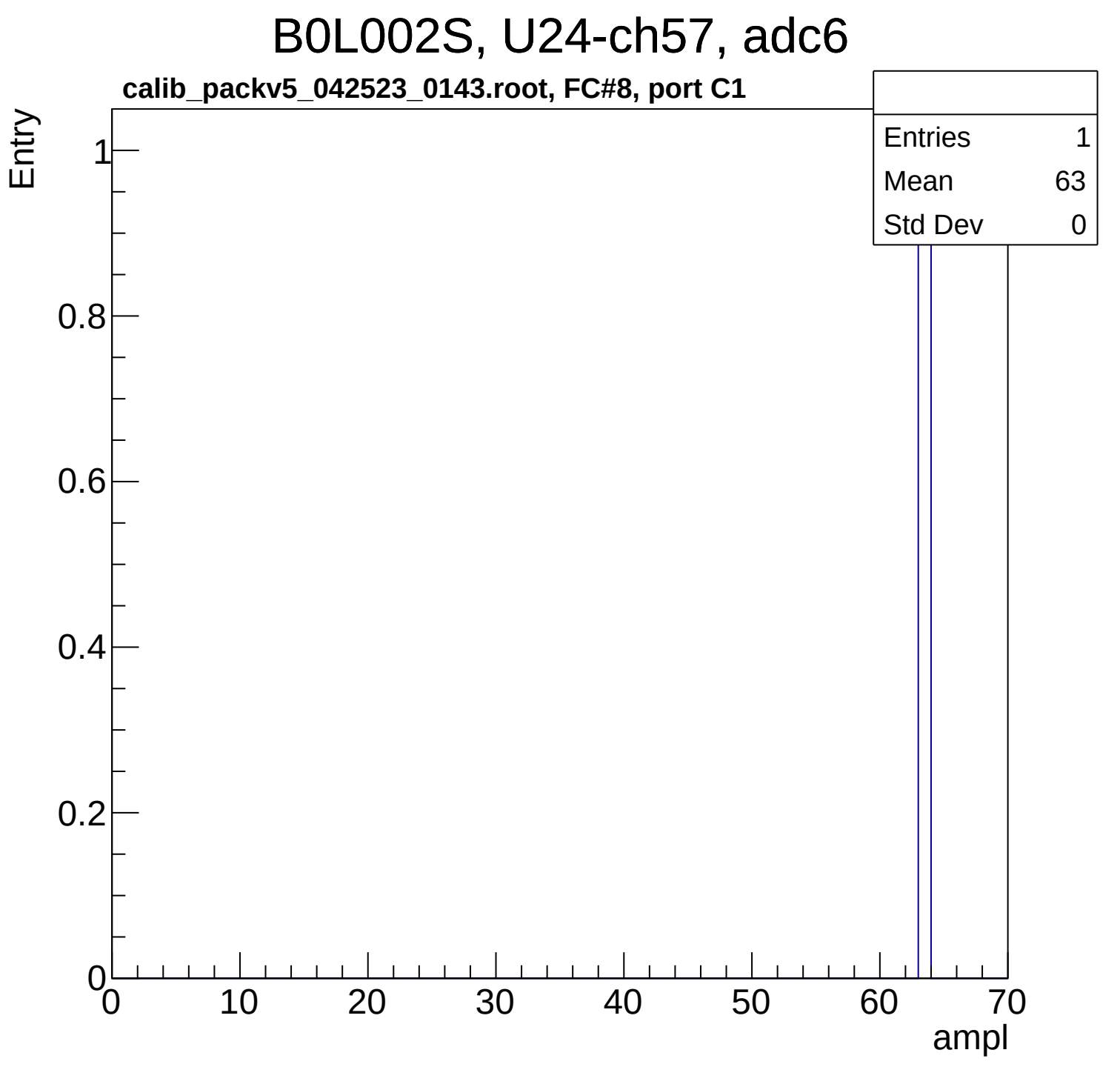
40
50
60
70


B0L002S, U24-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.43
Std Dev 13.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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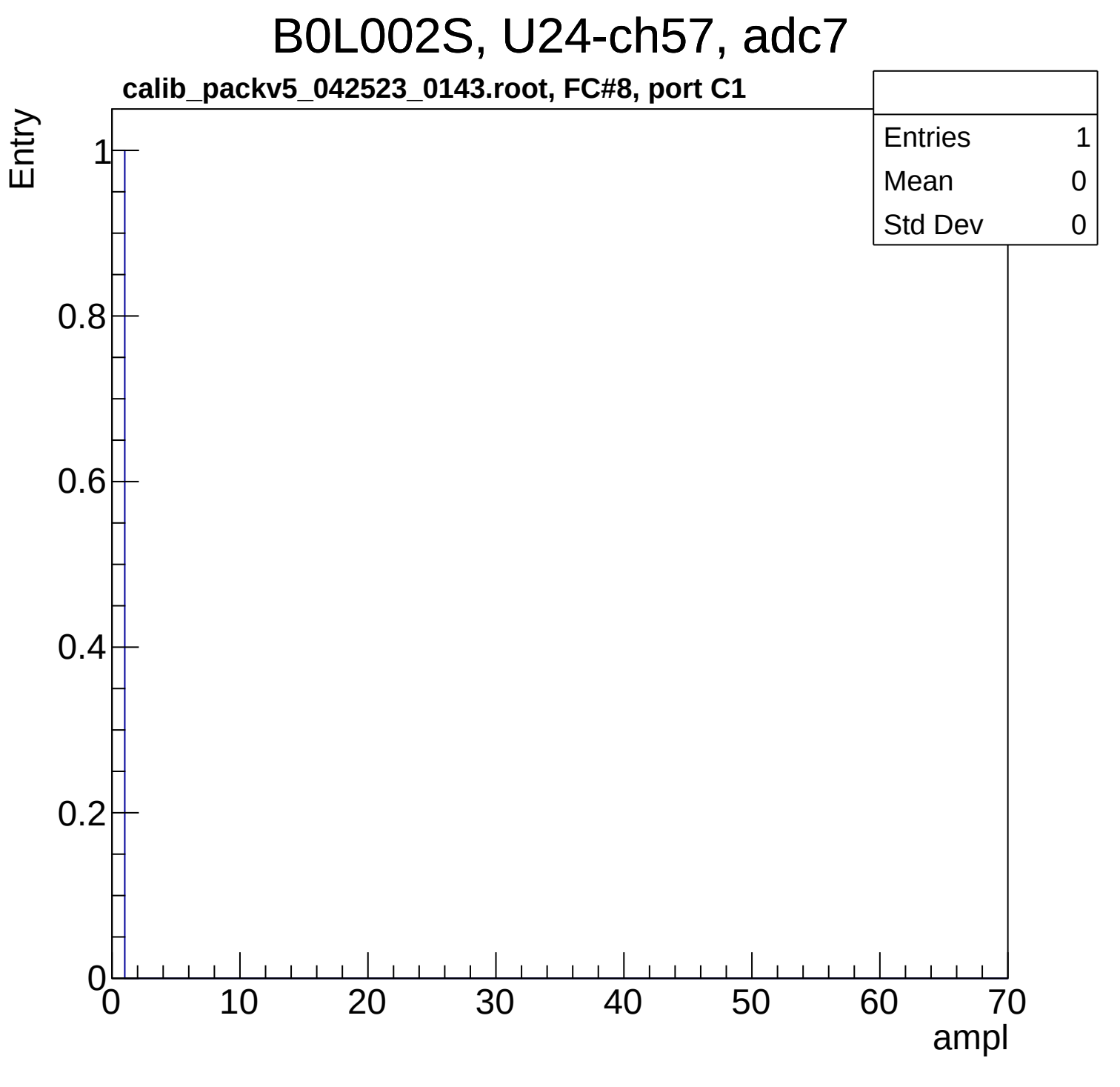
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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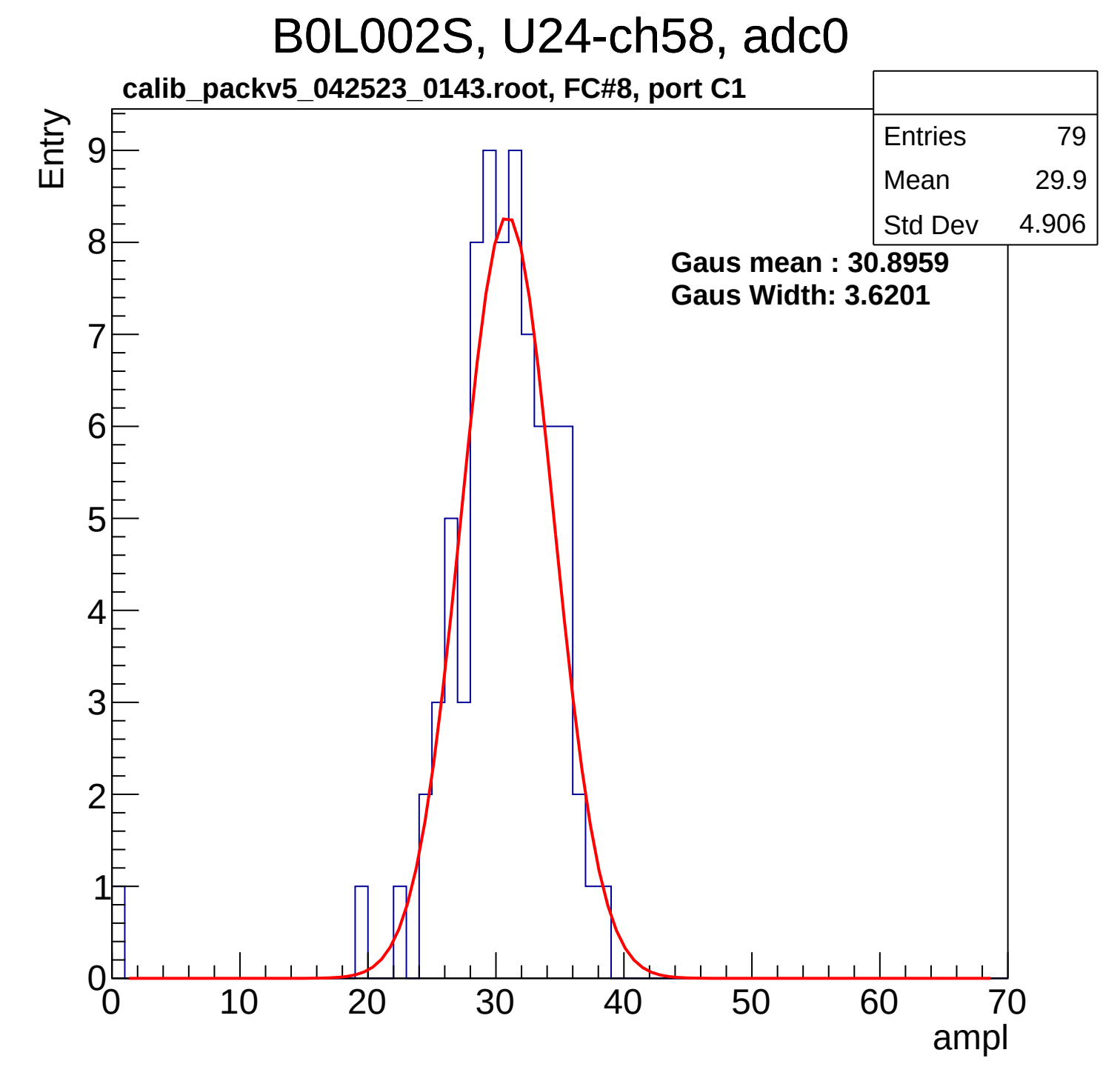
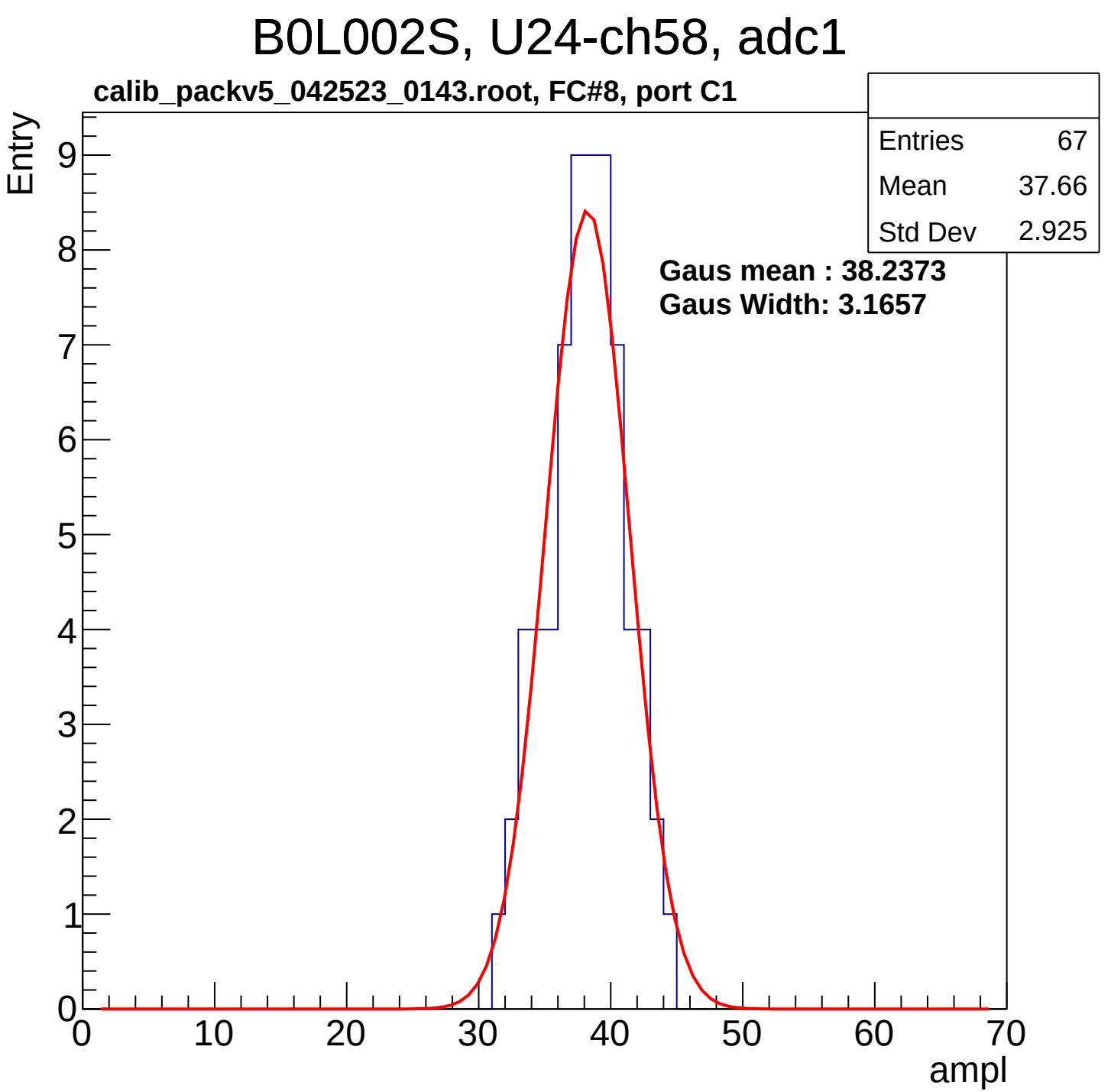
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.9
Std Dev 4.906
Gaus mean : 30.8959
Gaus Width: 3.6201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.66
Std Dev 2.925
Gaus mean : 38.2373
Gaus Width: 3.1657
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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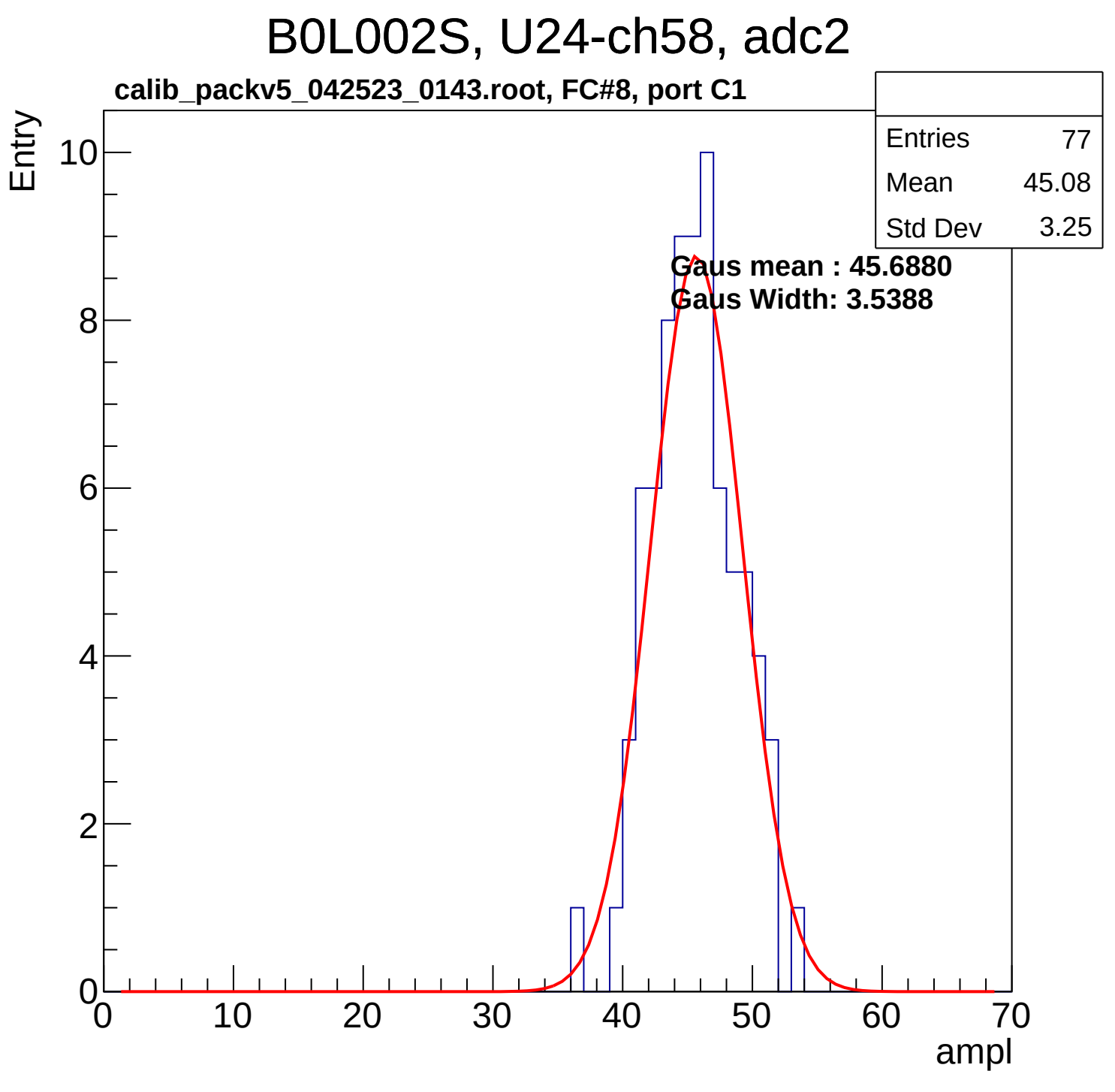
B0L002S, U24-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 45.08
Std Dev 3.25
Gaus mean : 45.6880
Gaus Width: 3.5388
Entry
ampl
0
2
4
6
8
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0
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60
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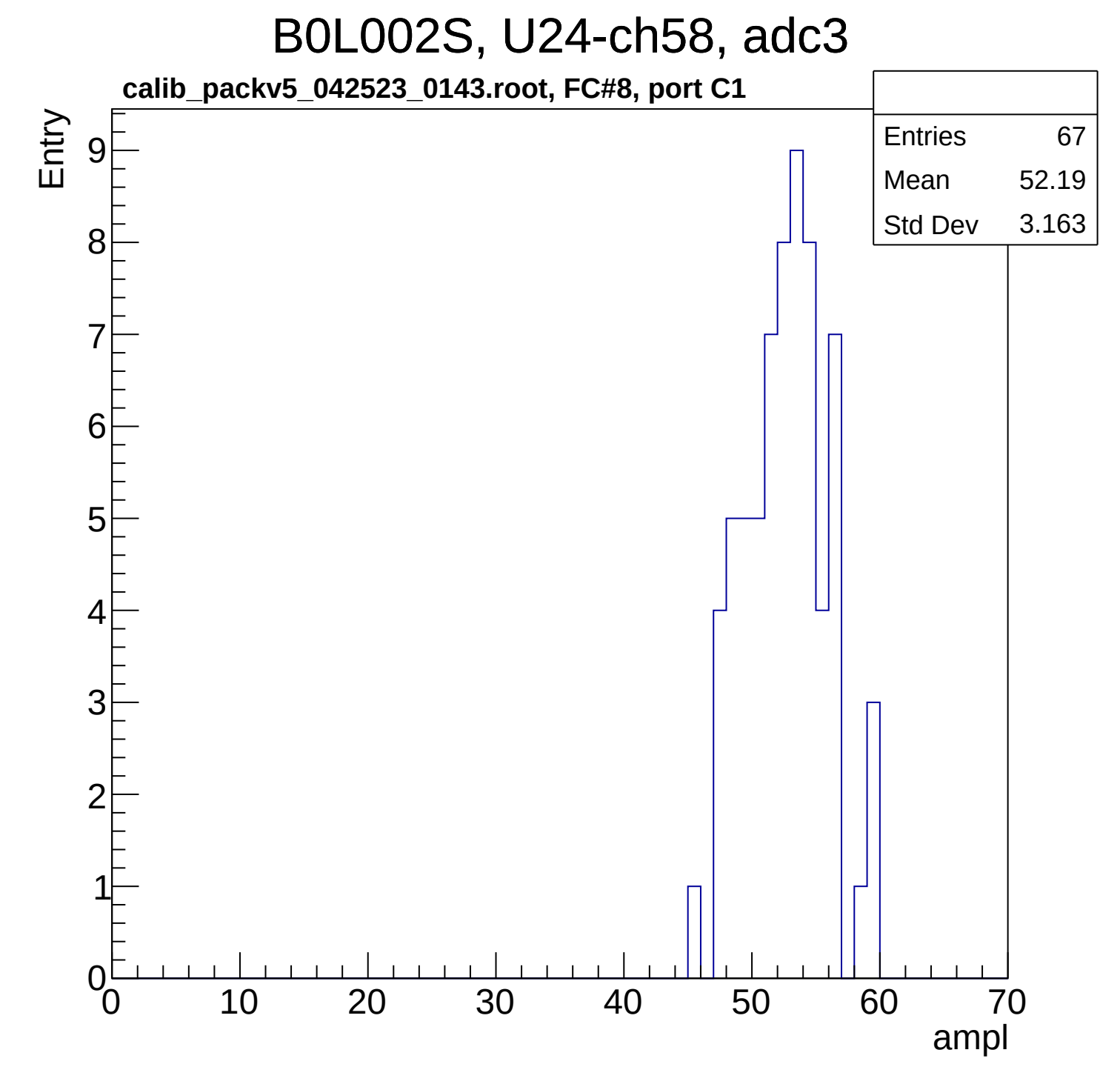
B0L002S, U24-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 52.19
Std Dev 3.163
Entry
ampl
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2
3
4
5
6
7
8
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0
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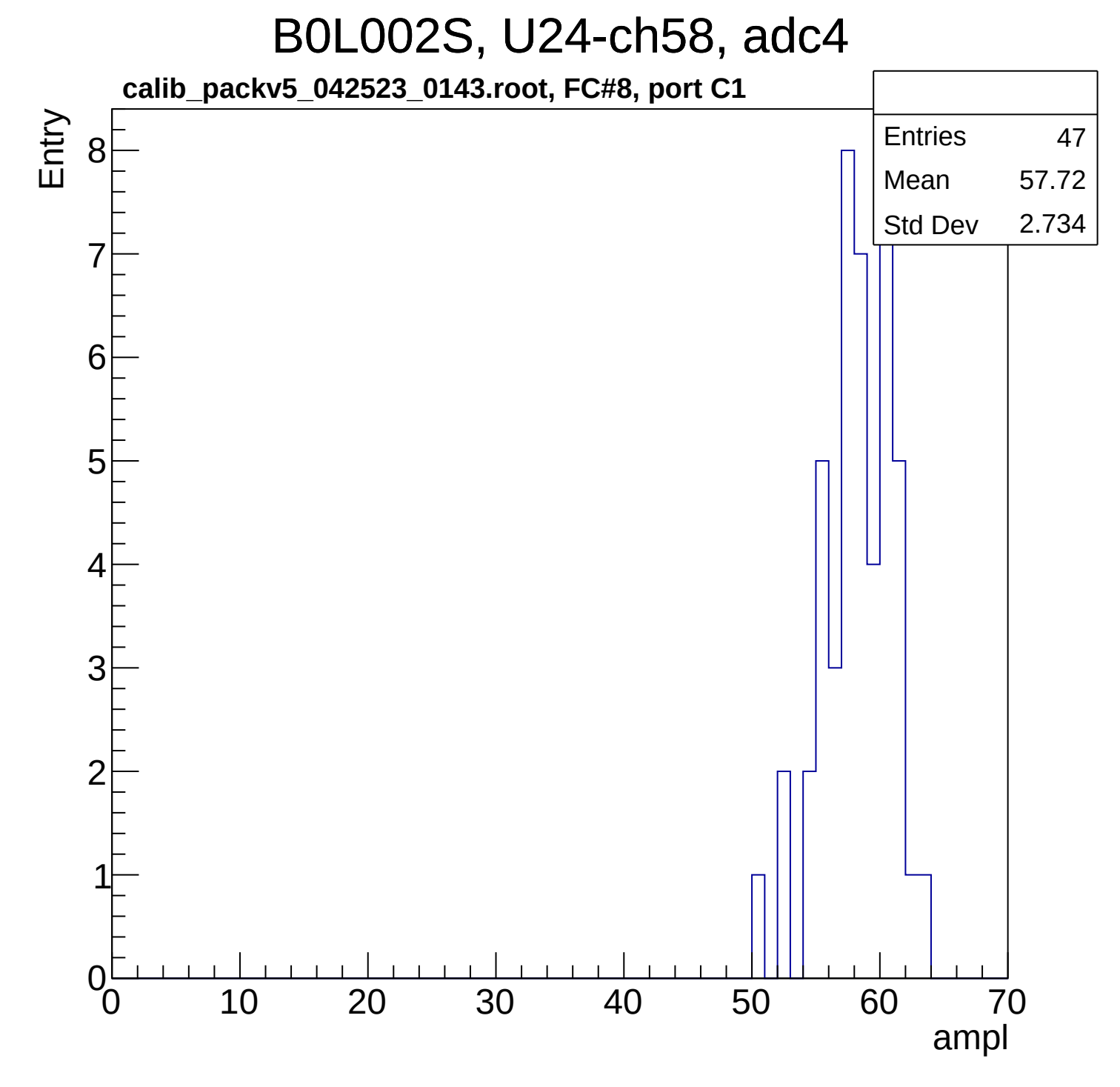
B0L002S, U24-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.72
Std Dev 2.734
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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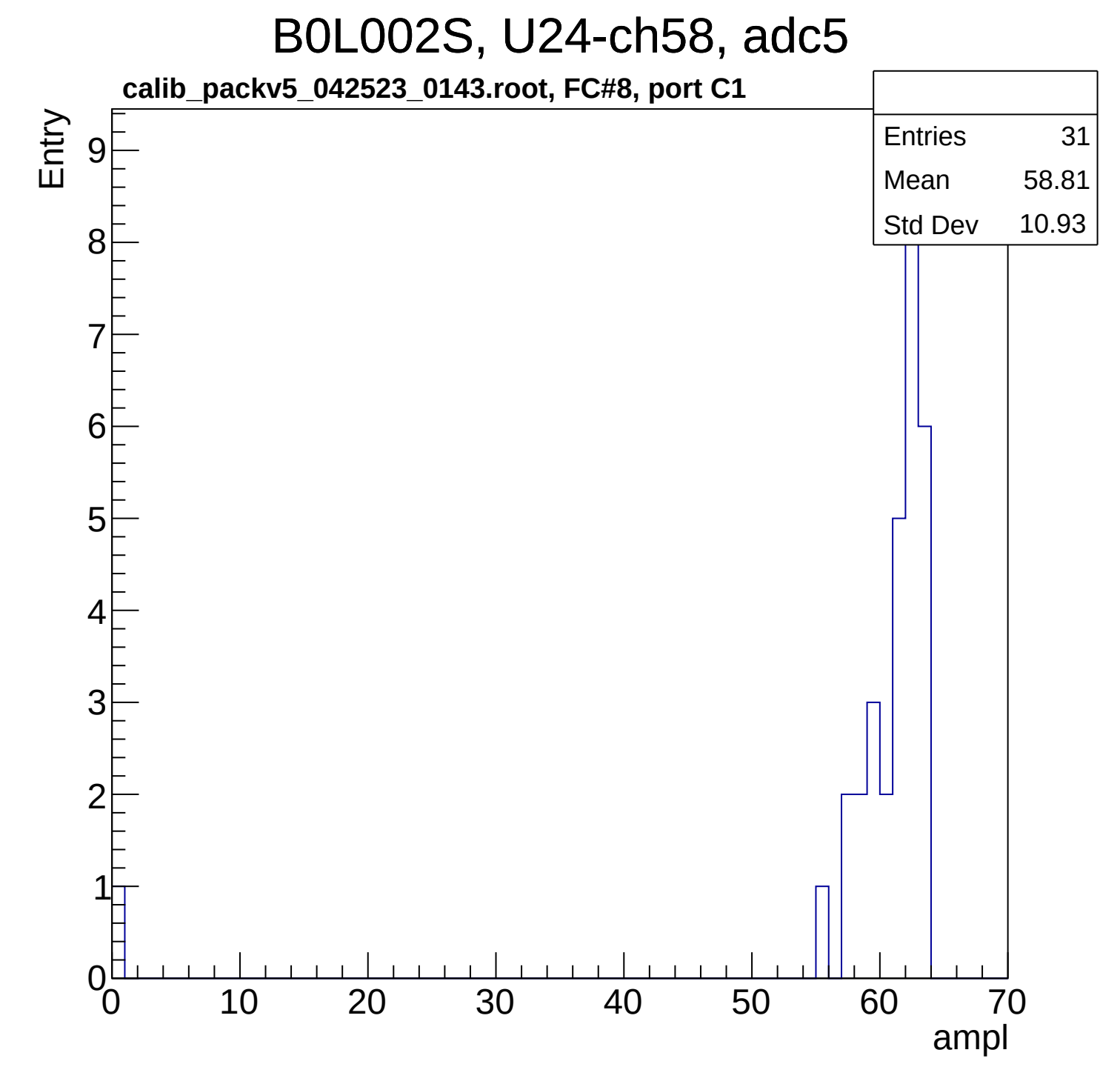
B0L002S, U24-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 58.81
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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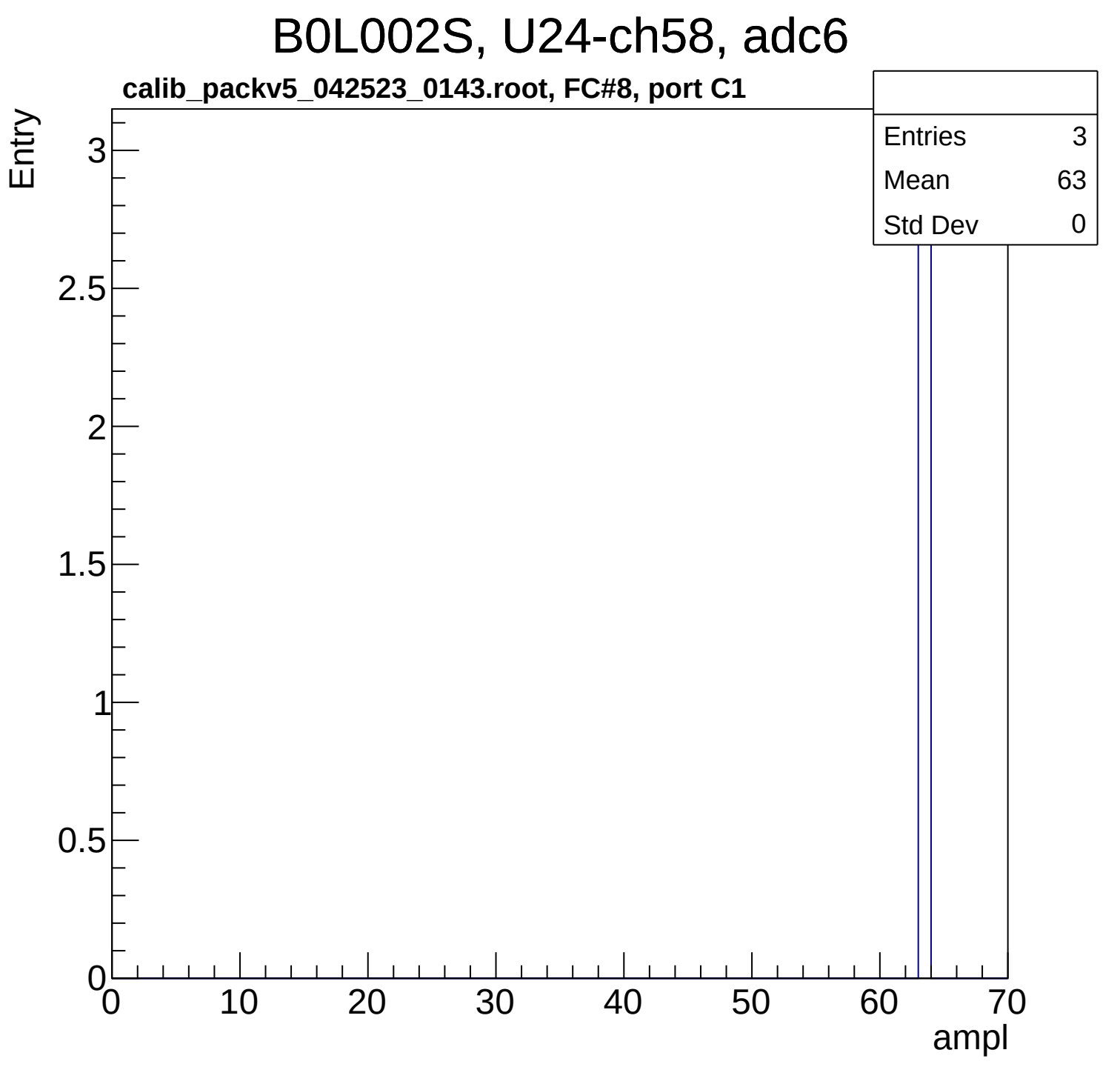
B0L002S, U24-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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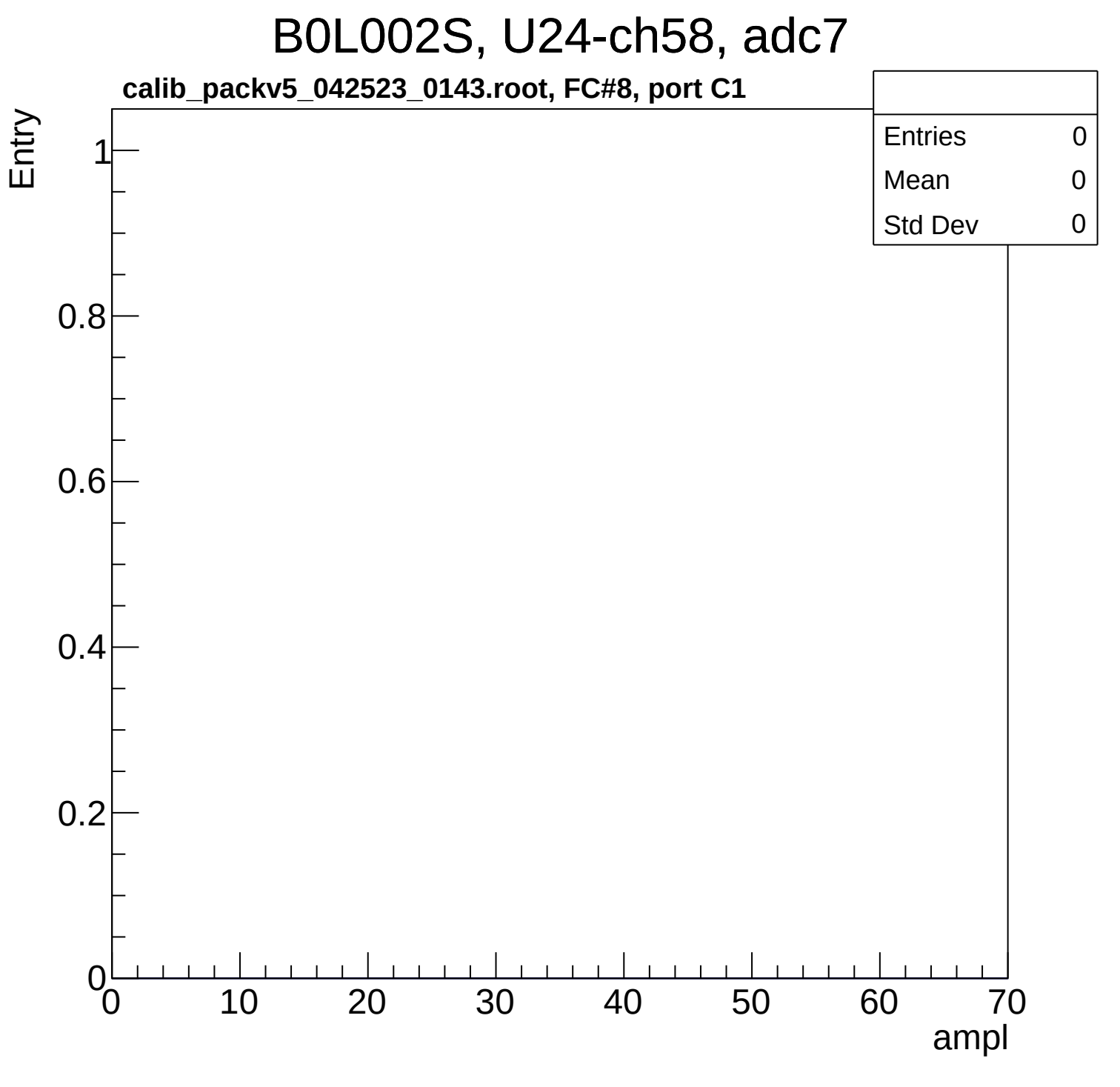
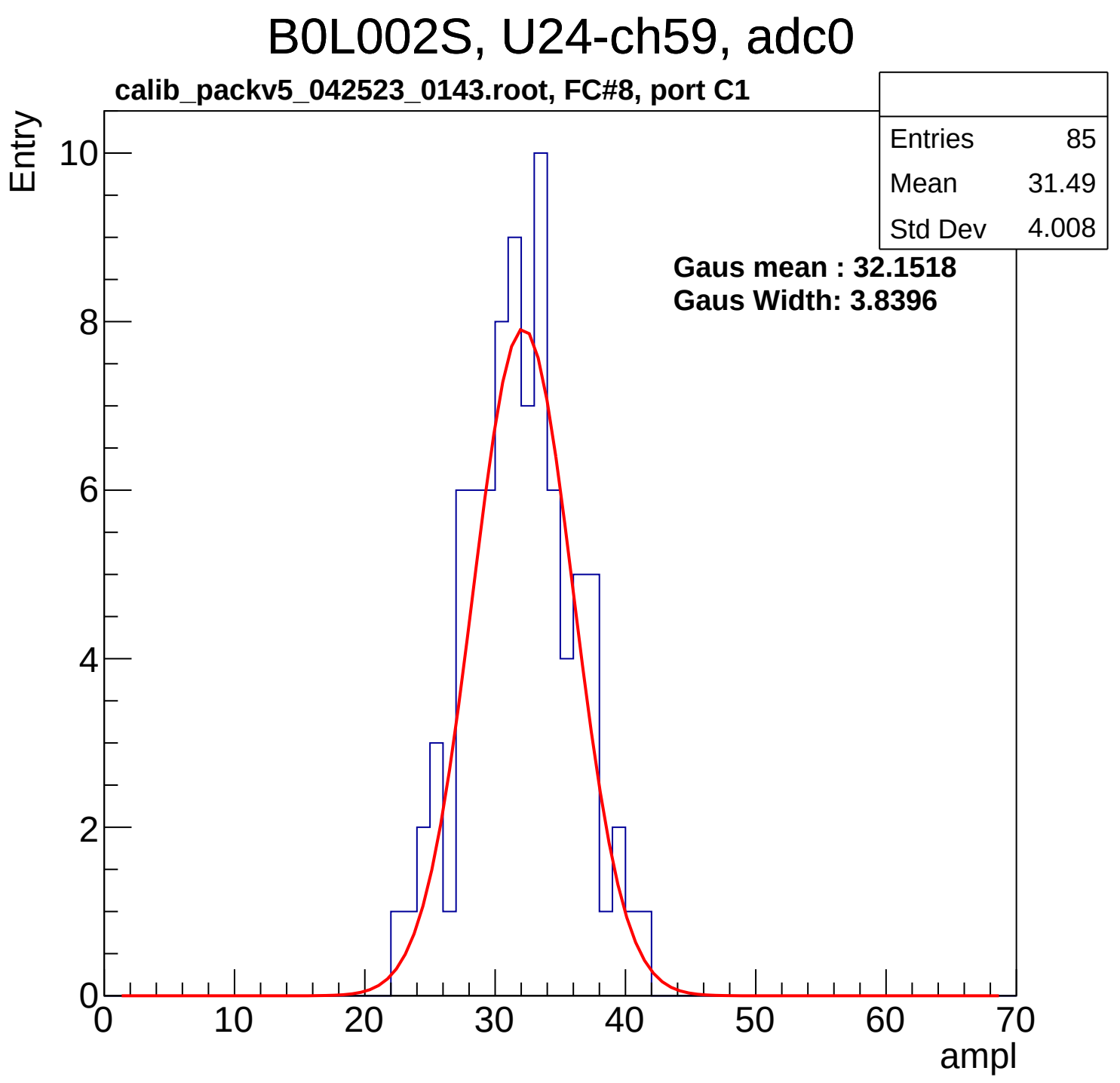

B0L002S, U24-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 31.49
Std Dev 4.008
Gaus mean : 32.1518
Gaus Width: 3.8396
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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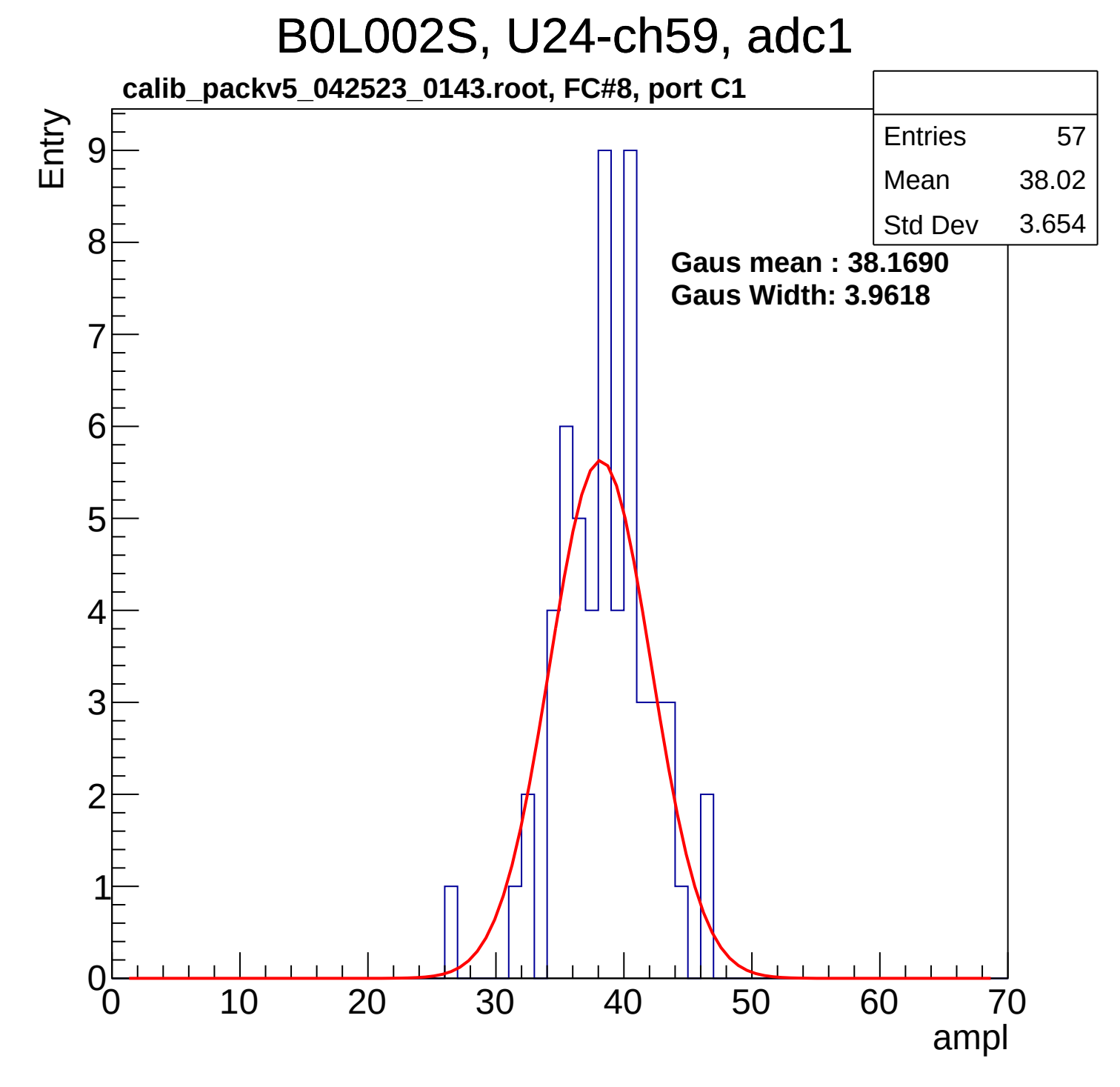
B0L002S, U24-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 38.02
Std Dev 3.654
Gaus mean : 38.1690
Gaus Width: 3.9618
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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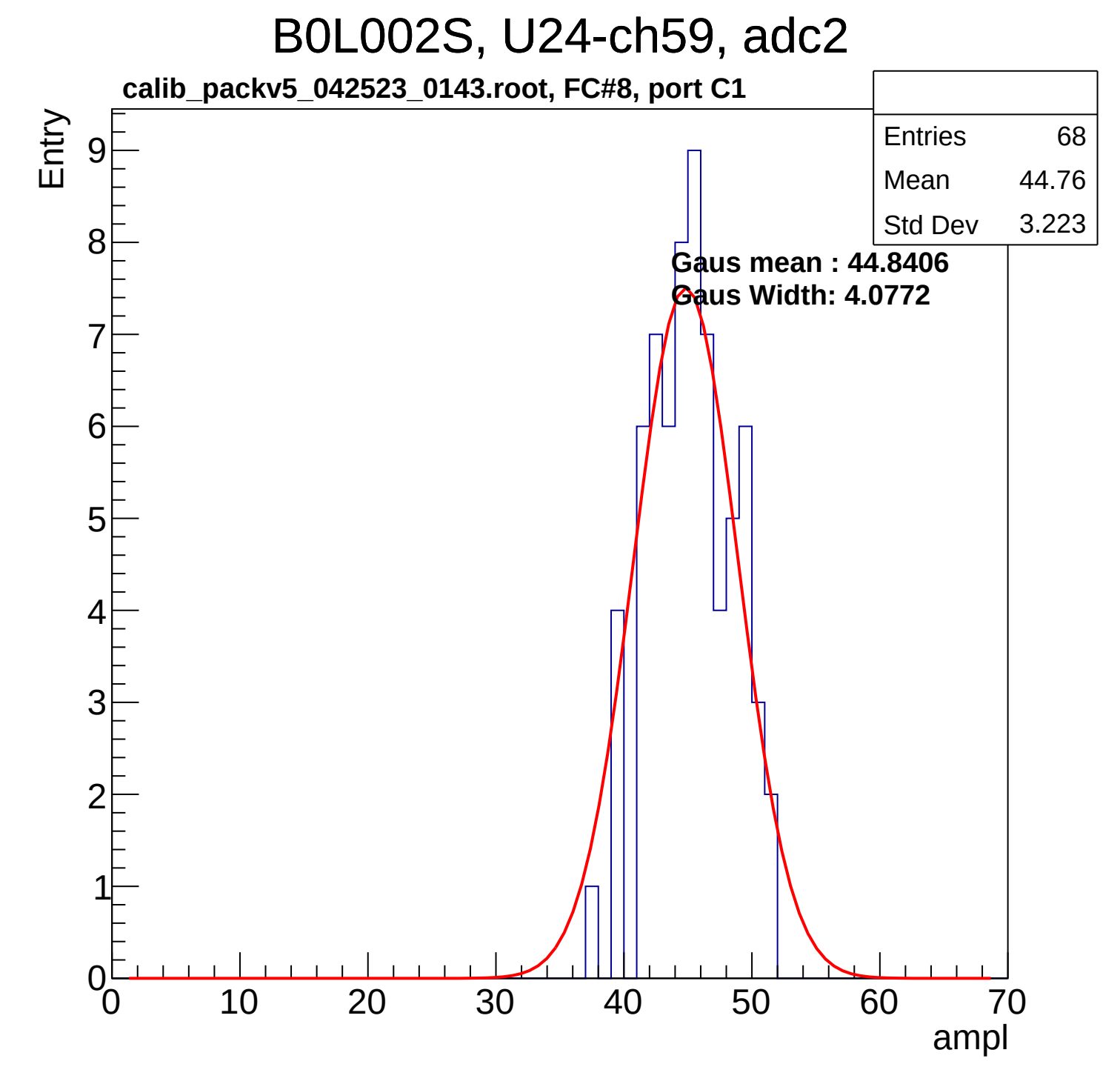
B0L002S, U24-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.76
Std Dev 3.223
Gaus mean : 44.8406
Gaus Width: 4.0772
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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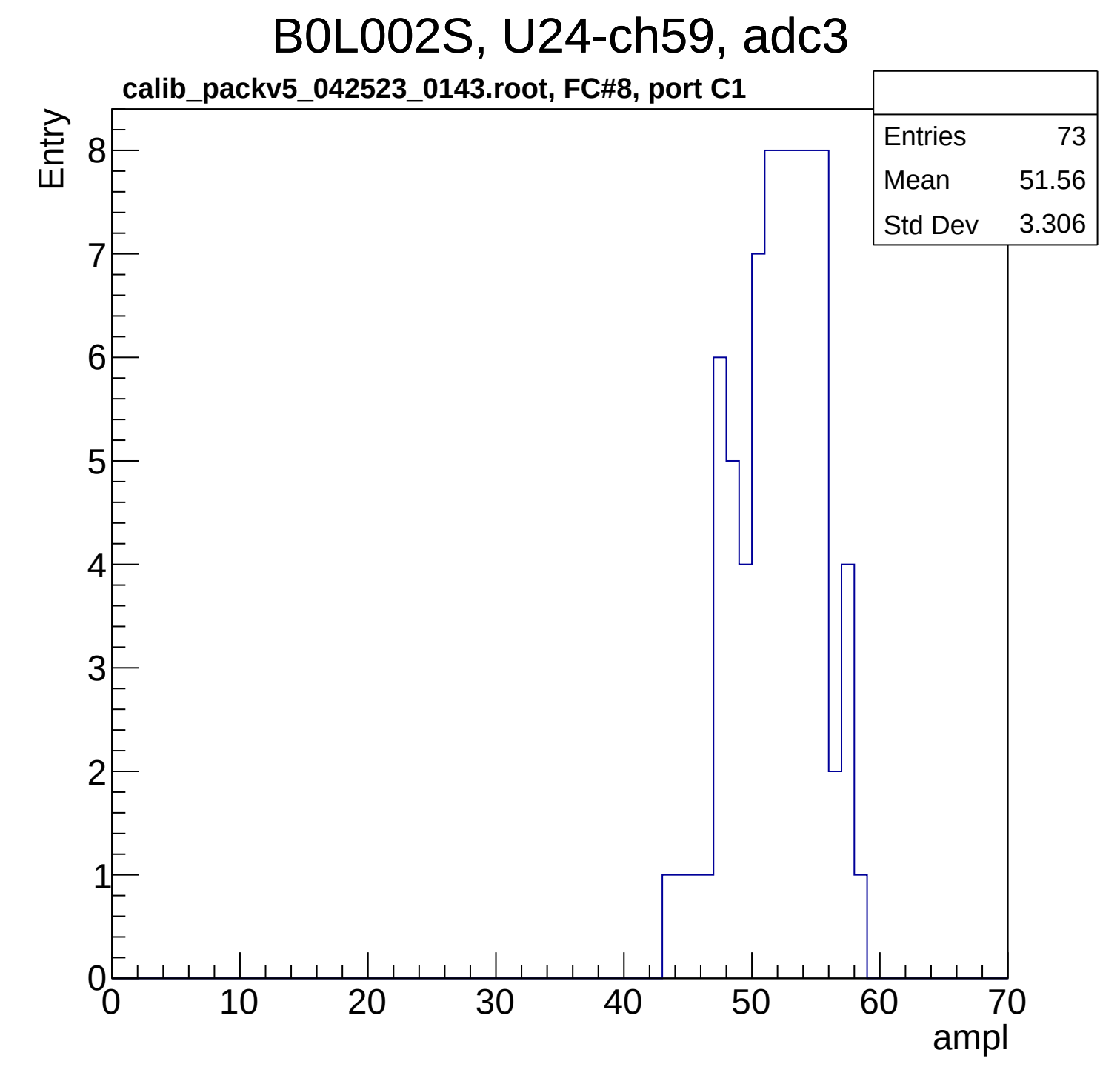
B0L002S, U24-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.56
Std Dev 3.306
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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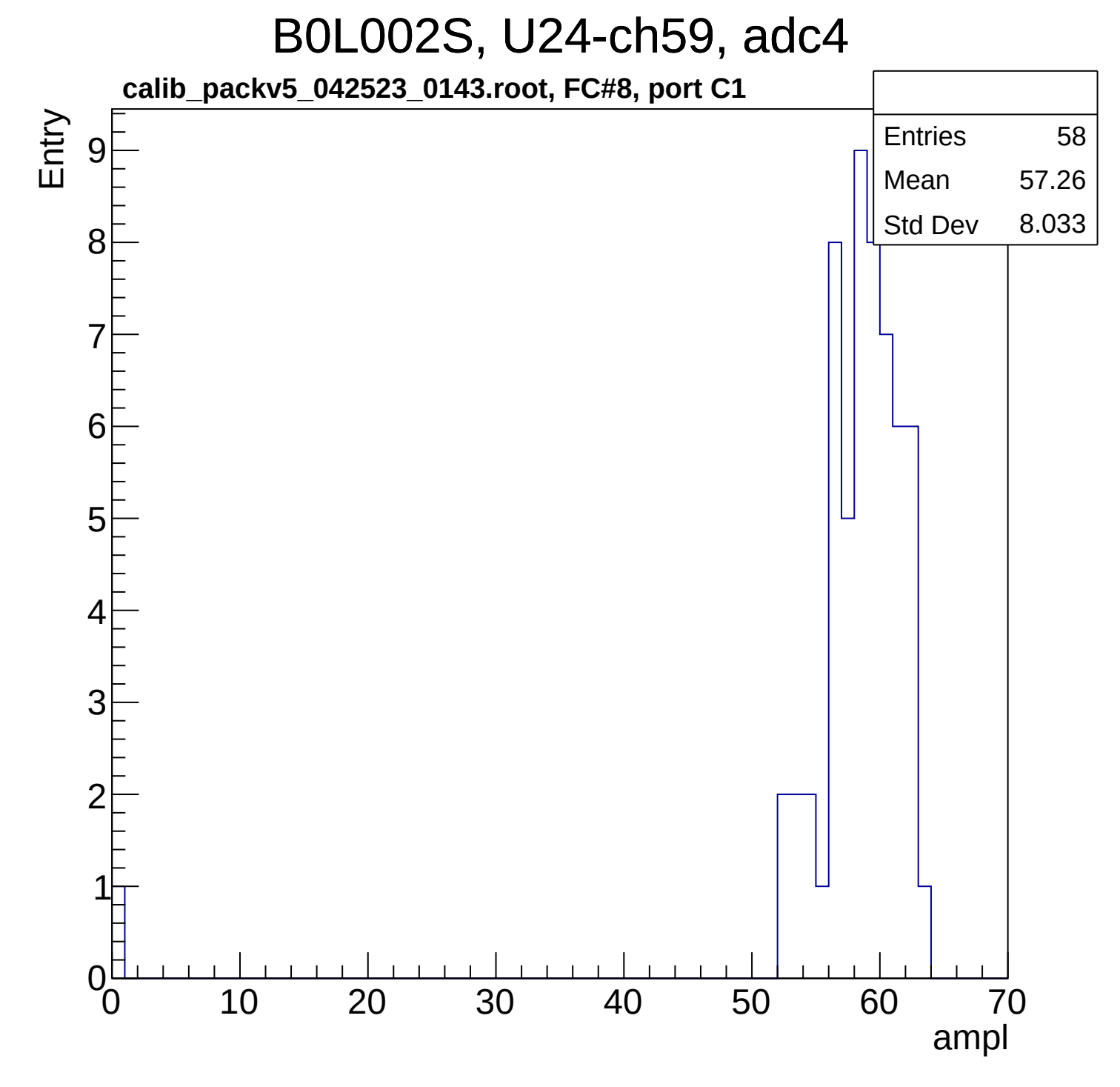
B0L002S, U24-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.26
Std Dev 8.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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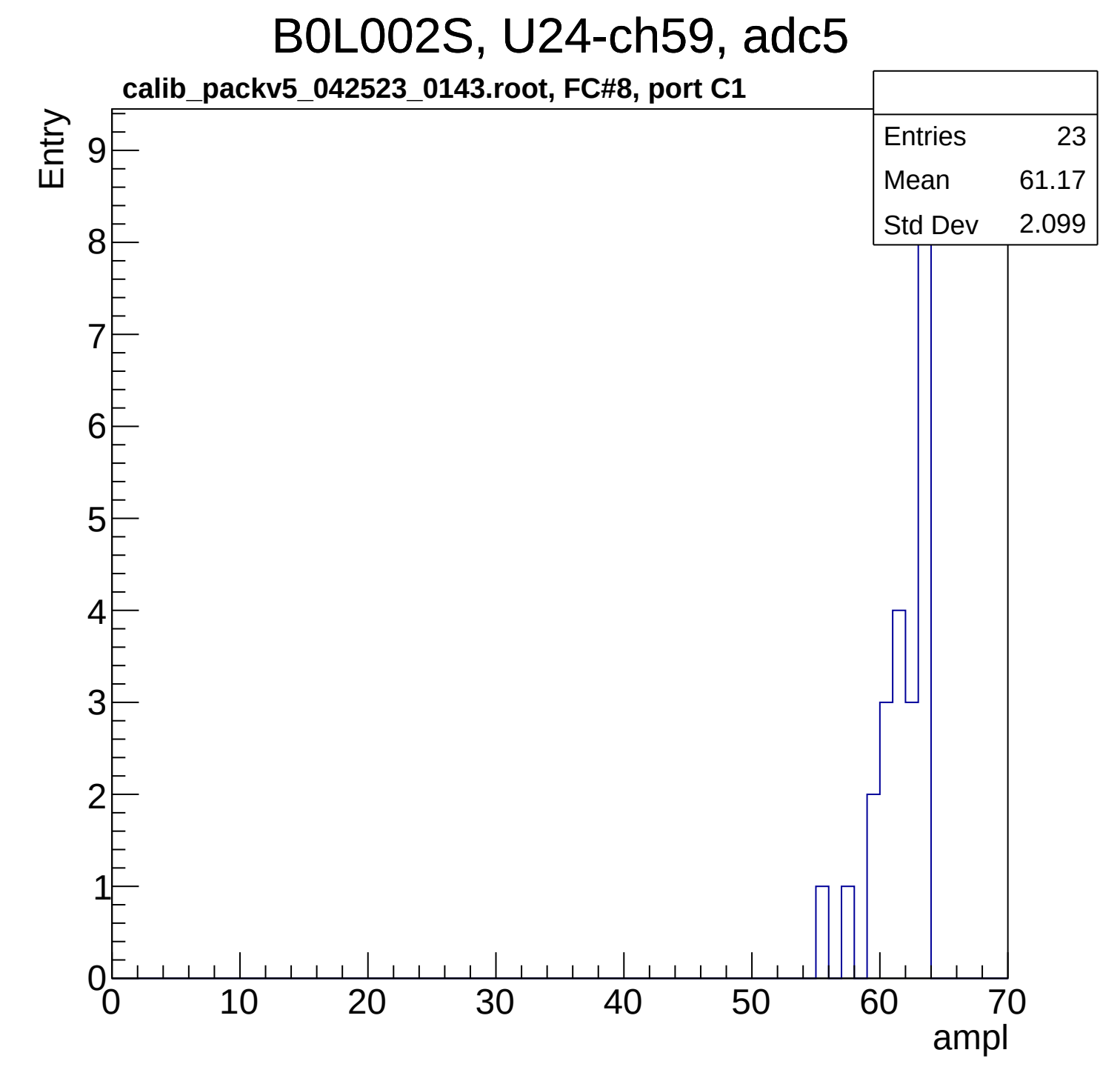
B0L002S, U24-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 61.17
Std Dev 2.099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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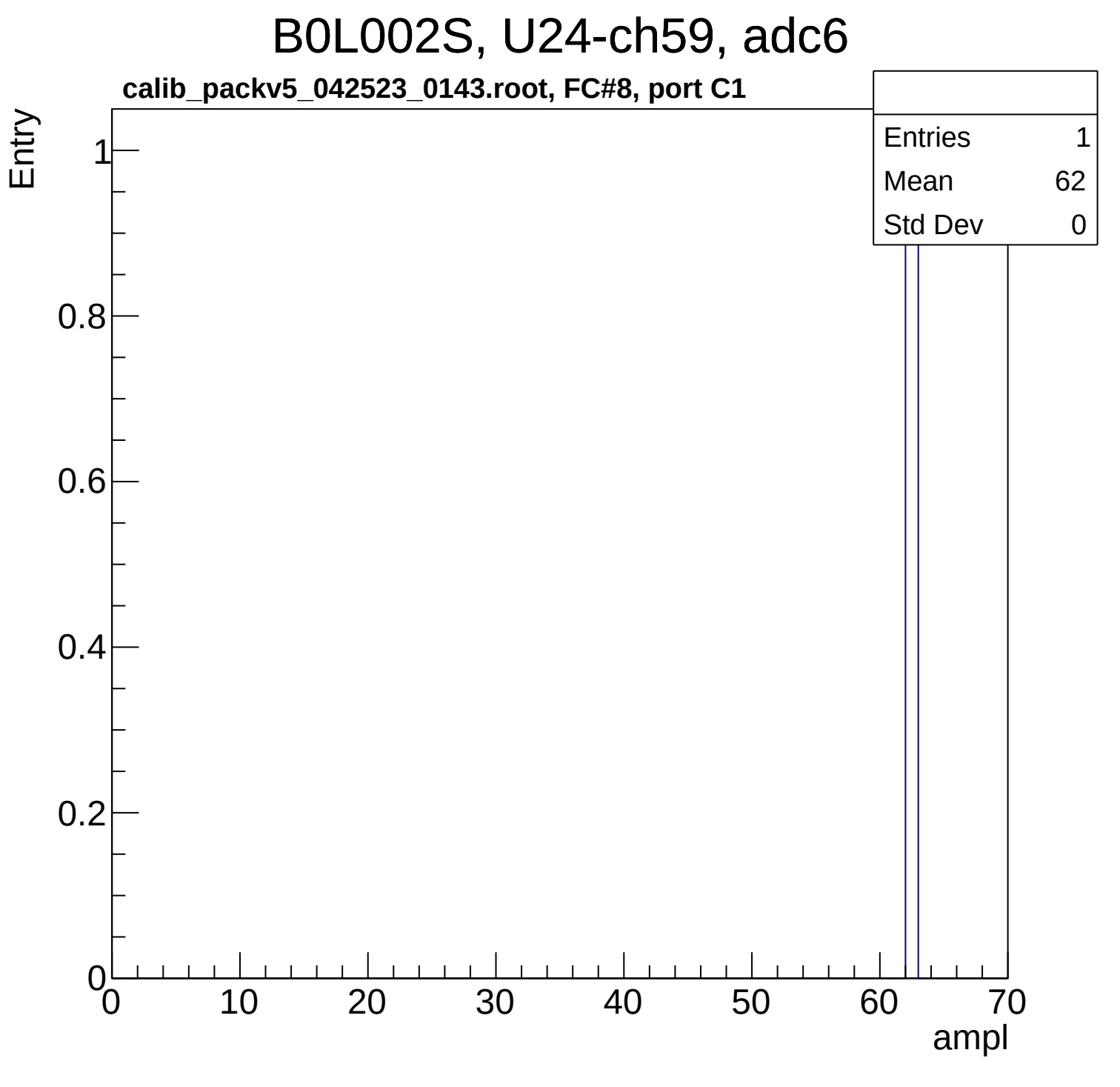
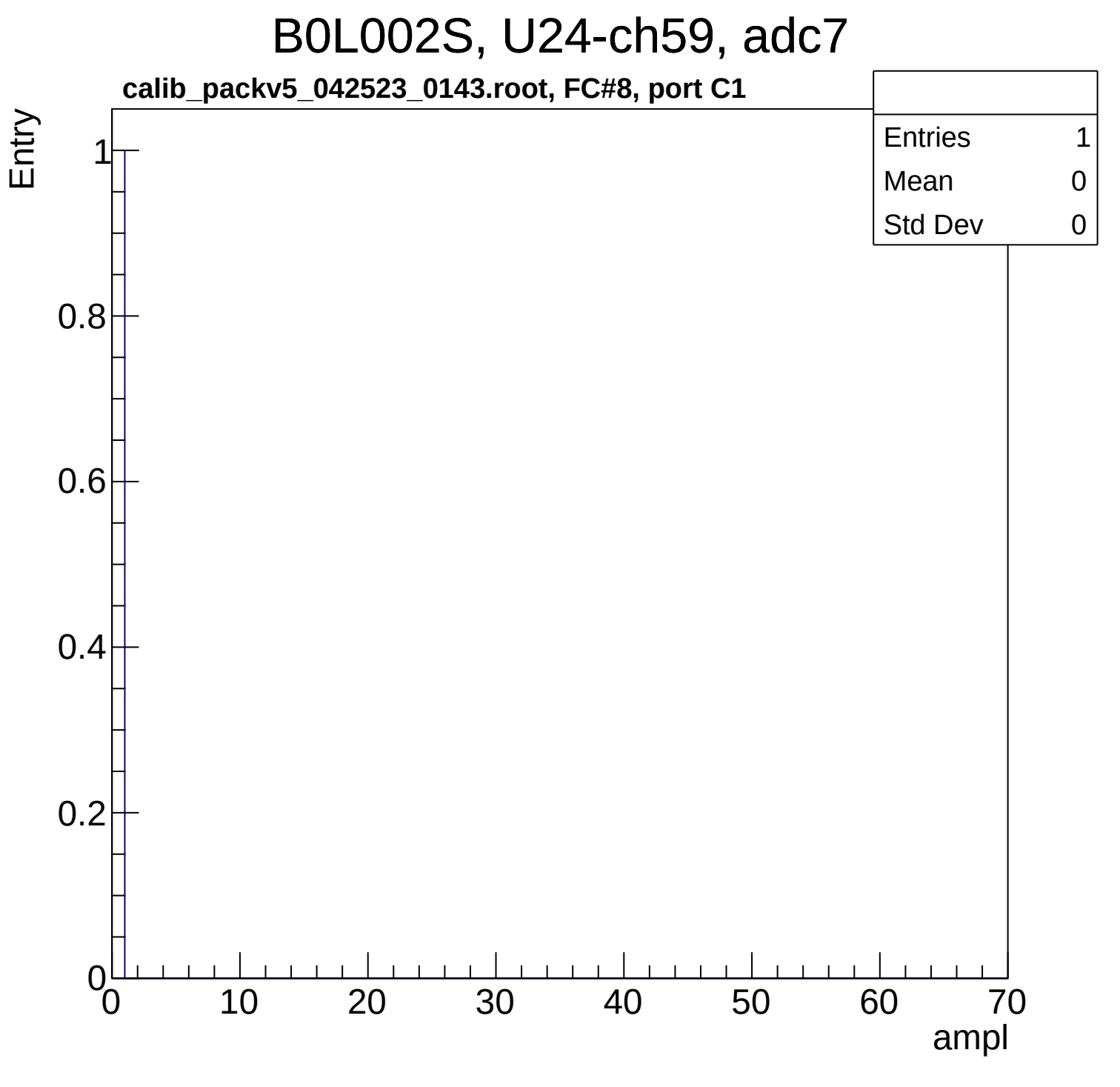

B0L002S, U24-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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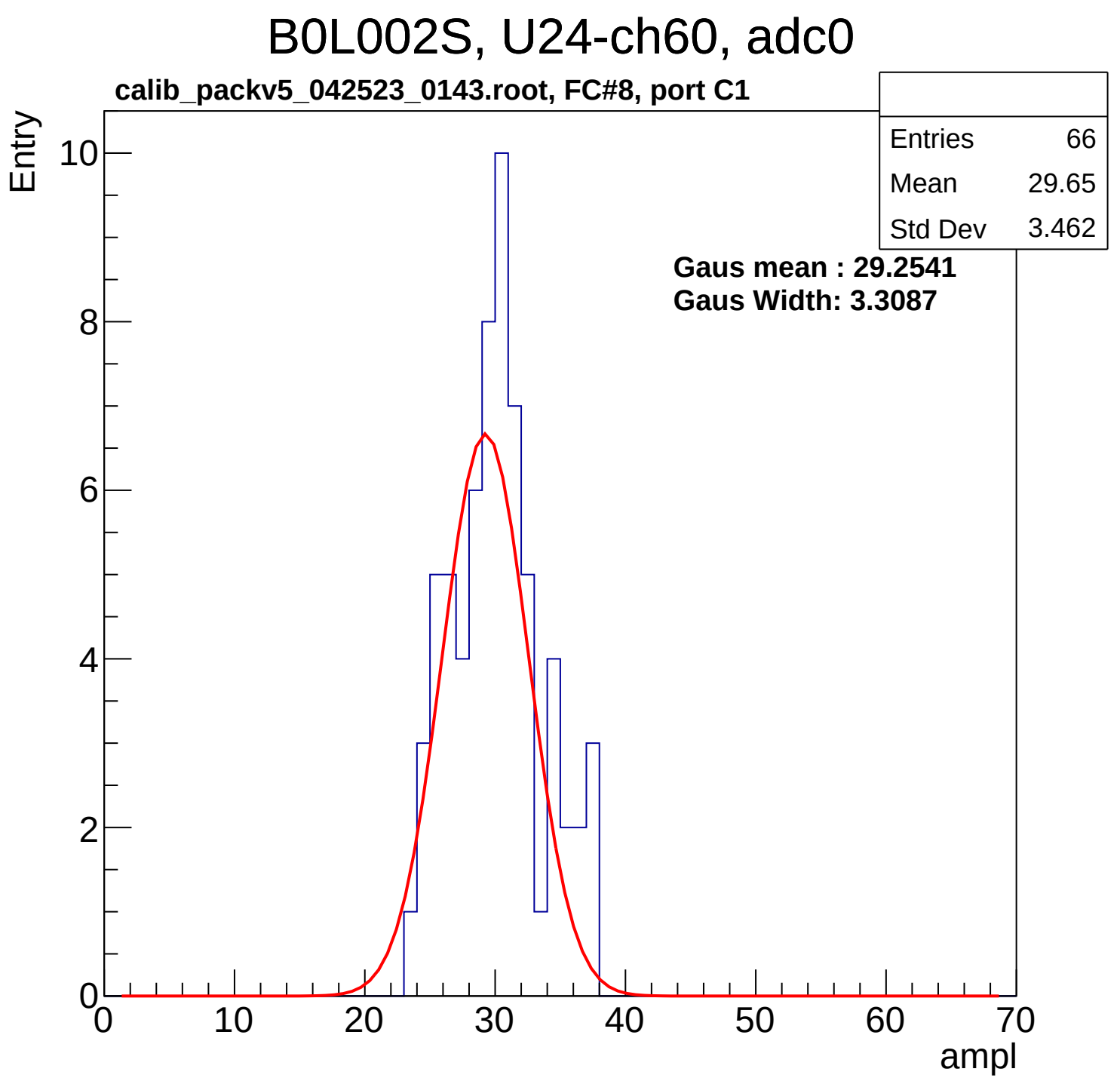
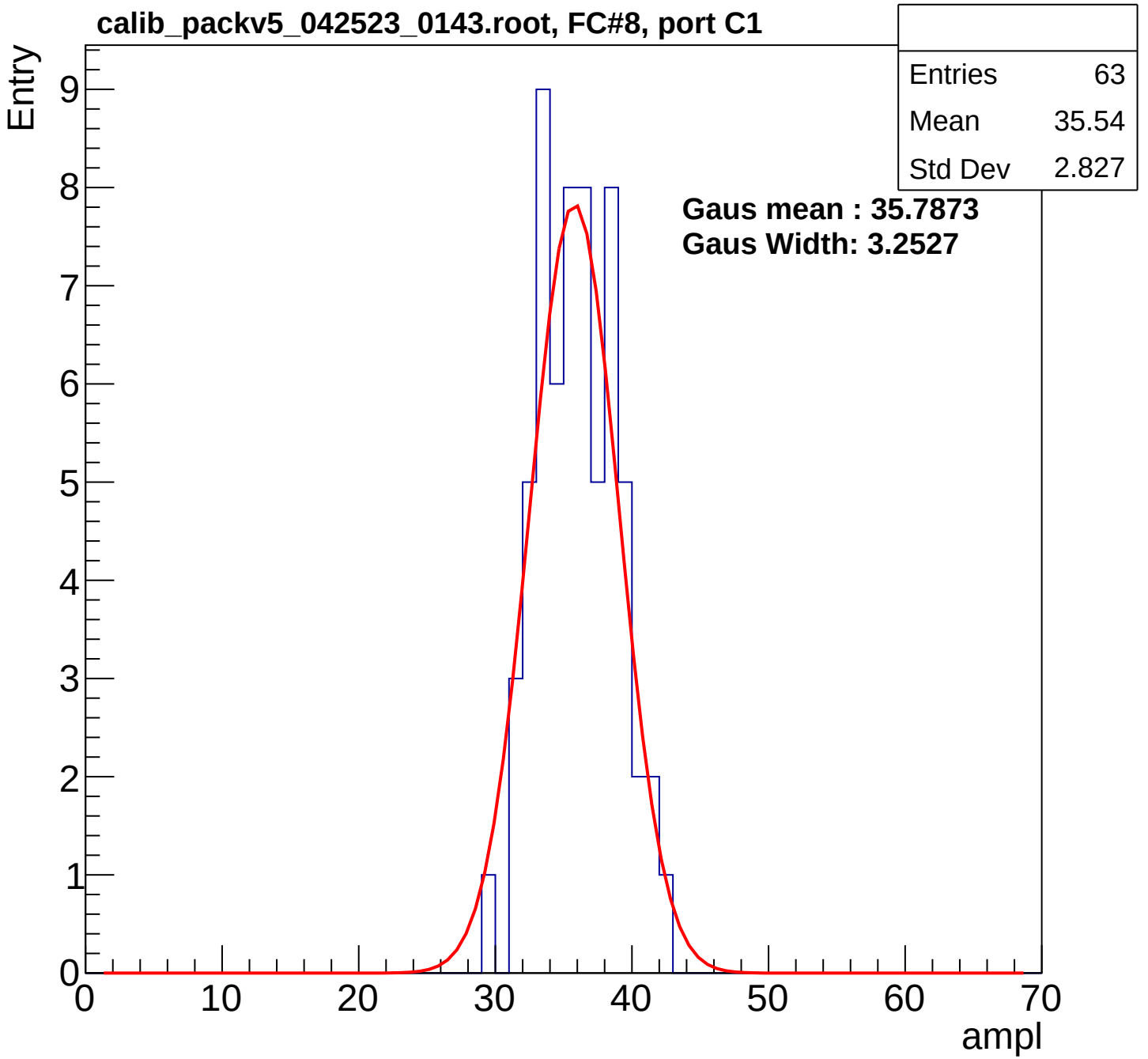
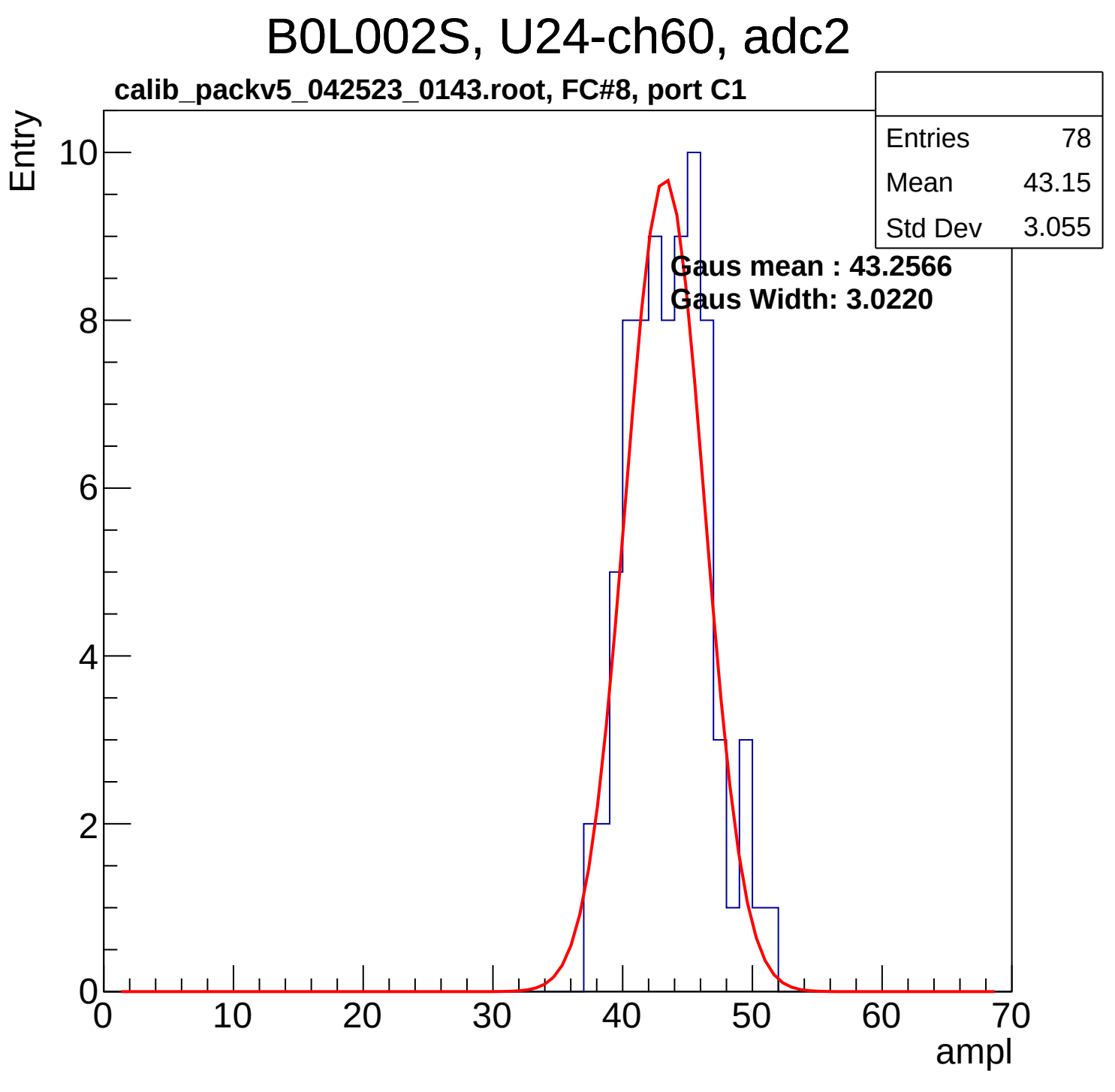
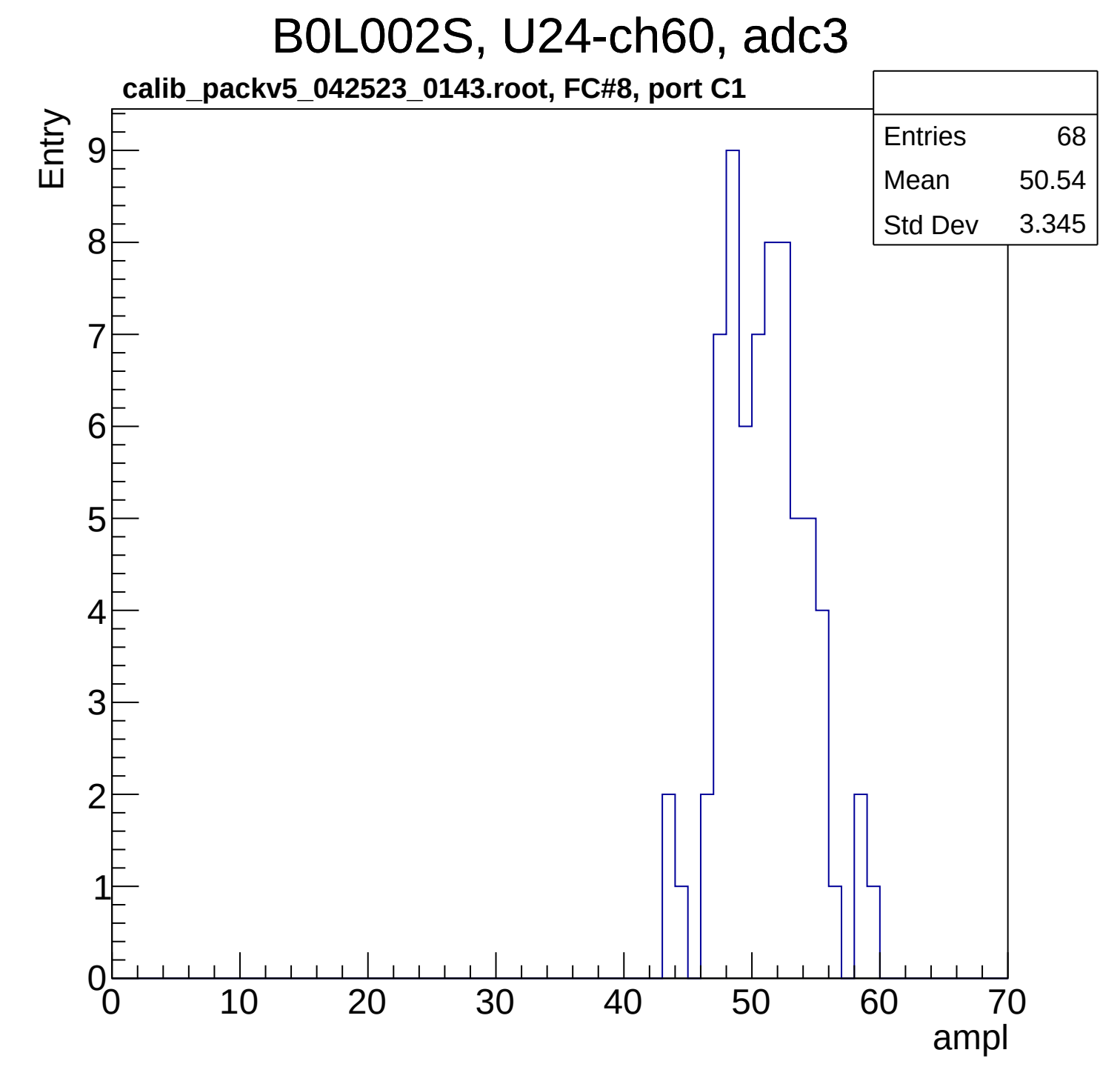
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.65
Std Dev 3.462
Gaus mean : 29.2541
Gaus Width: 3.3087
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 35.54
Std Dev 2.827
Gaus mean : 35.7873
Gaus Width: 3.2527
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 43.15
Std Dev 3.055
Gaus mean : 43.2566
Gaus Width: 3.0220
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.54
Std Dev 3.345
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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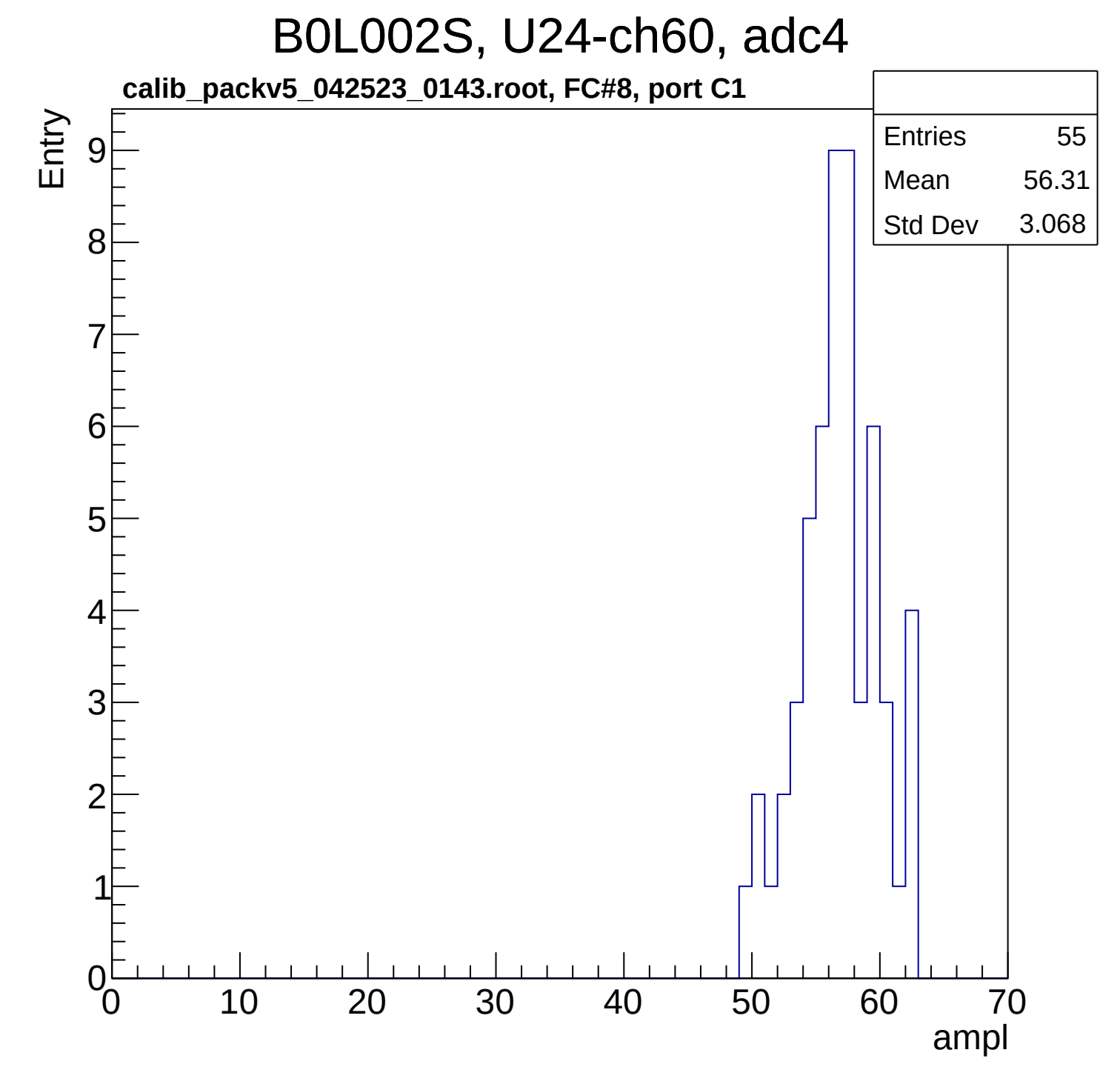
B0L002S, U24-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.31
Std Dev 3.068
Entry
ampl
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6
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0
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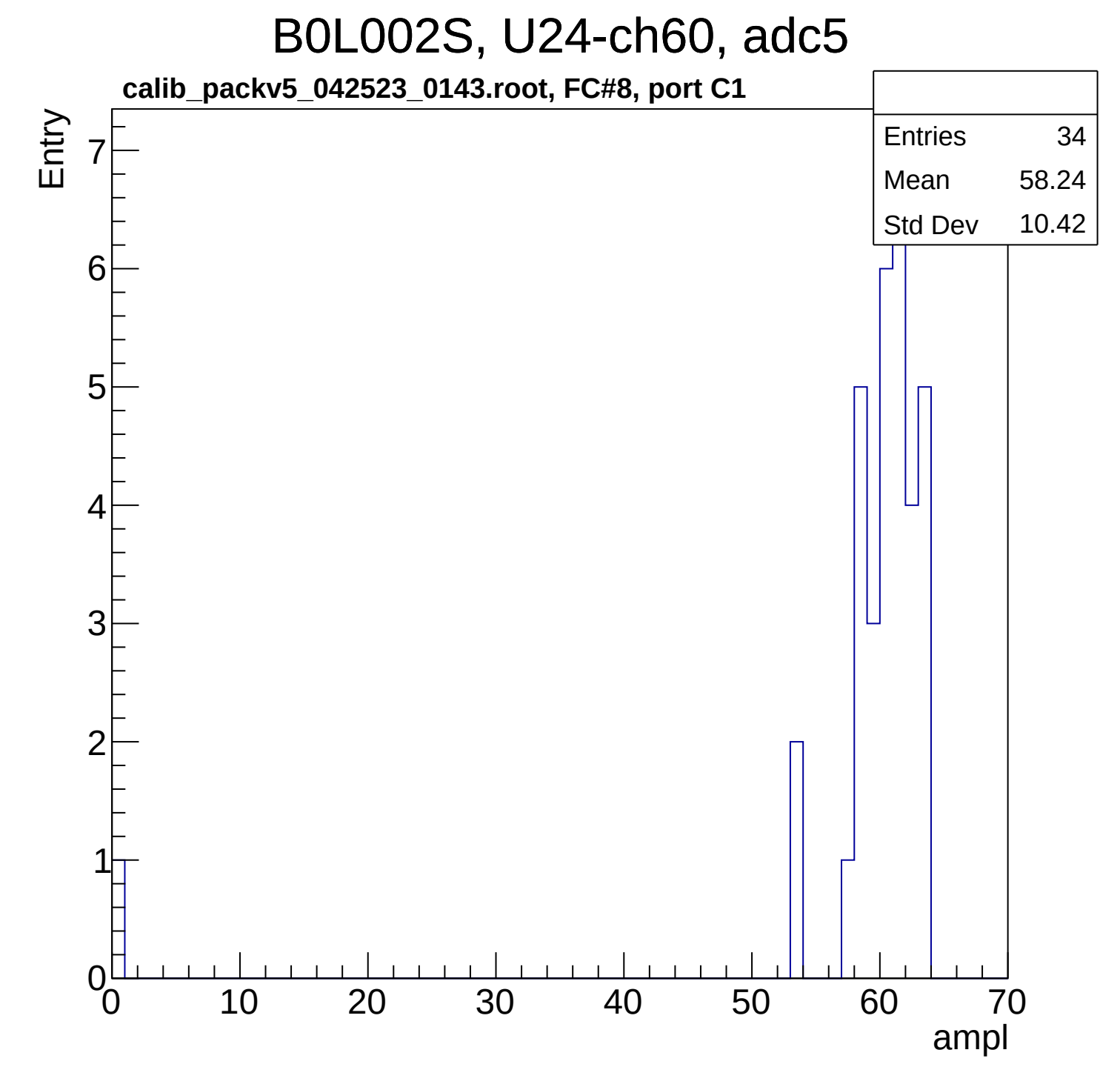
B0L002S, U24-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.24
Std Dev 10.42
Entry
ampl
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0
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40
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60
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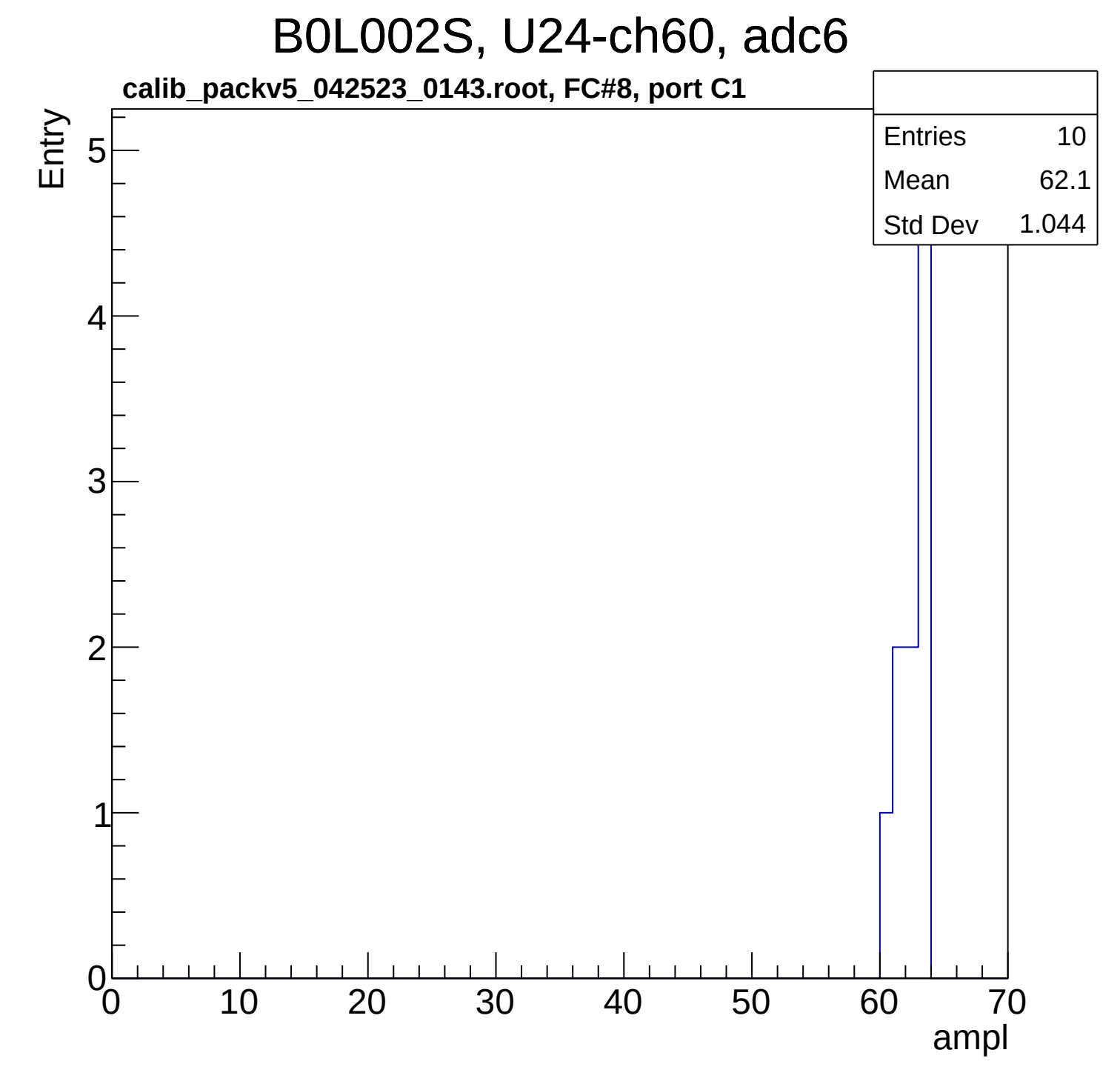
B0L002S, U24-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 62.1
Std Dev 1.044
Entry
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1
2
3
4
5
0
10
20
30
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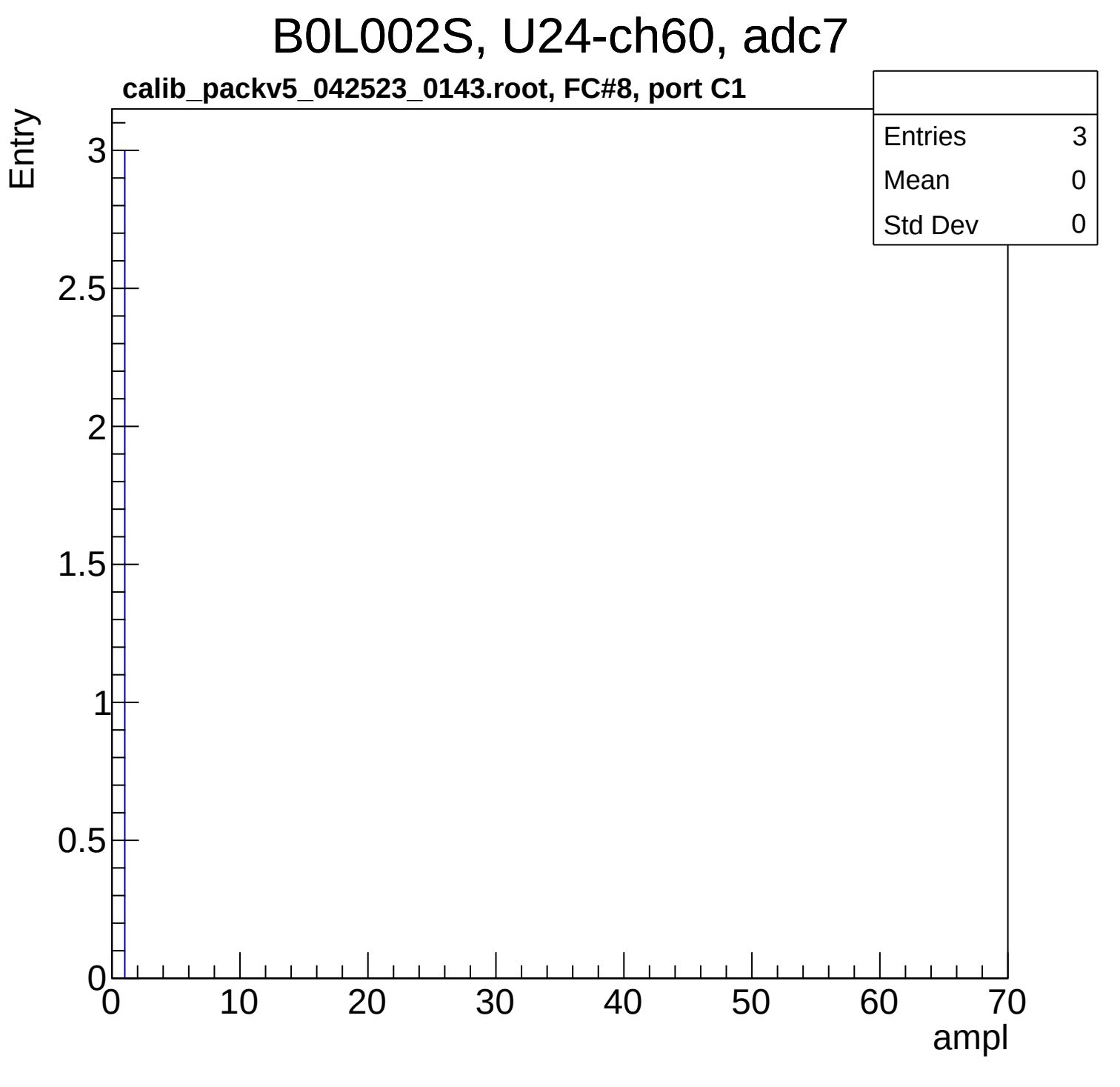
50
60
70
ampl


B0L002S, U24-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
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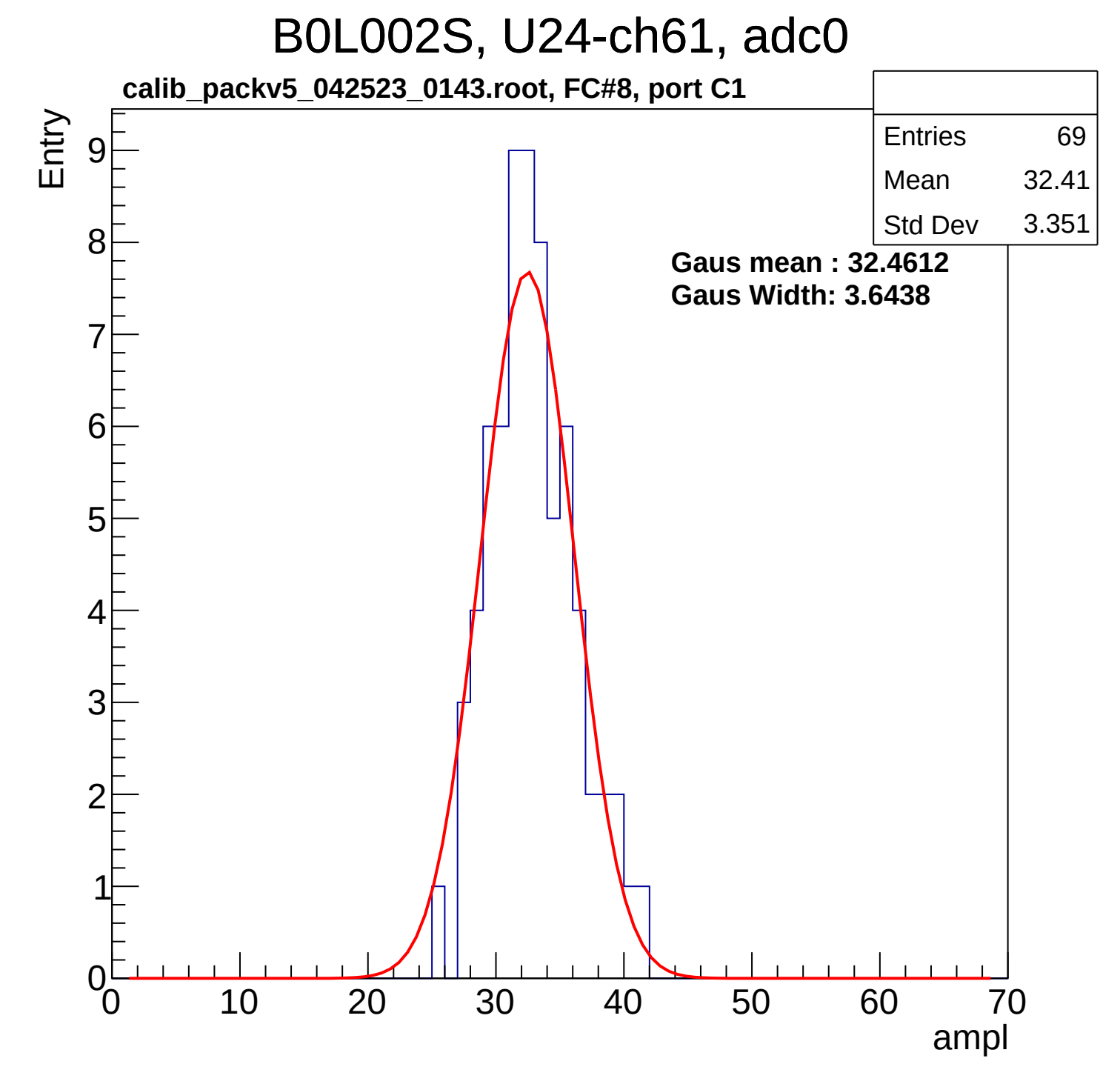
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 32.41
Std Dev 3.351
Gaus mean : 32.4612
Gaus Width: 3.6438
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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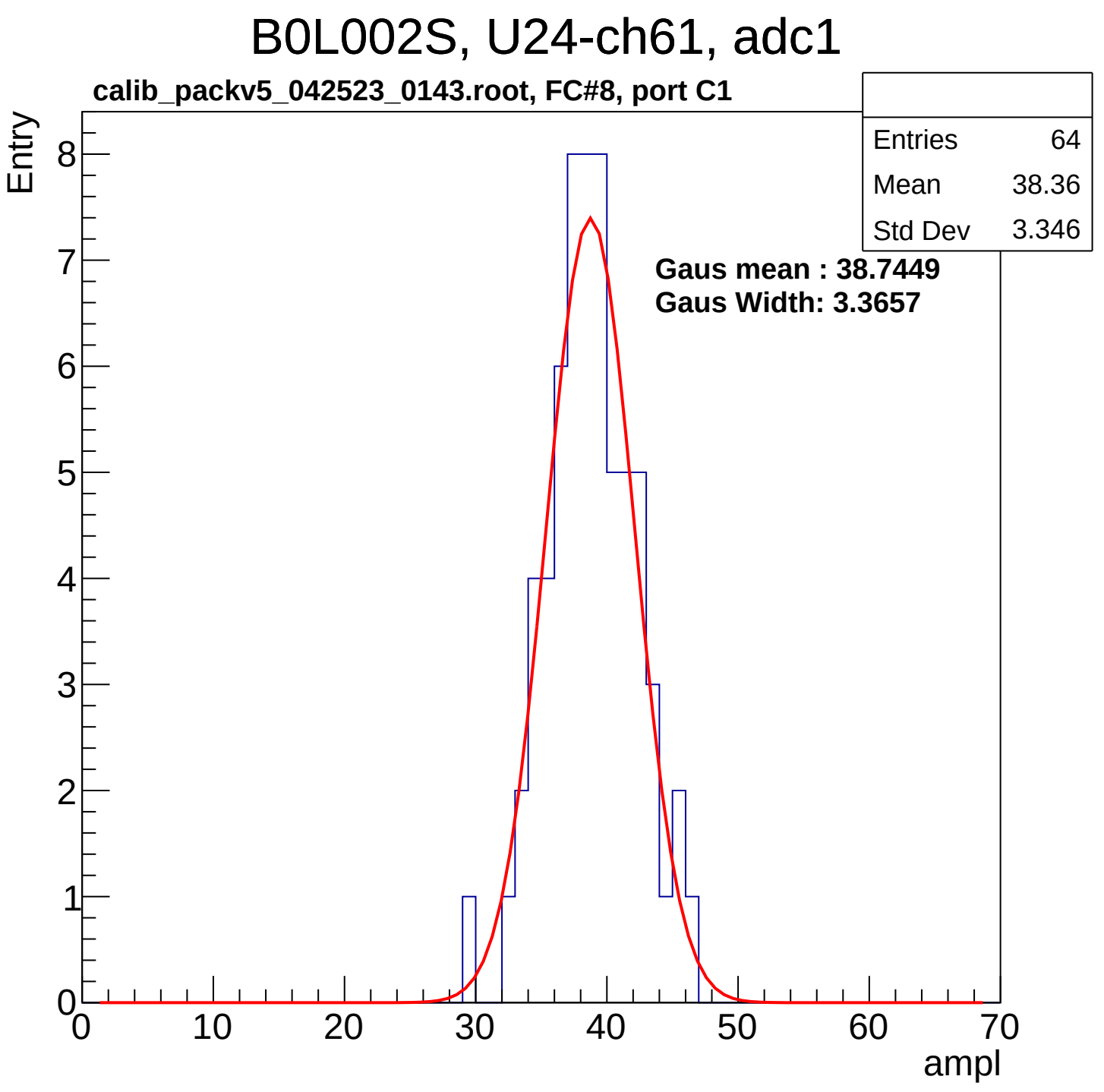
B0L002S, U24-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.36
Std Dev 3.346
Gaus mean : 38.7449
Gaus Width: 3.3657
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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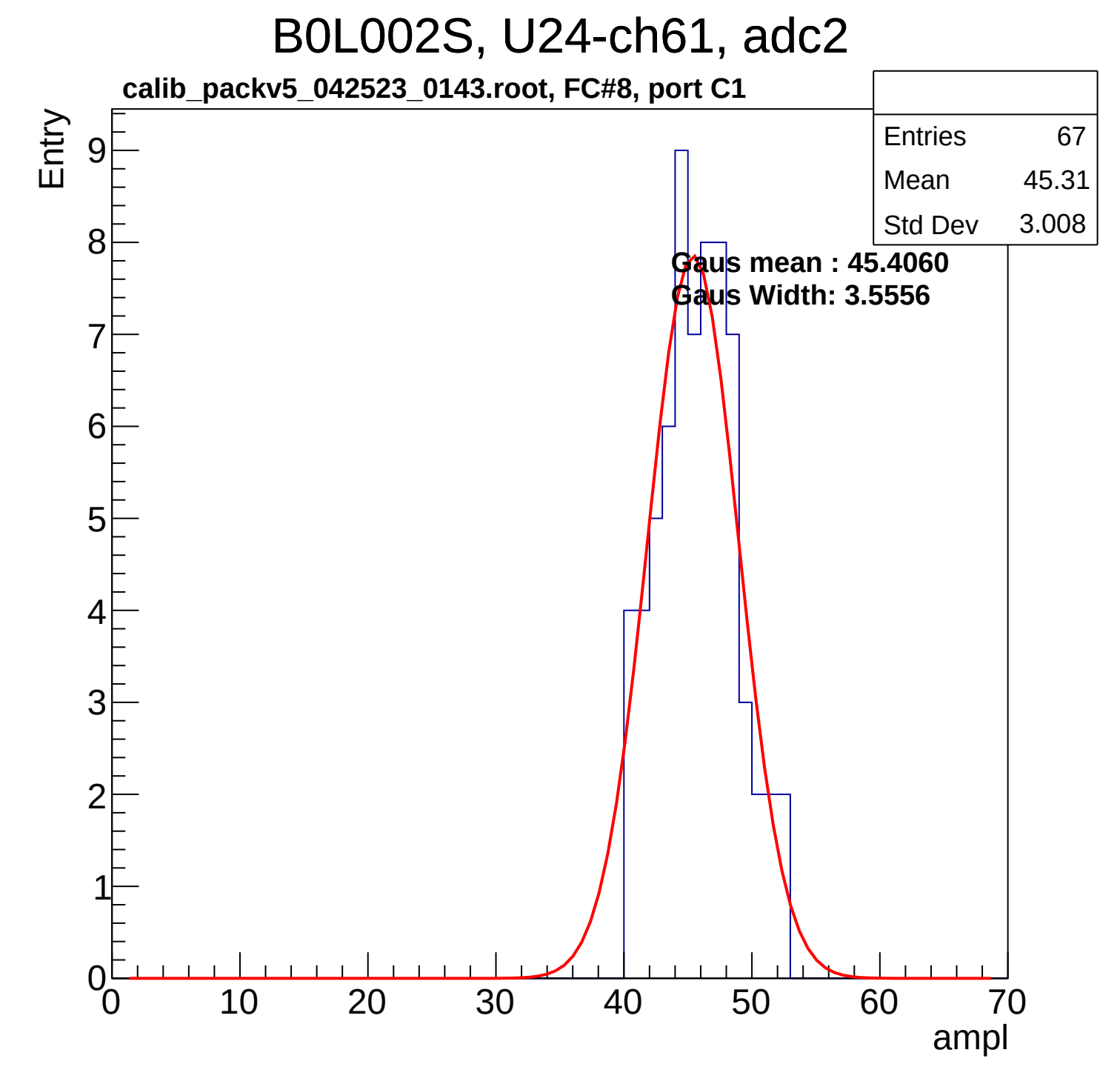
B0L002S, U24-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.31
Std Dev 3.008
Gaus mean : 45.4060
Gaus Width: 3.5556
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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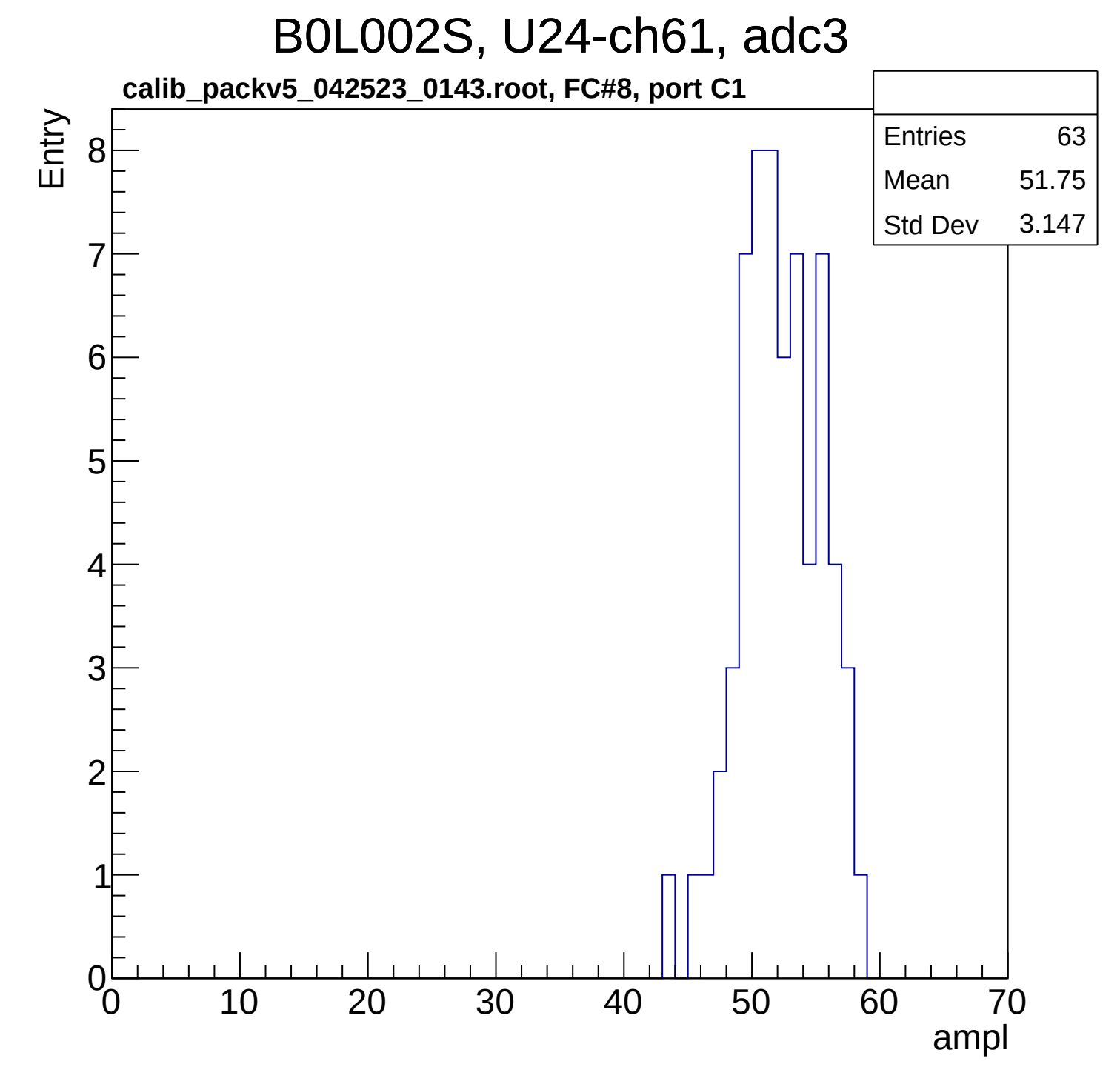
B0L002S, U24-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.75
Std Dev 3.147
Entry
ampl
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2
3
4
5
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0
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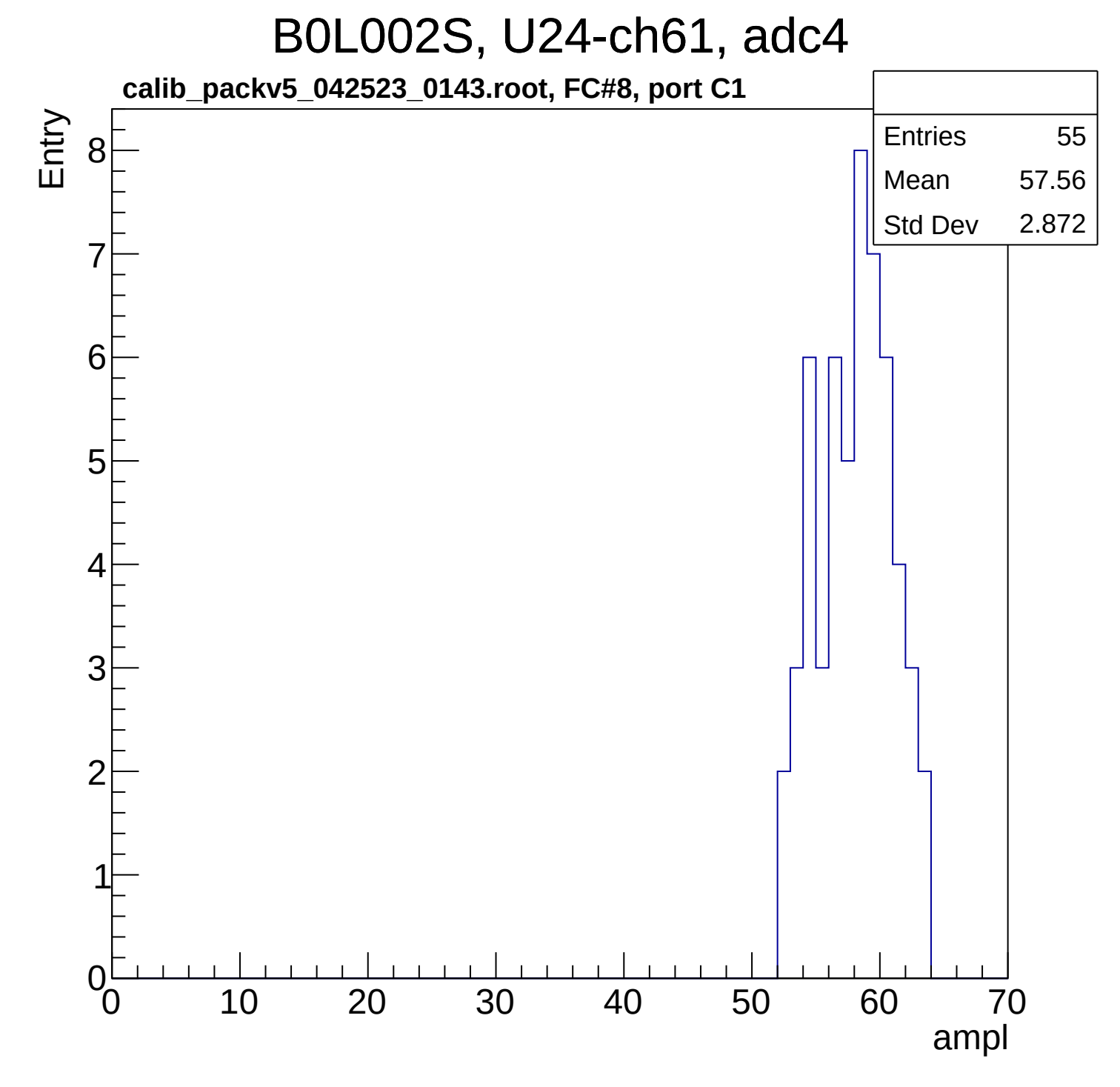
B0L002S, U24-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.56
Std Dev 2.872
Entry
ampl
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2
3
4
5
6
7
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40
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60
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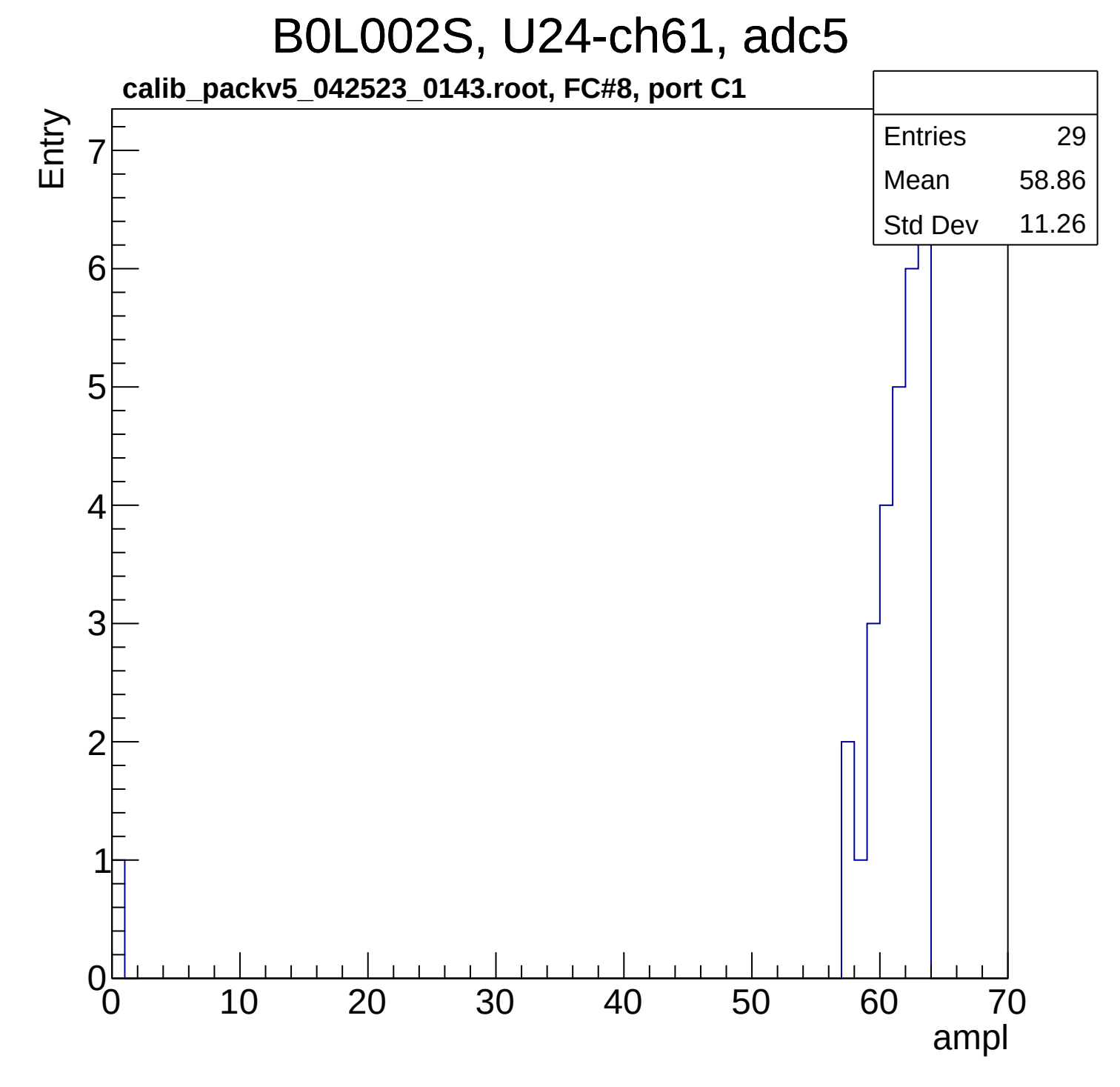
B0L002S, U24-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.86
Std Dev 11.26
Entry
ampl
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0
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40
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60
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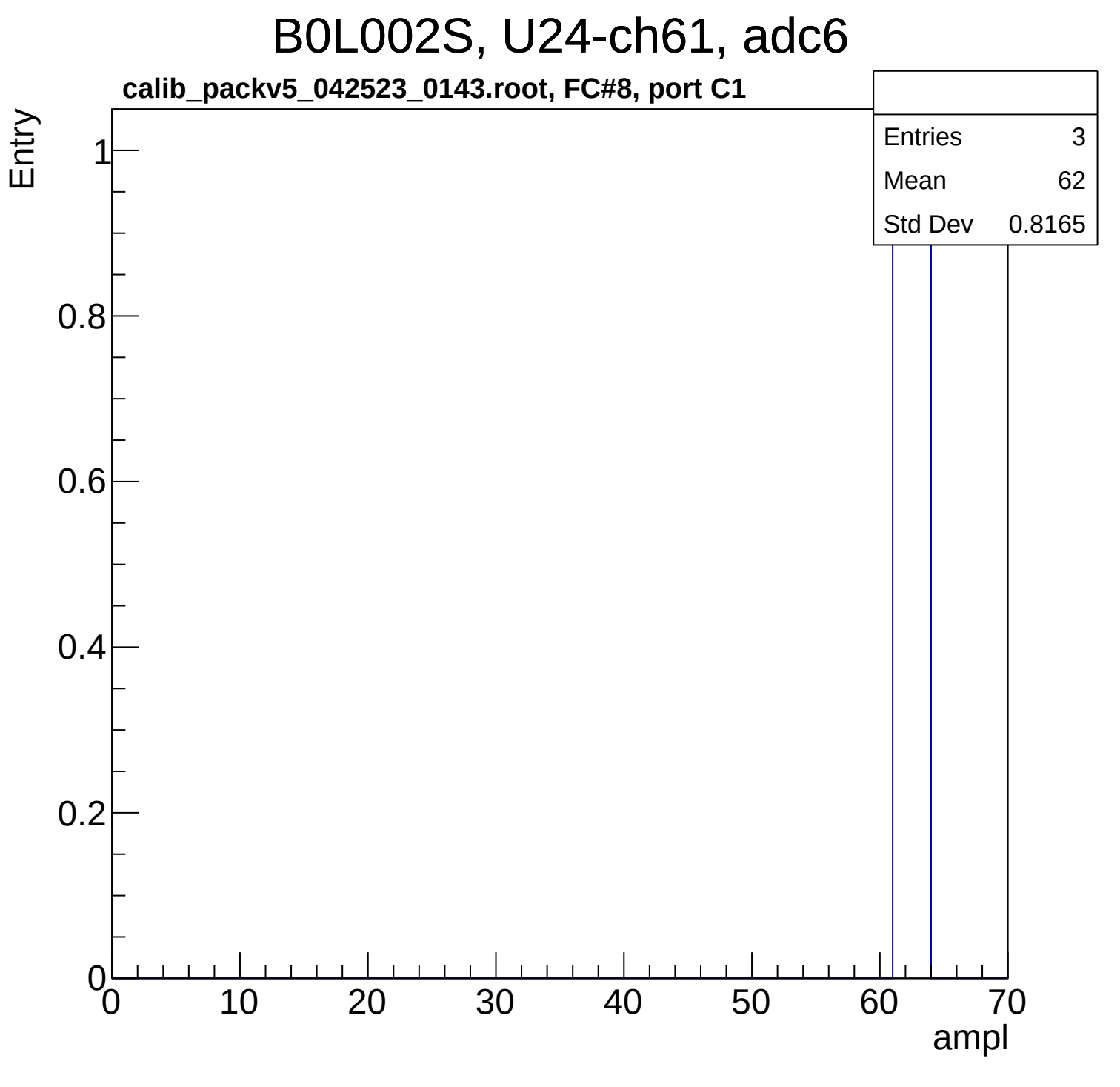
B0L002S, U24-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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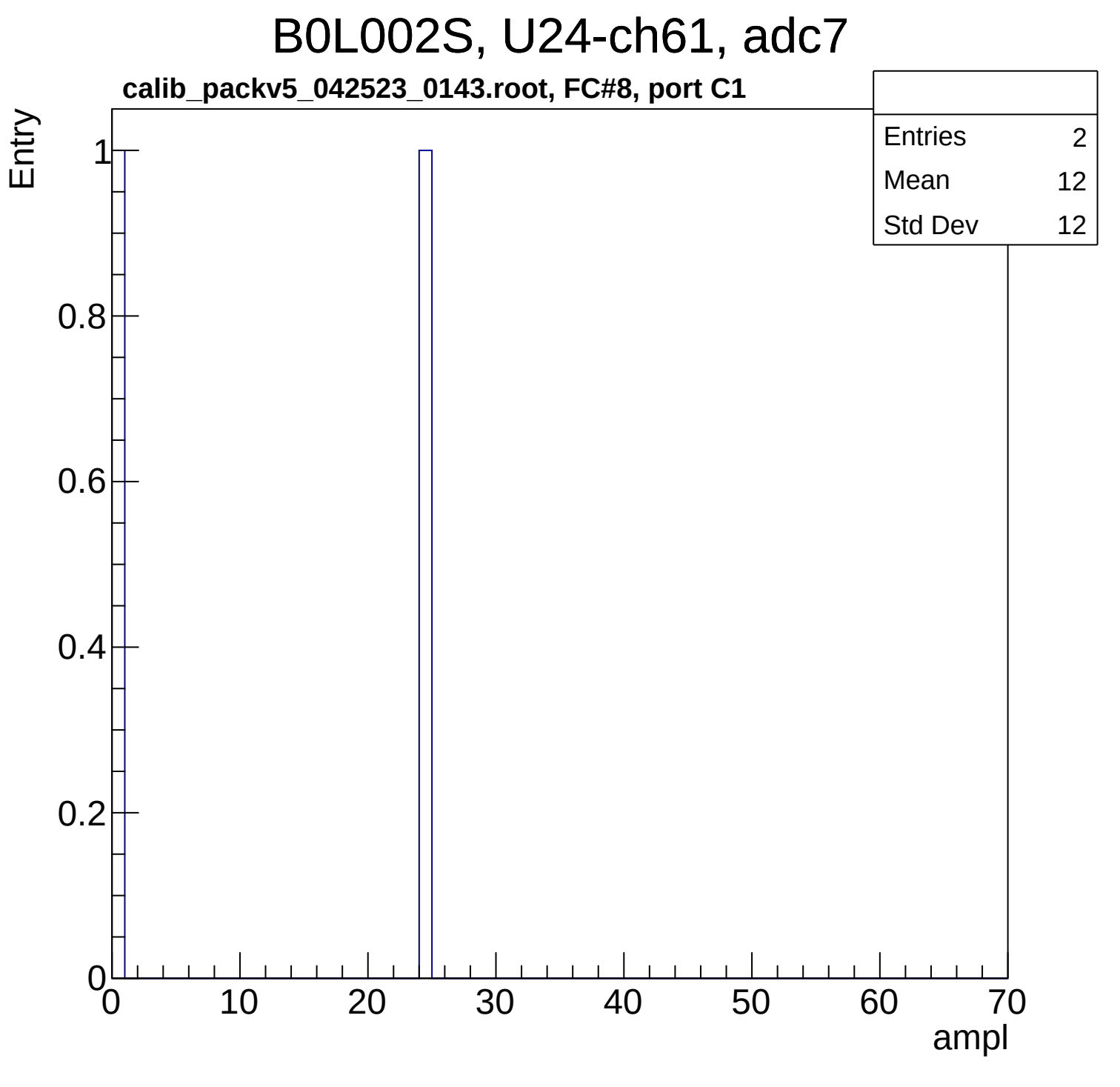
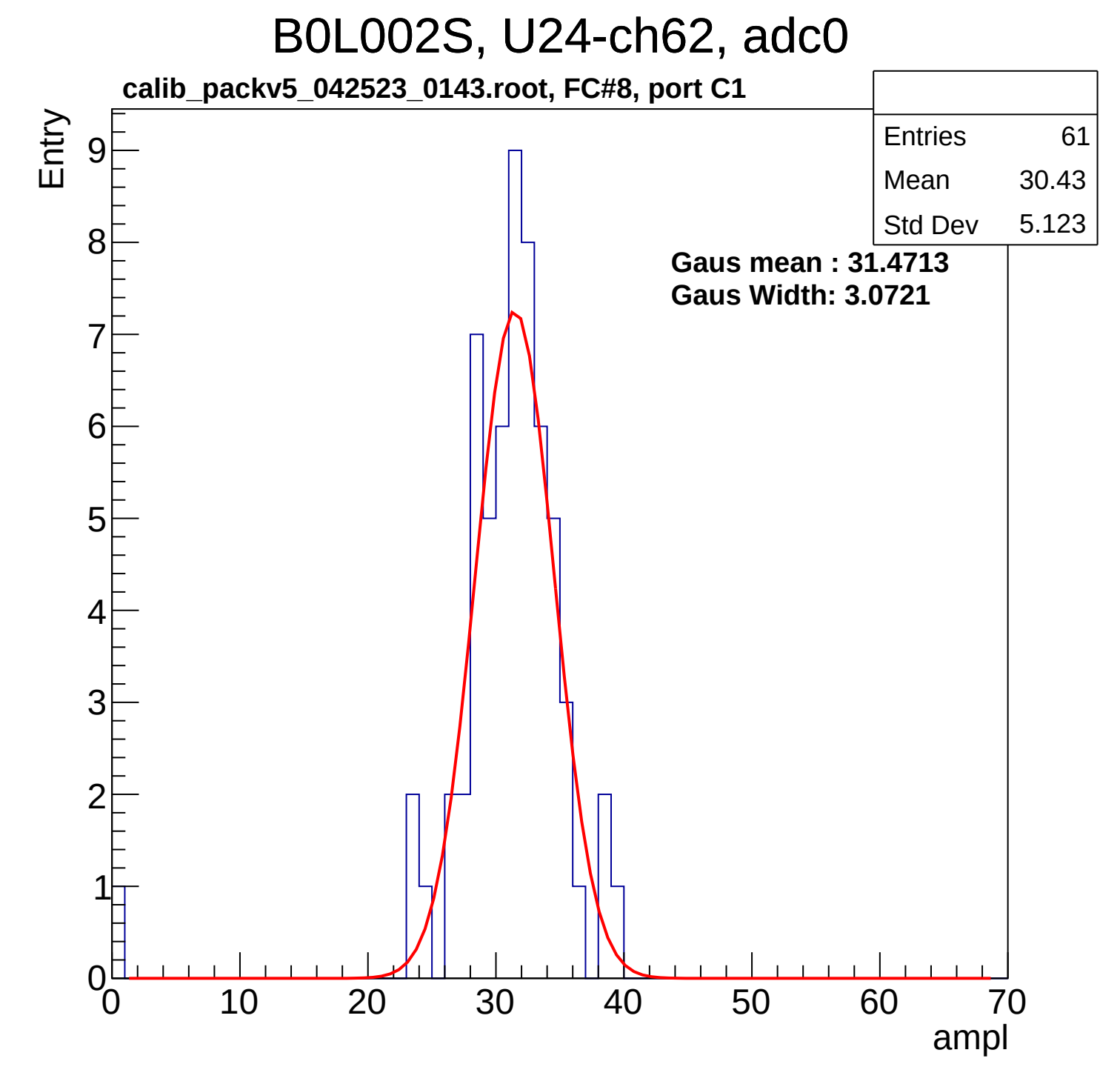
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.43
Std Dev 5.123
Gaus mean : 31.4713
Gaus Width: 3.0721
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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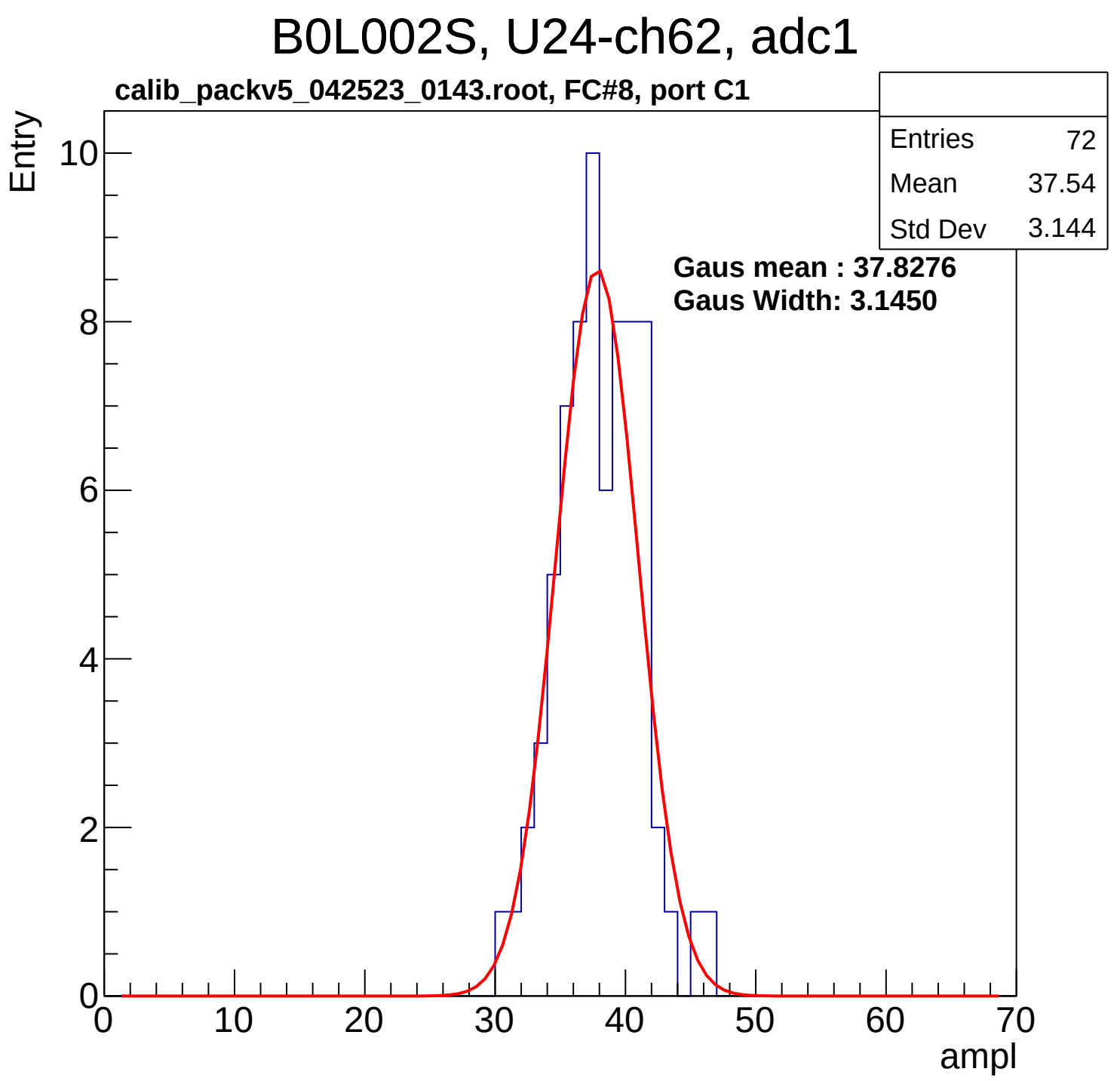
B0L002S, U24-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.54
Std Dev 3.144
Gaus mean : 37.8276
Gaus Width: 3.1450
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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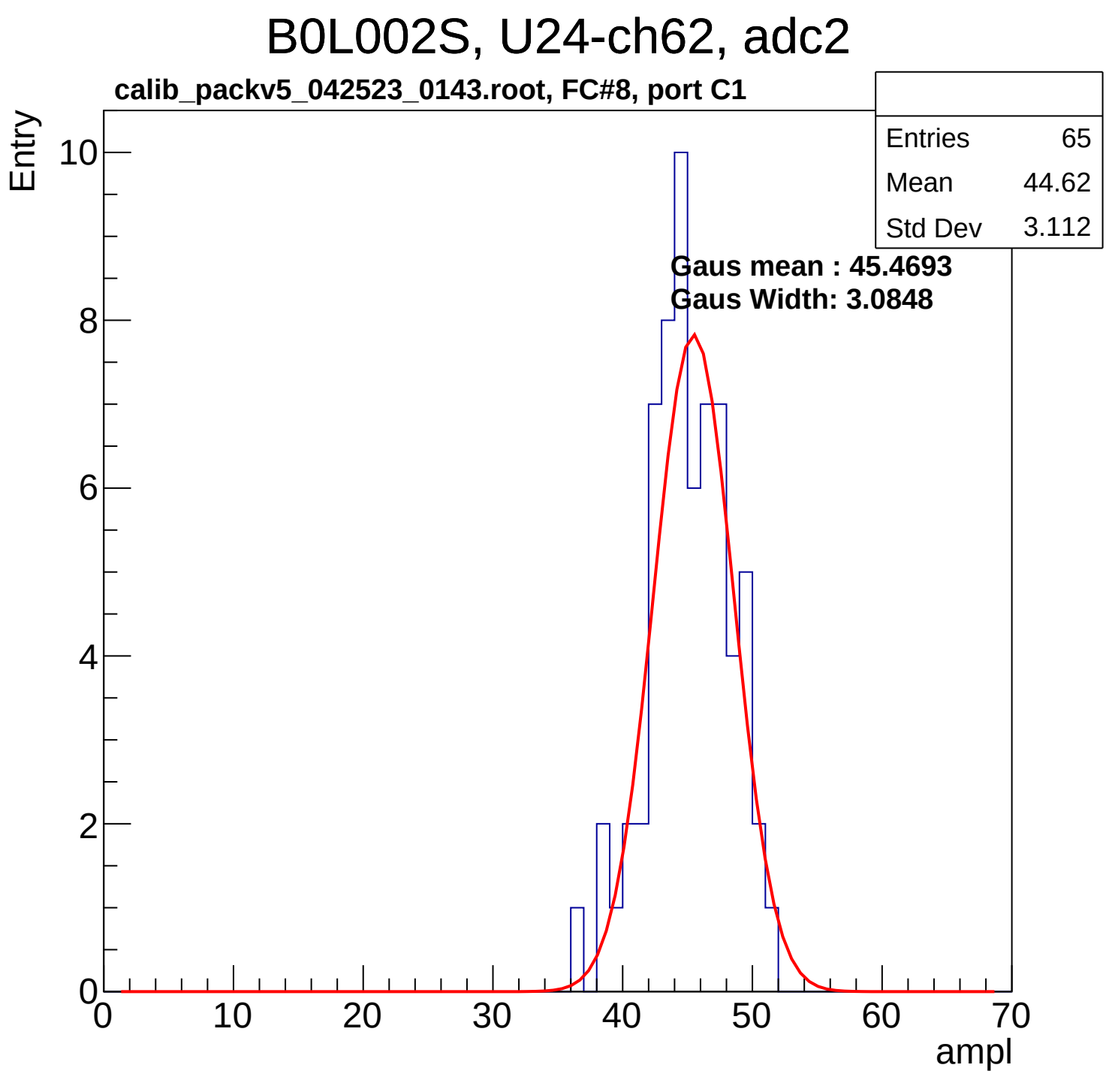
B0L002S, U24-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.62
Std Dev 3.112
Gaus mean : 45.4693
Gaus Width: 3.0848
Entry
ampl
0
2
4
6
8
10
0
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60
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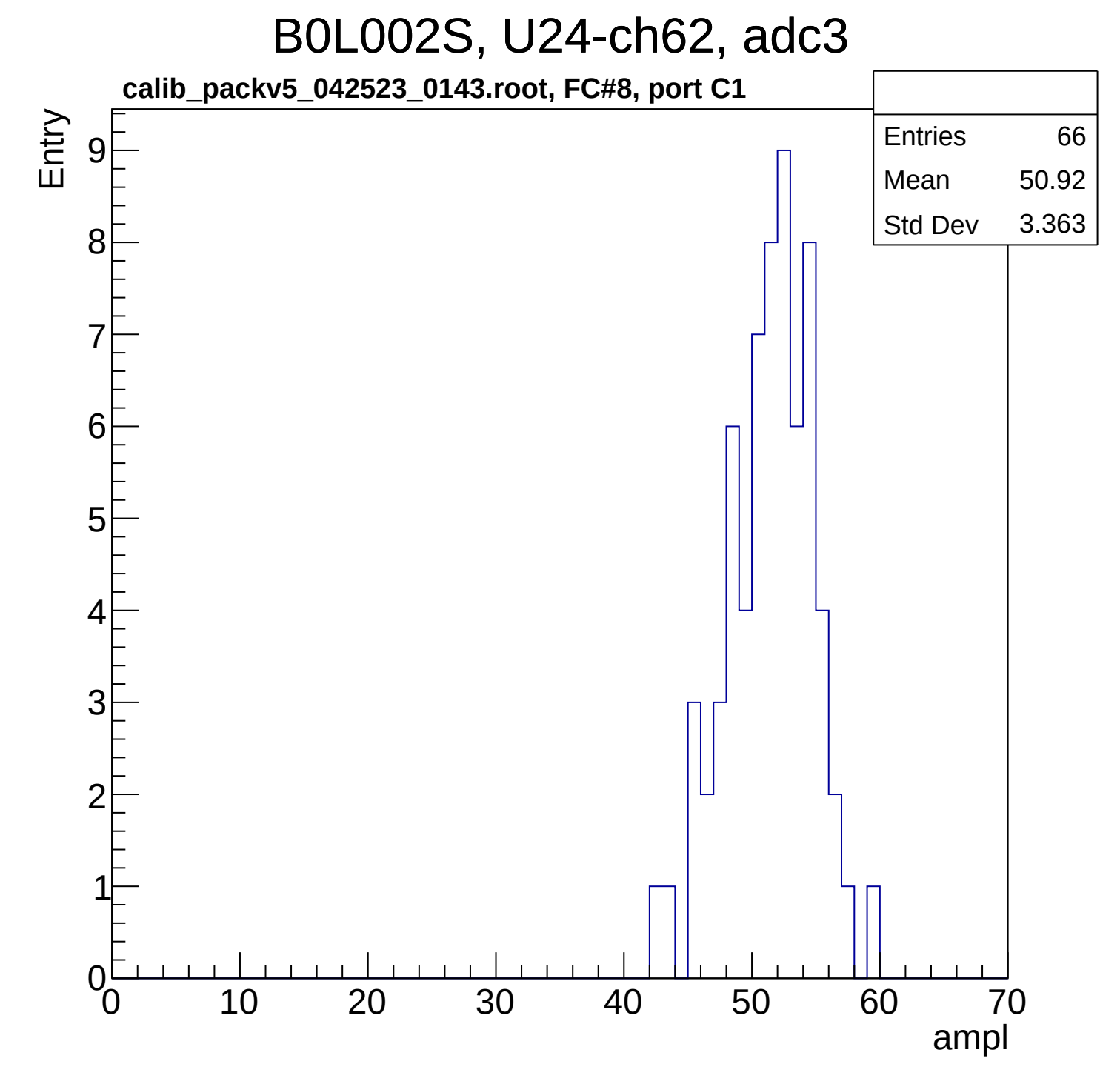
B0L002S, U24-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.92
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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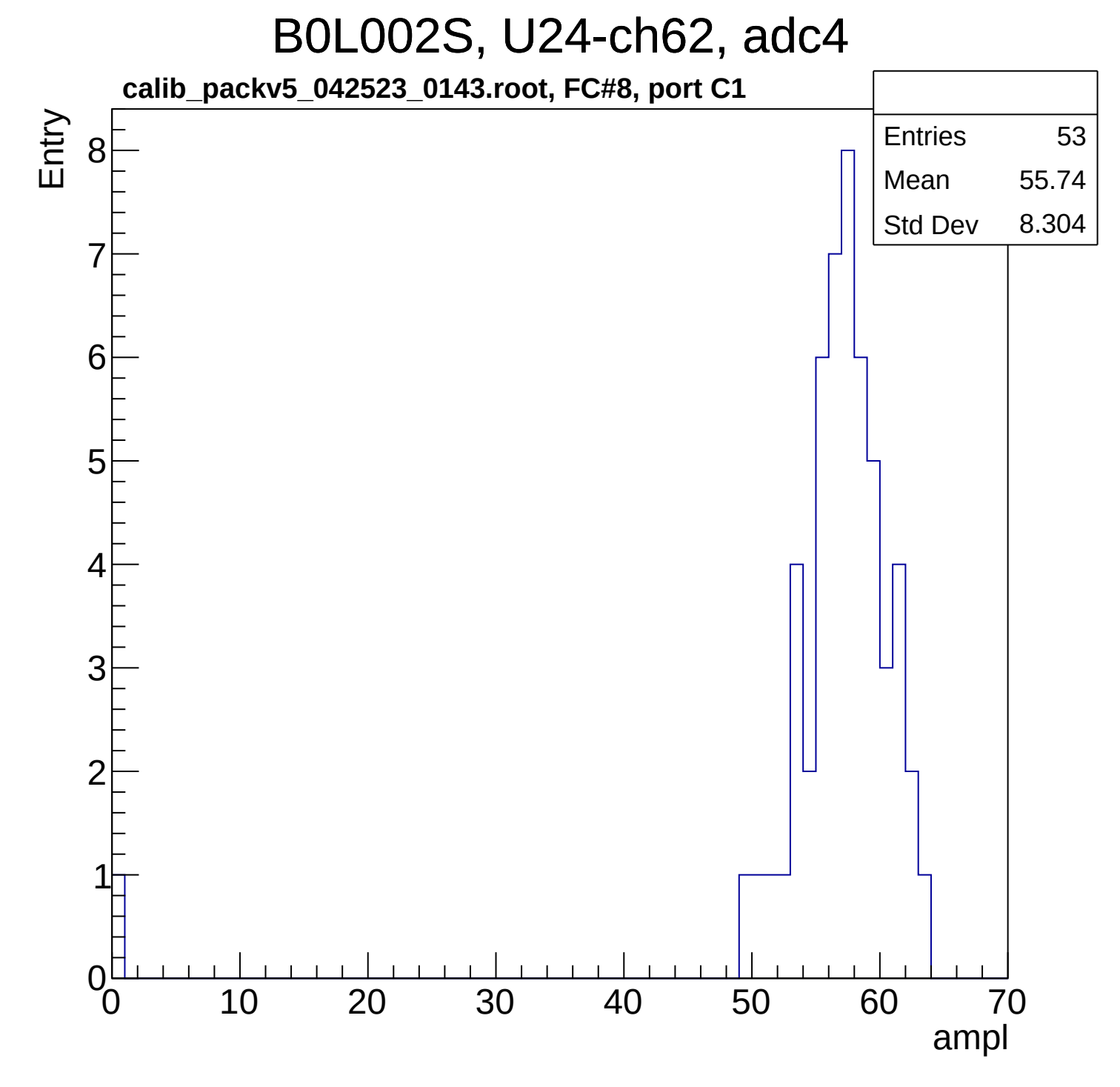
B0L002S, U24-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 55.74
Std Dev 8.304
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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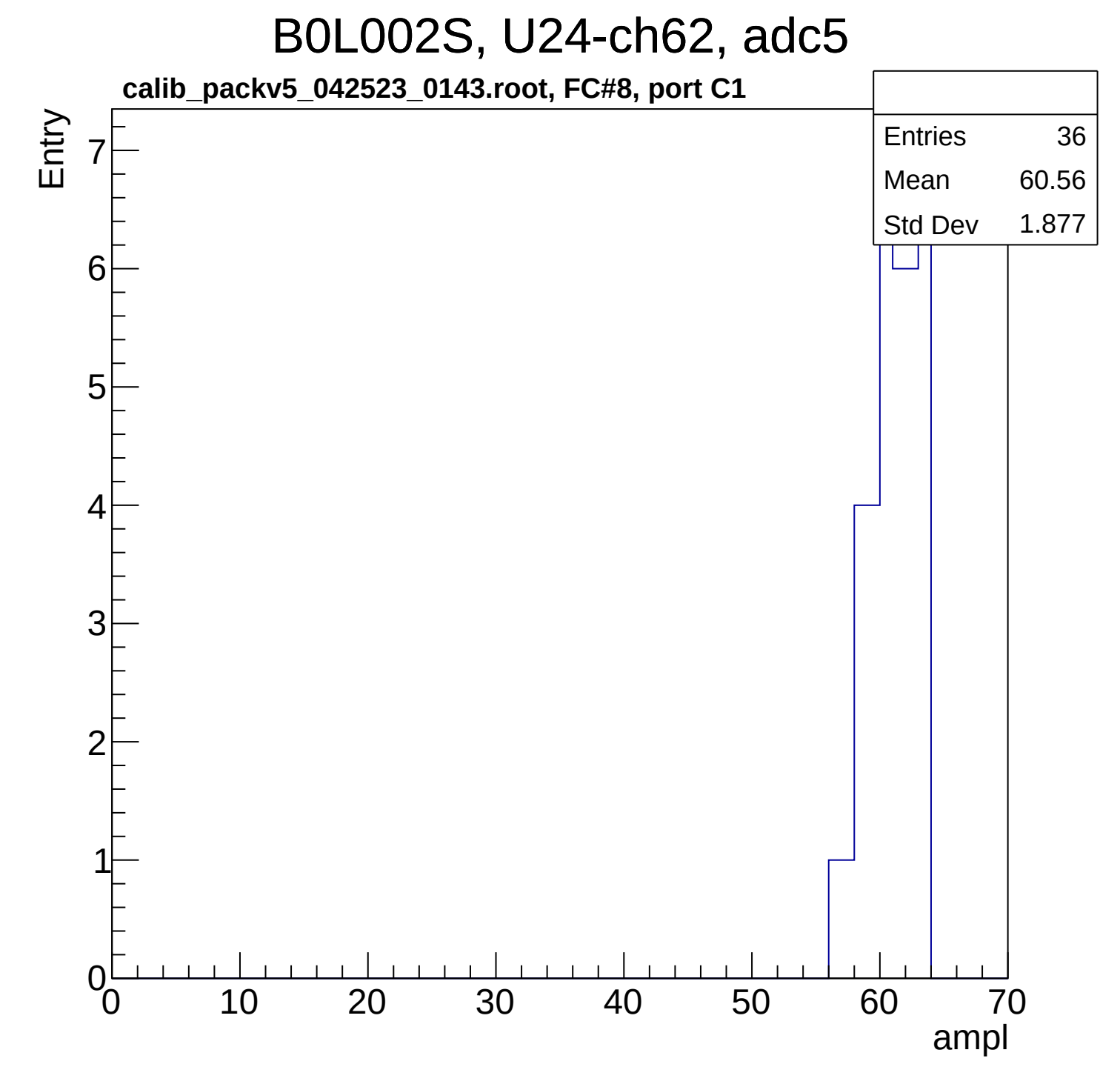
B0L002S, U24-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 60.56
Std Dev 1.877
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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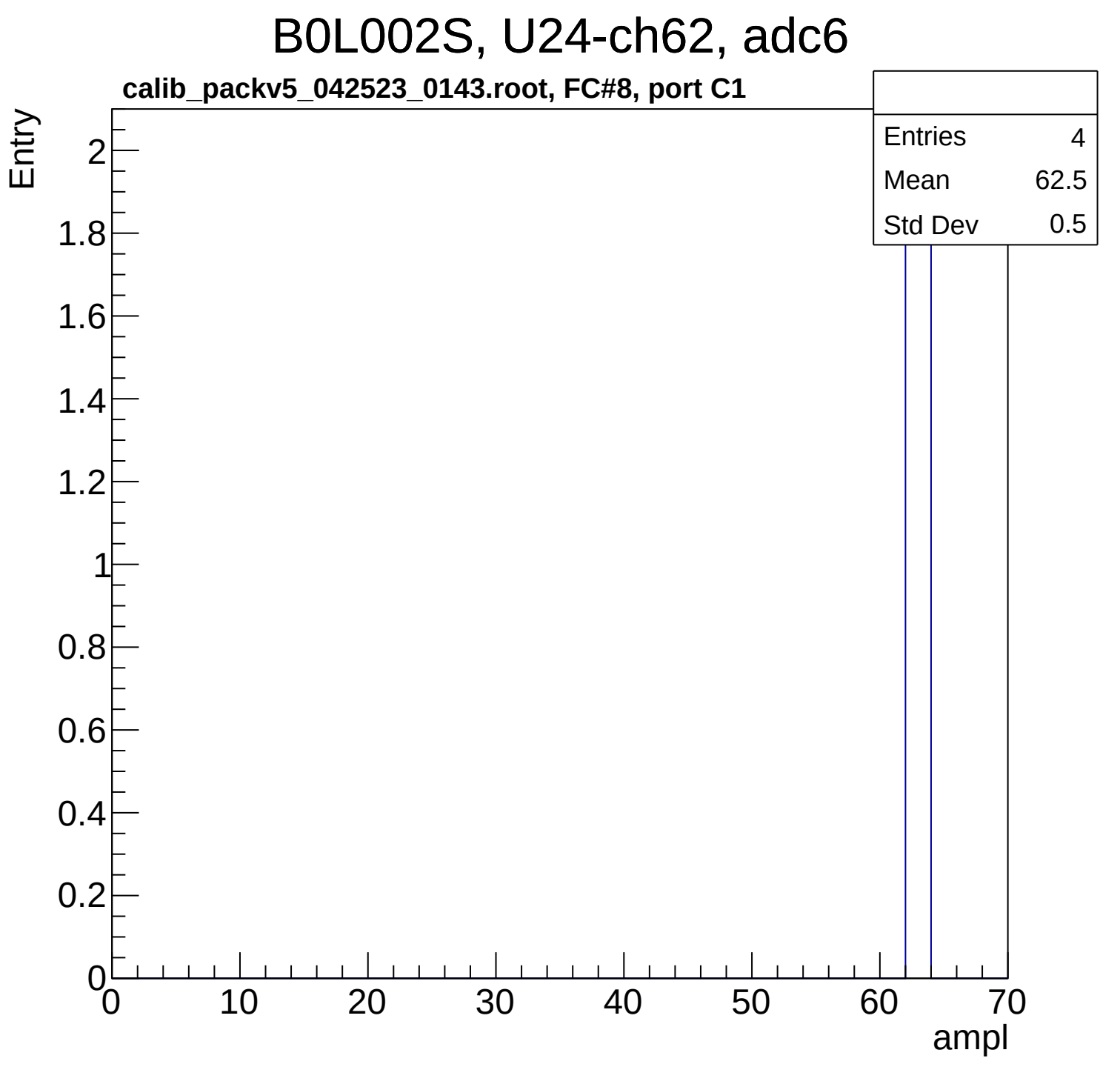
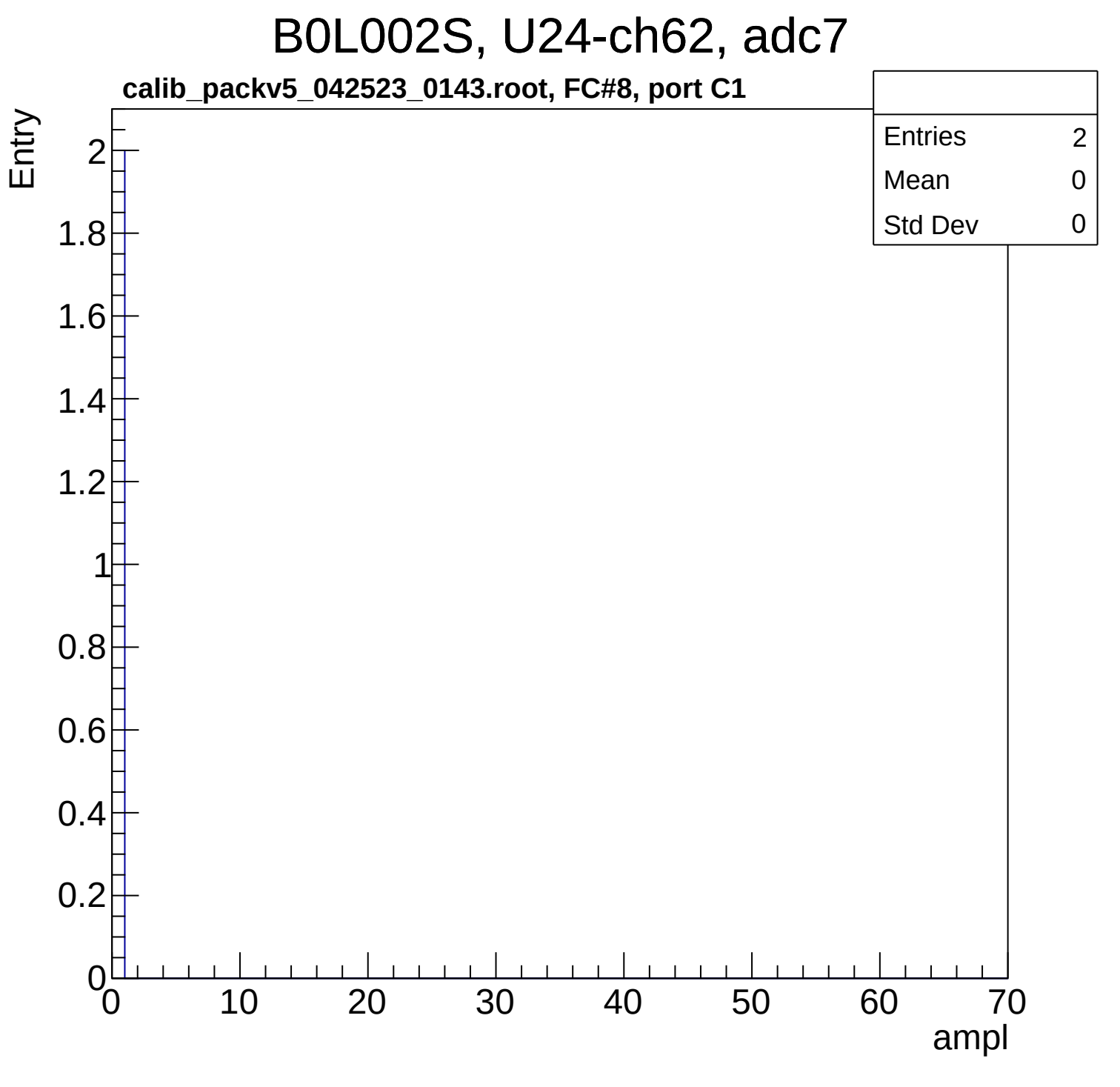
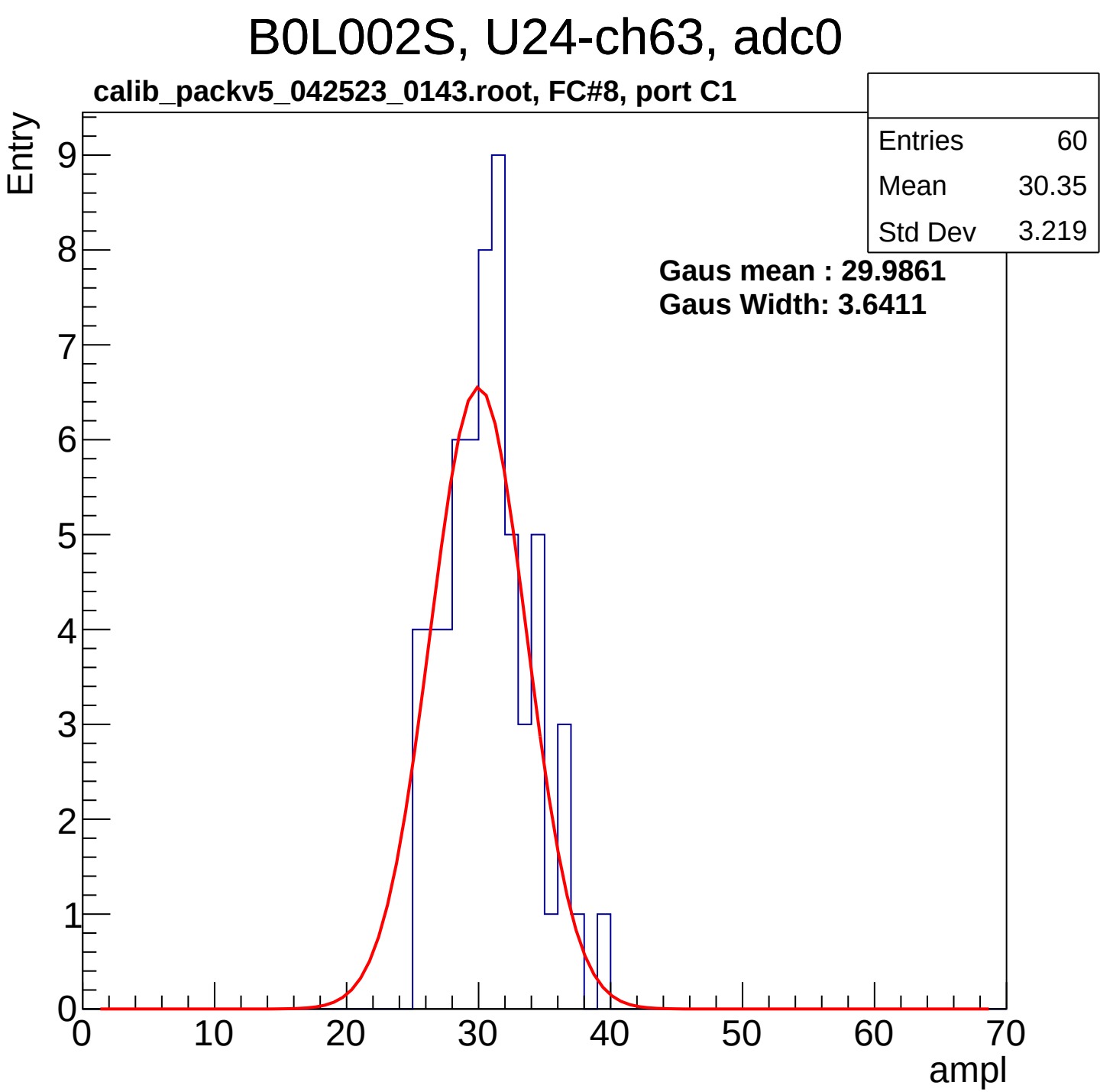
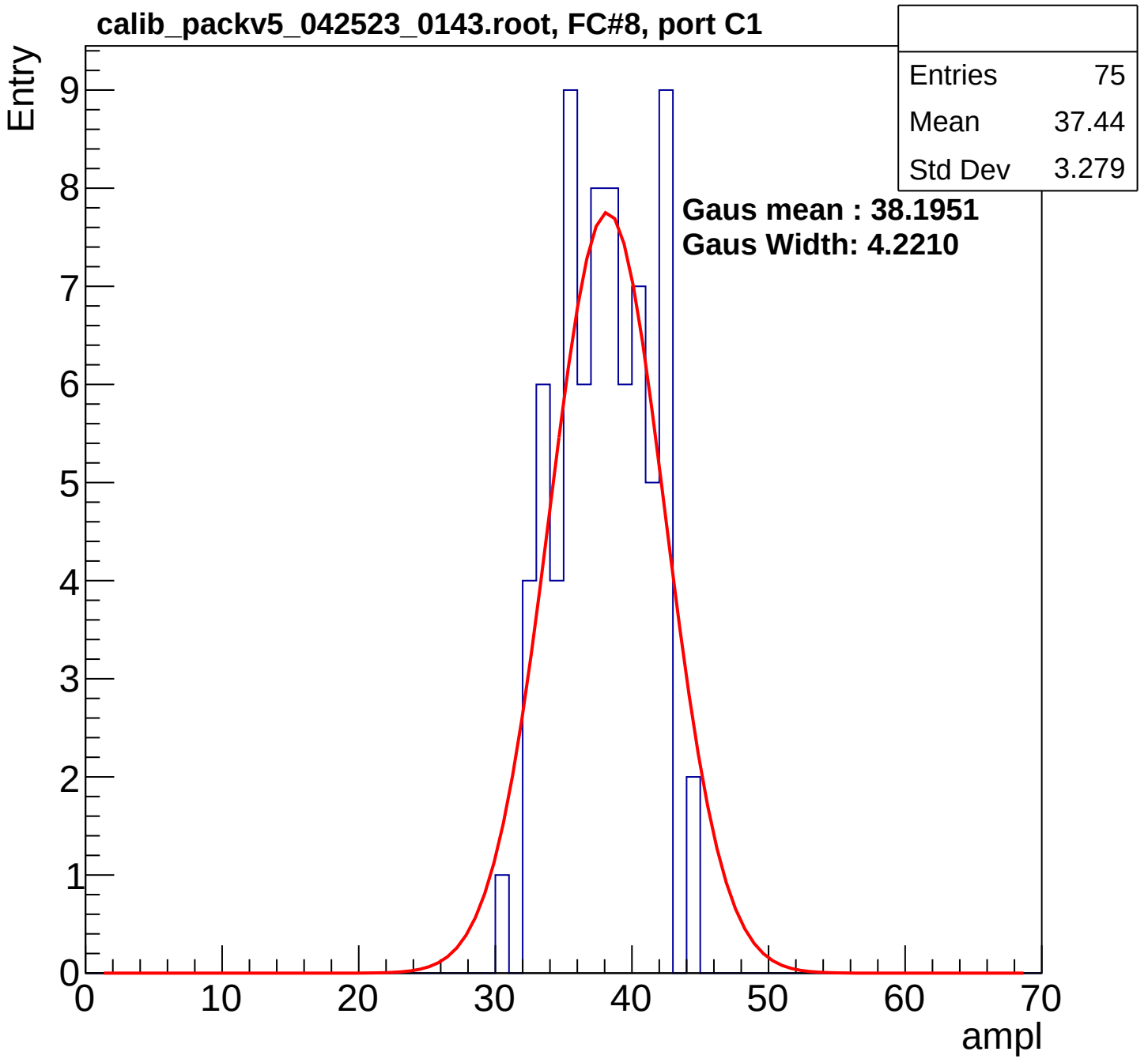
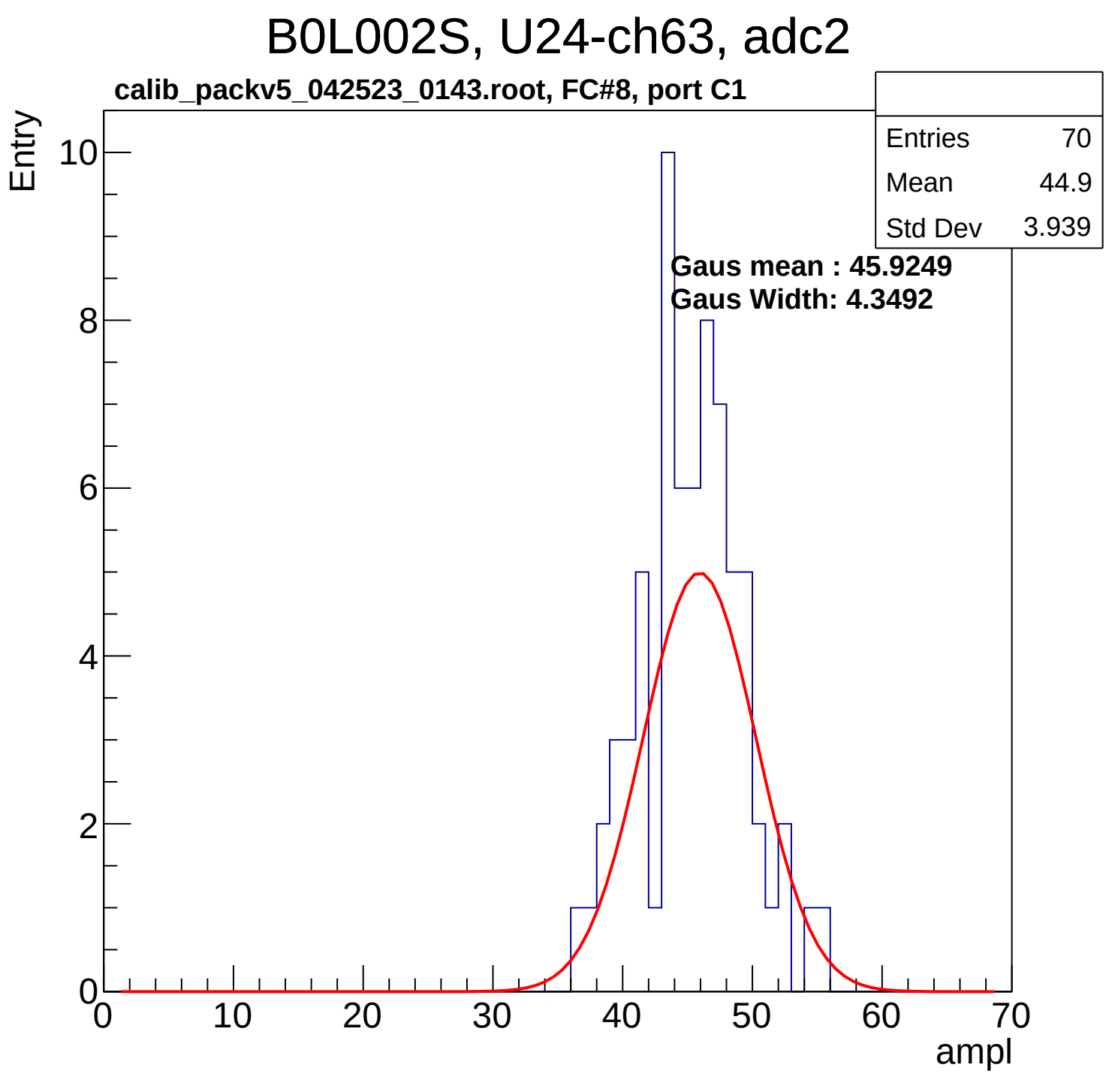

B0L002S, U24-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 30.35
Std Dev 3.219
Gaus mean : 29.9861
Gaus Width: 3.6411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.44
Std Dev 3.279
Gaus mean : 38.1951
Gaus Width: 4.2210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.9
Std Dev 3.939
Gaus mean : 45.9249
Gaus Width: 4.3492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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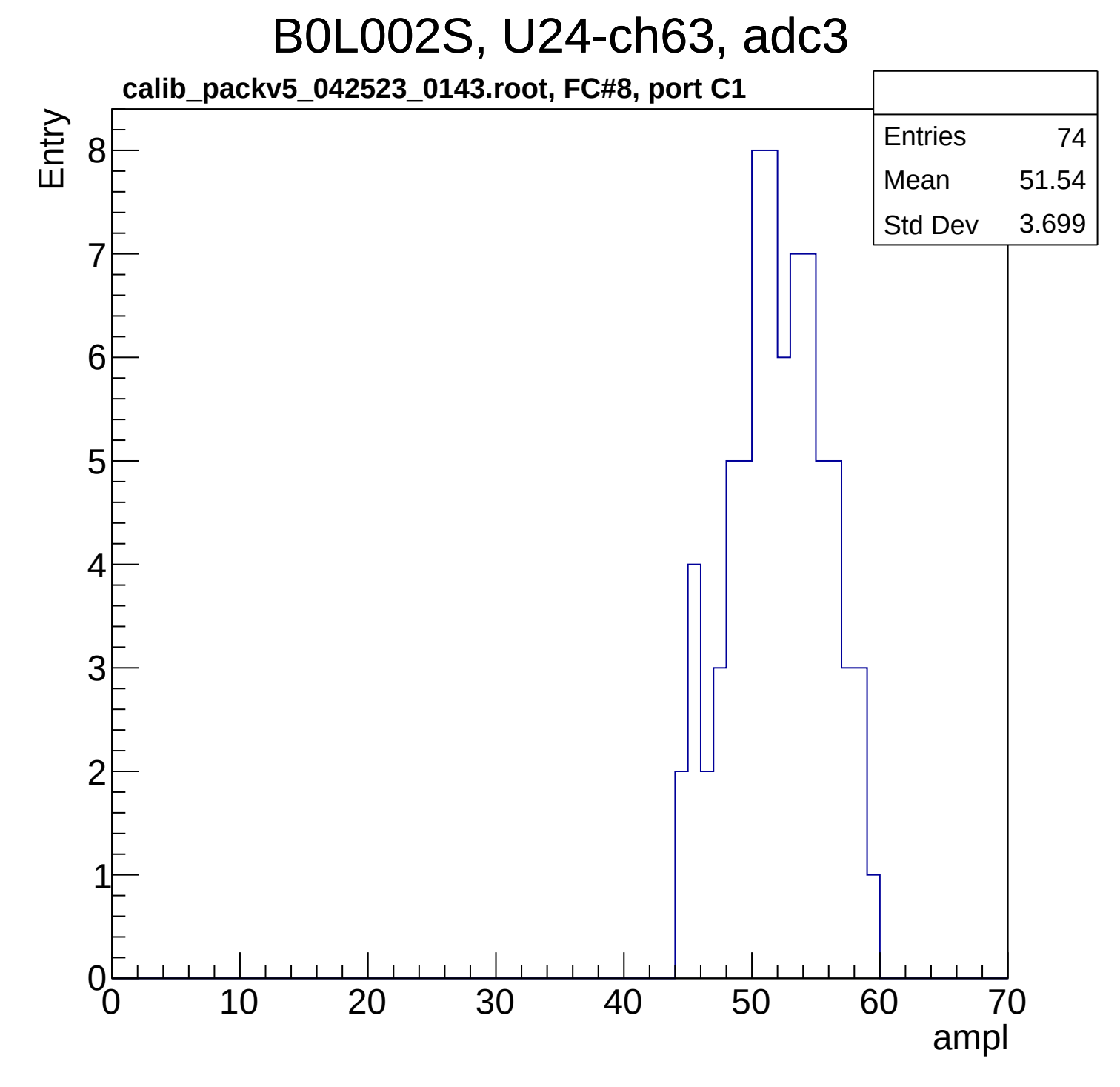
B0L002S, U24-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 51.54
Std Dev 3.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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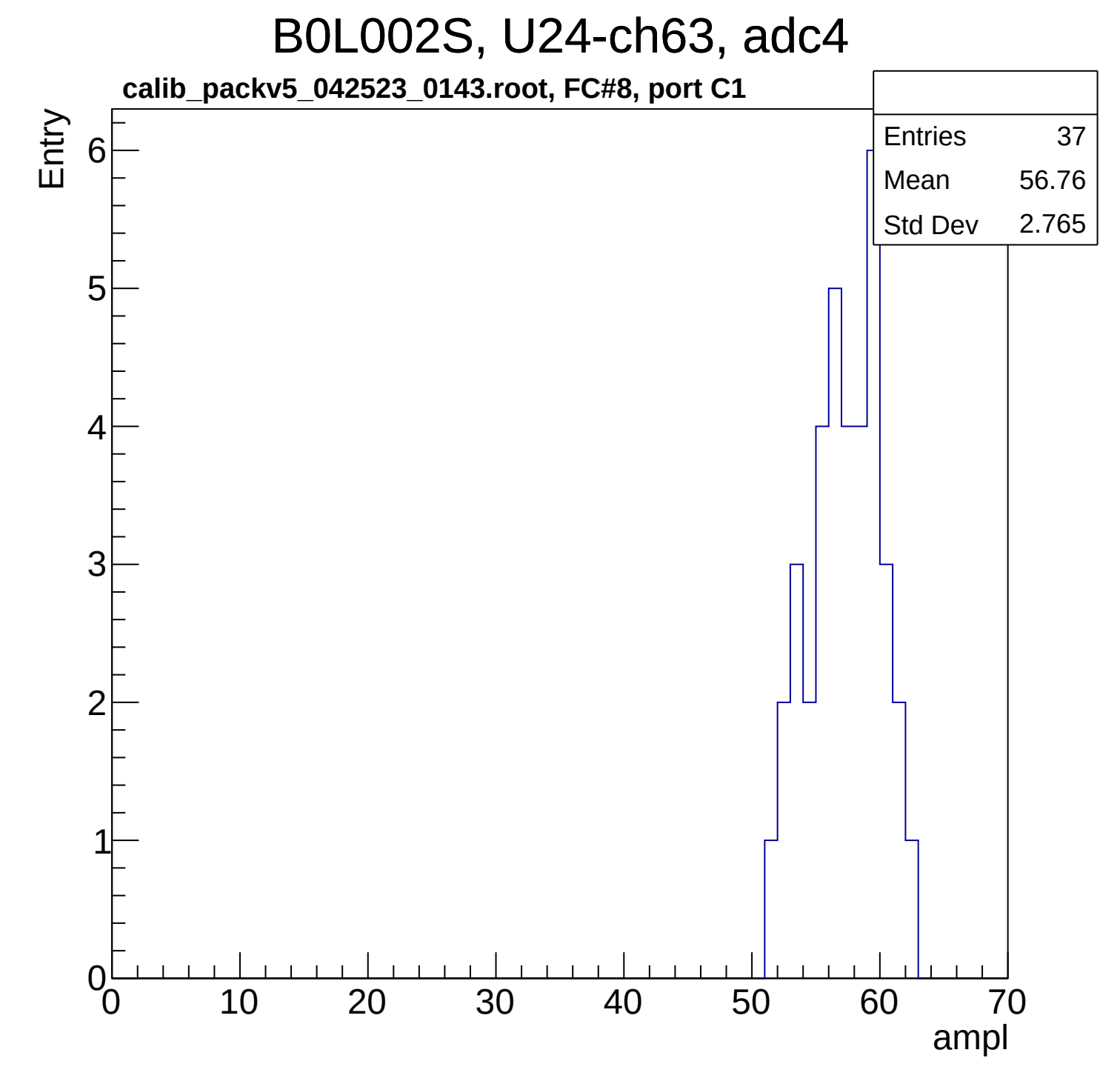
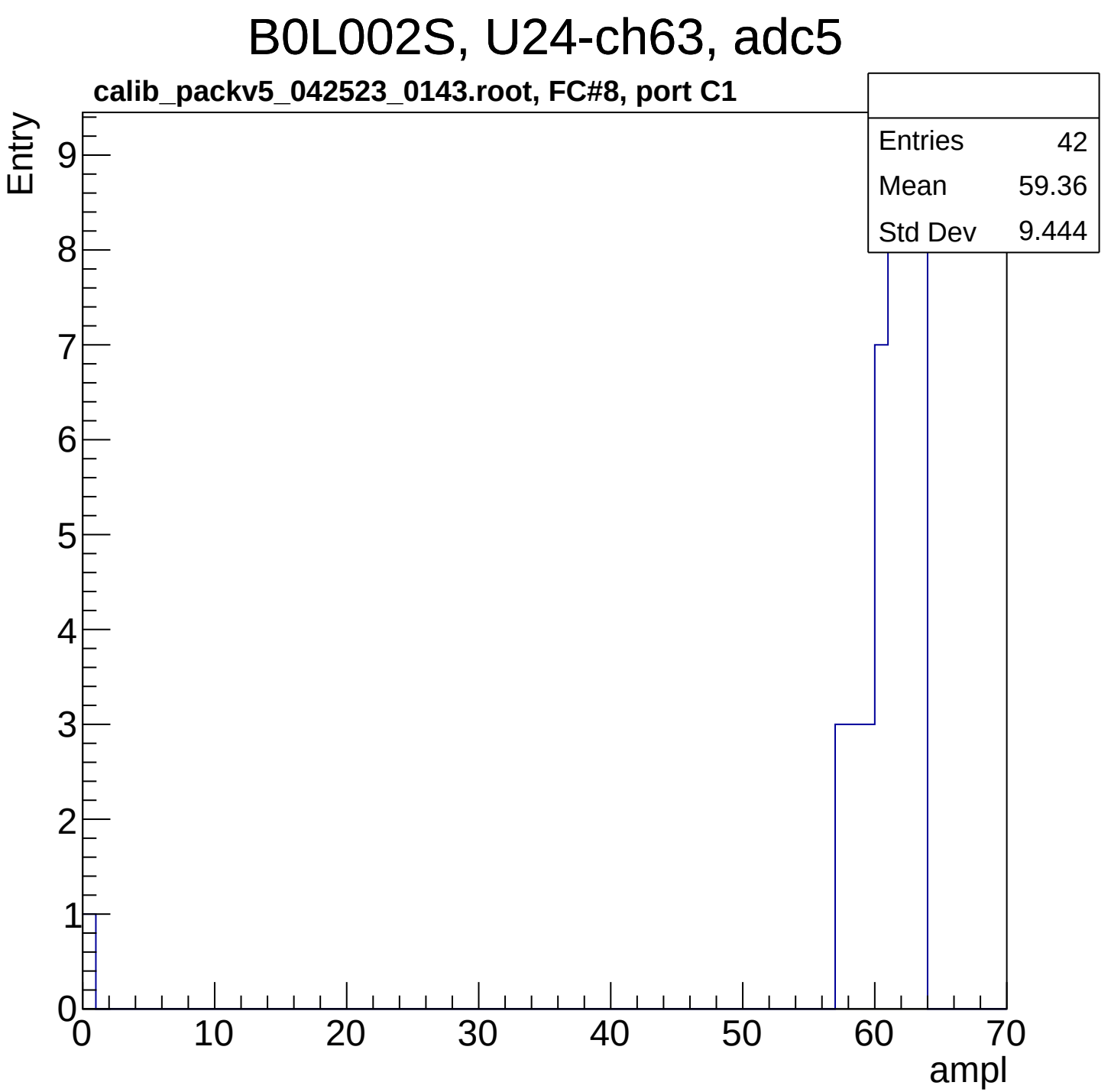

B0L002S, U24-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 56.76
Std Dev 2.765
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U24-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.36
Std Dev 9.444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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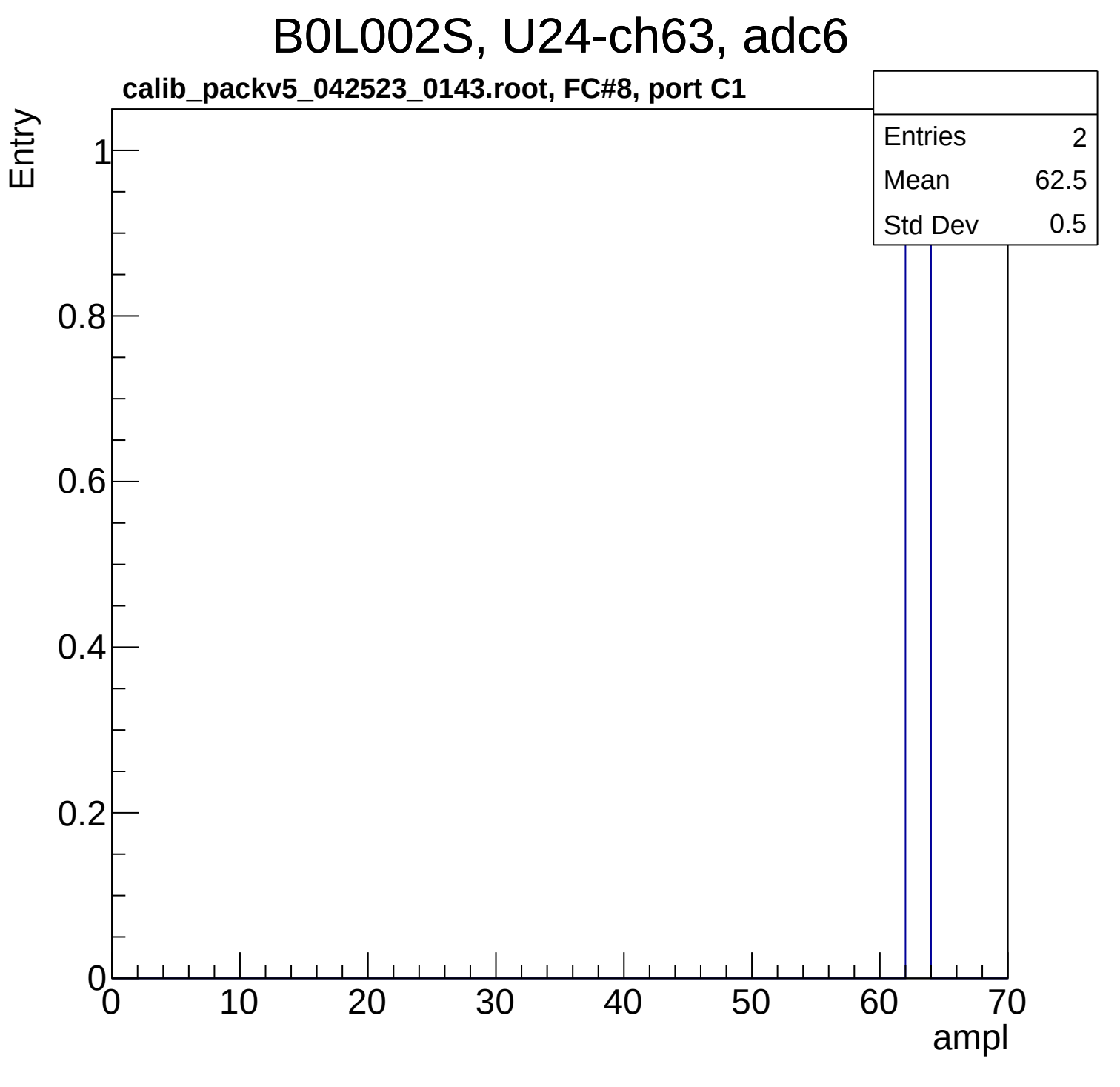
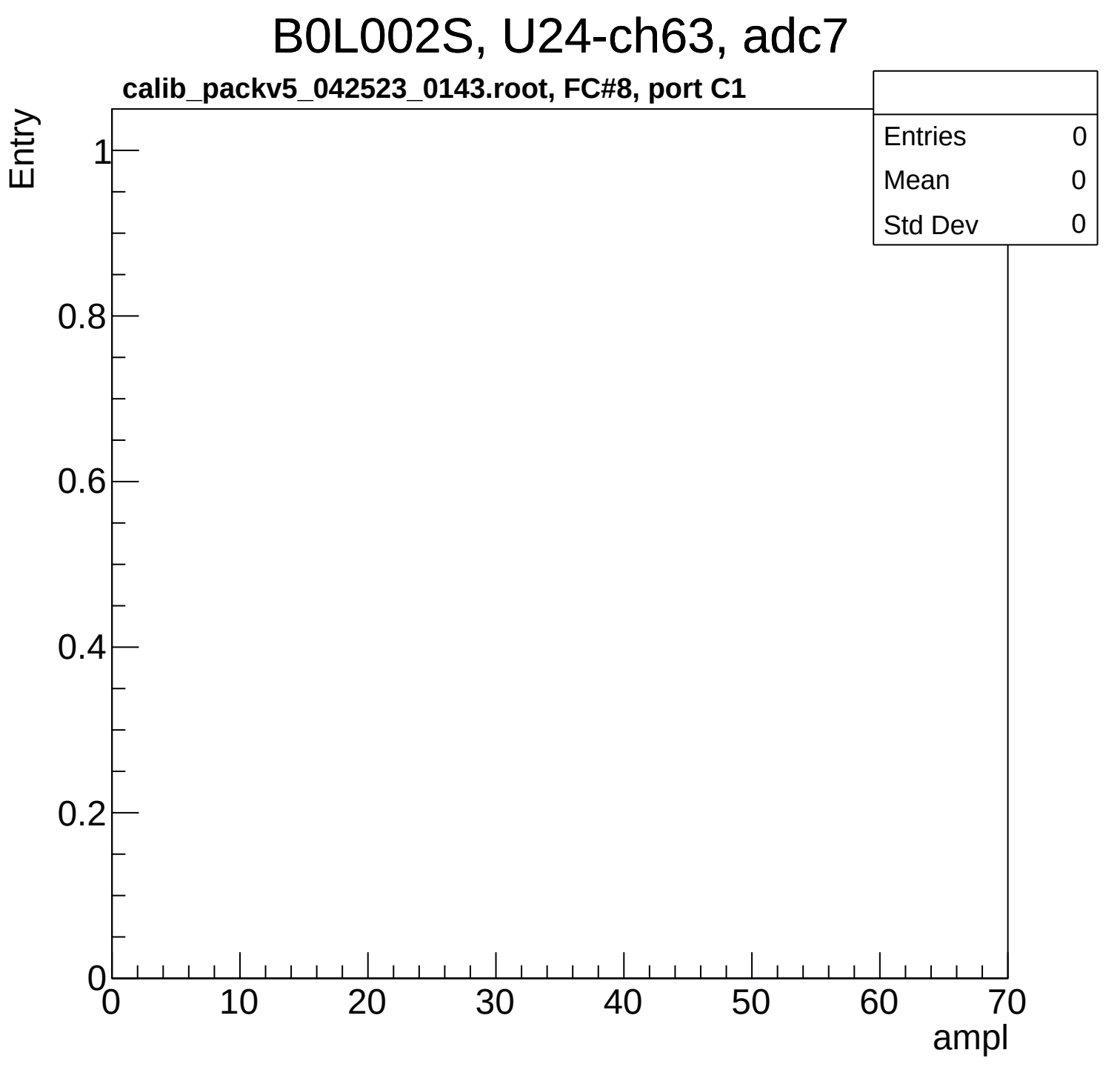
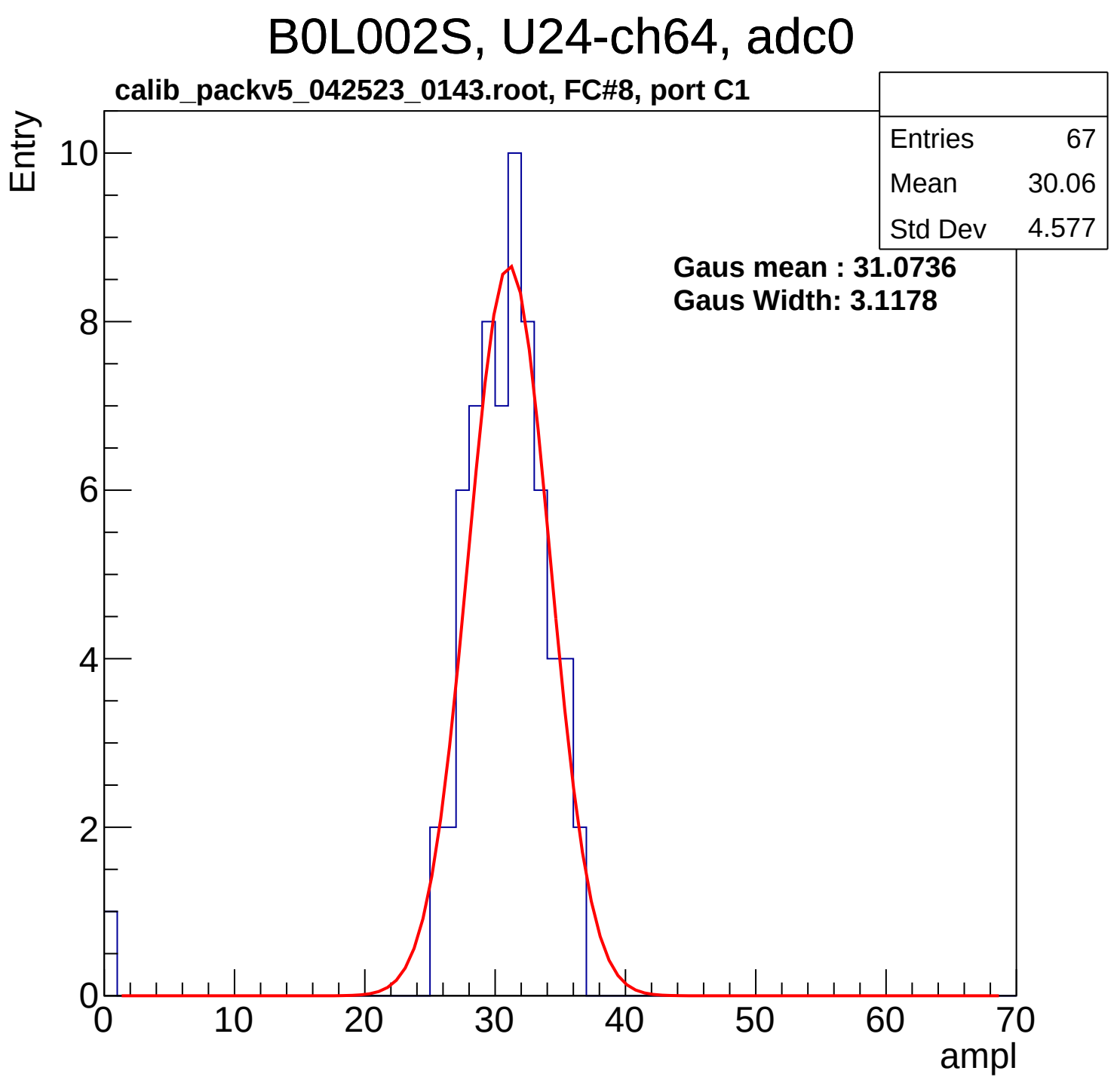
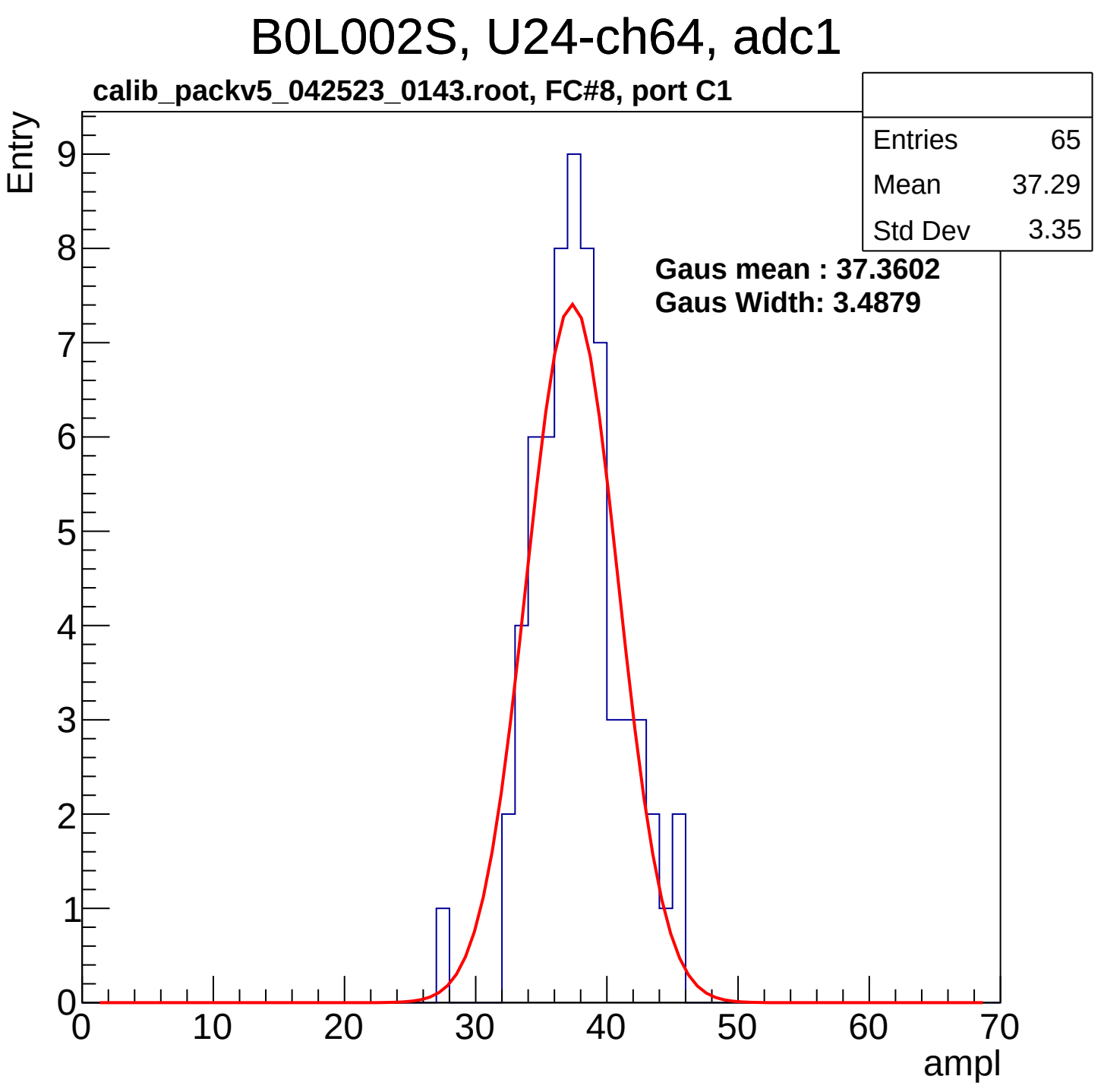

B0L002S, U24-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.06
Std Dev 4.577
Gaus mean : 31.0736
Gaus Width: 3.1178
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.29
Std Dev 3.35
Gaus mean : 37.3602
Gaus Width: 3.4879
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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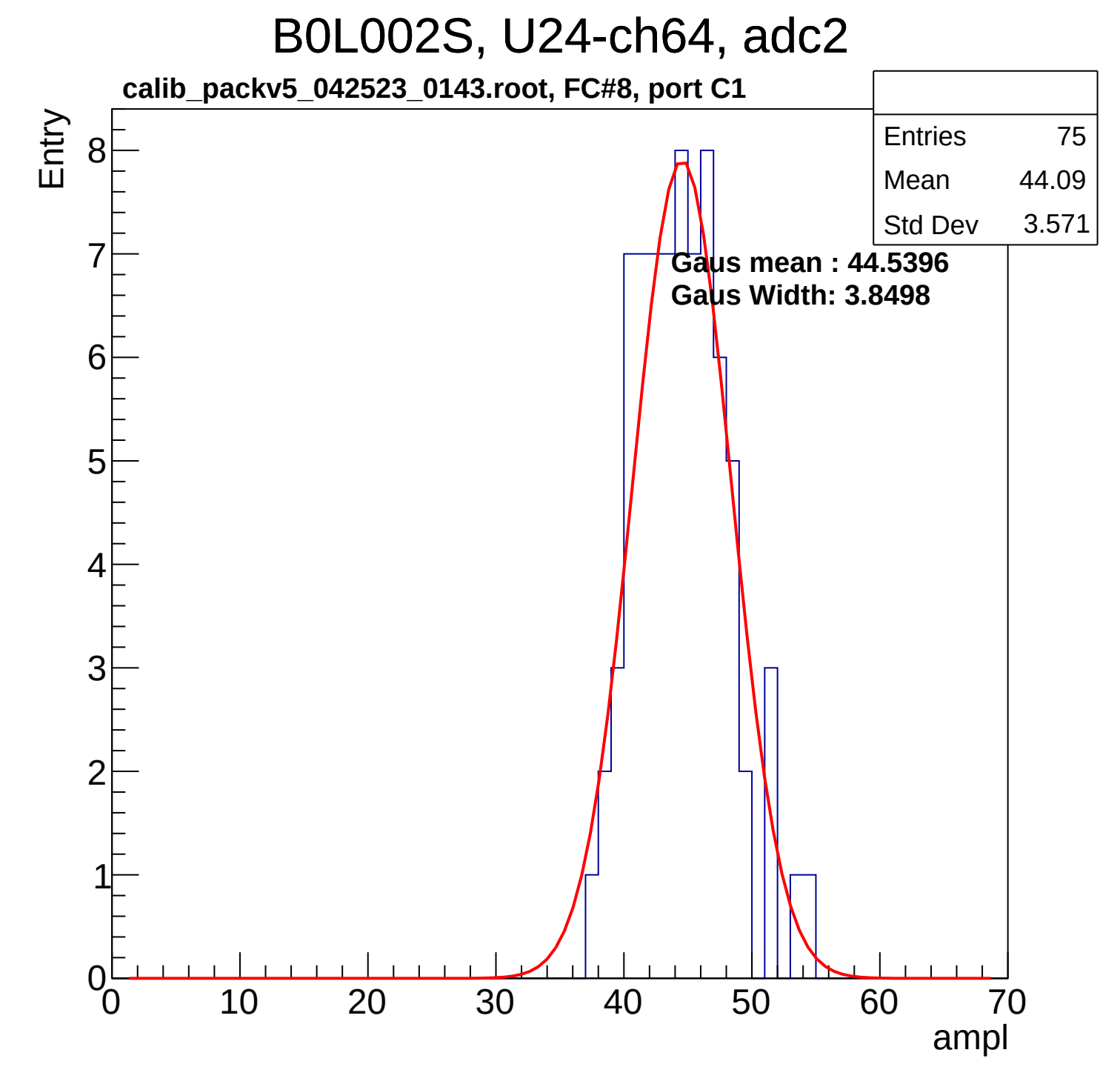
60
70


B0L002S, U24-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.09
Std Dev 3.571
Gaus mean : 44.5396
Gaus Width: 3.8498
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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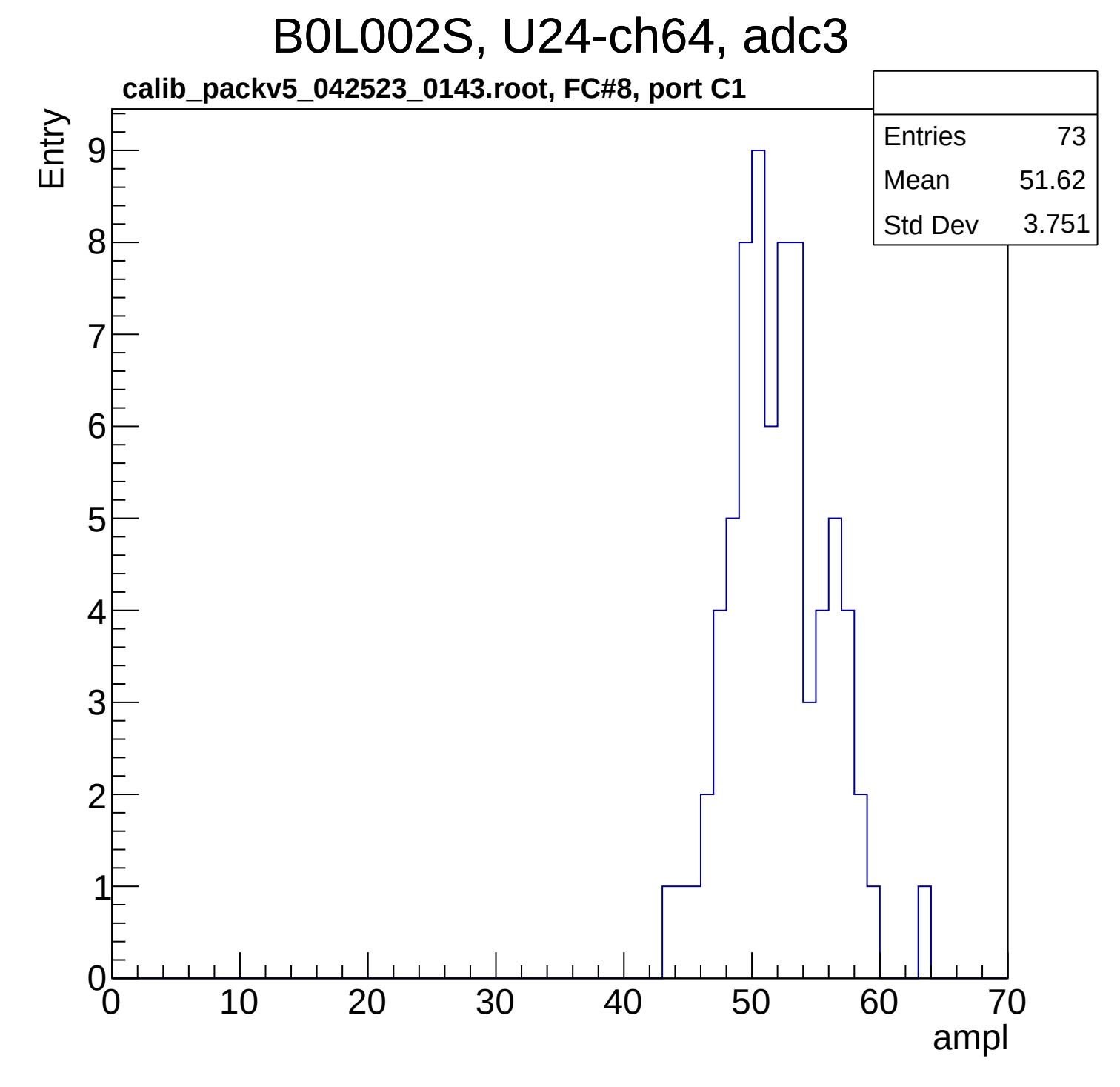
B0L002S, U24-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.62
Std Dev 3.751
Entry
ampl
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3
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60
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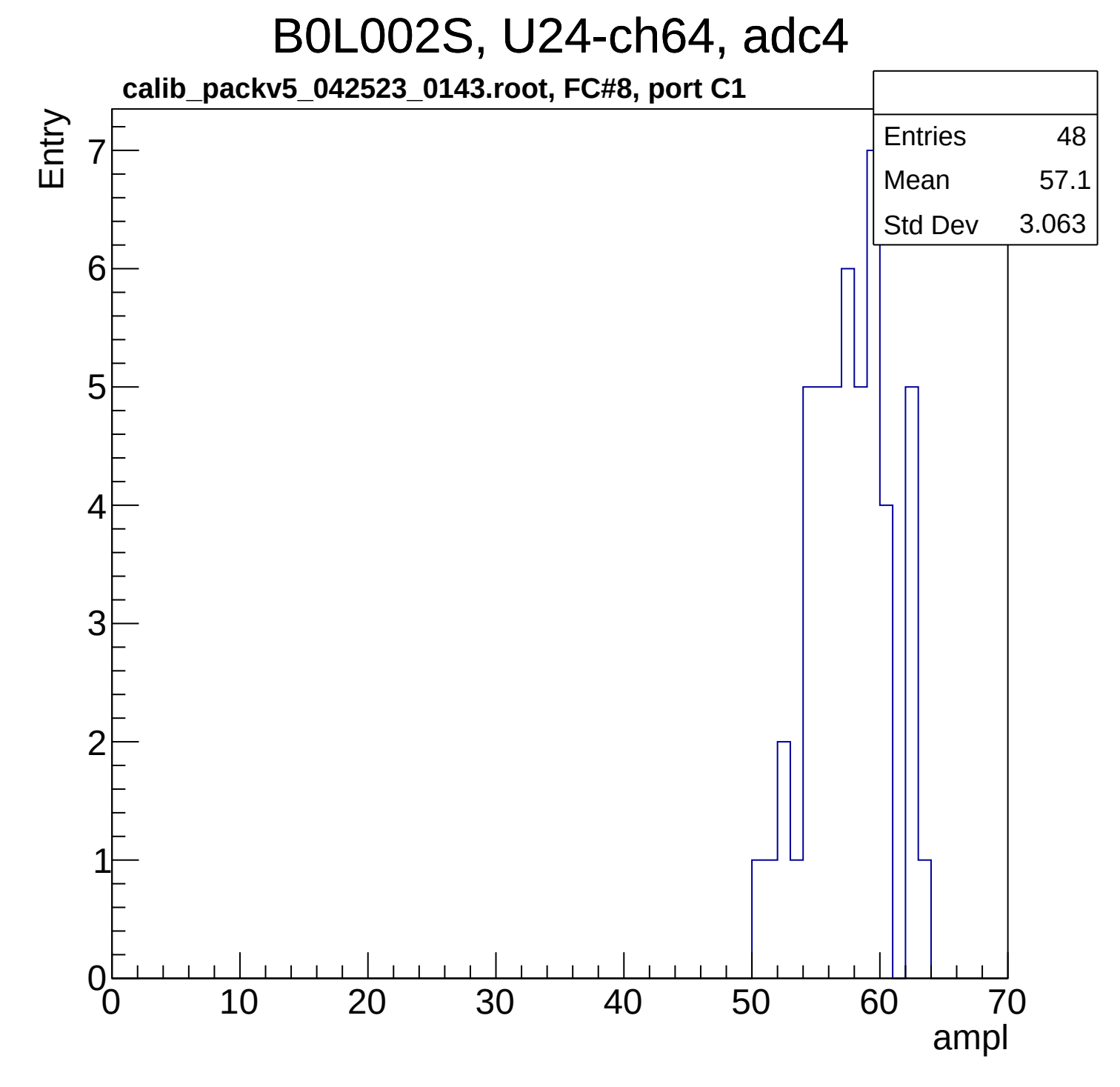
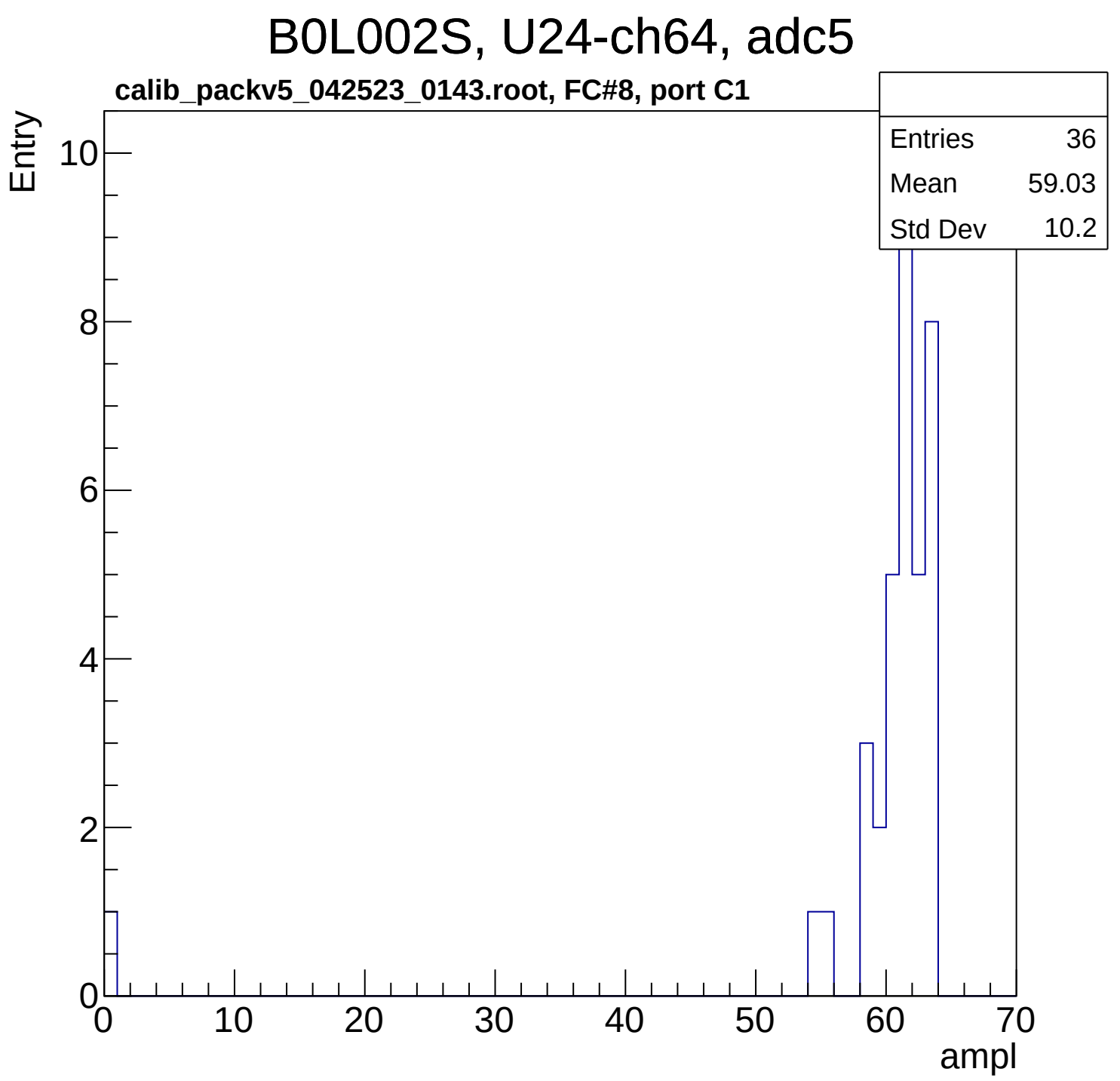

B0L002S, U24-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.1
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.03
Std Dev 10.2
Entry
0
2
4
6
8
10
0
10
20
30
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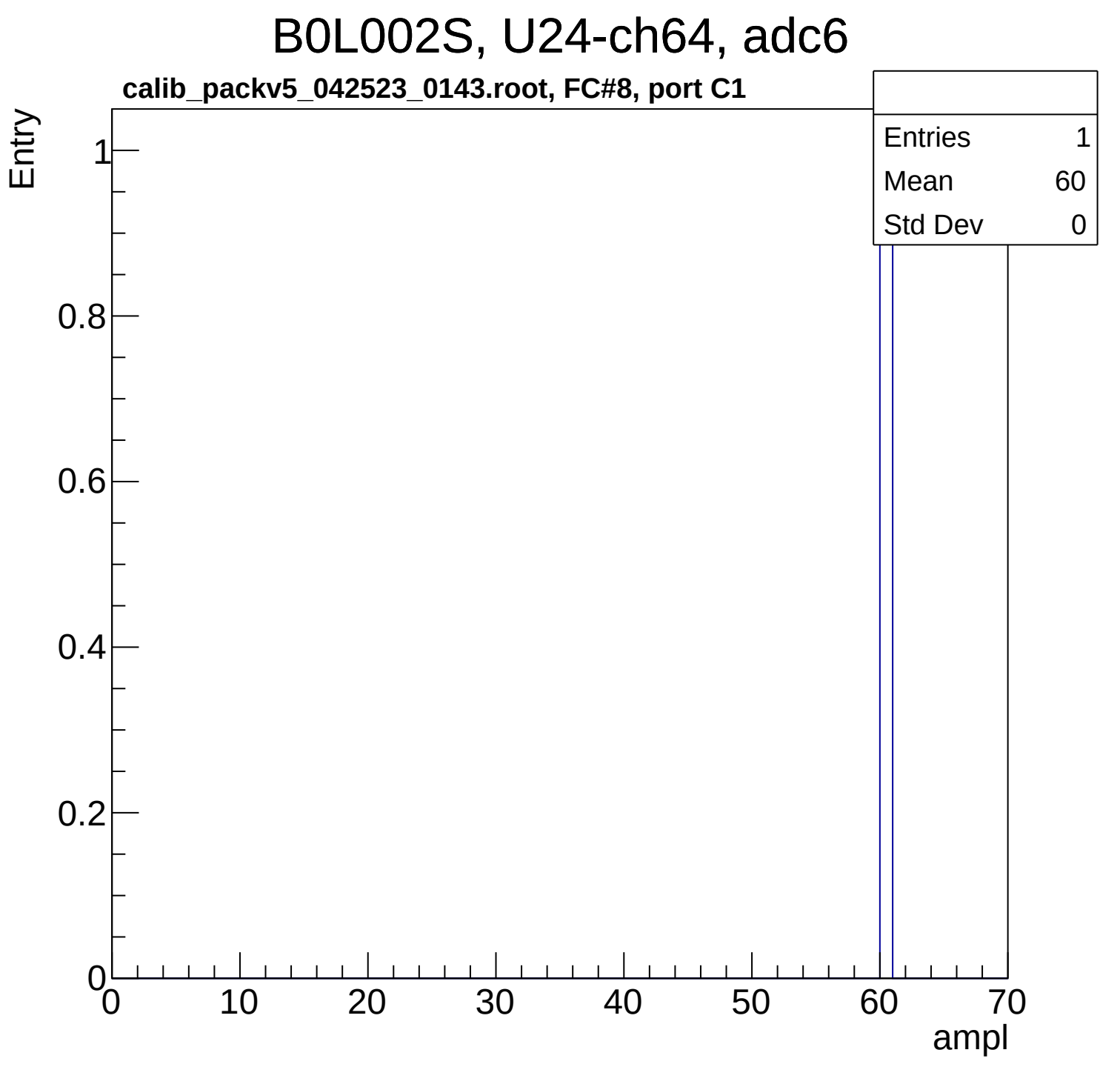
50
60
70
ampl


B0L002S, U24-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 60
Std Dev 0
Entry
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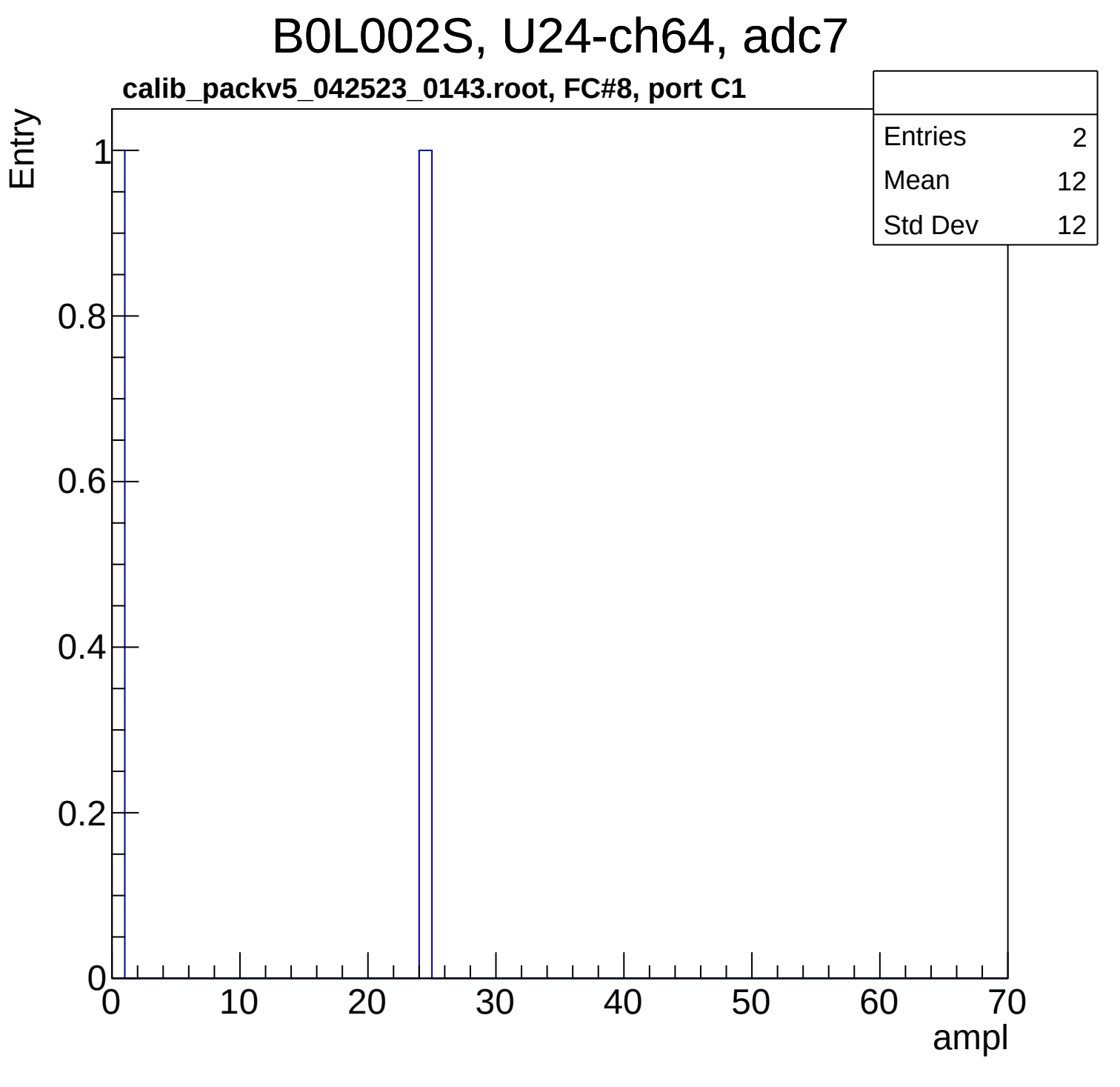
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
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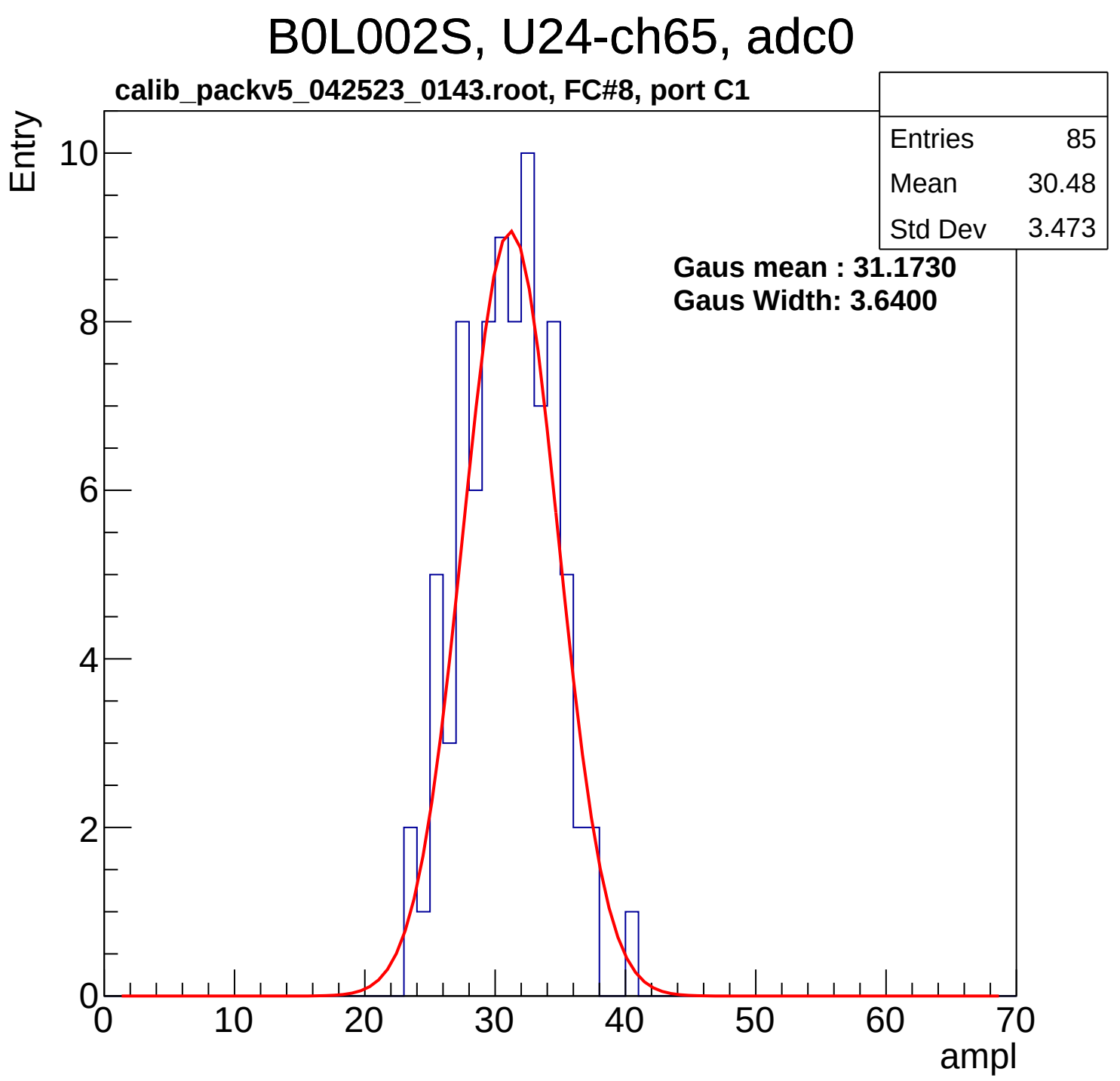
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 30.48
Std Dev 3.473
Gaus mean : 31.1730
Gaus Width: 3.6400
Entry
ampl
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6
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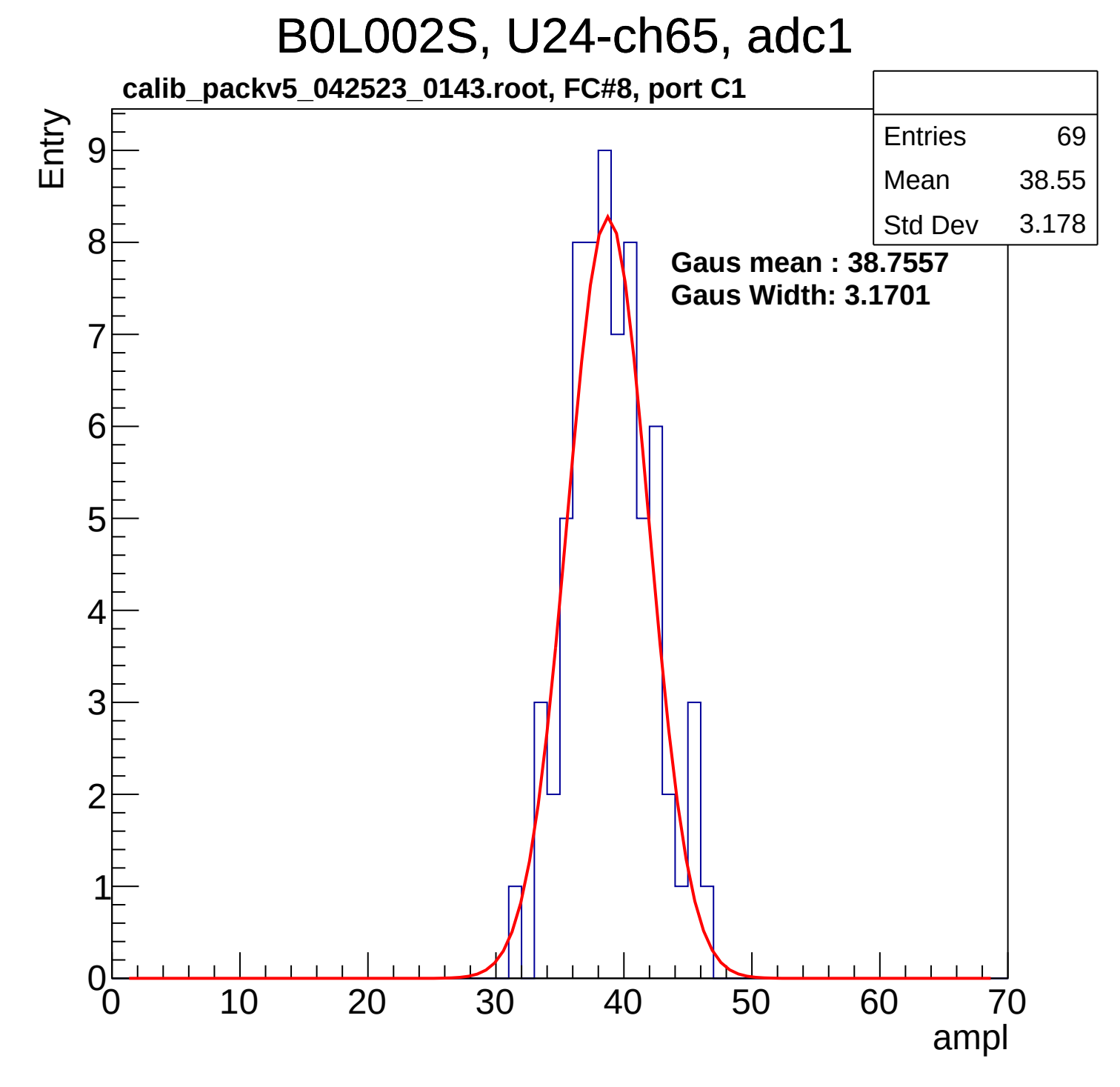
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.55
Std Dev 3.178
Gaus mean : 38.7557
Gaus Width: 3.1701
Entry
ampl
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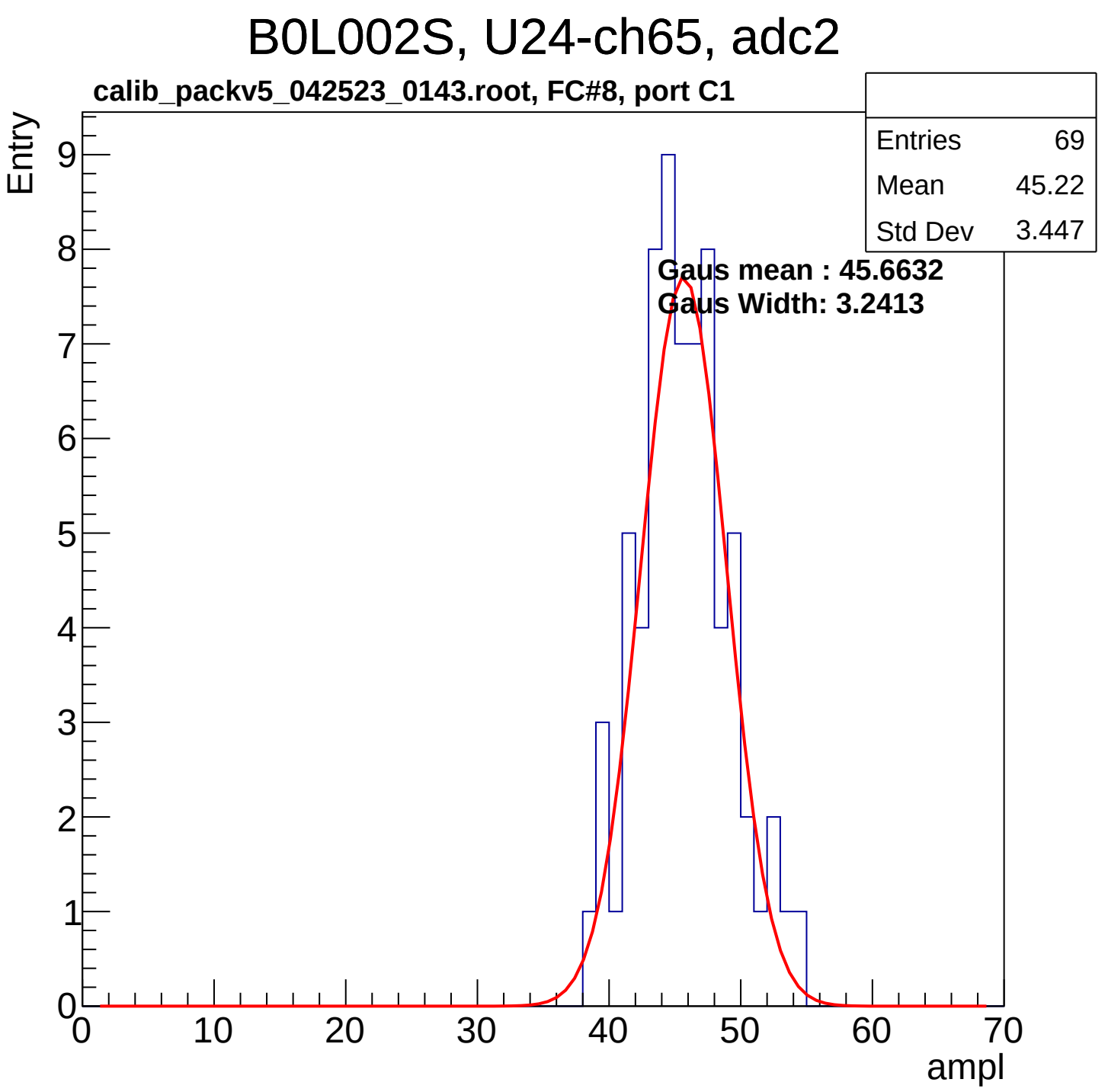
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.22
Std Dev 3.447
Gaus mean : 45.6632
Gaus Width: 3.2413
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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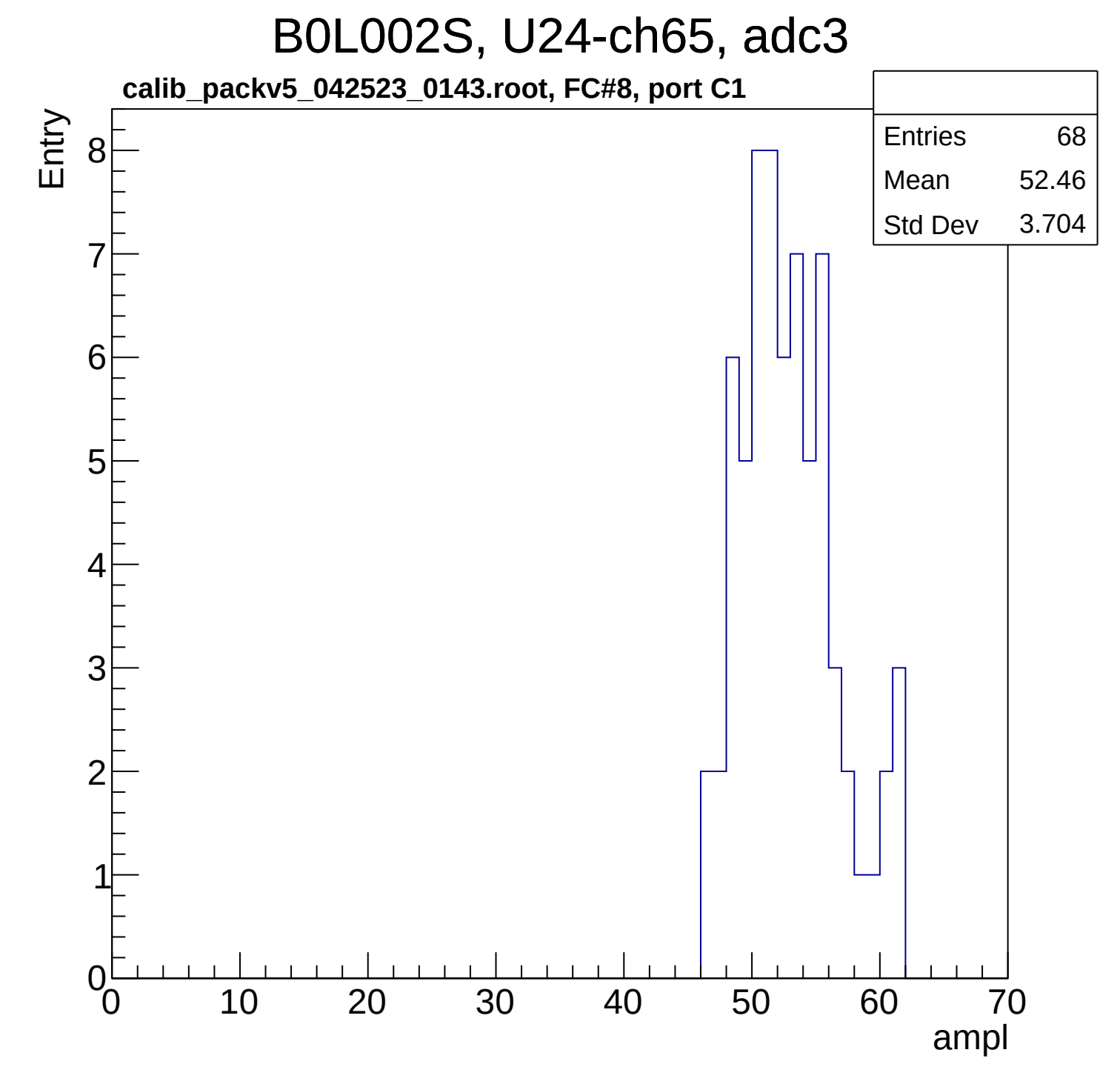
B0L002S, U24-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 52.46
Std Dev 3.704
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
30
40
50
60
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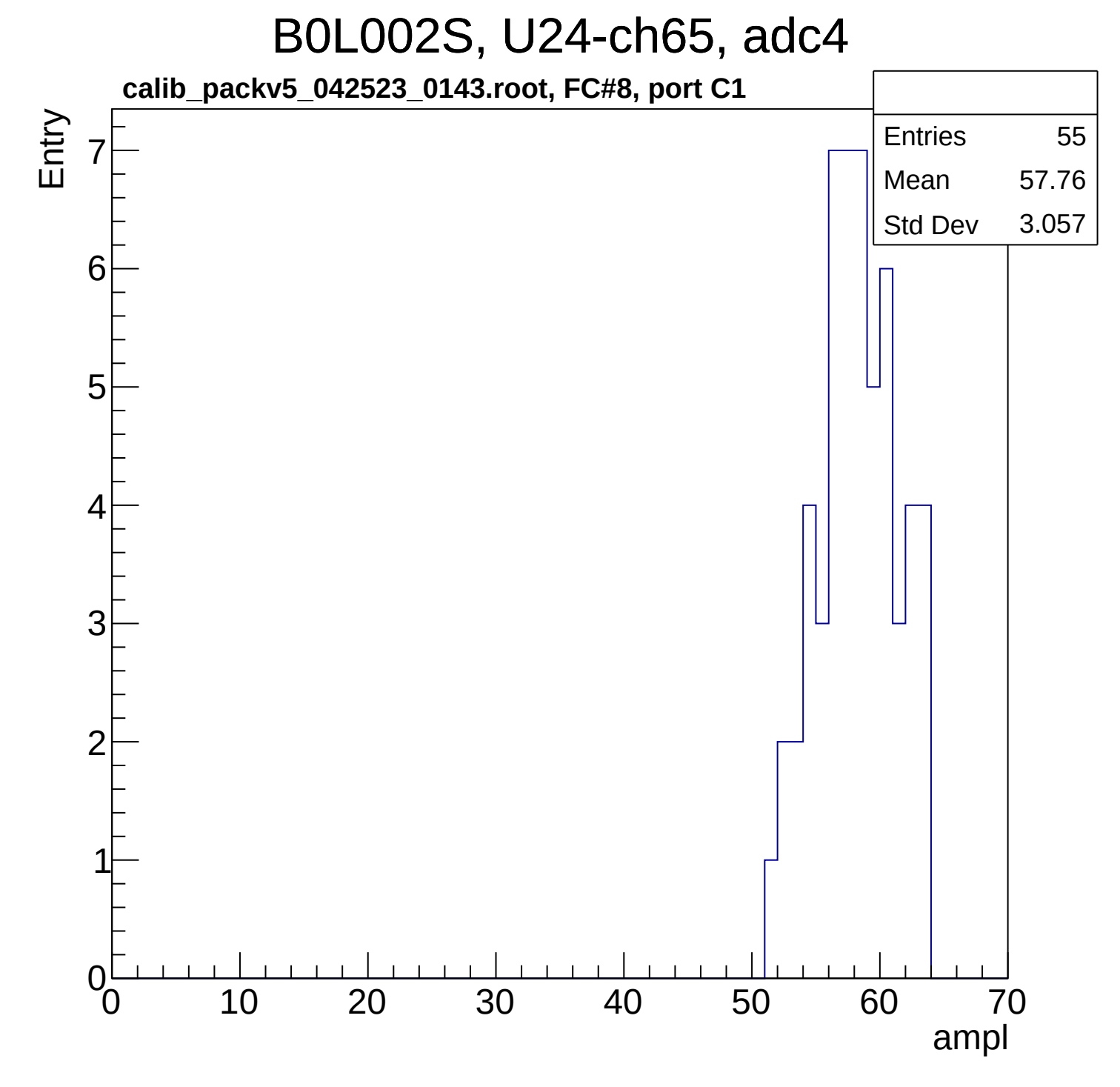
B0L002S, U24-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.76
Std Dev 3.057
Entry
ampl
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1
2
3
4
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0
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50
60
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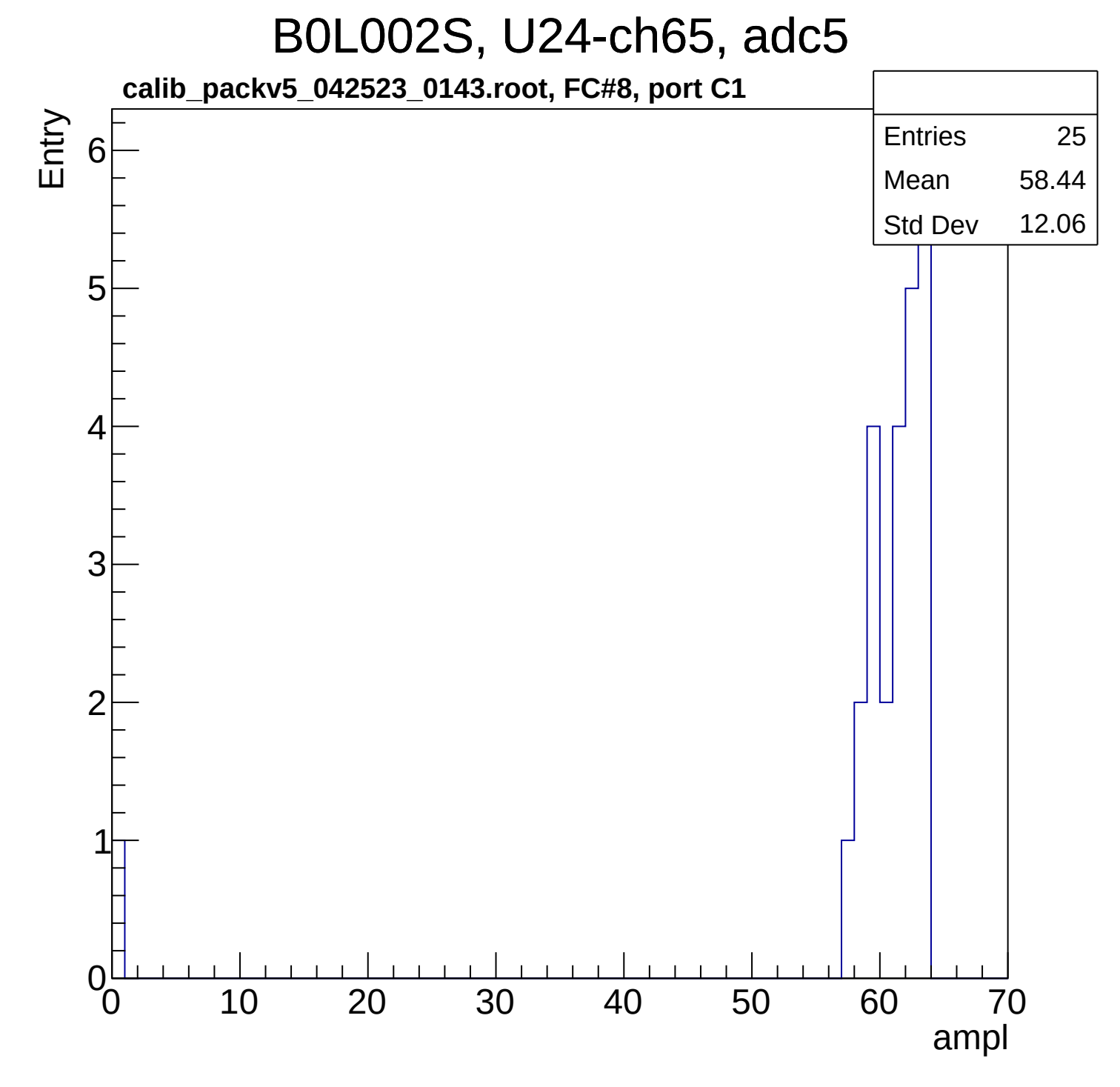
B0L002S, U24-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.44
Std Dev 12.06
Entry
0
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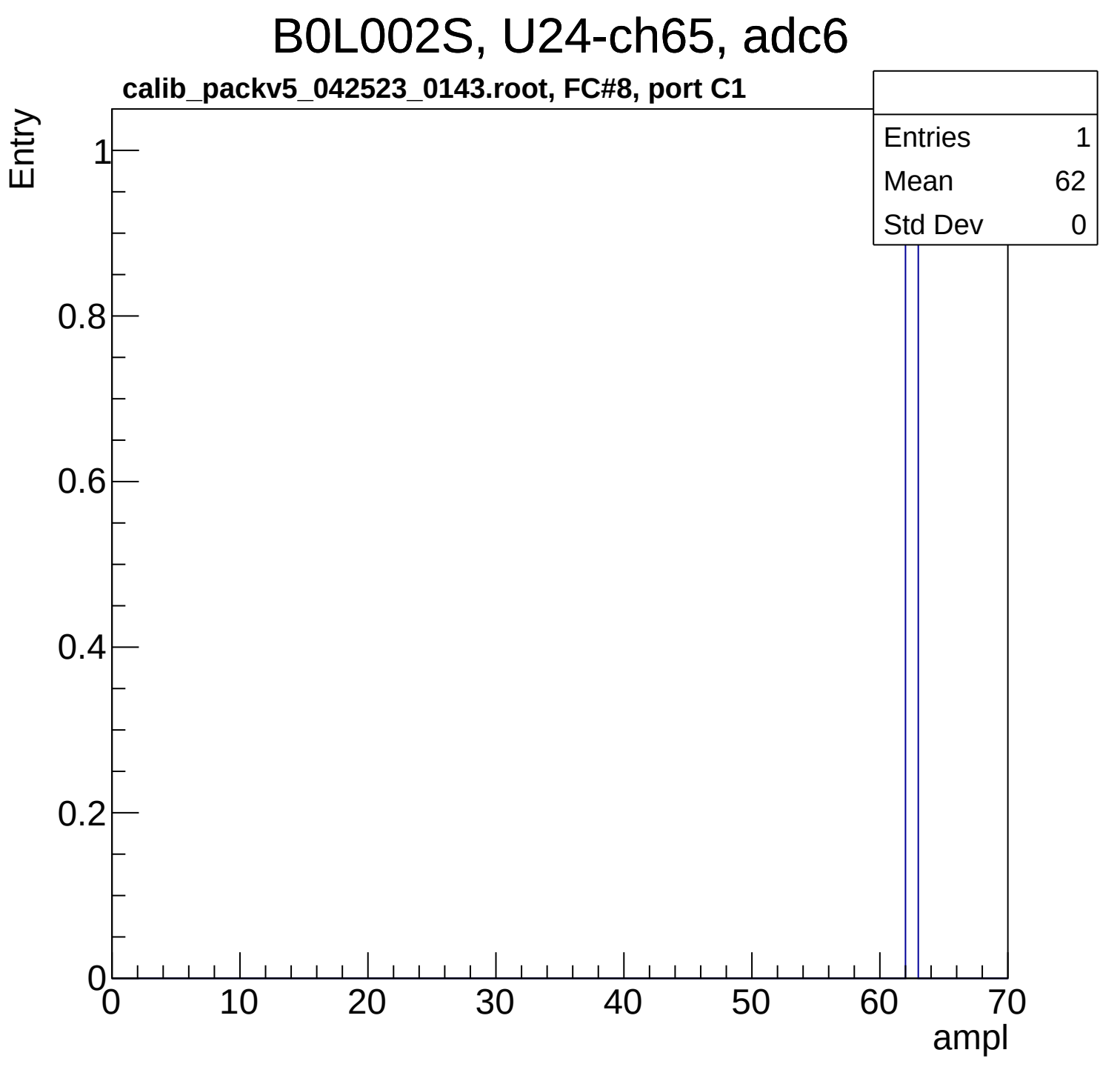
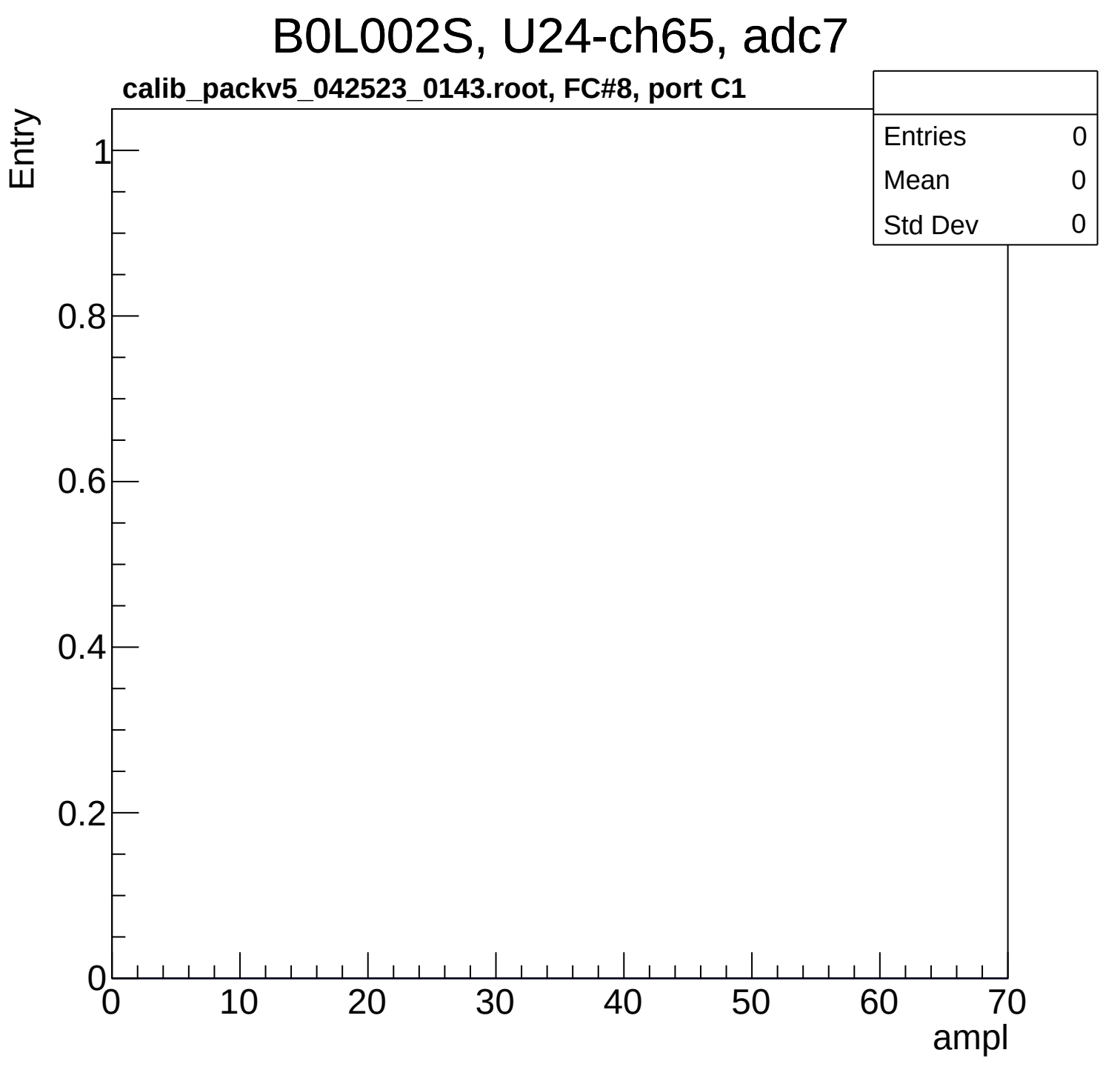
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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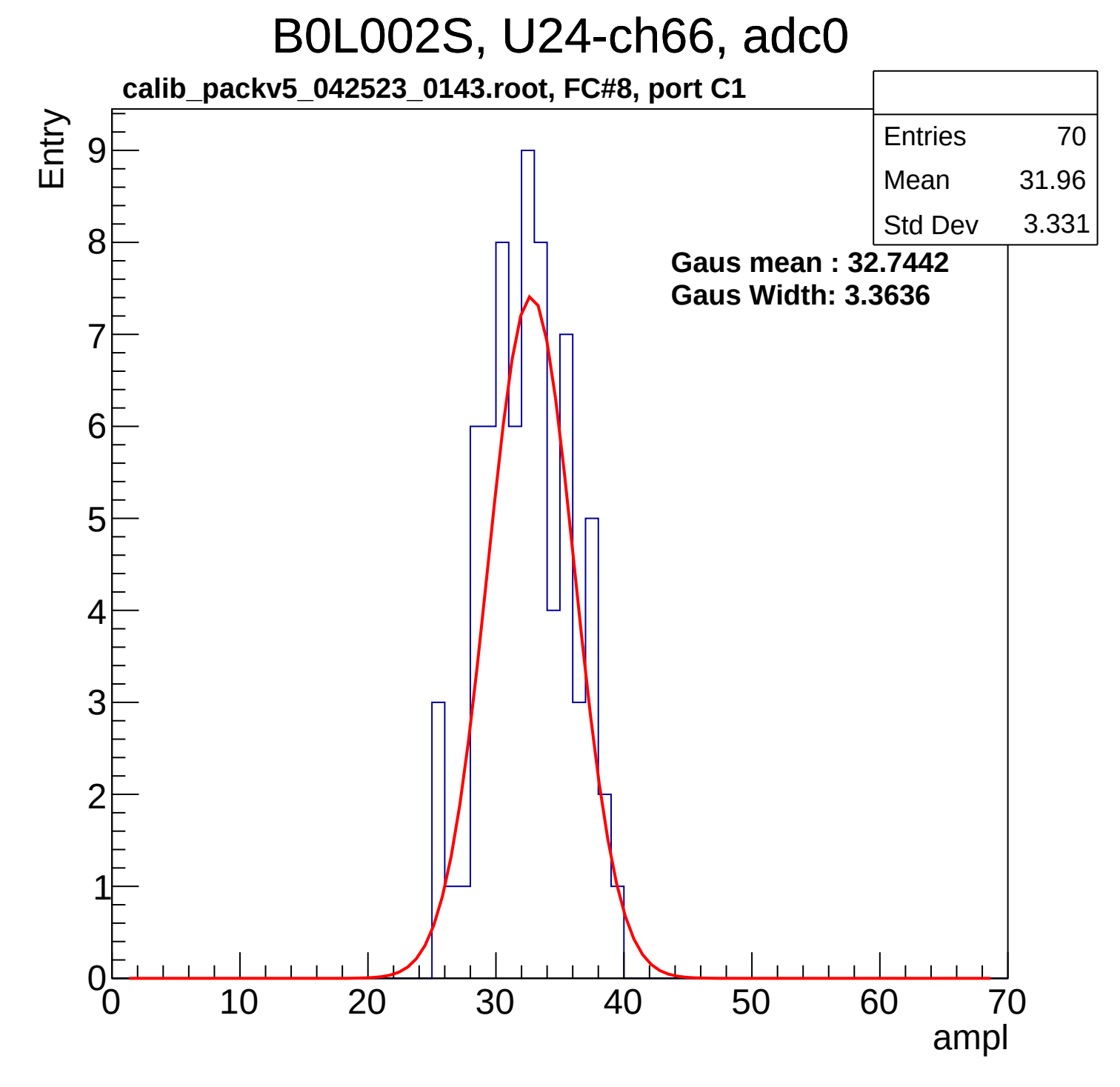
50
60
70
ampl


B0L002S, U24-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.96
Std Dev 3.331
Gaus mean : 32.7442
Gaus Width: 3.3636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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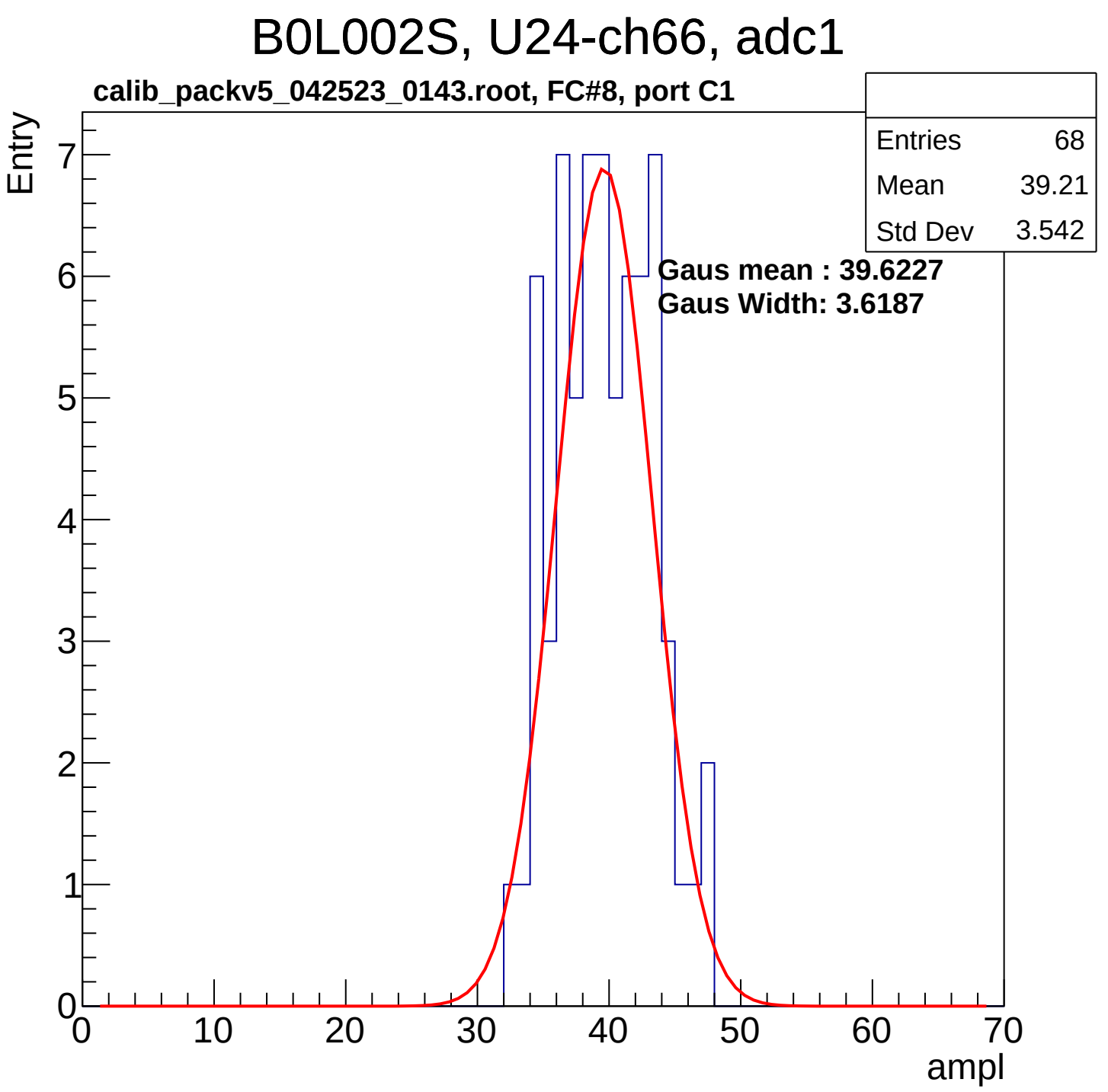
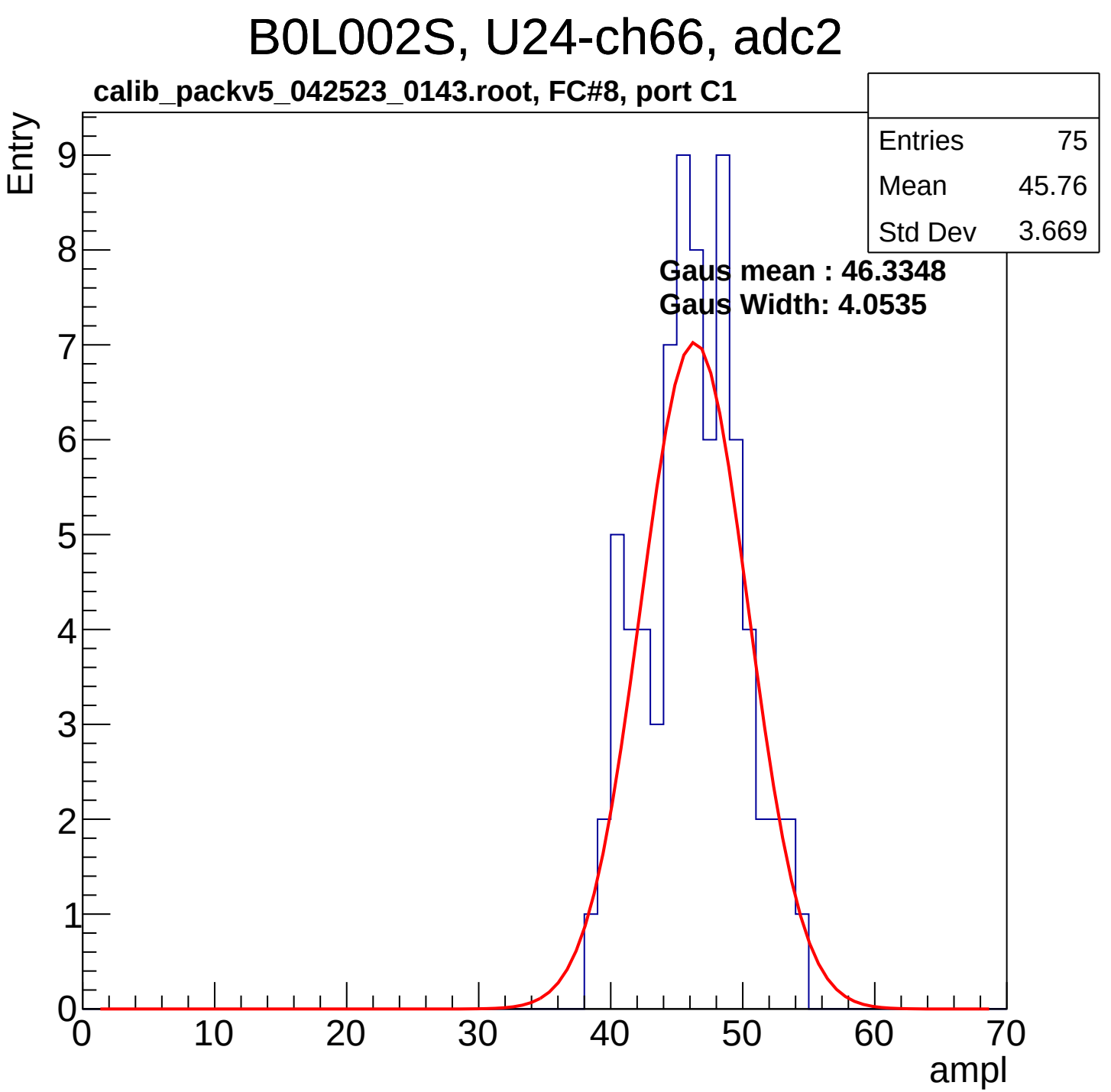

B0L002S, U24-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 39.21
Std Dev 3.542
Gaus mean : 39.6227
Gaus Width: 3.6187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.76
Std Dev 3.669
Gaus mean : 46.3348
Gaus Width: 4.0535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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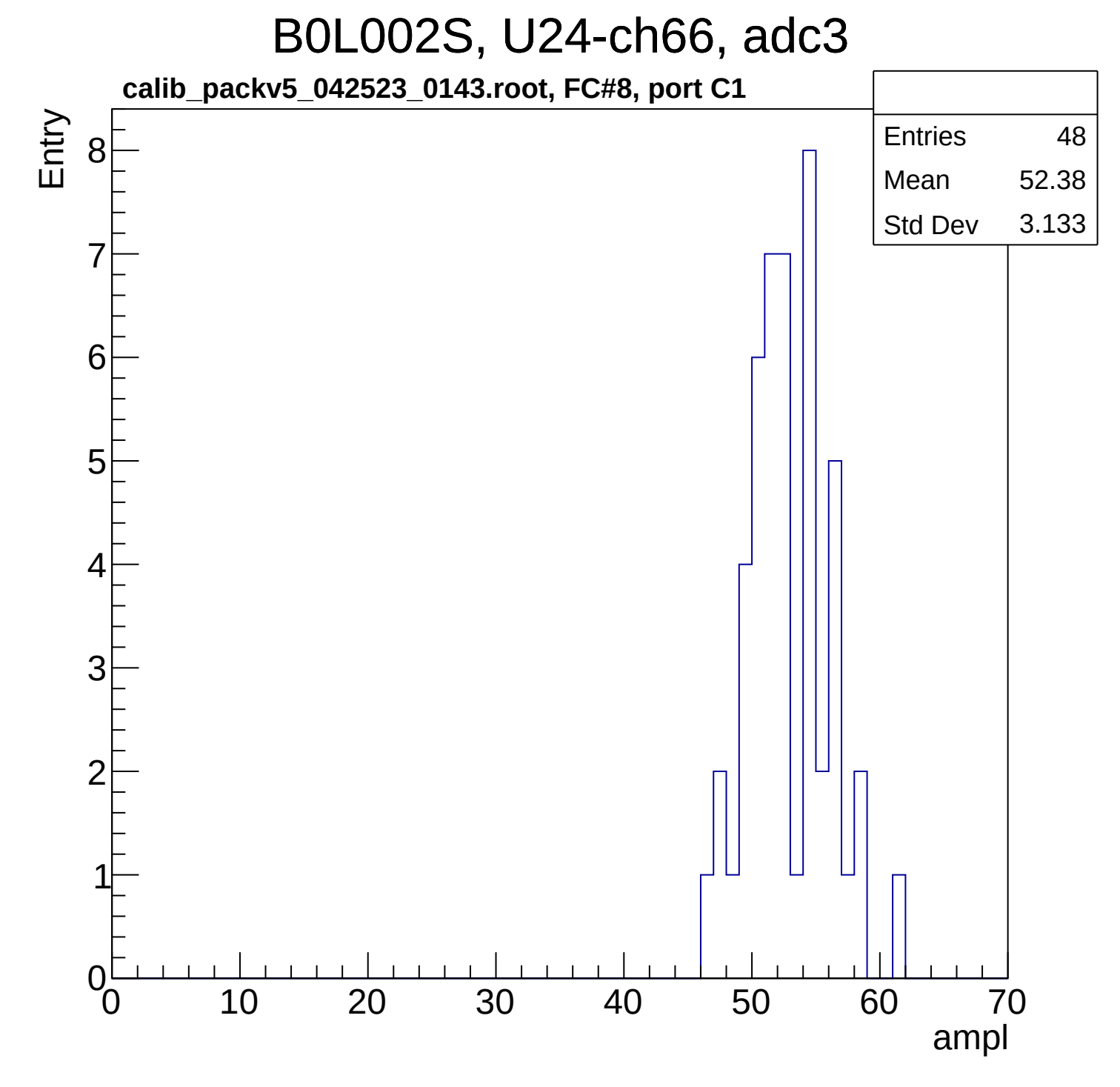
B0L002S, U24-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 52.38
Std Dev 3.133
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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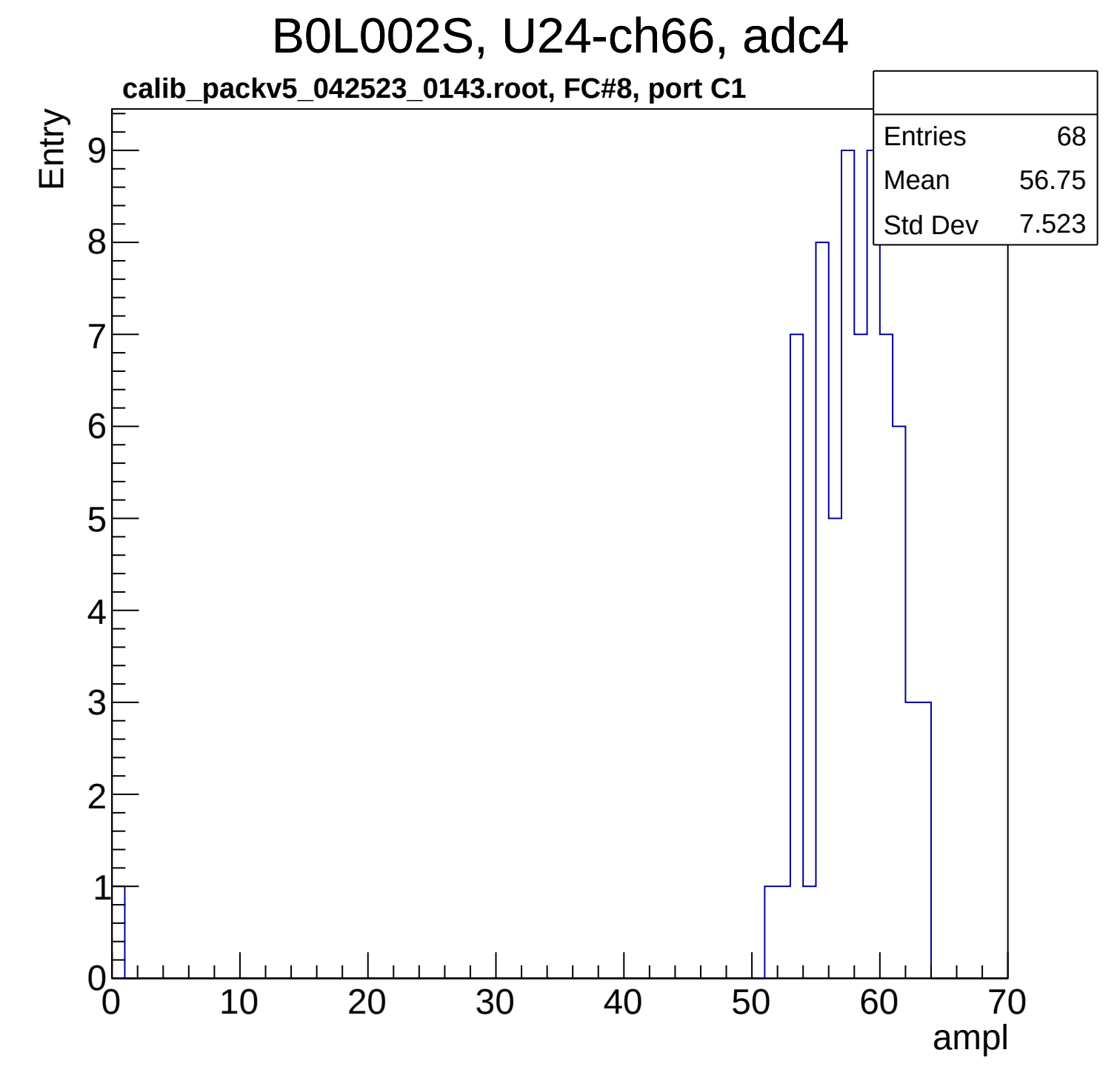
B0L002S, U24-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.75
Std Dev 7.523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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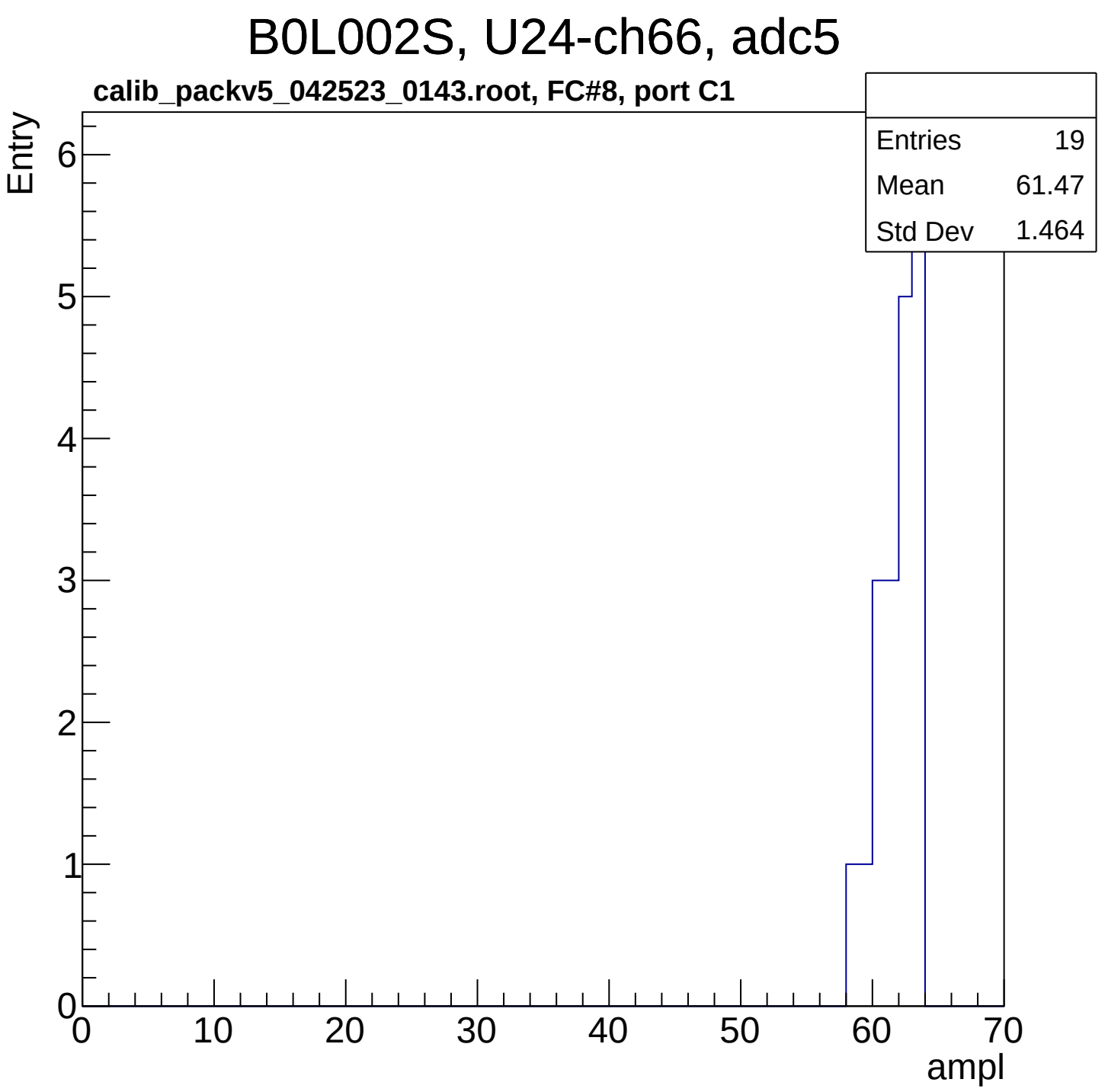
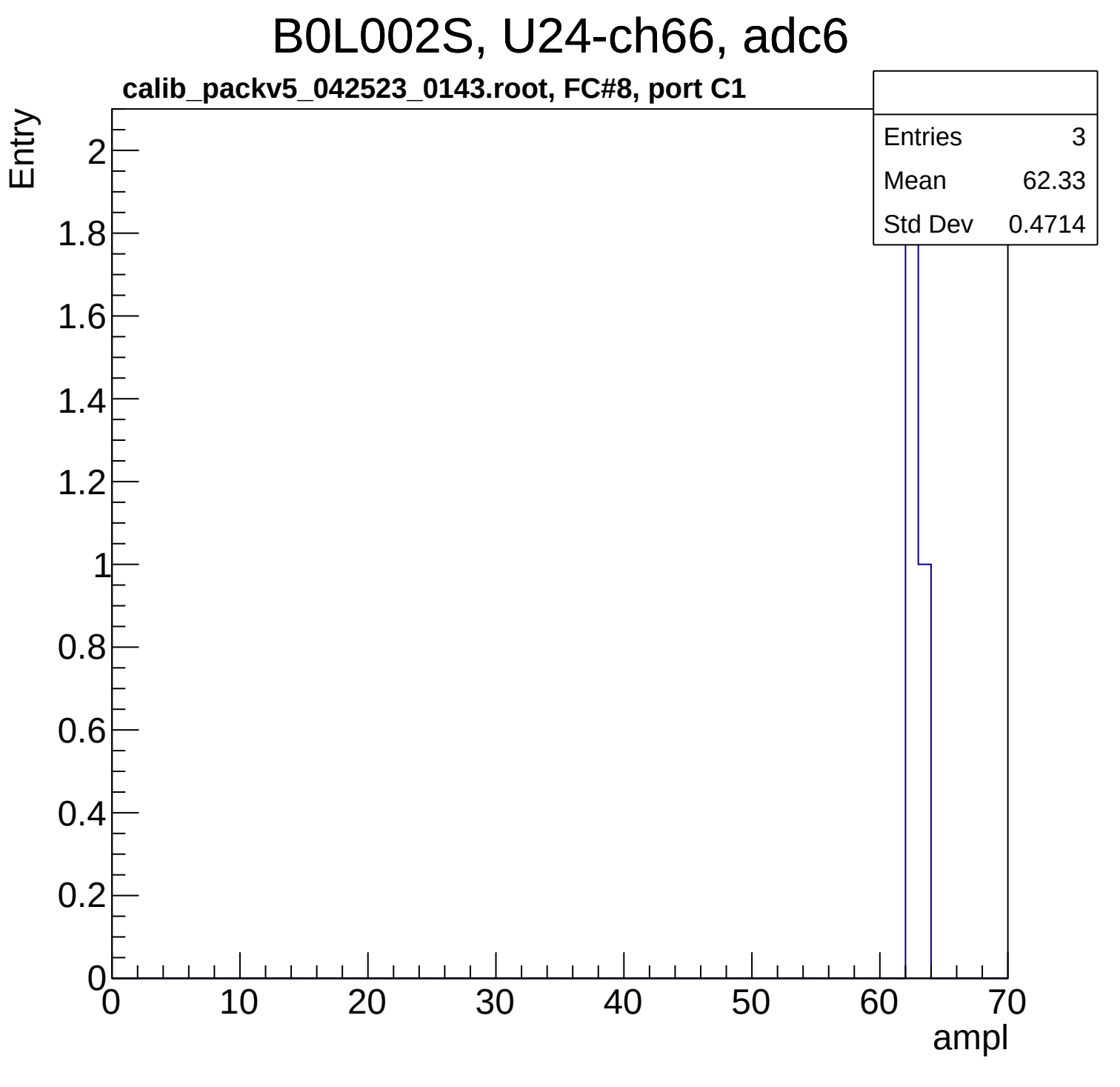
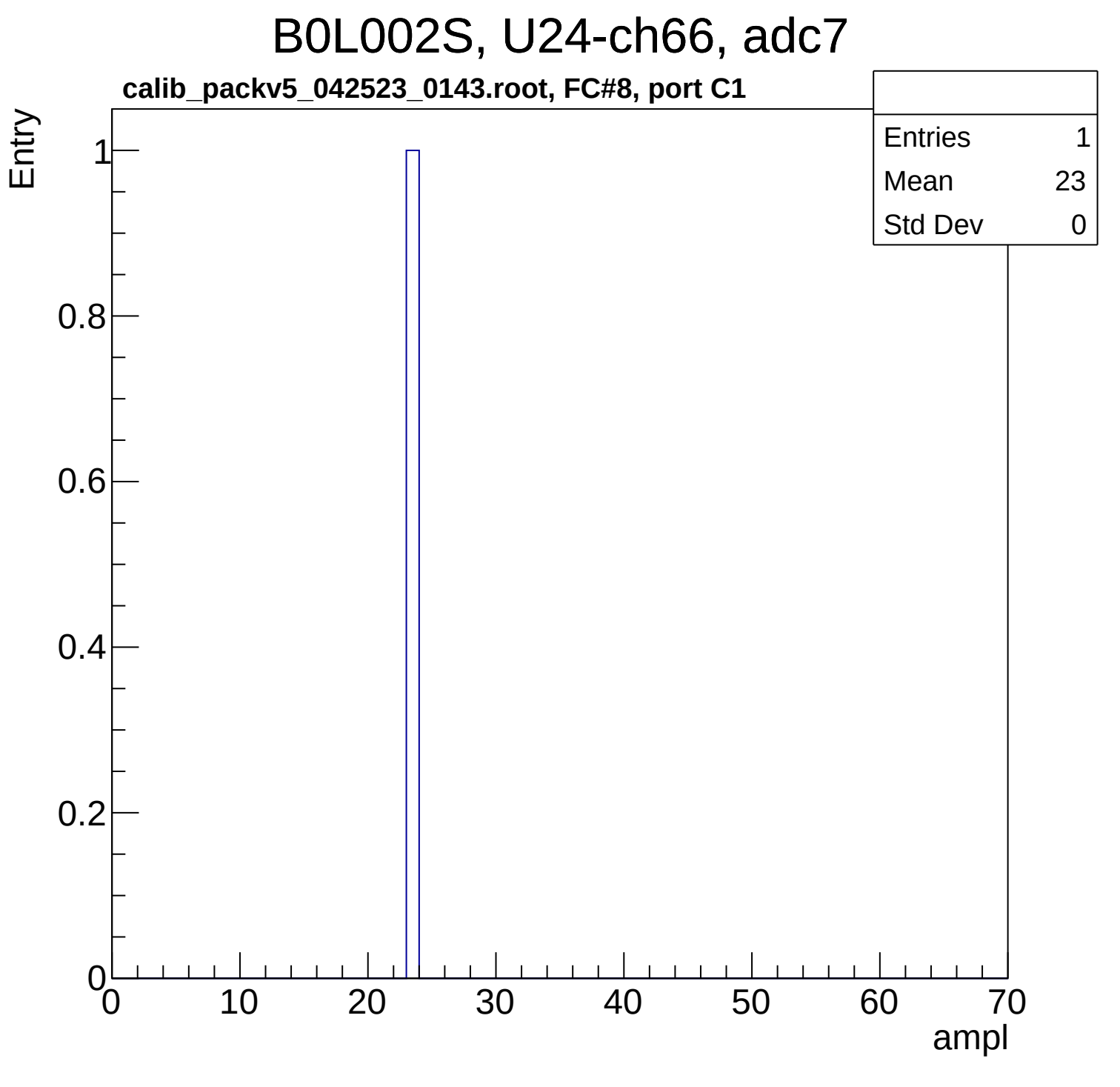

B0L002S, U24-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.47
Std Dev 1.464
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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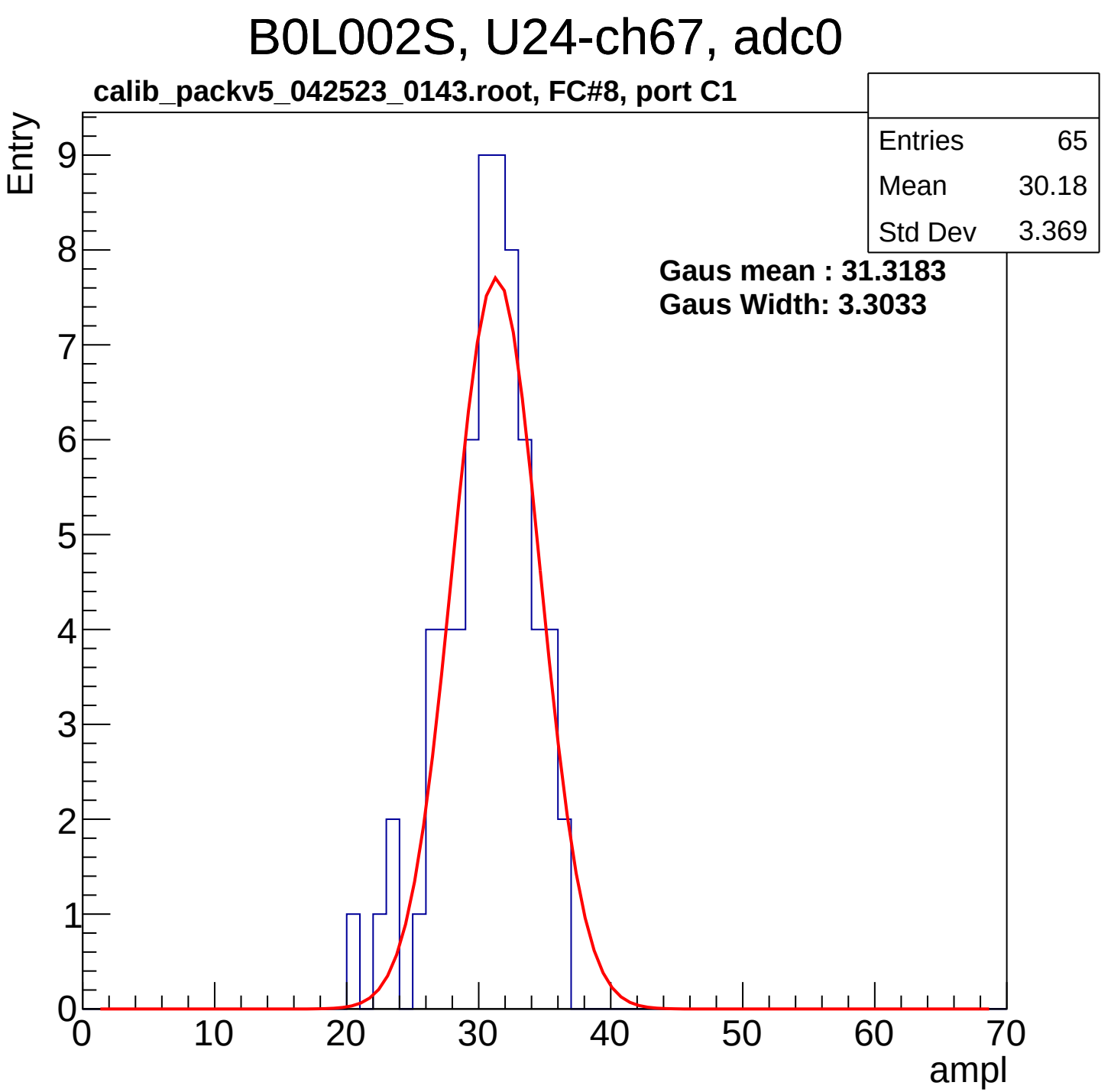
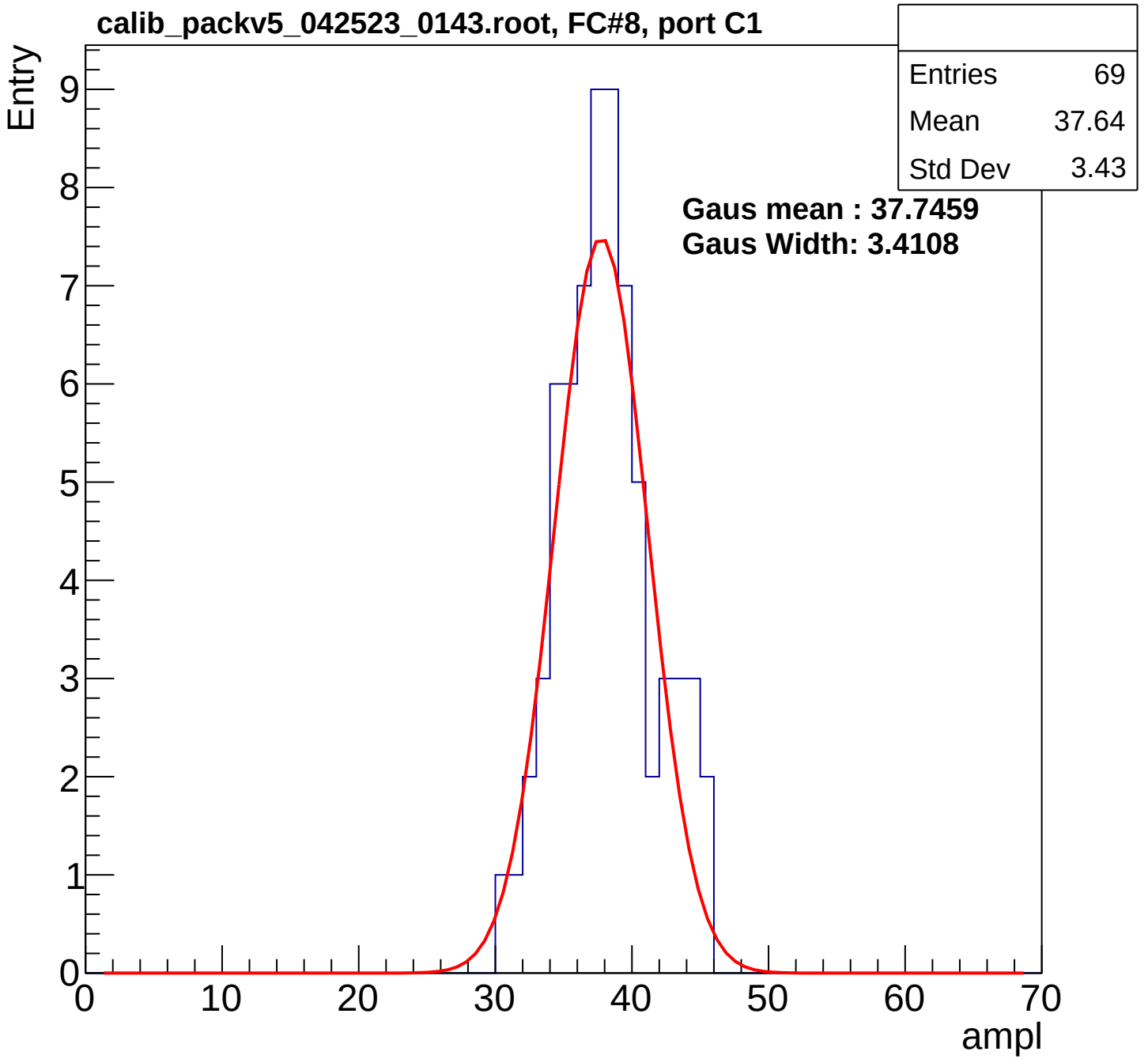
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.18
Std Dev 3.369
Gaus mean : 31.3183
Gaus Width: 3.3033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.64
Std Dev 3.43
Gaus mean : 37.7459
Gaus Width: 3.4108
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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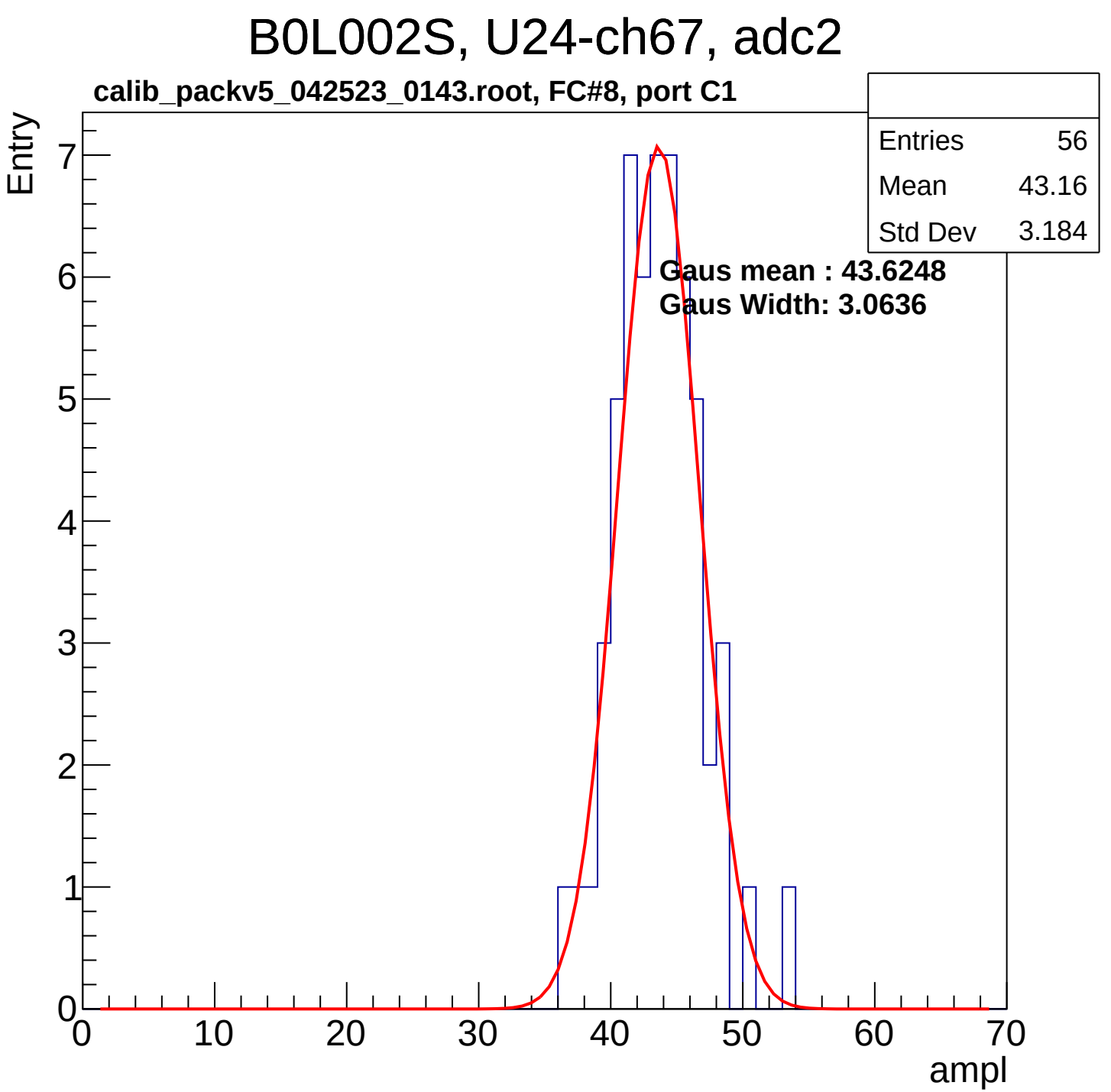
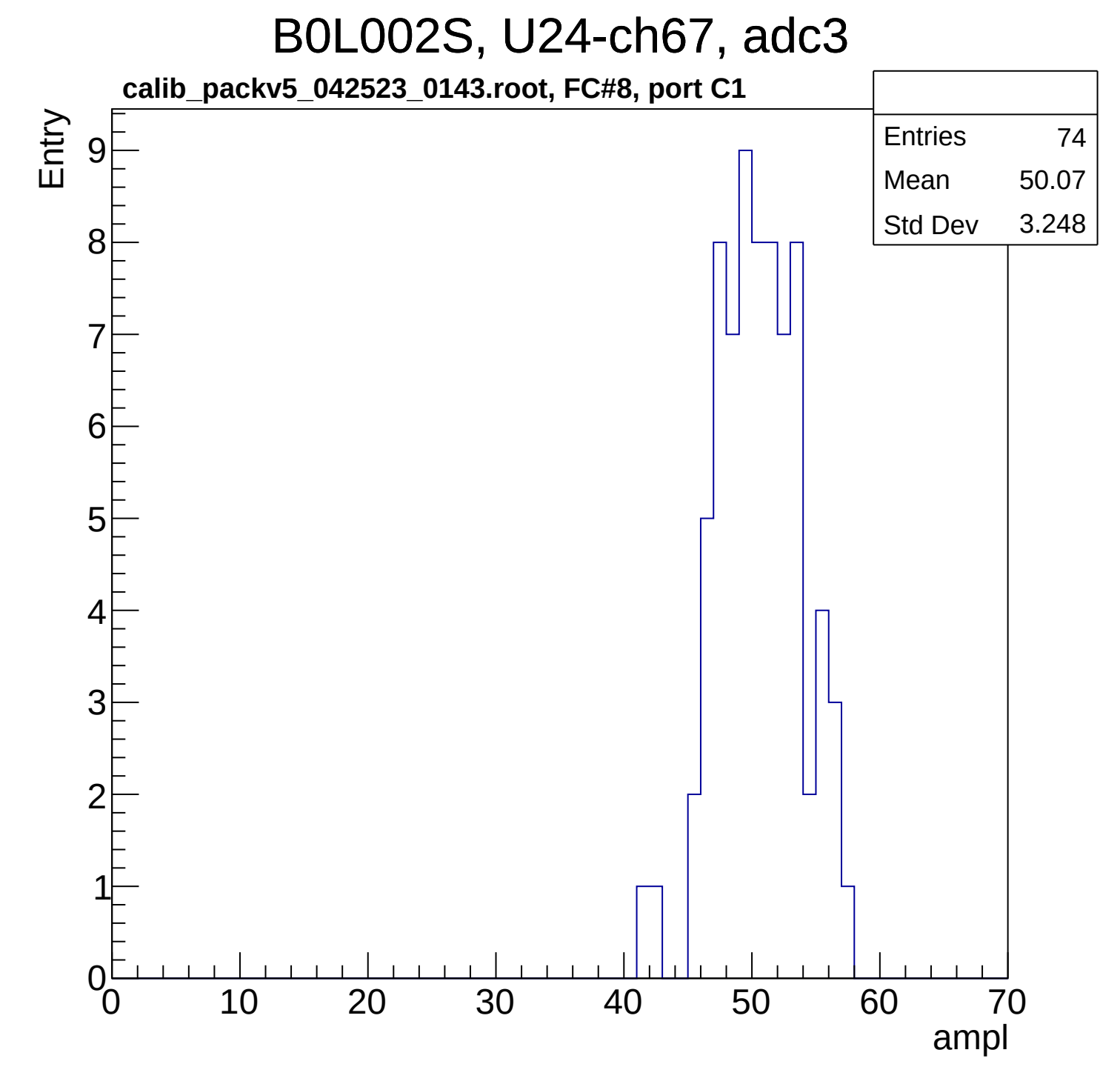

B0L002S, U24-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 43.16
Std Dev 3.184
Gaus mean : 43.6248
Gaus Width: 3.0636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 50.07
Std Dev 3.248
Entry
ampl
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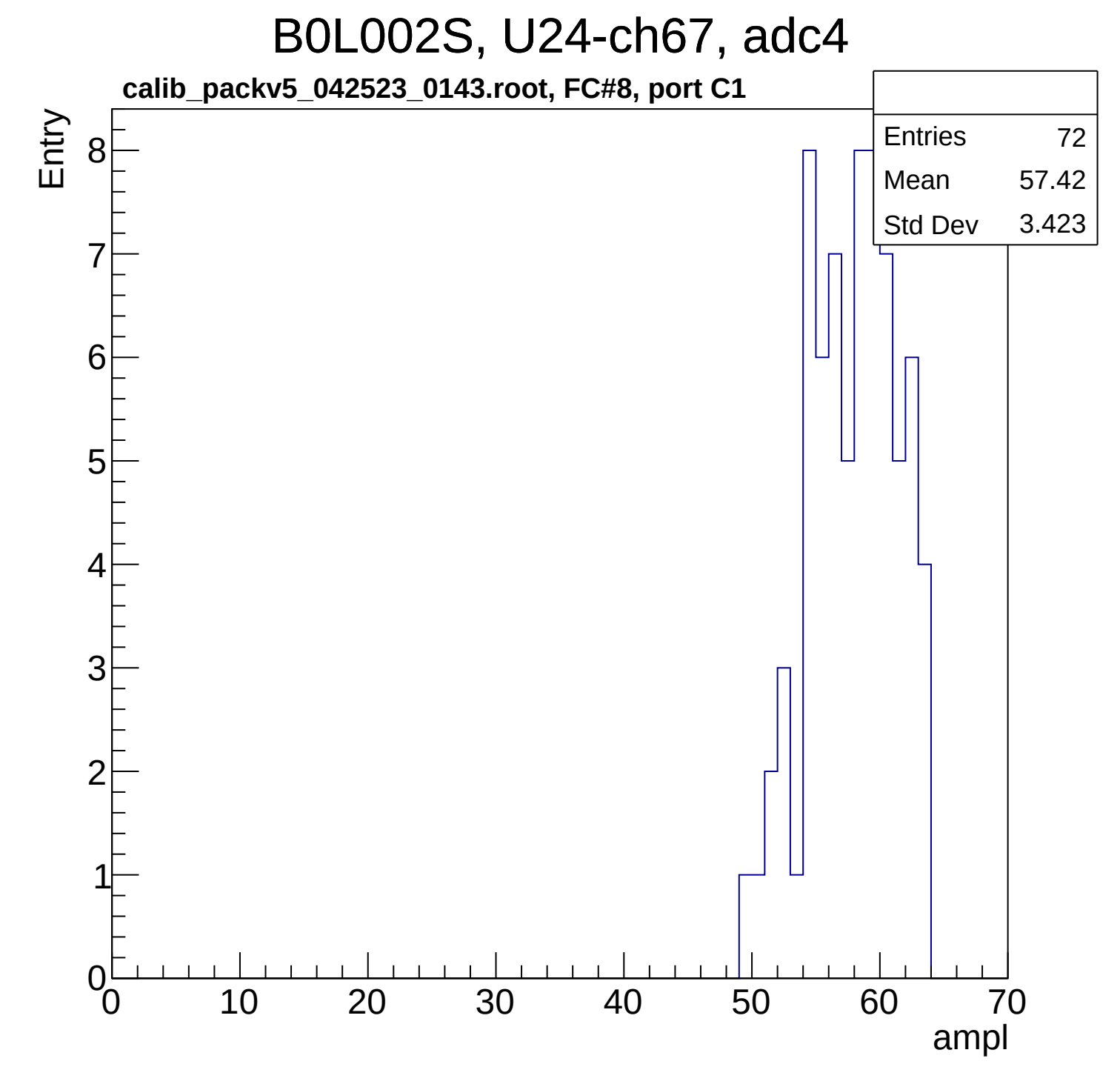
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 57.42
Std Dev 3.423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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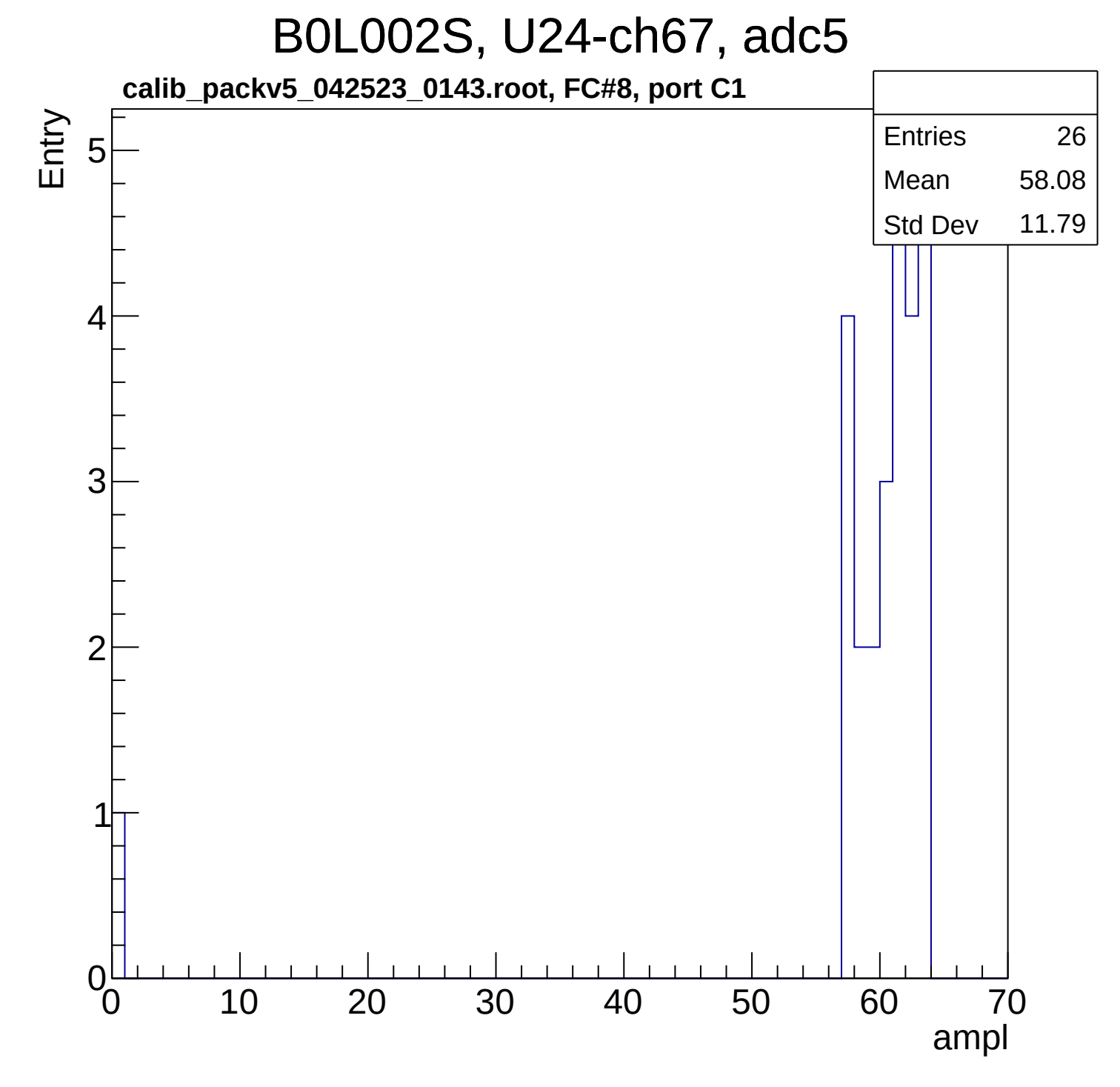
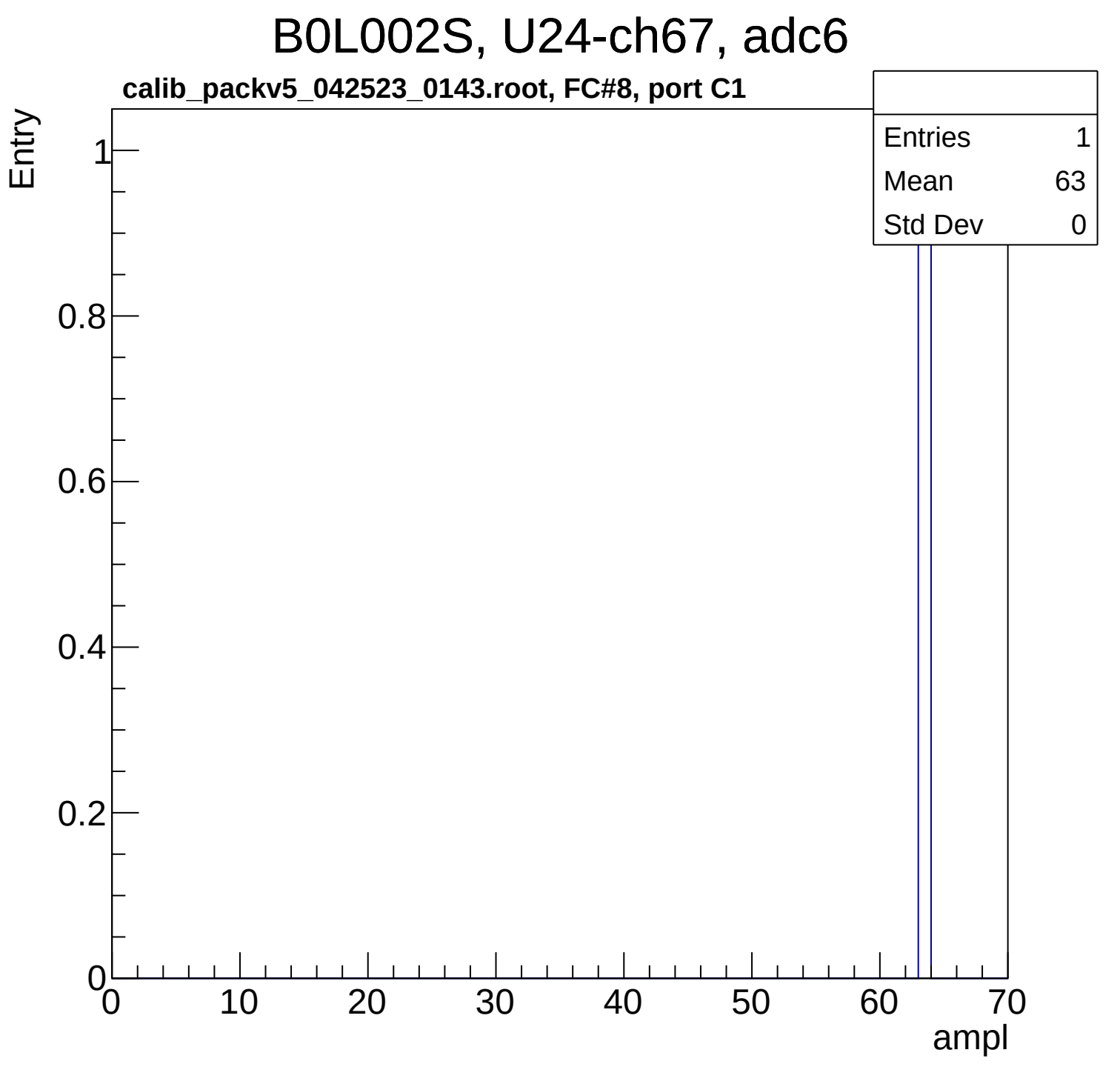

B0L002S, U24-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.08
Std Dev 11.79
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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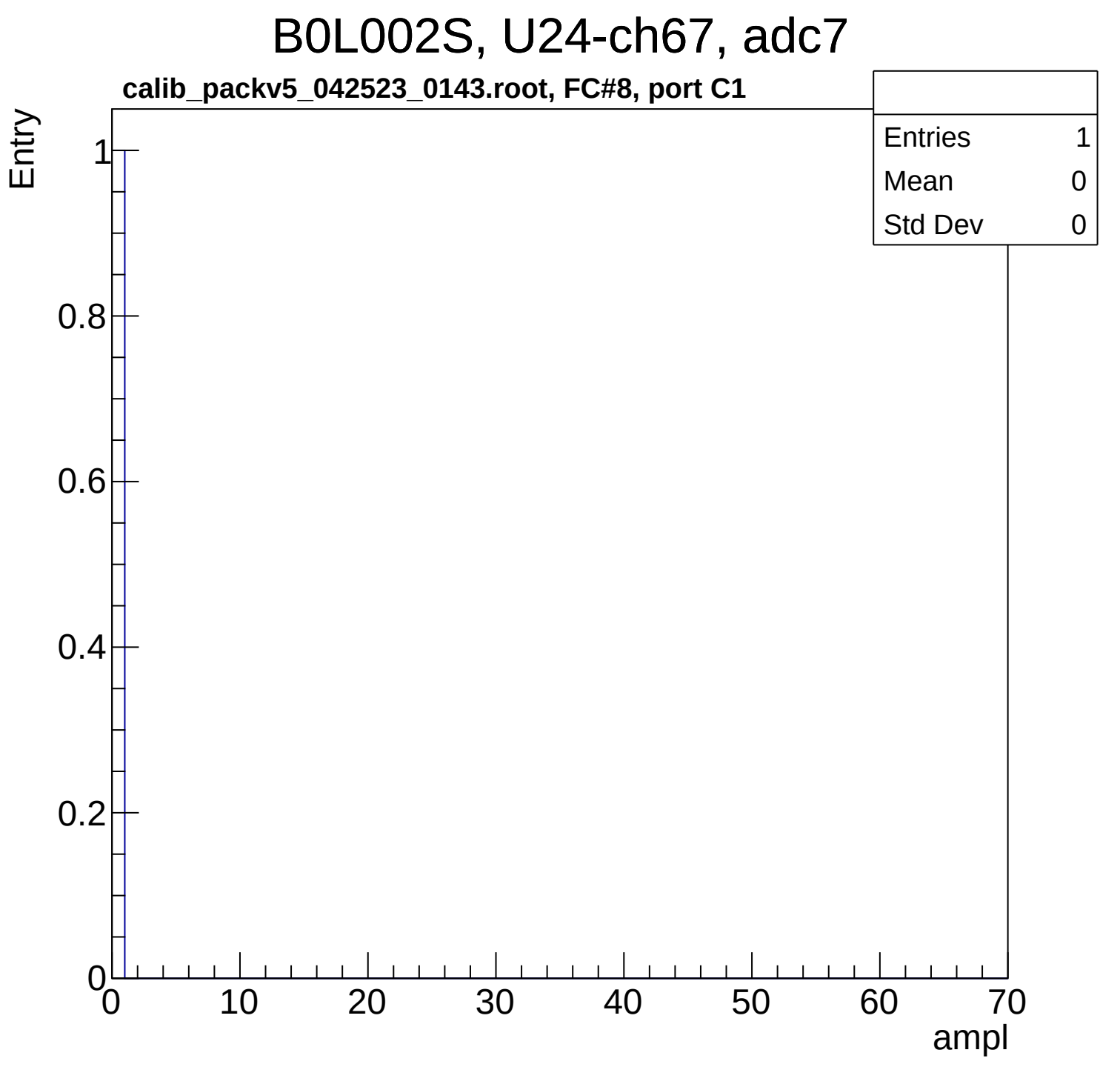
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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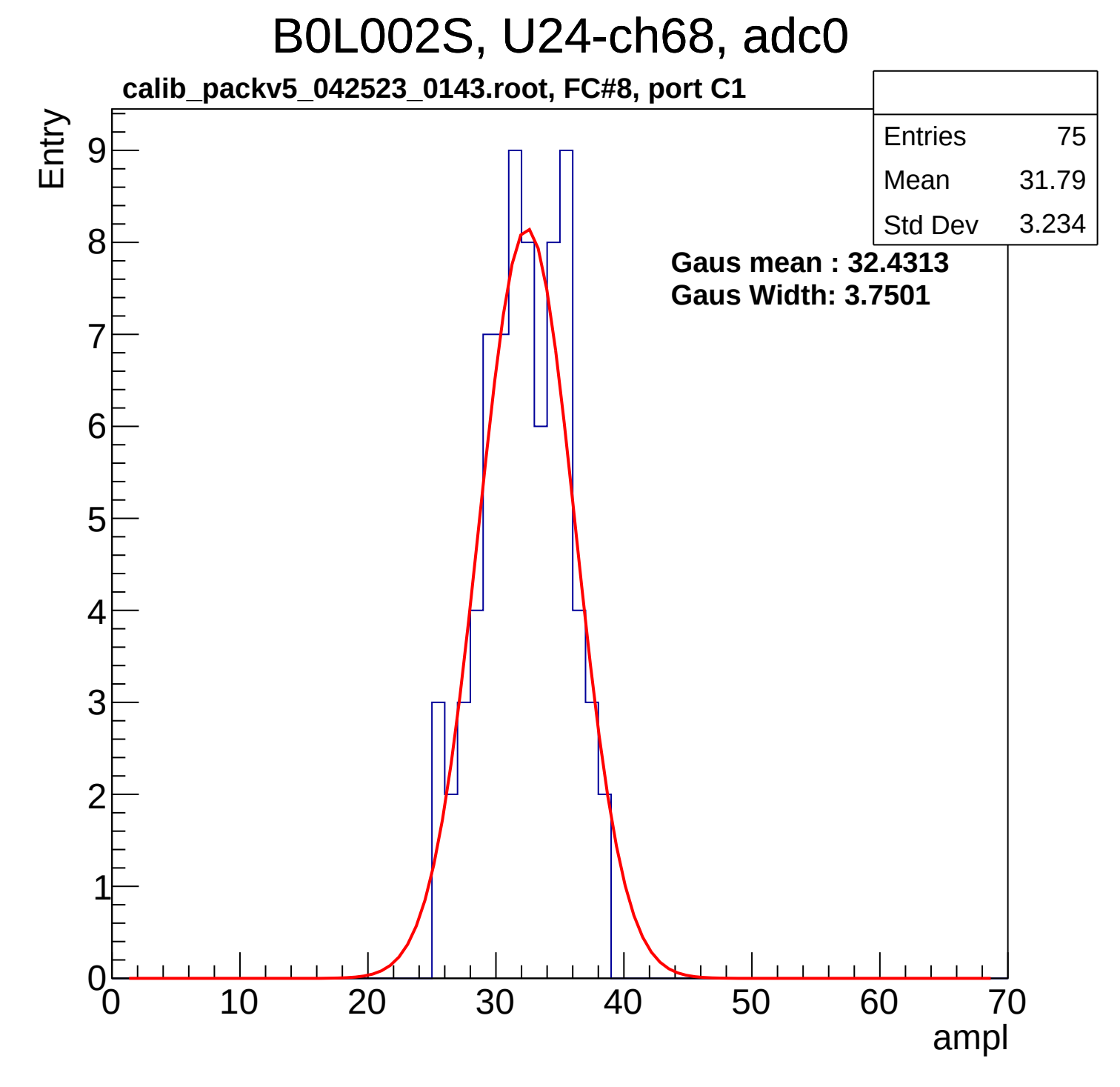
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.79
Std Dev 3.234
Gaus mean : 32.4313
Gaus Width: 3.7501
Entry
ampl
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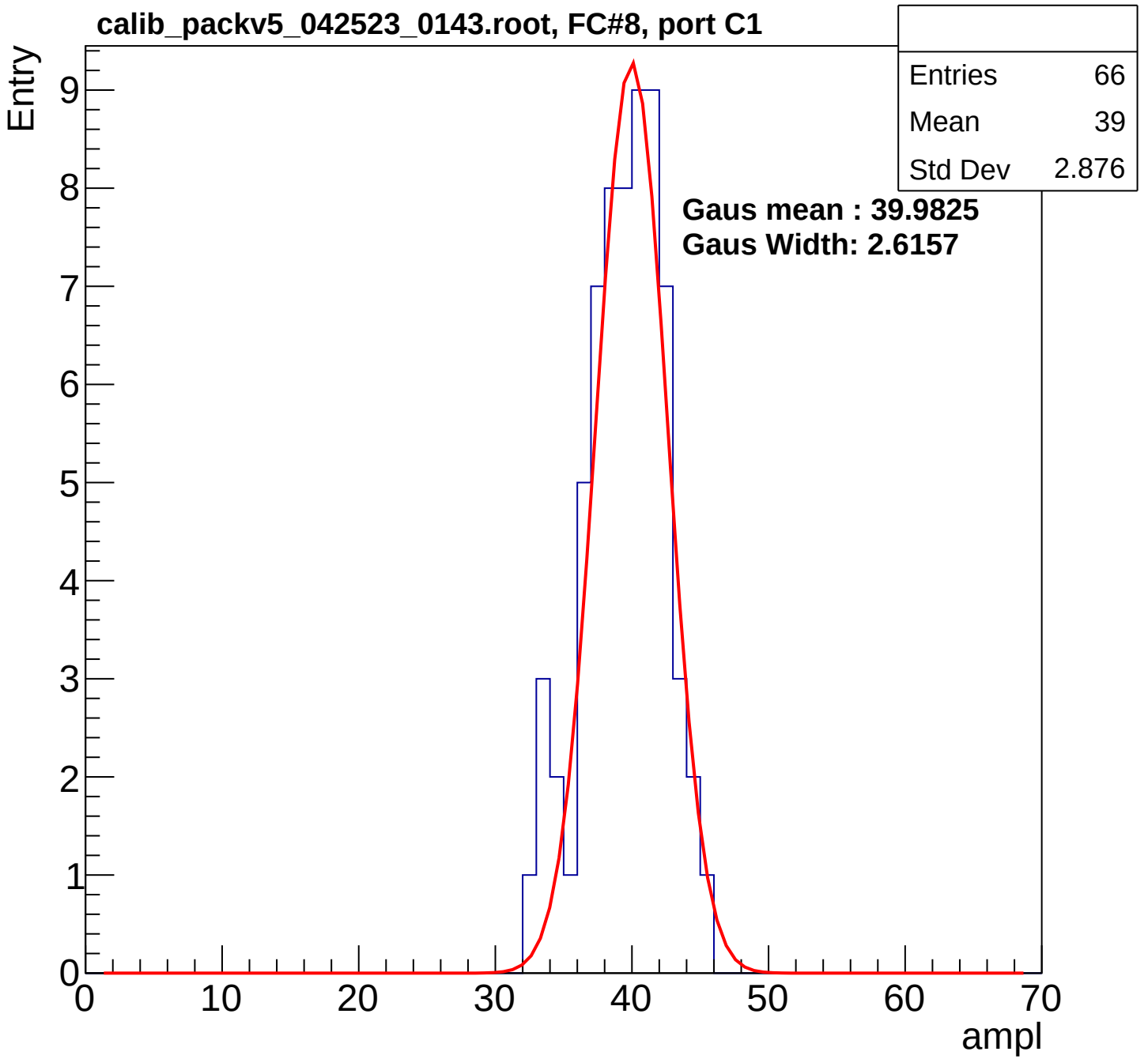
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 39
Std Dev 2.876
Gaus mean : 39.9825
Gaus Width: 2.6157
Entry
ampl
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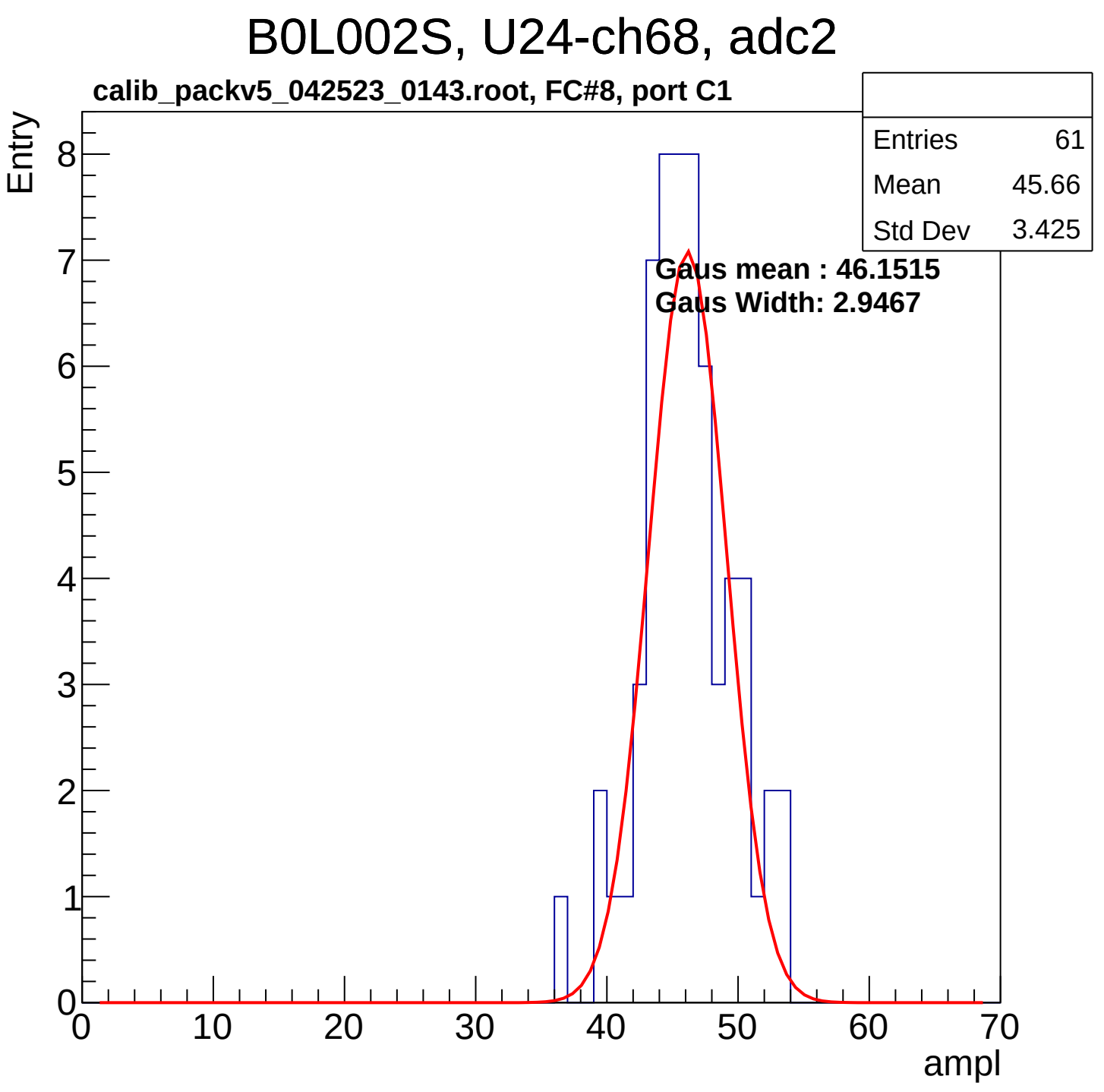
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 45.66
Std Dev 3.425
Gaus mean : 46.1515
Gaus Width: 2.9467
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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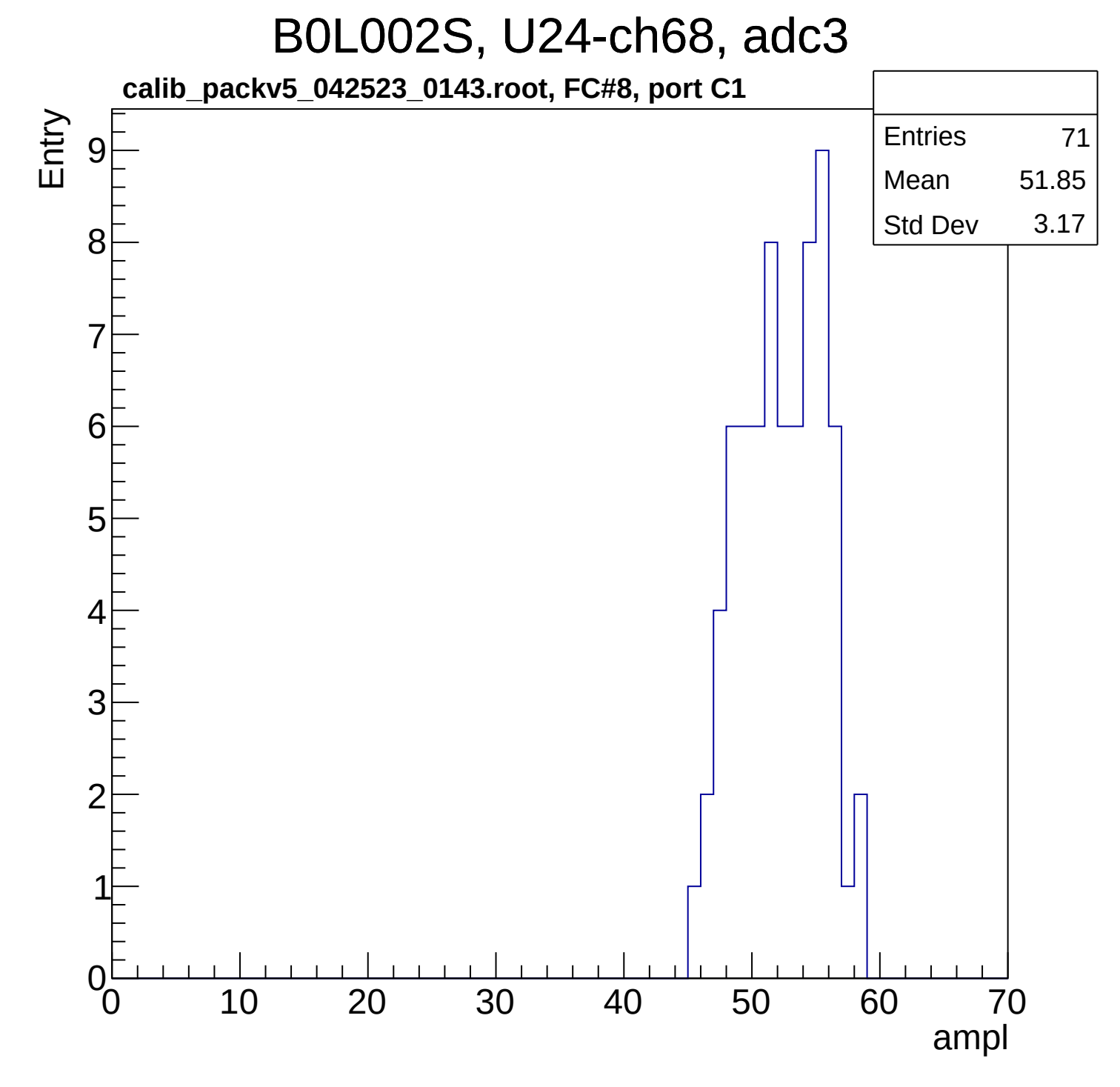
40
50
60
70


B0L002S, U24-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.85
Std Dev 3.17
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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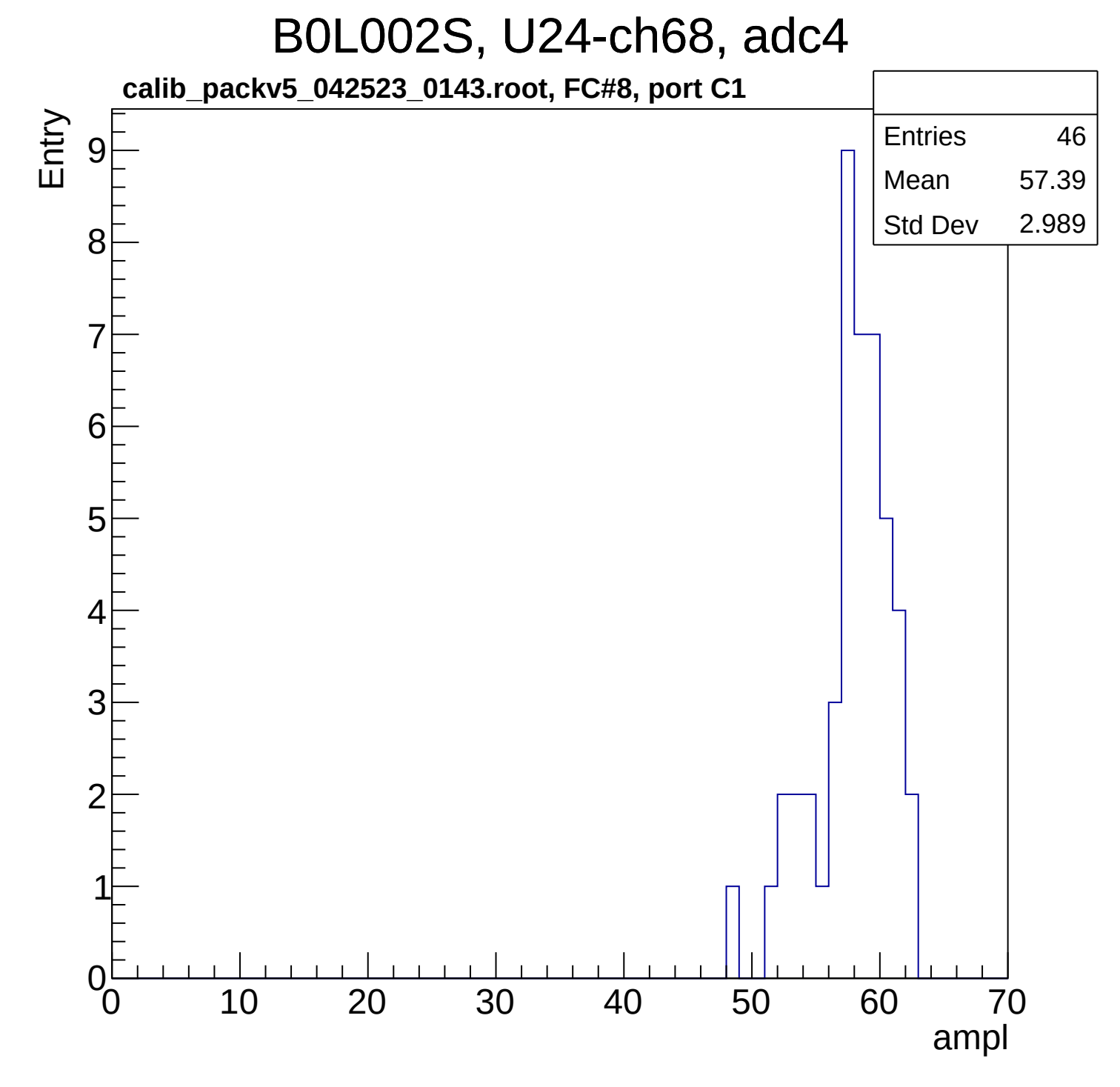
B0L002S, U24-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 57.39
Std Dev 2.989
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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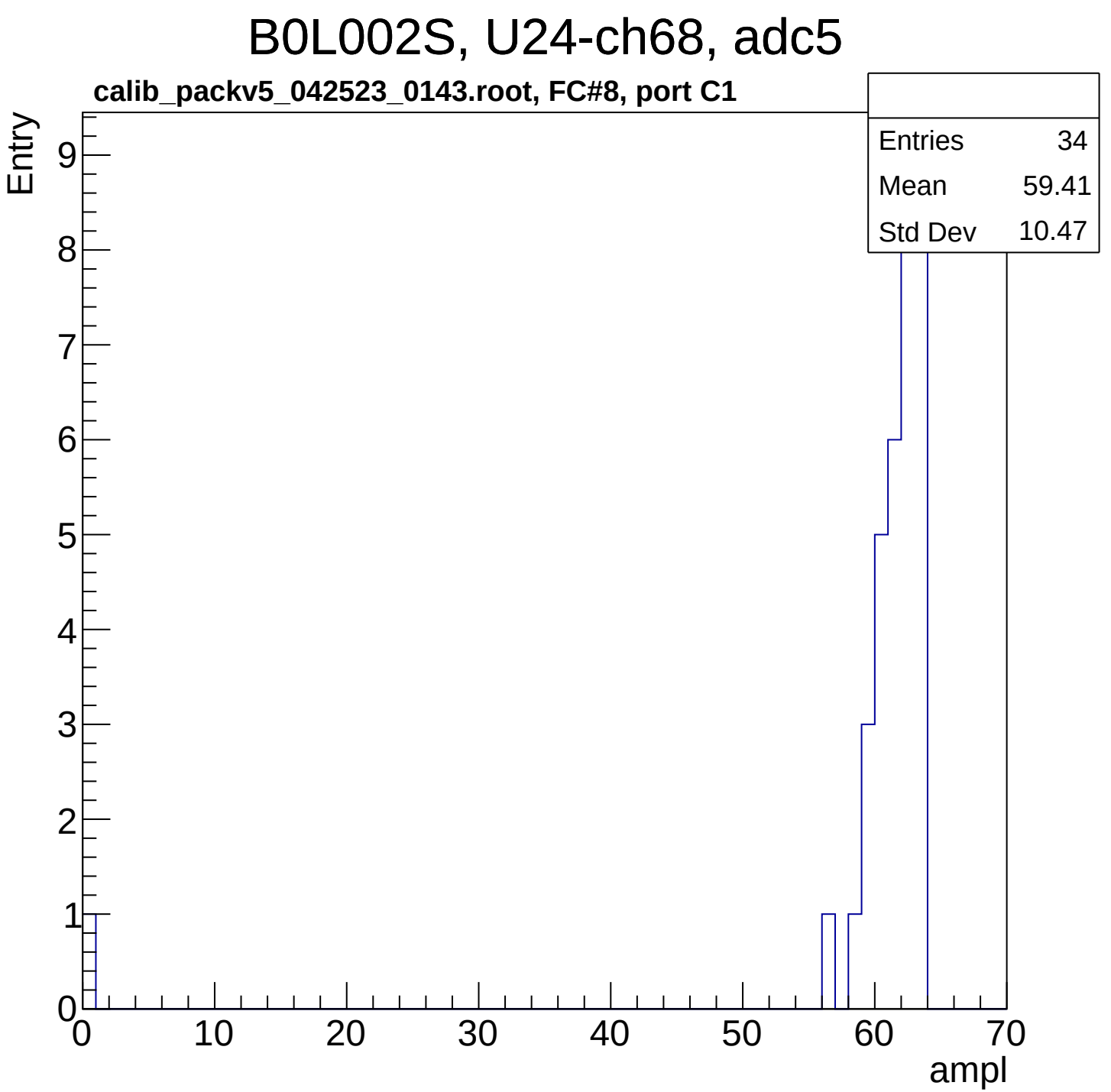
B0L002S, U24-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.41
Std Dev 10.47
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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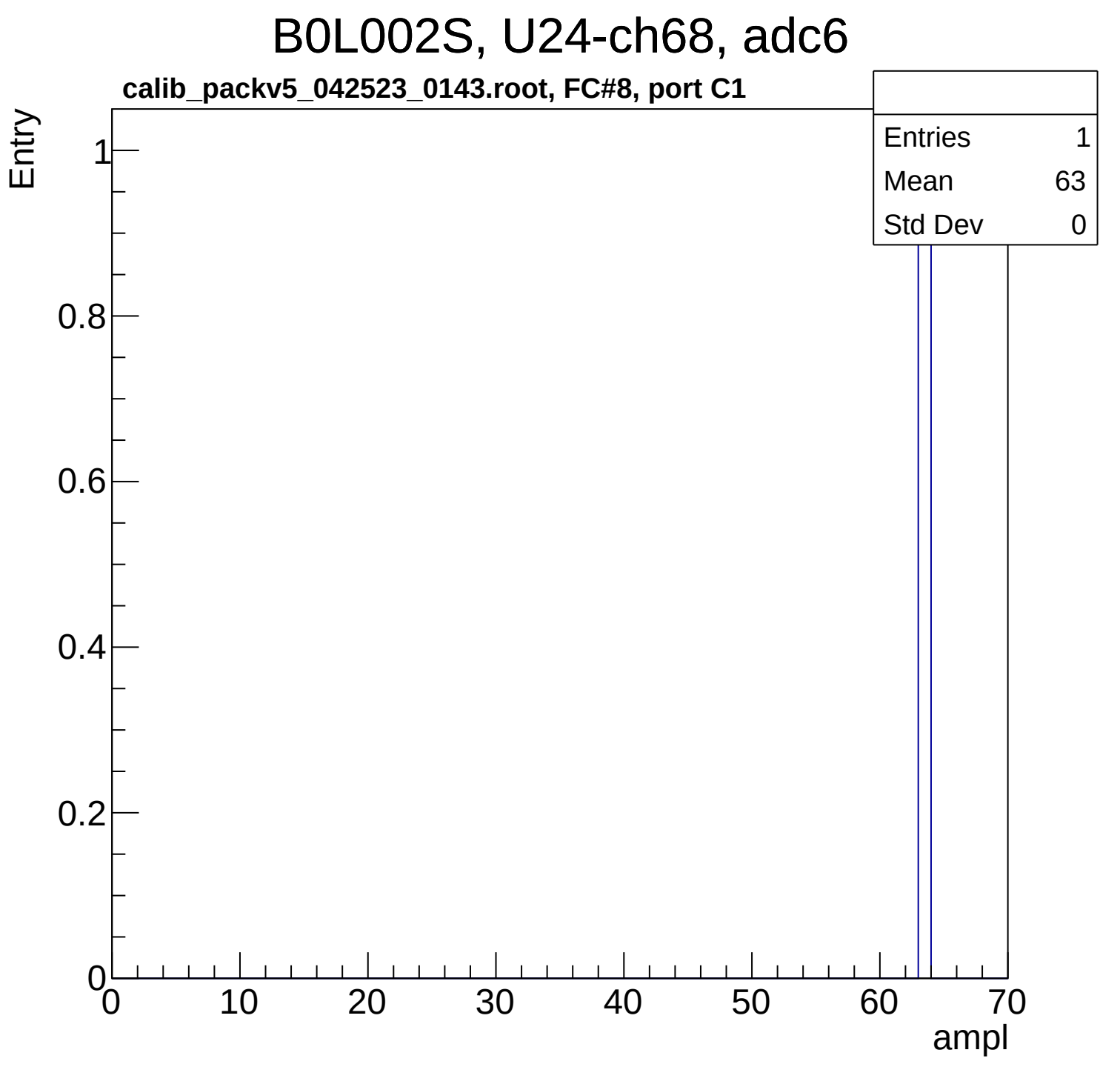
B0L002S, U24-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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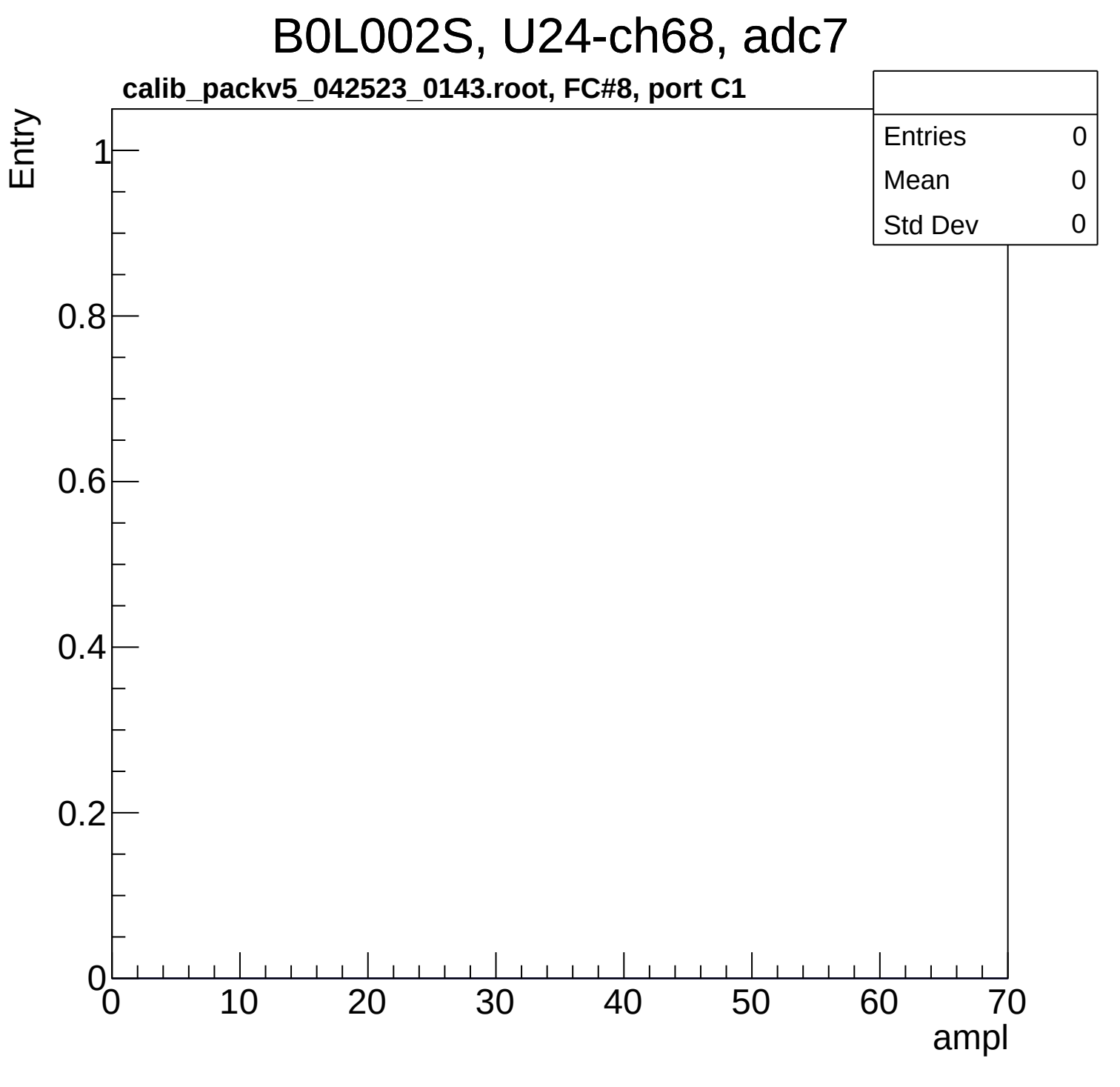
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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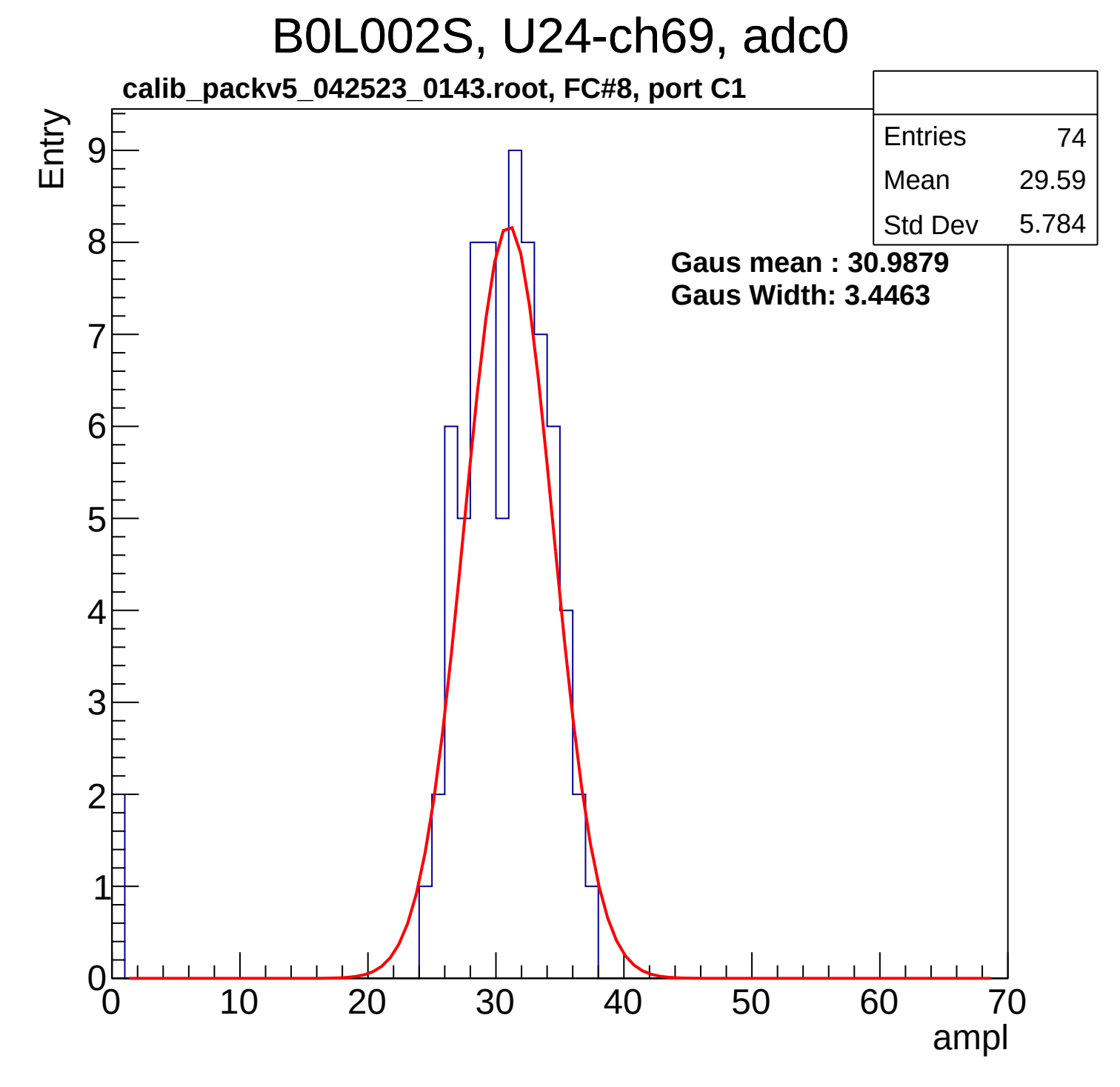
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.59
Std Dev 5.784
Gaus mean : 30.9879
Gaus Width: 3.4463
Entry
ampl
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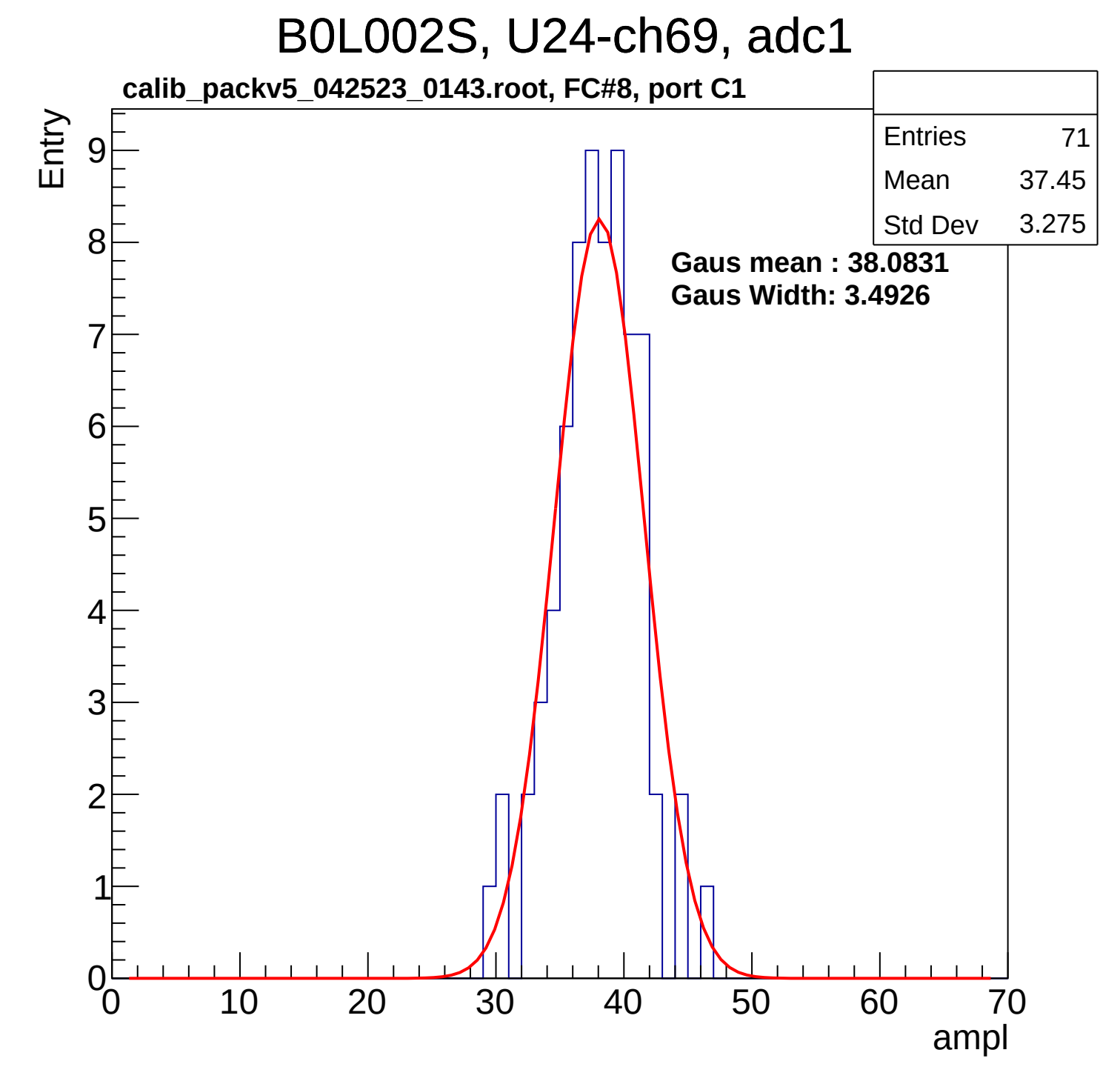
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.45
Std Dev 3.275
Gaus mean : 38.0831
Gaus Width: 3.4926
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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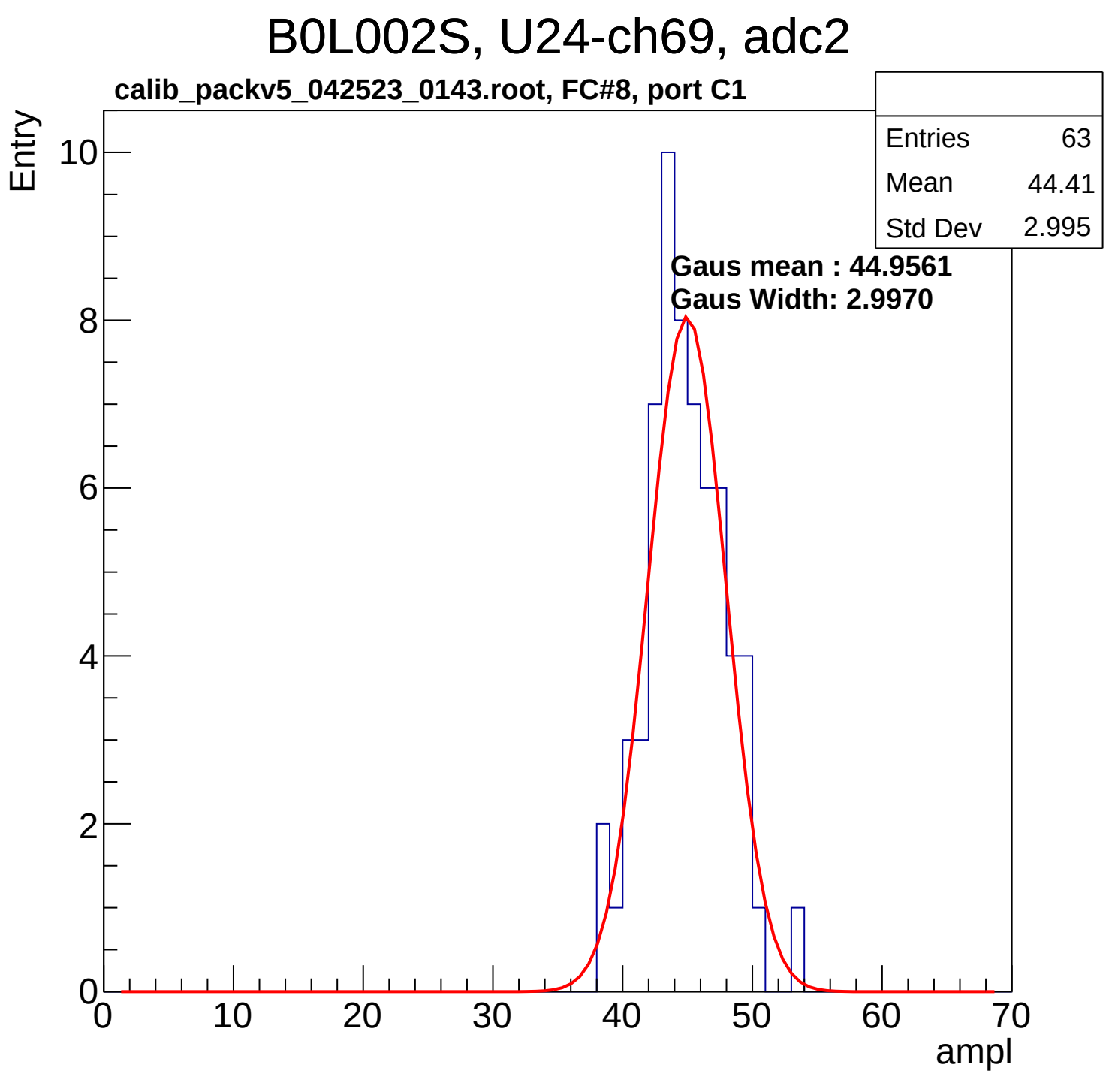
B0L002S, U24-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.41
Std Dev 2.995
Gaus mean : 44.9561
Gaus Width: 2.9970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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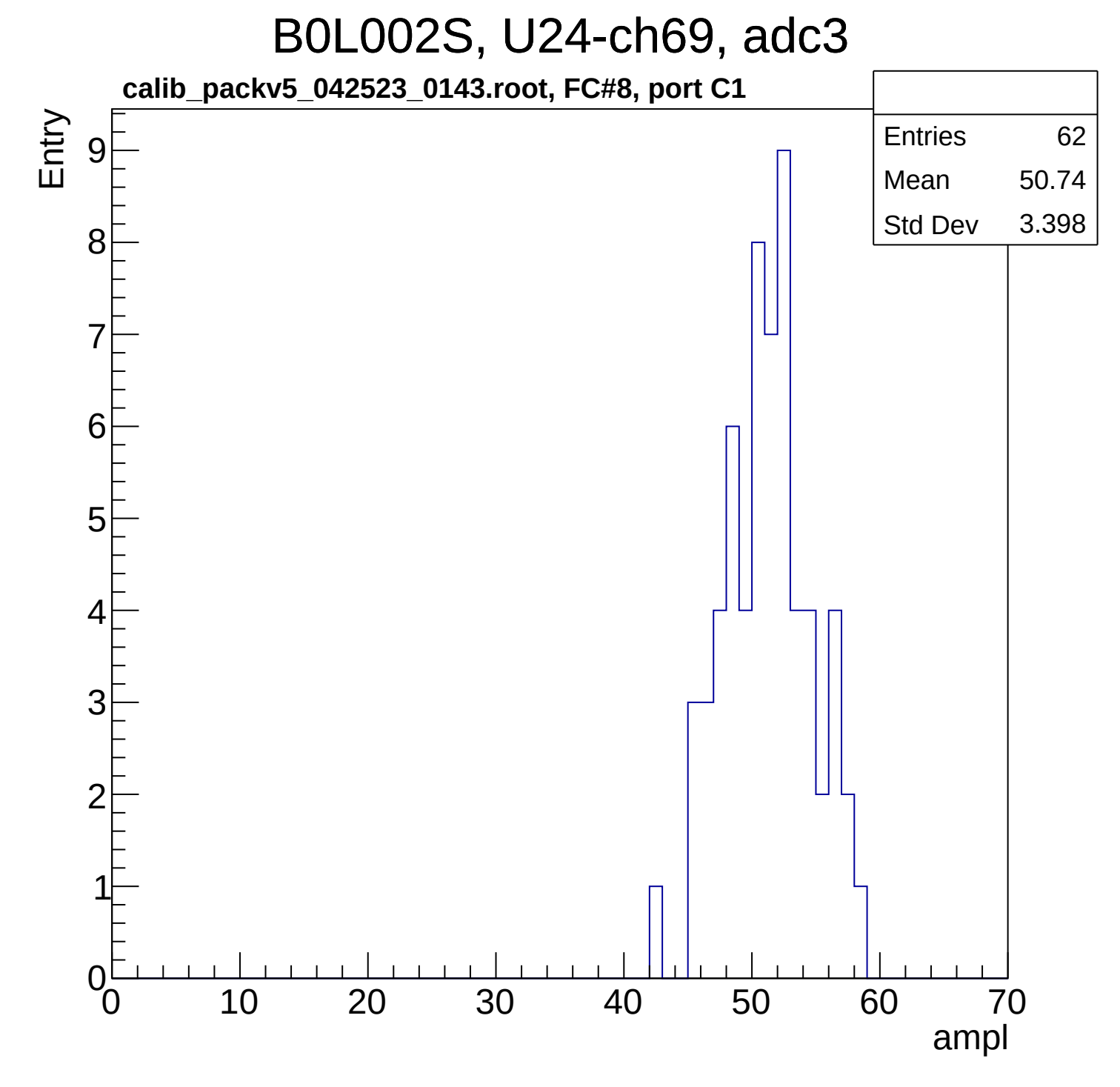
B0L002S, U24-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.74
Std Dev 3.398
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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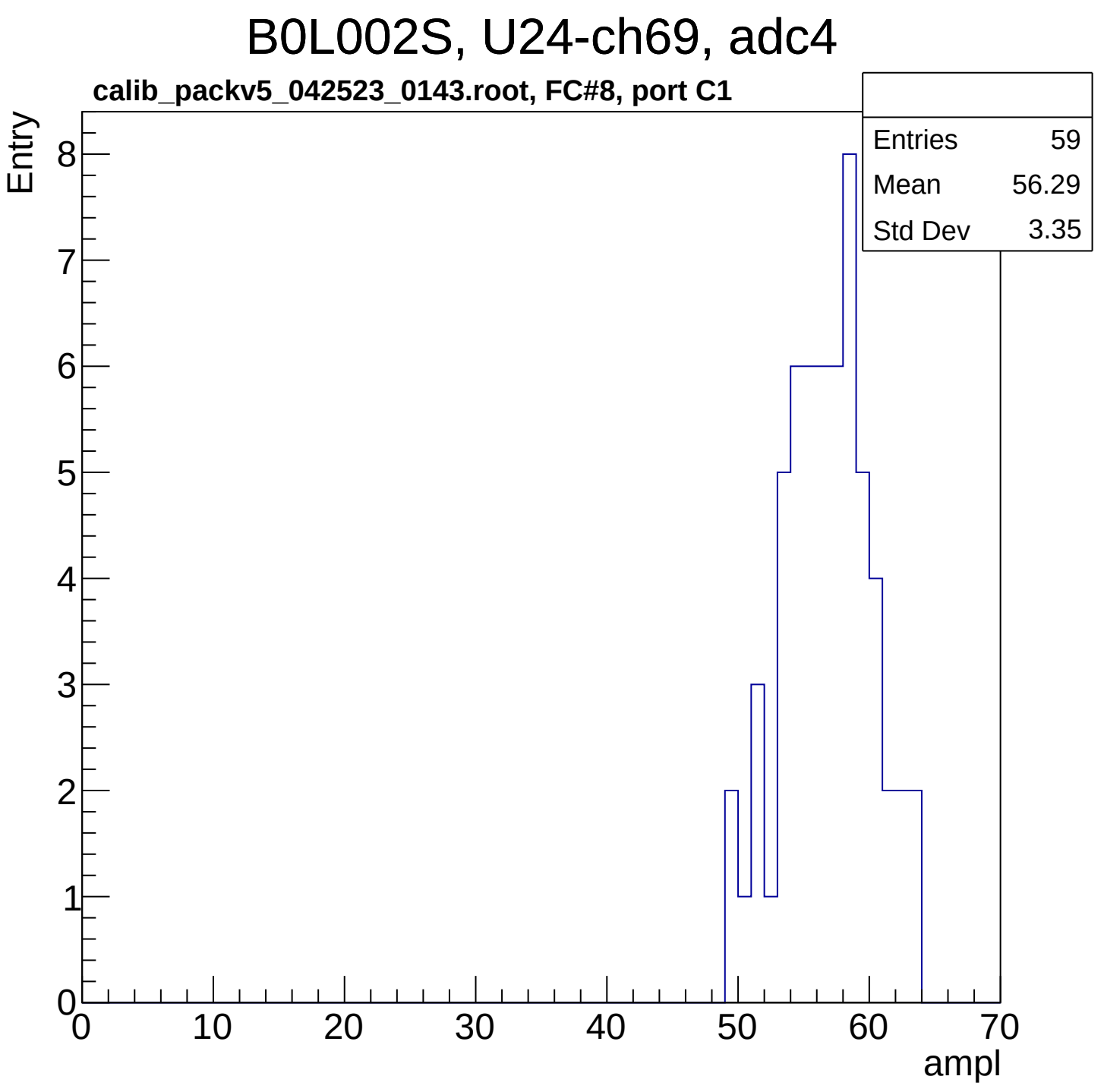
B0L002S, U24-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.29
Std Dev 3.35
Entry
ampl
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1
2
3
4
5
6
7
8
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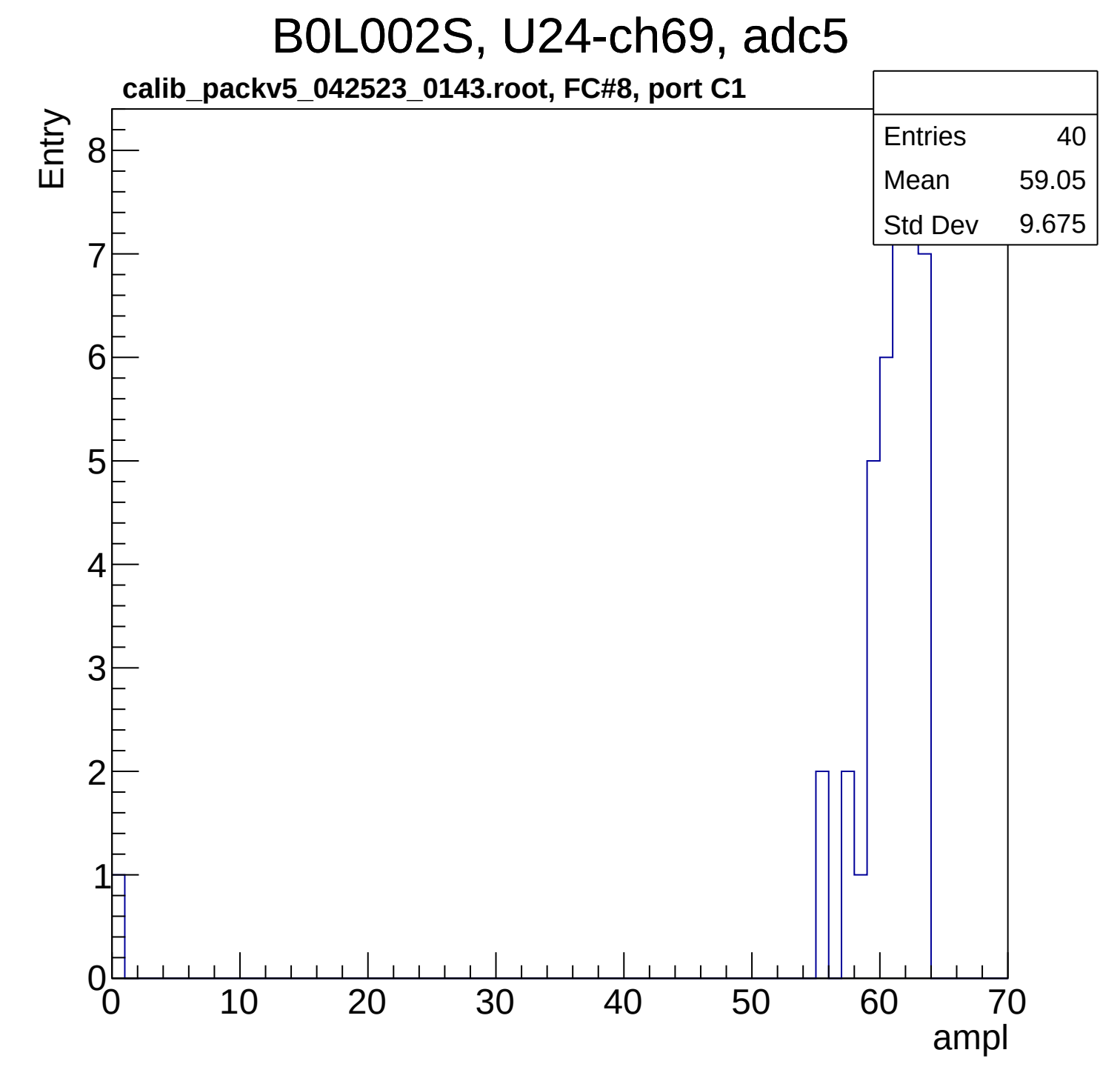
10
20
30
40
50
60
70


B0L002S, U24-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.05
Std Dev 9.675
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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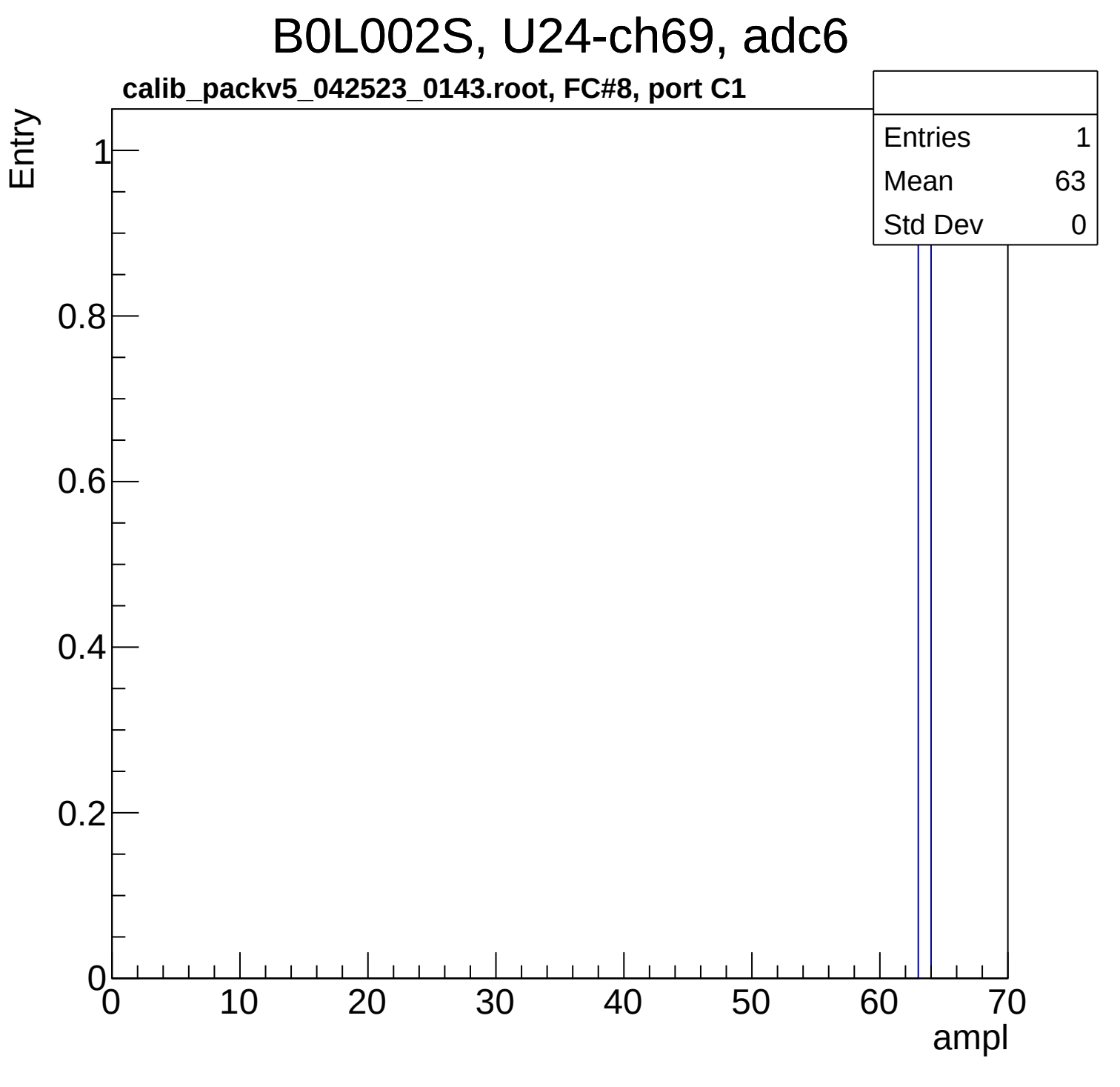
B0L002S, U24-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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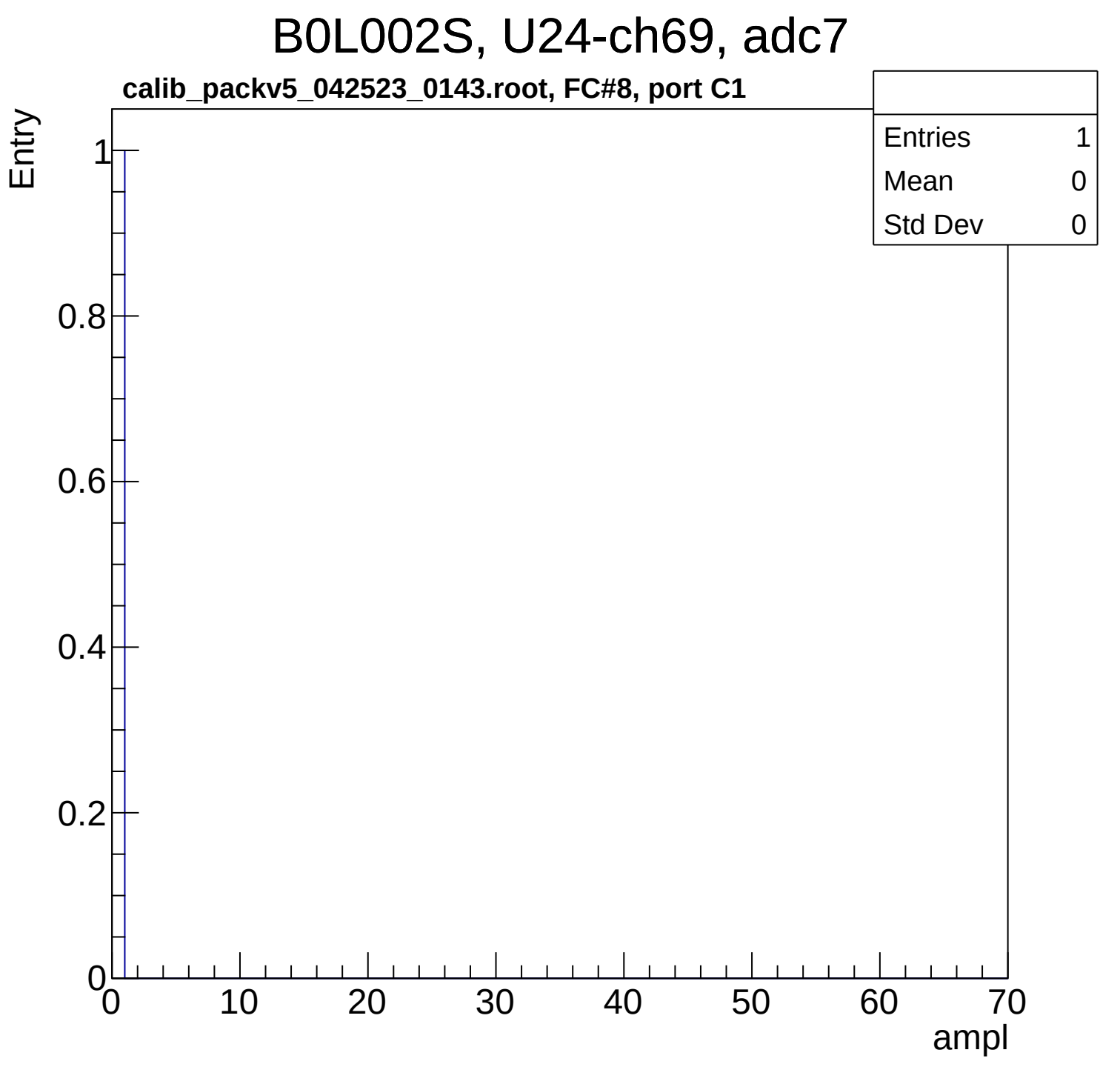
70
ampl


B0L002S, U24-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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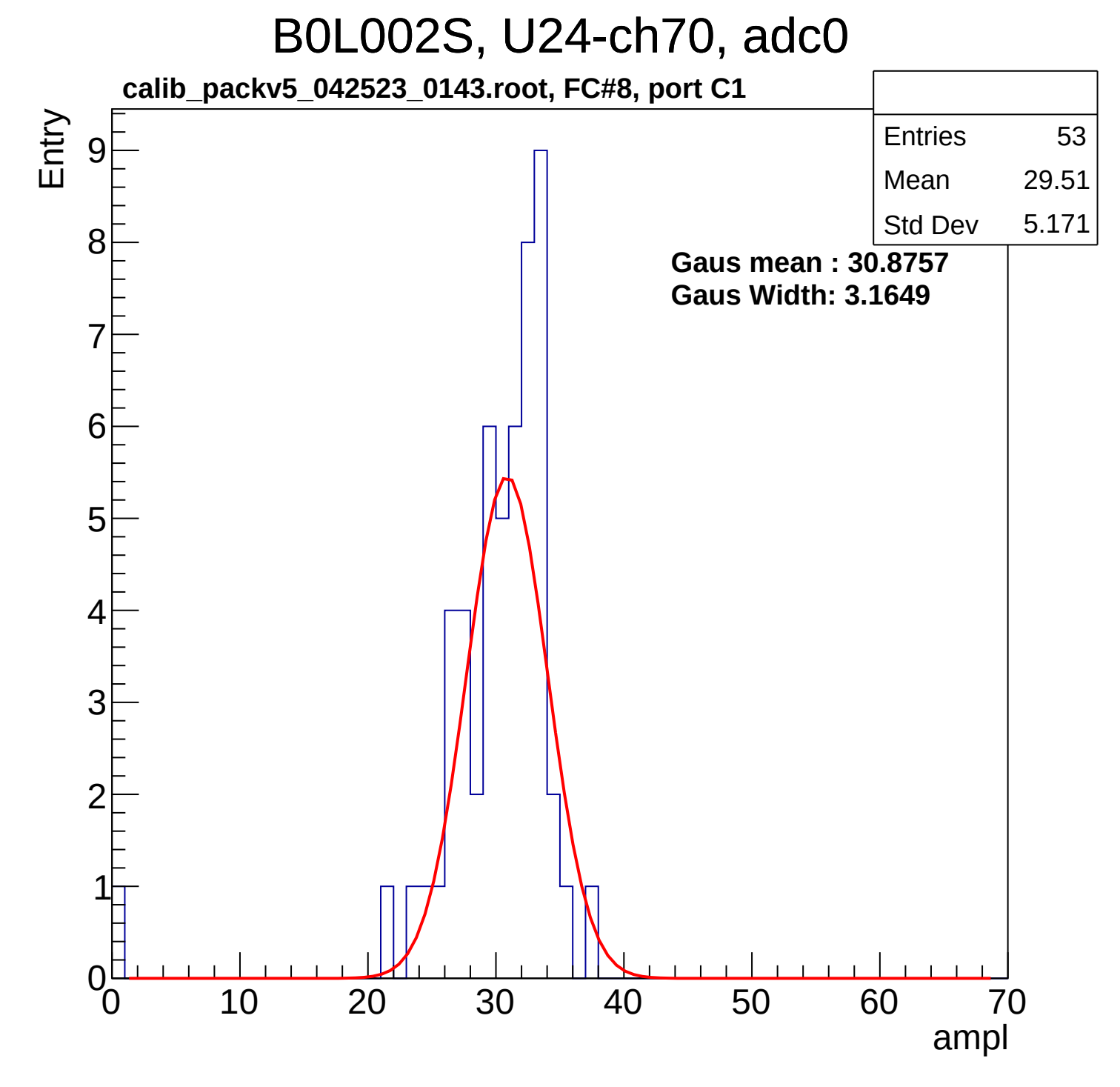
50
60
70
ampl


B0L002S, U24-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 29.51
Std Dev 5.171
Gaus mean : 30.8757
Gaus Width: 3.1649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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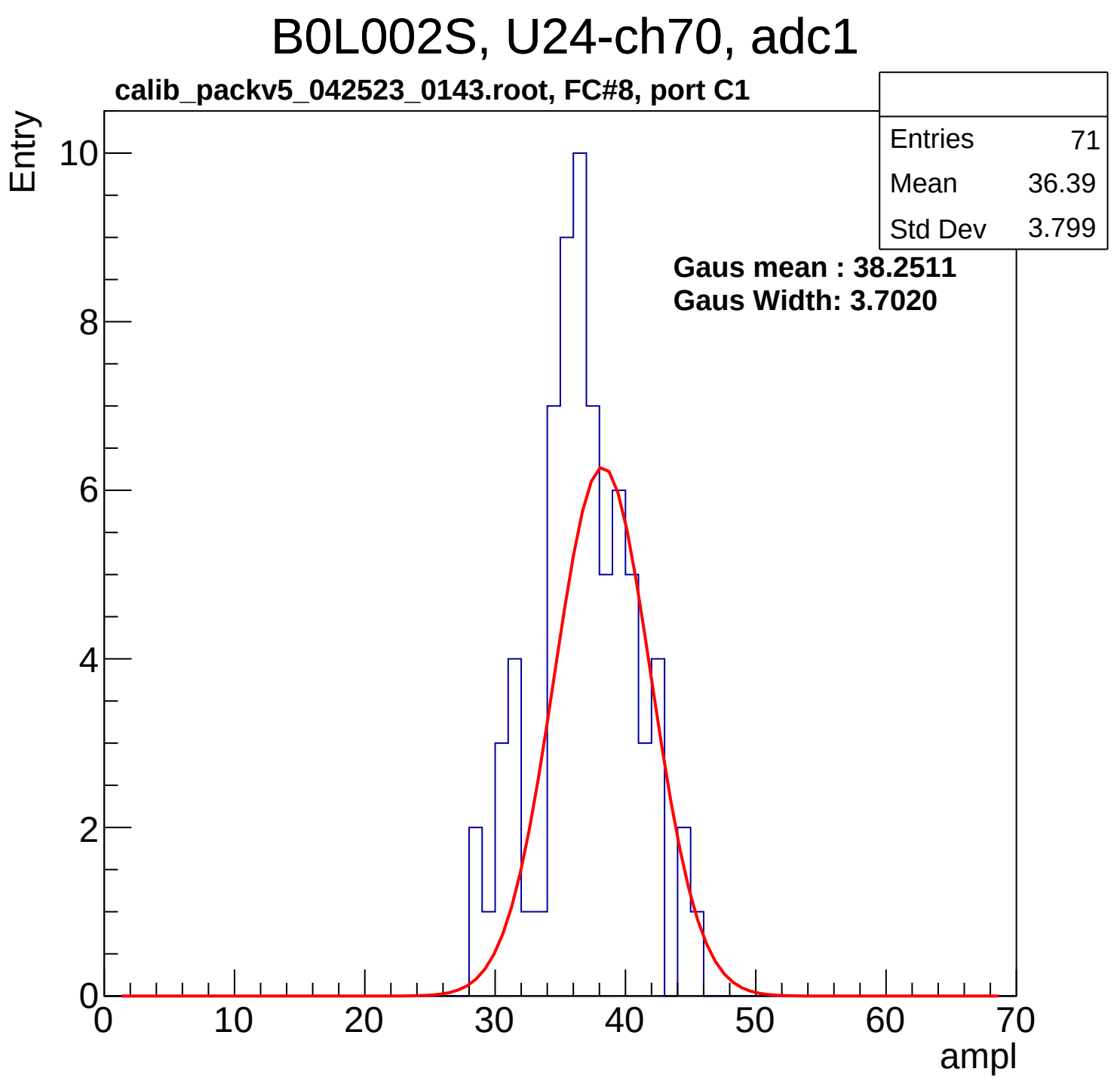
B0L002S, U24-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.39
Std Dev 3.799
Gaus mean : 38.2511
Gaus Width: 3.7020
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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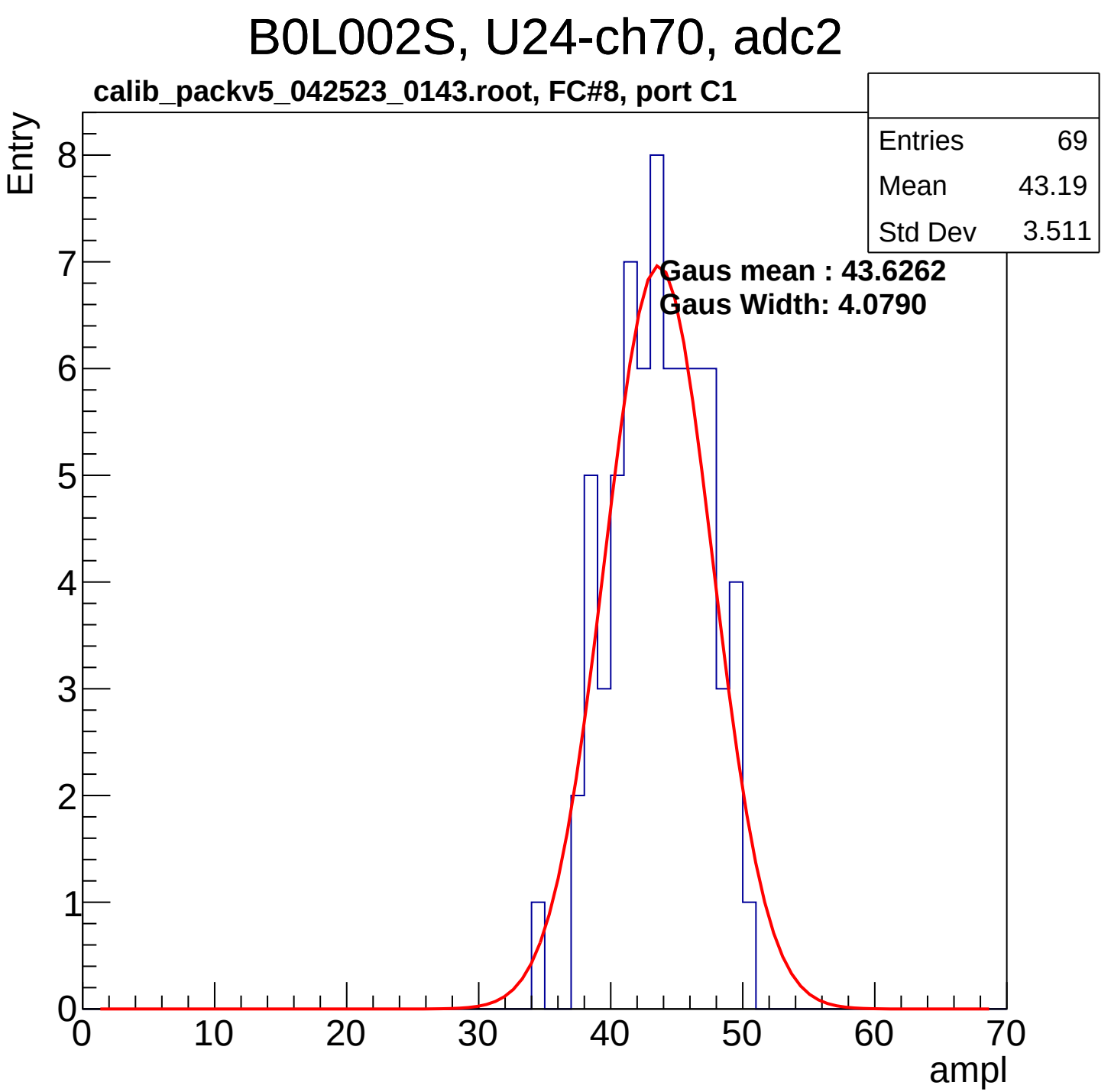
B0L002S, U24-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.19
Std Dev 3.511
Gaus mean : 43.6262
Gaus Width: 4.0790
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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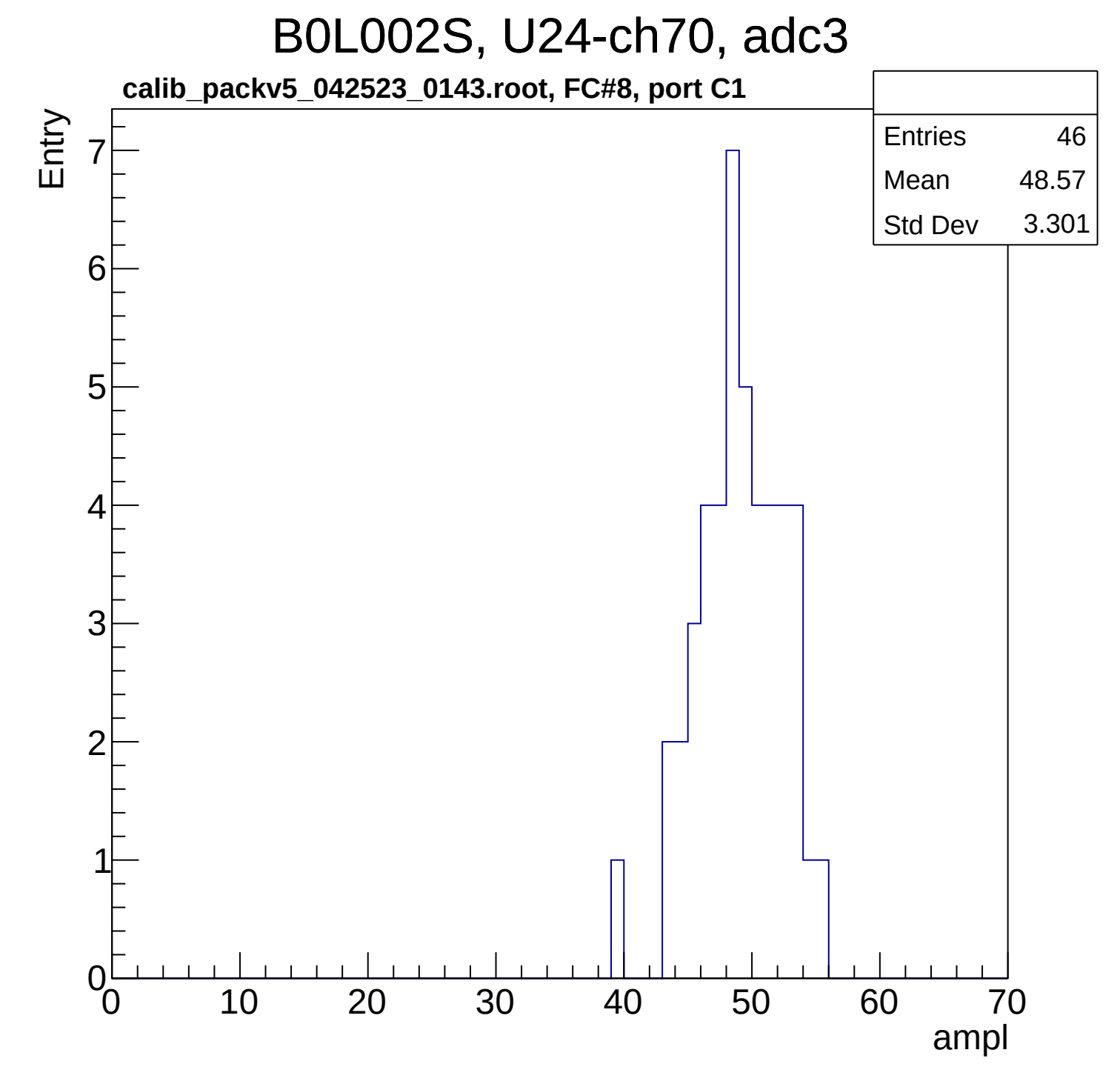
B0L002S, U24-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 48.57
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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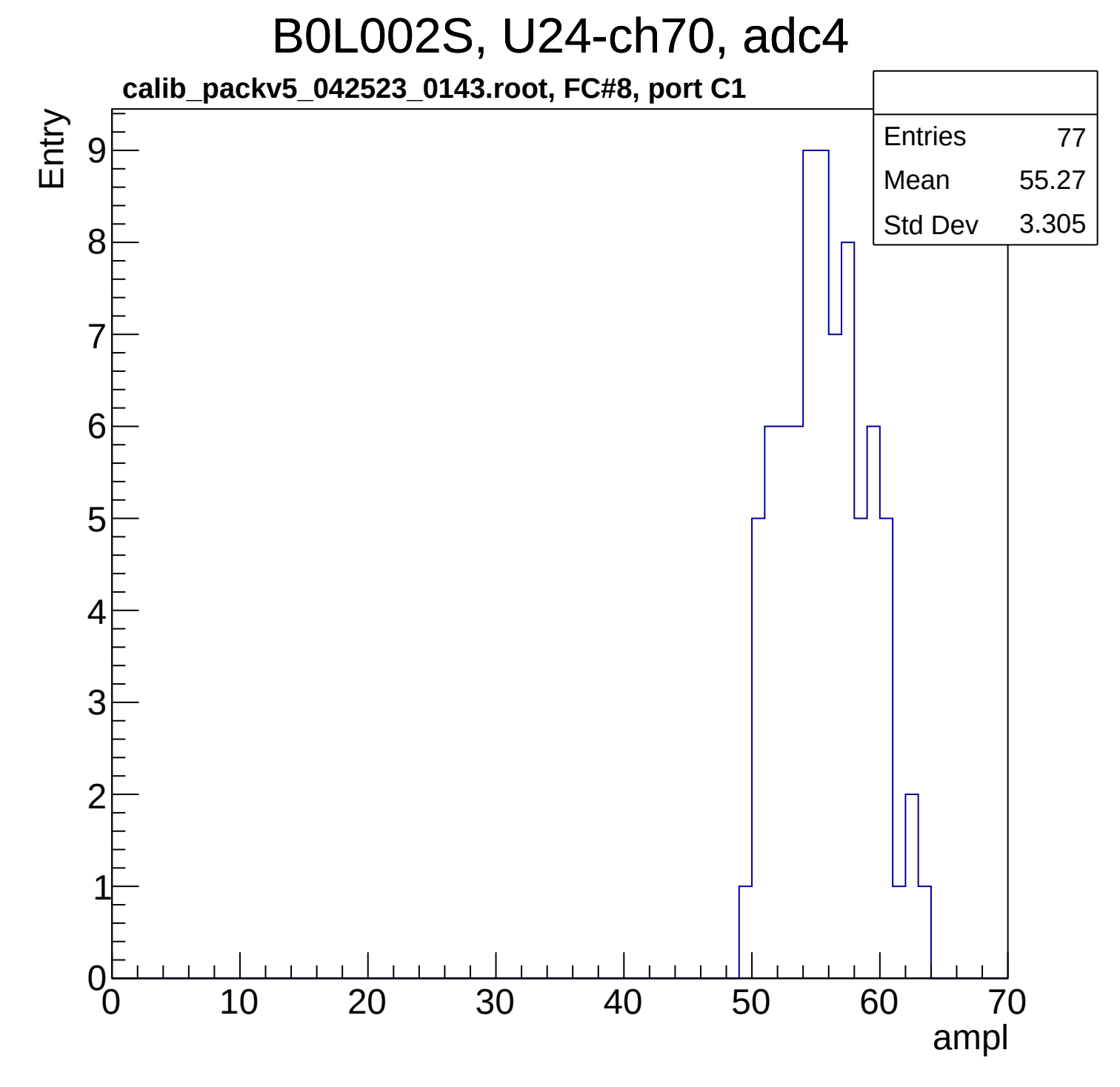
B0L002S, U24-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 55.27
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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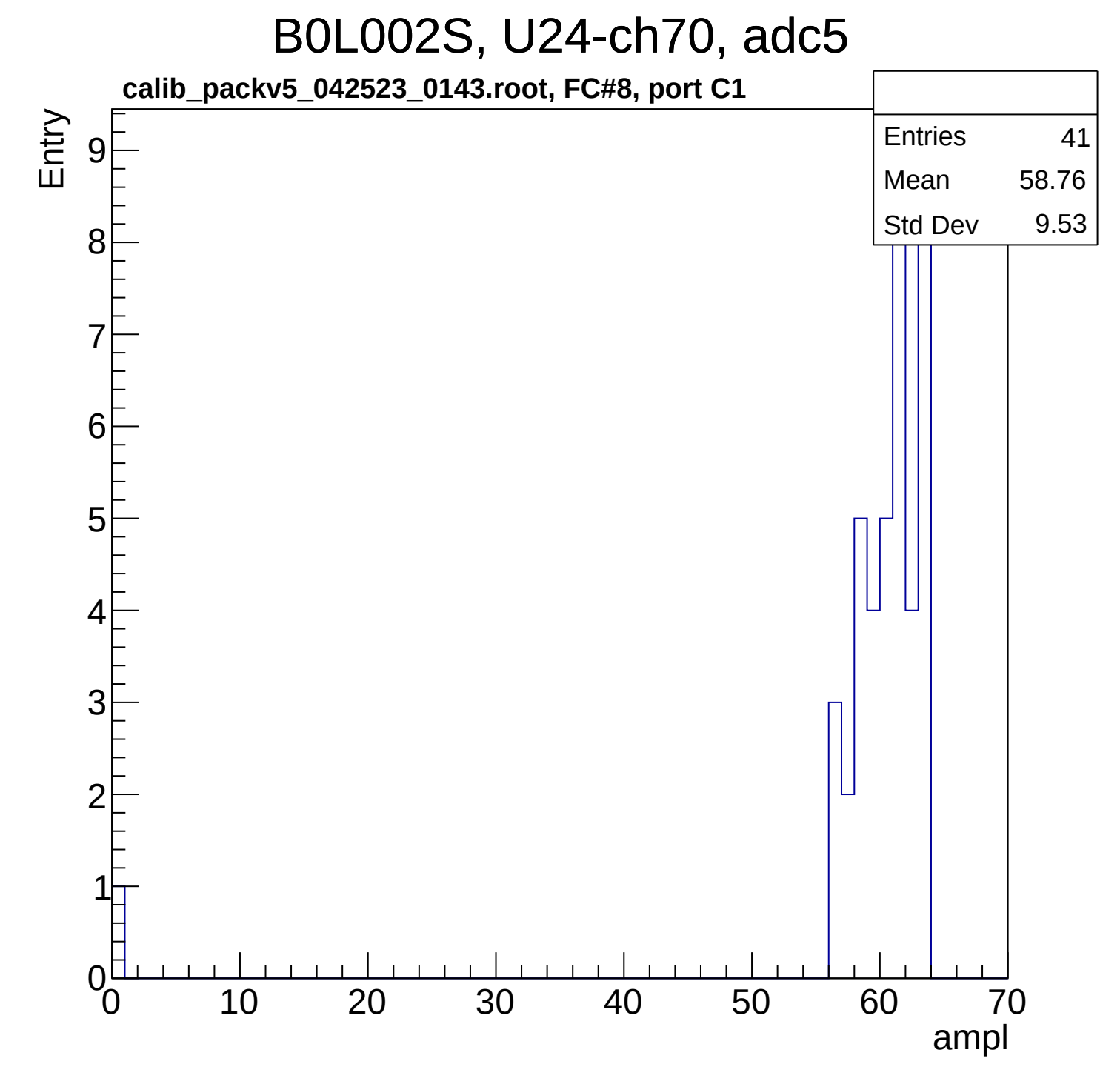
B0L002S, U24-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.76
Std Dev 9.53
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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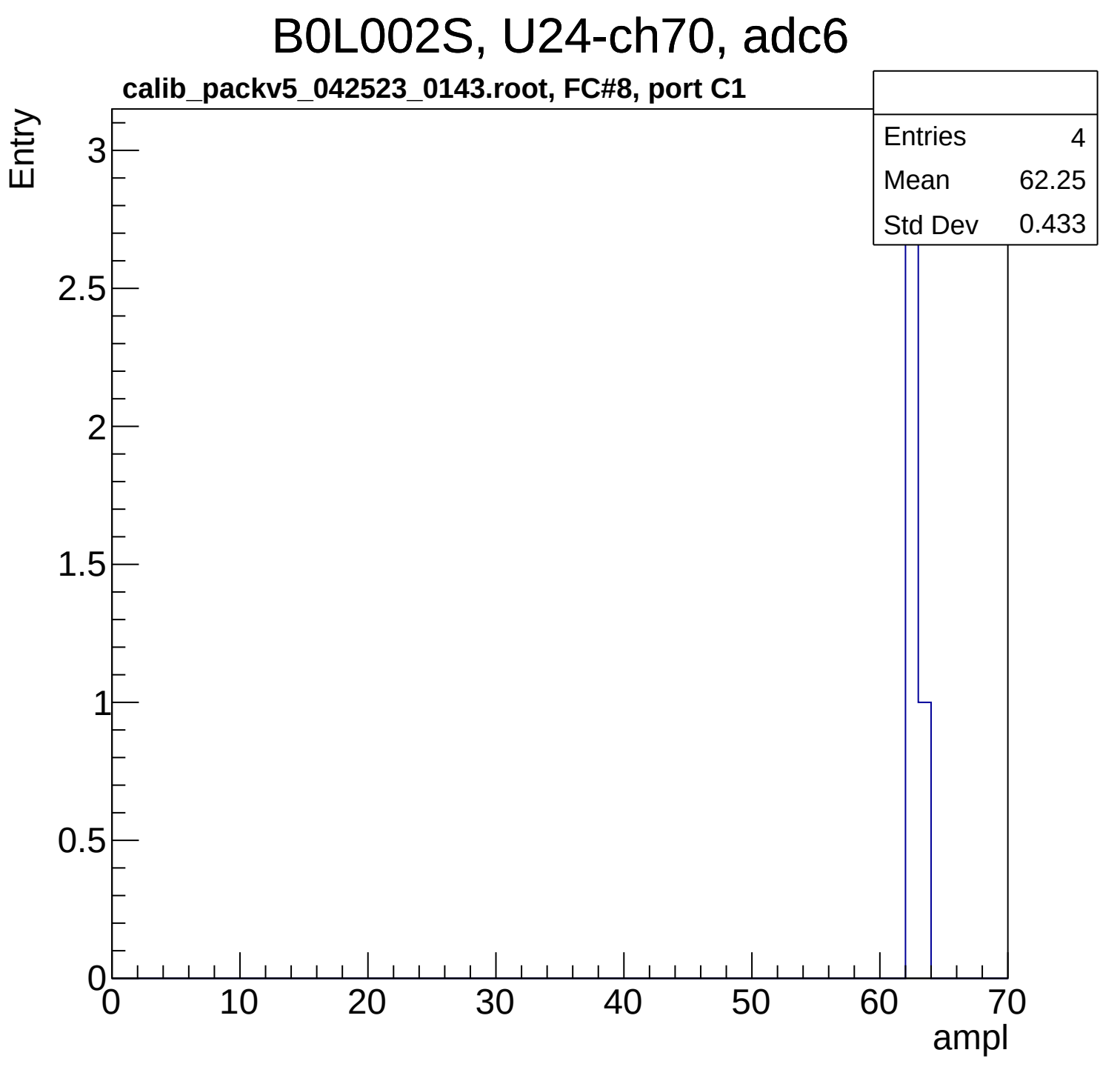
B0L002S, U24-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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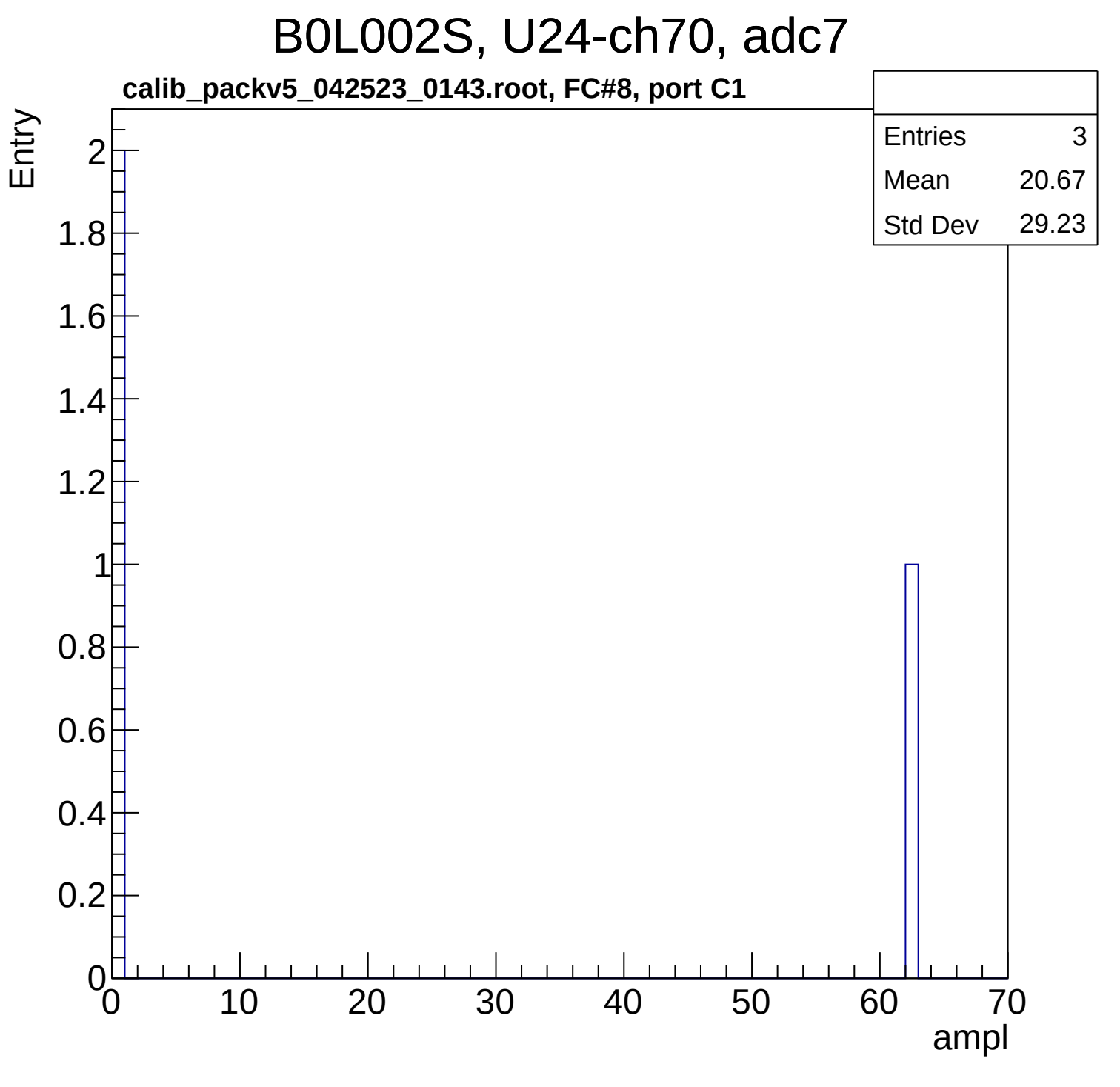
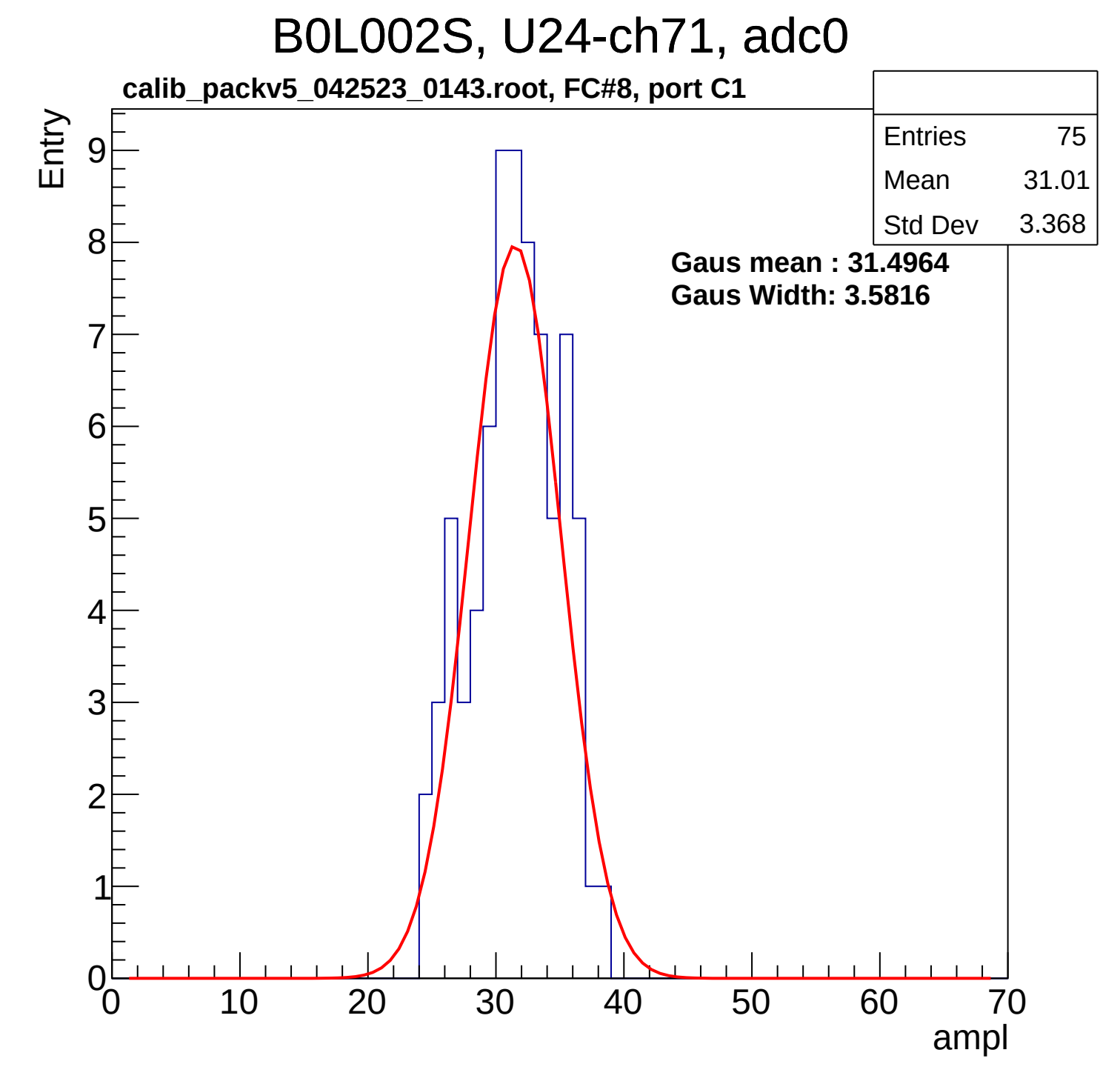
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 31.01
Std Dev 3.368
Gaus mean : 31.4964
Gaus Width: 3.5816
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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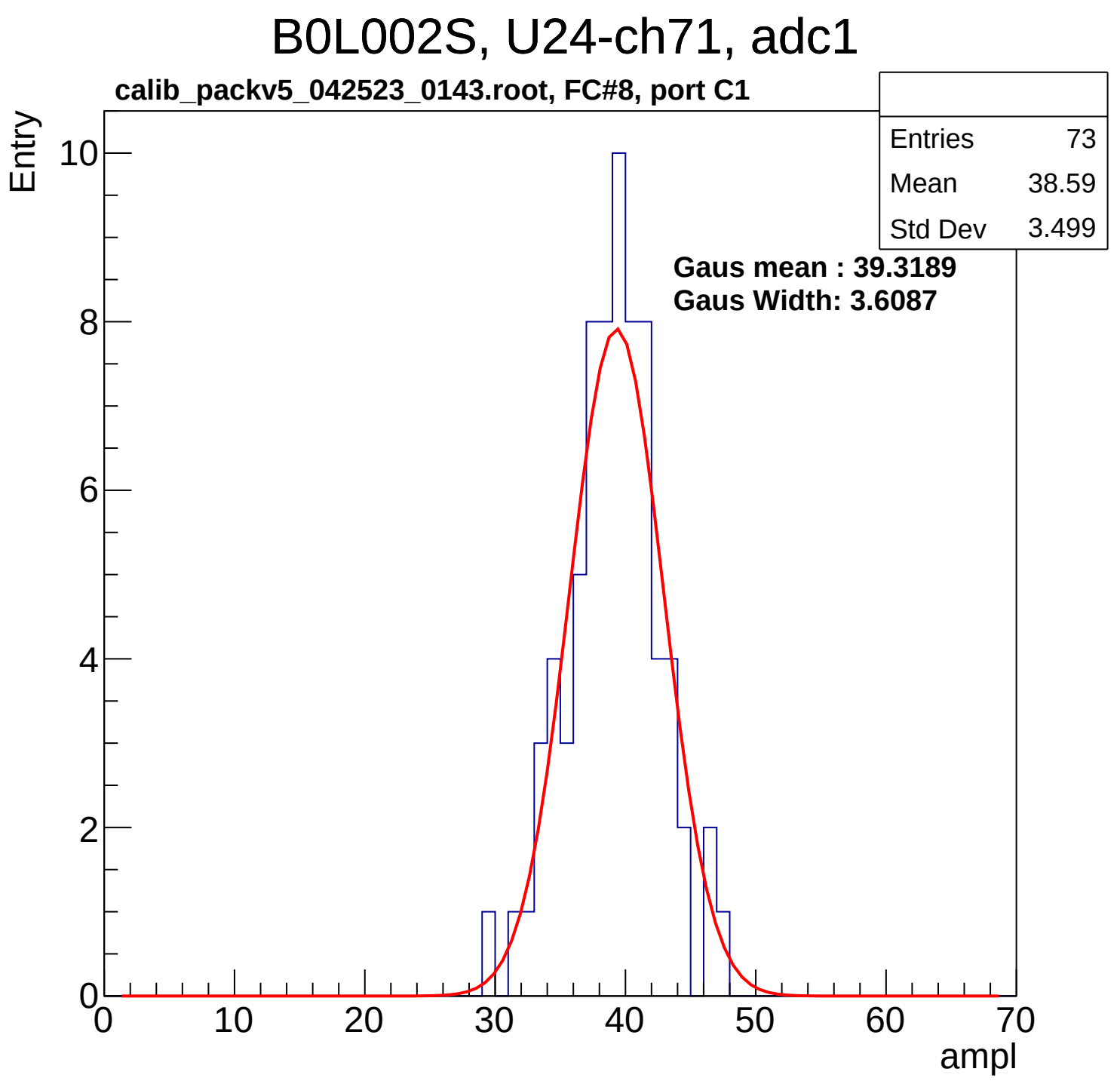
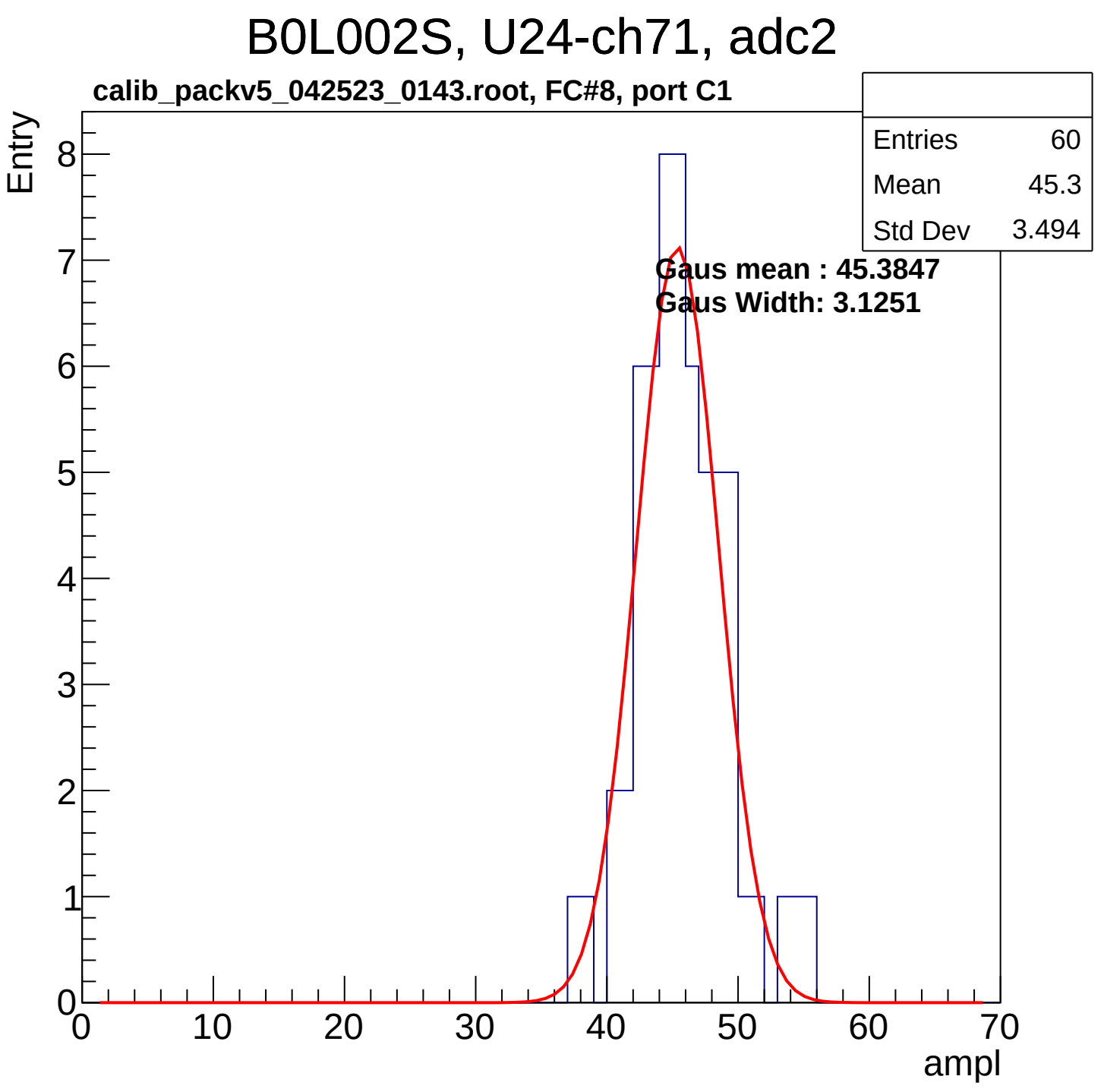

B0L002S, U24-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.59
Std Dev 3.499
Gaus mean : 39.3189
Gaus Width: 3.6087
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.3
Std Dev 3.494
Gaus mean : 45.3847
Gaus Width: 3.1251
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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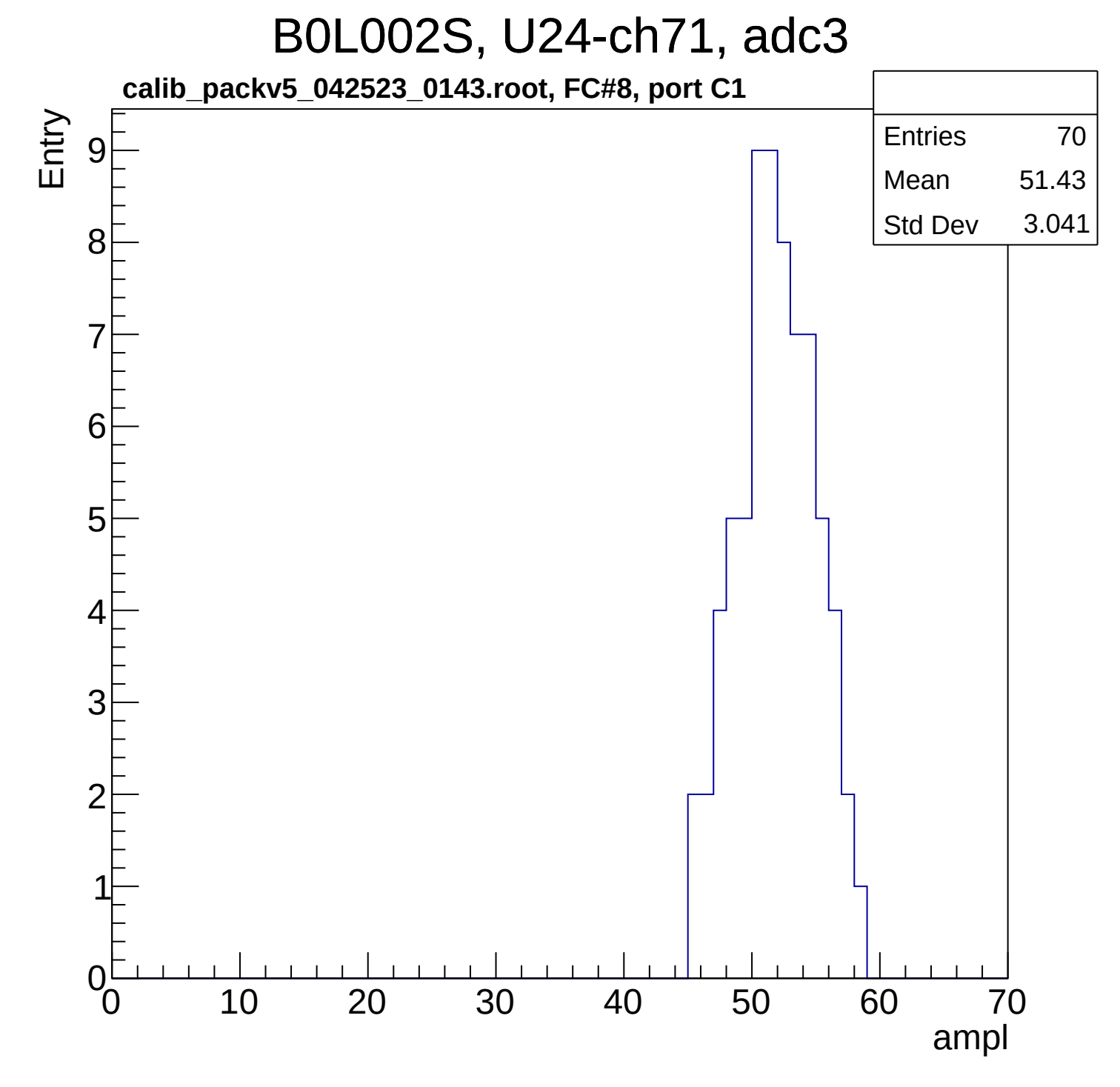
B0L002S, U24-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.43
Std Dev 3.041
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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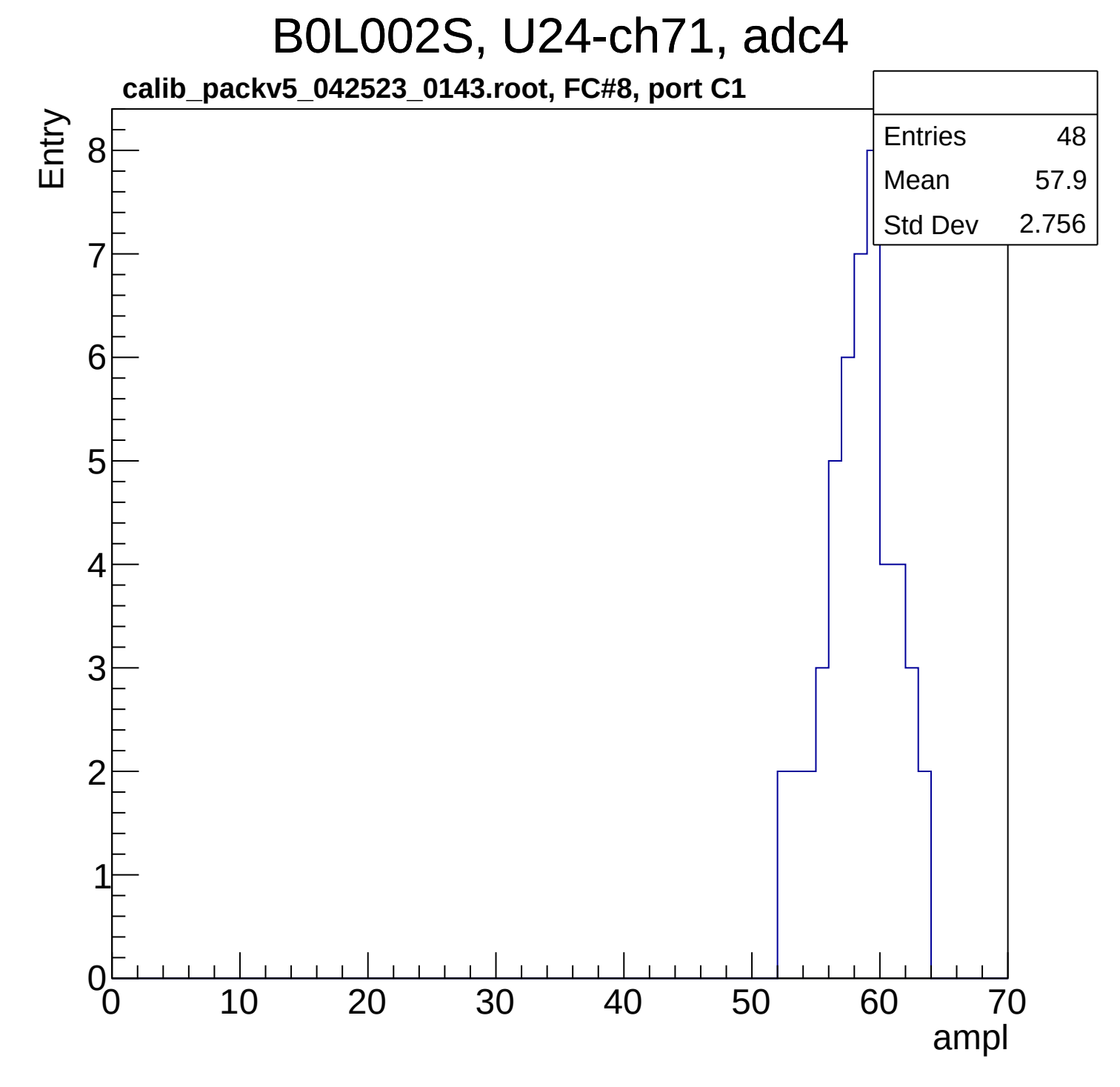
B0L002S, U24-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.9
Std Dev 2.756
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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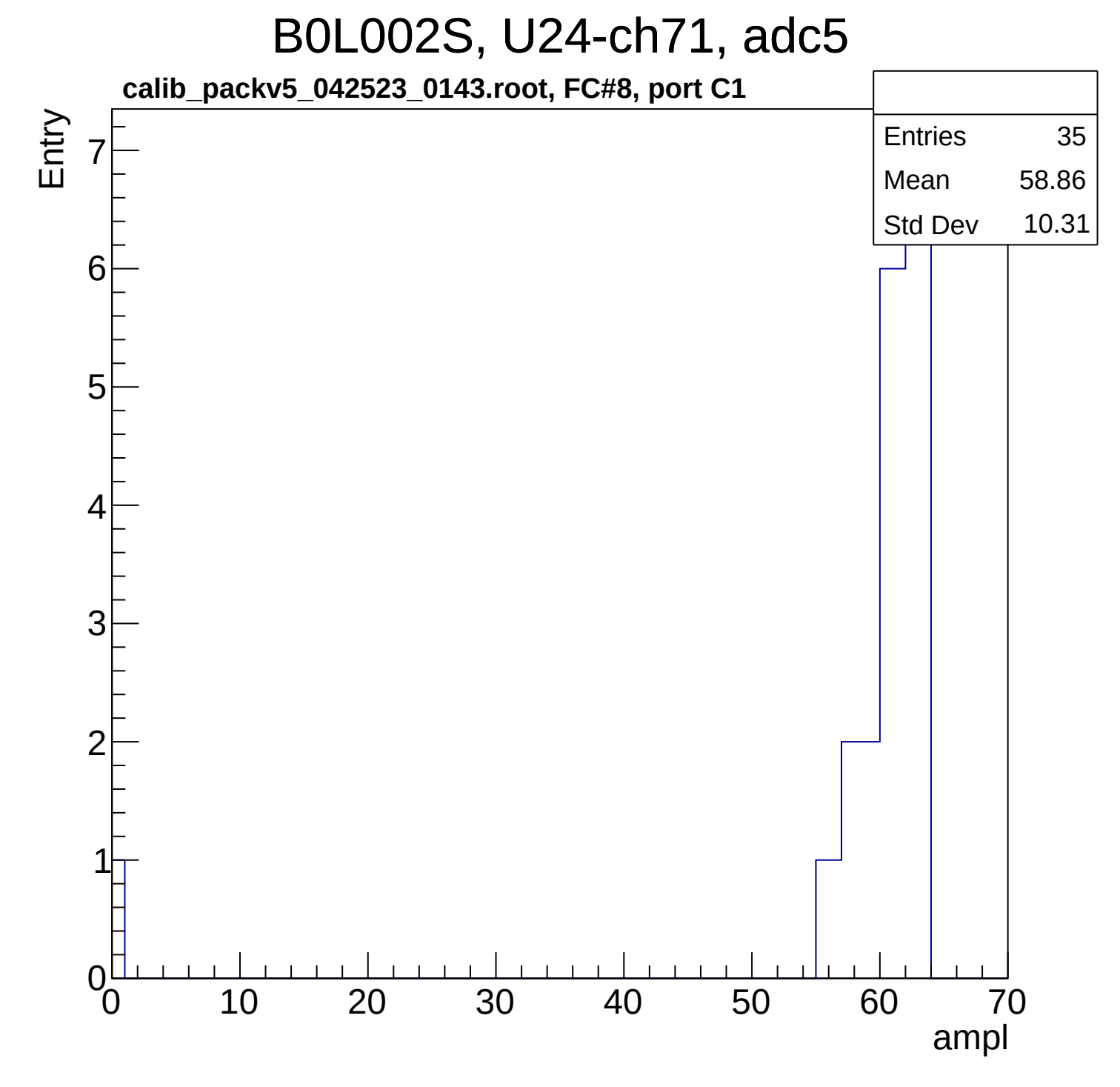
B0L002S, U24-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.86
Std Dev 10.31
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
50
60
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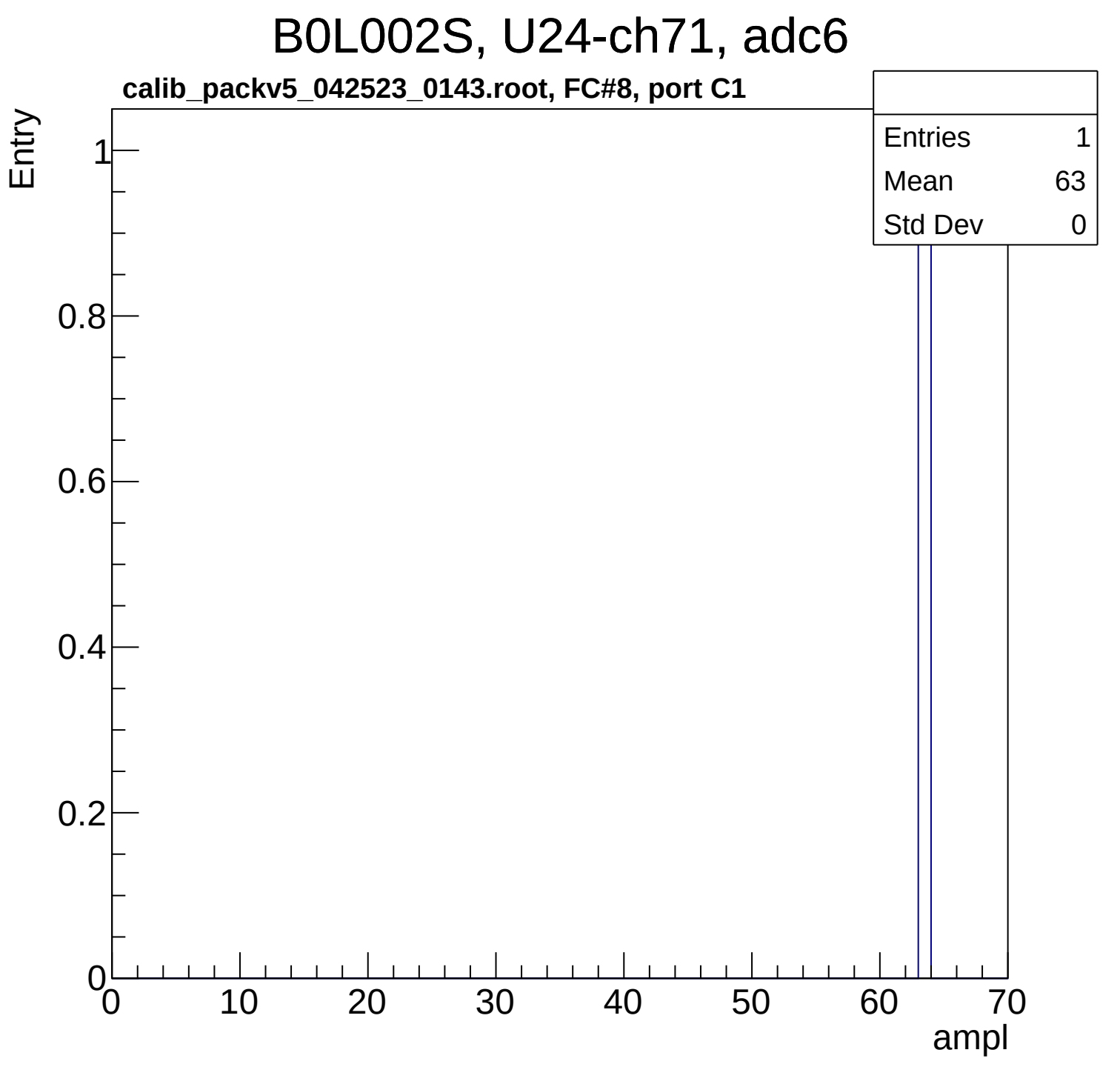
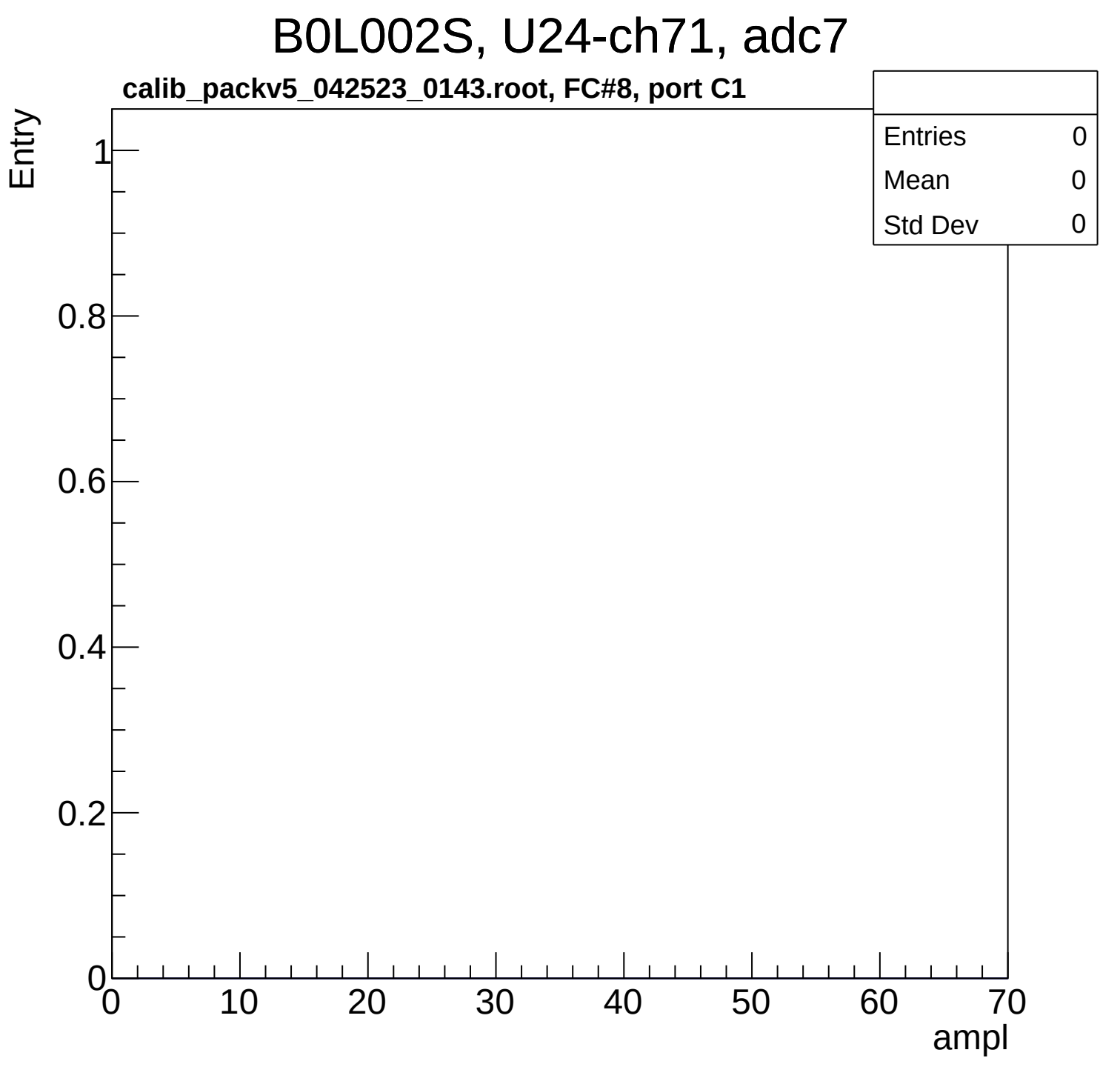

B0L002S, U24-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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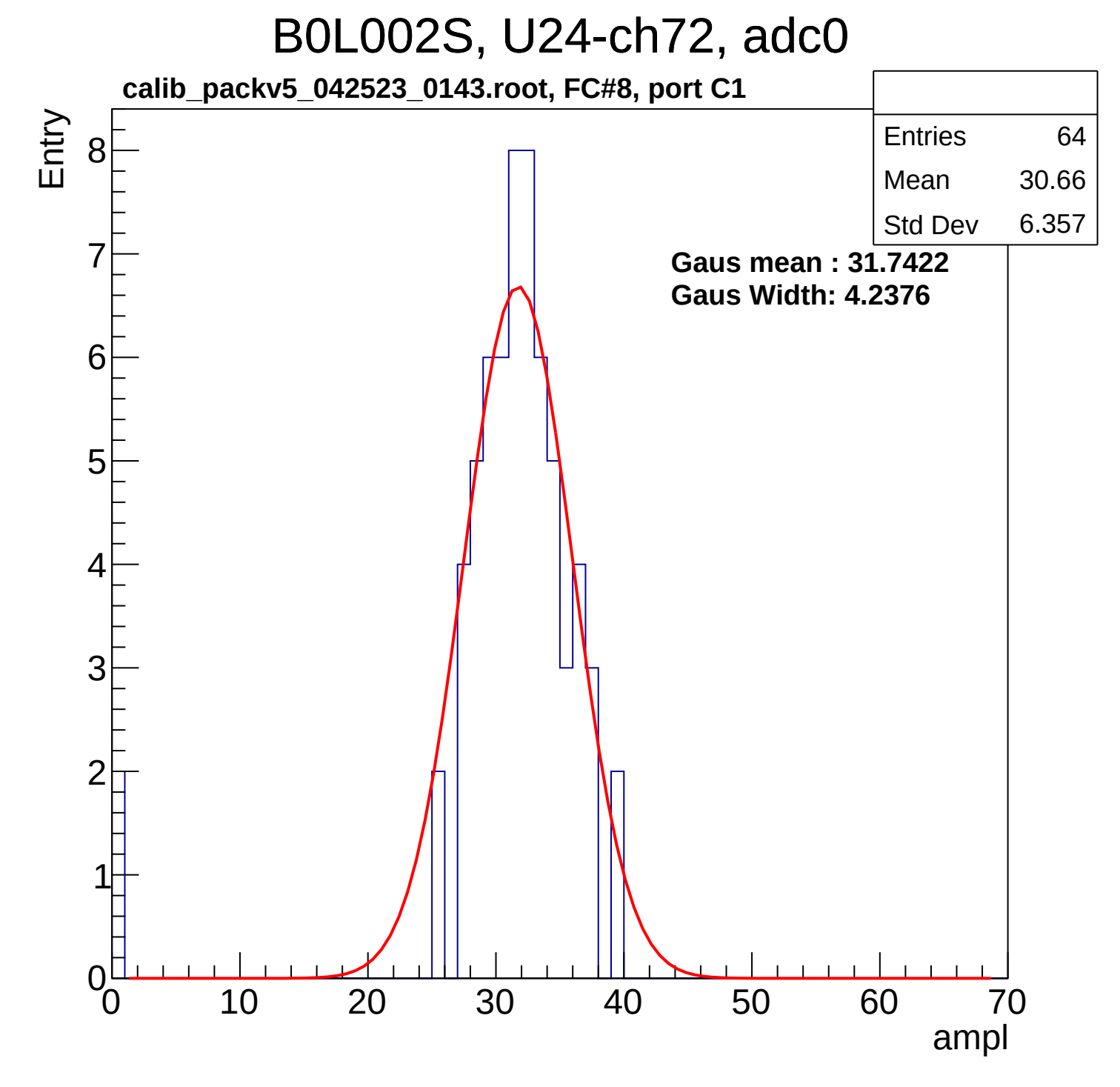
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 30.66
Std Dev 6.357
Gaus mean : 31.7422
Gaus Width: 4.2376
Entry
ampl
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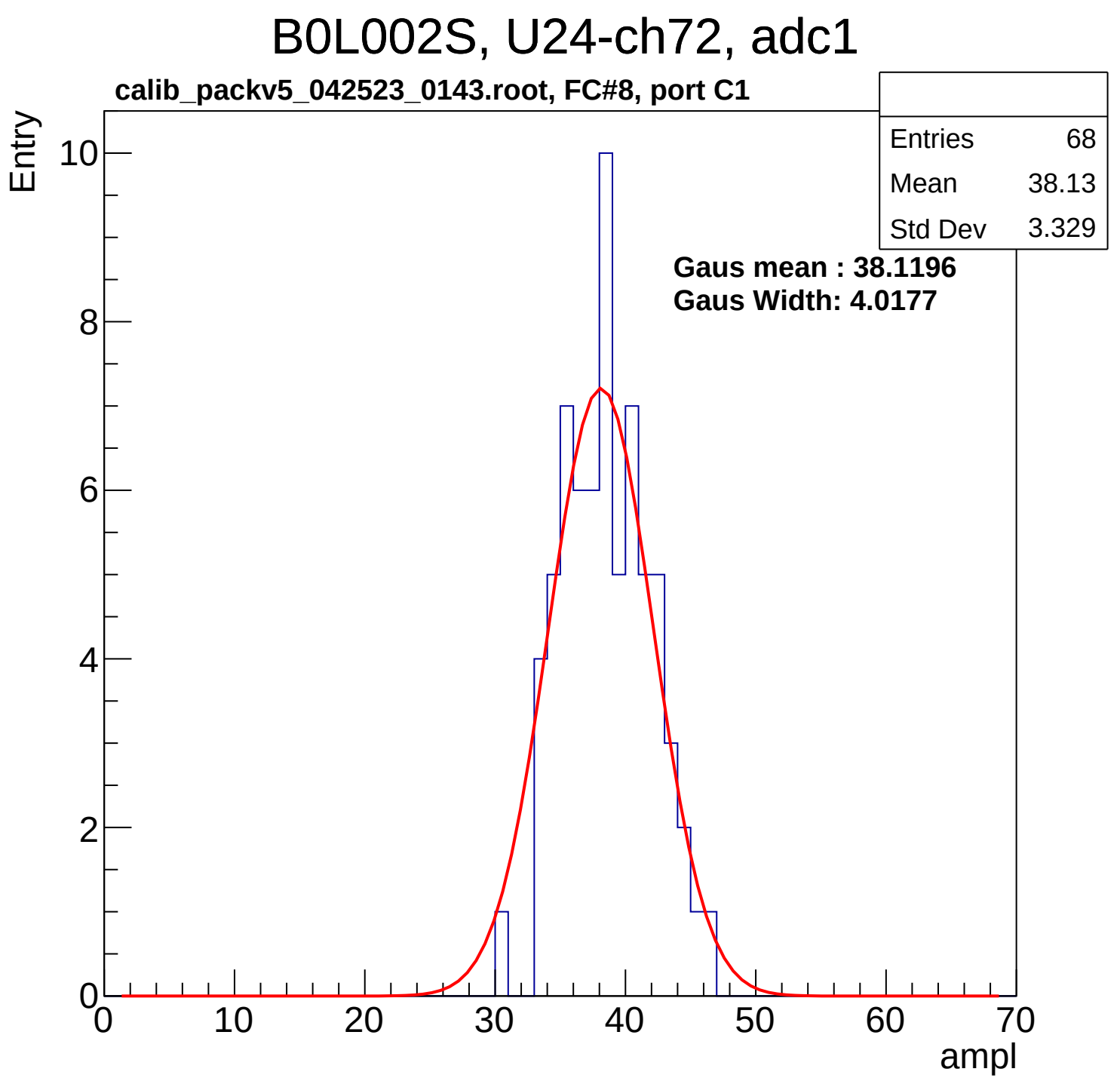
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.13
Std Dev 3.329
Gaus mean : 38.1196
Gaus Width: 4.0177
Entry
ampl
0
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4
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8
10
0
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60
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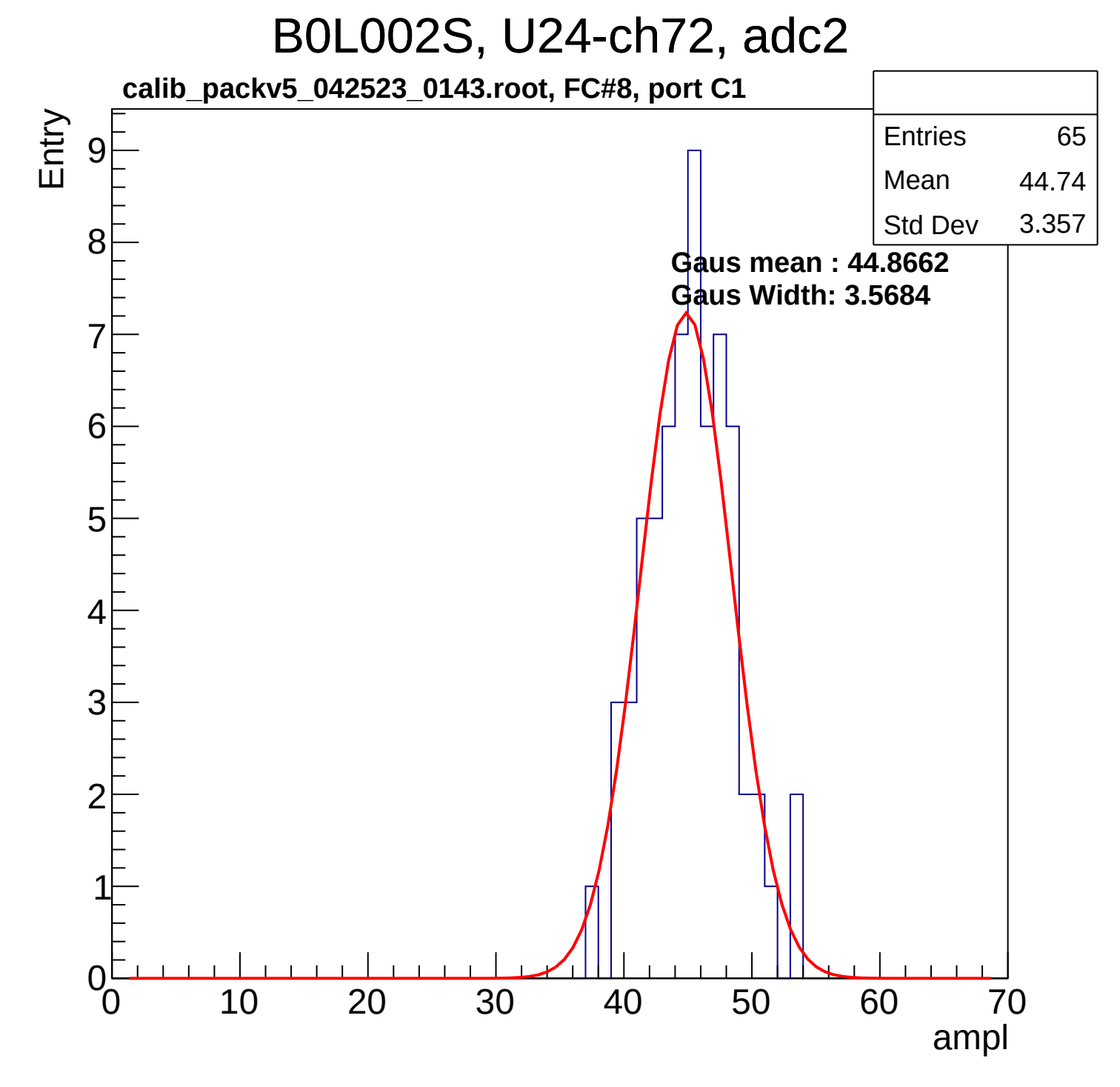
B0L002S, U24-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.74
Std Dev 3.357
Gaus mean : 44.8662
Gaus Width: 3.5684
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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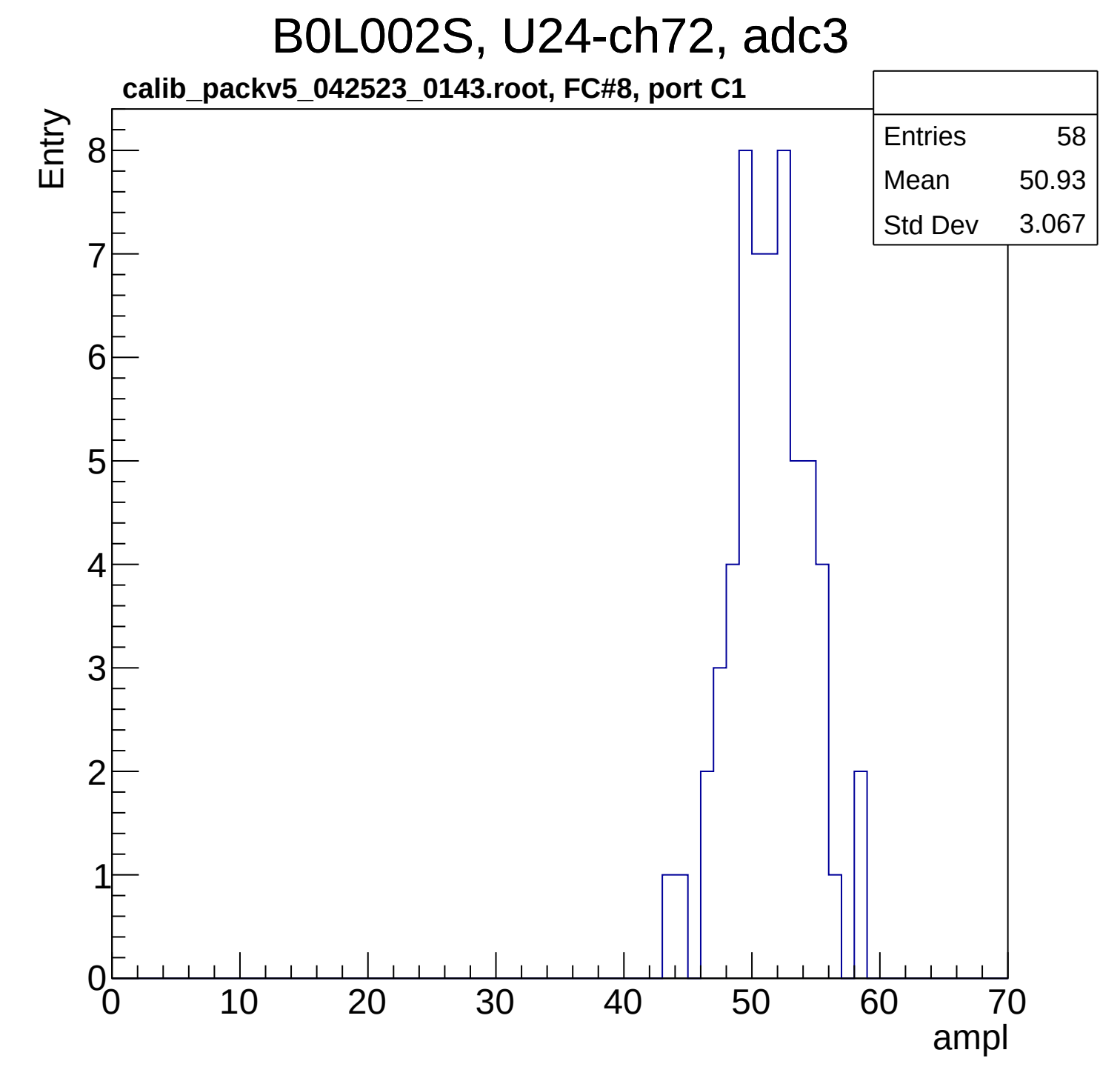
B0L002S, U24-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 50.93
Std Dev 3.067
Entry
ampl
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2
3
4
5
6
7
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0
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60
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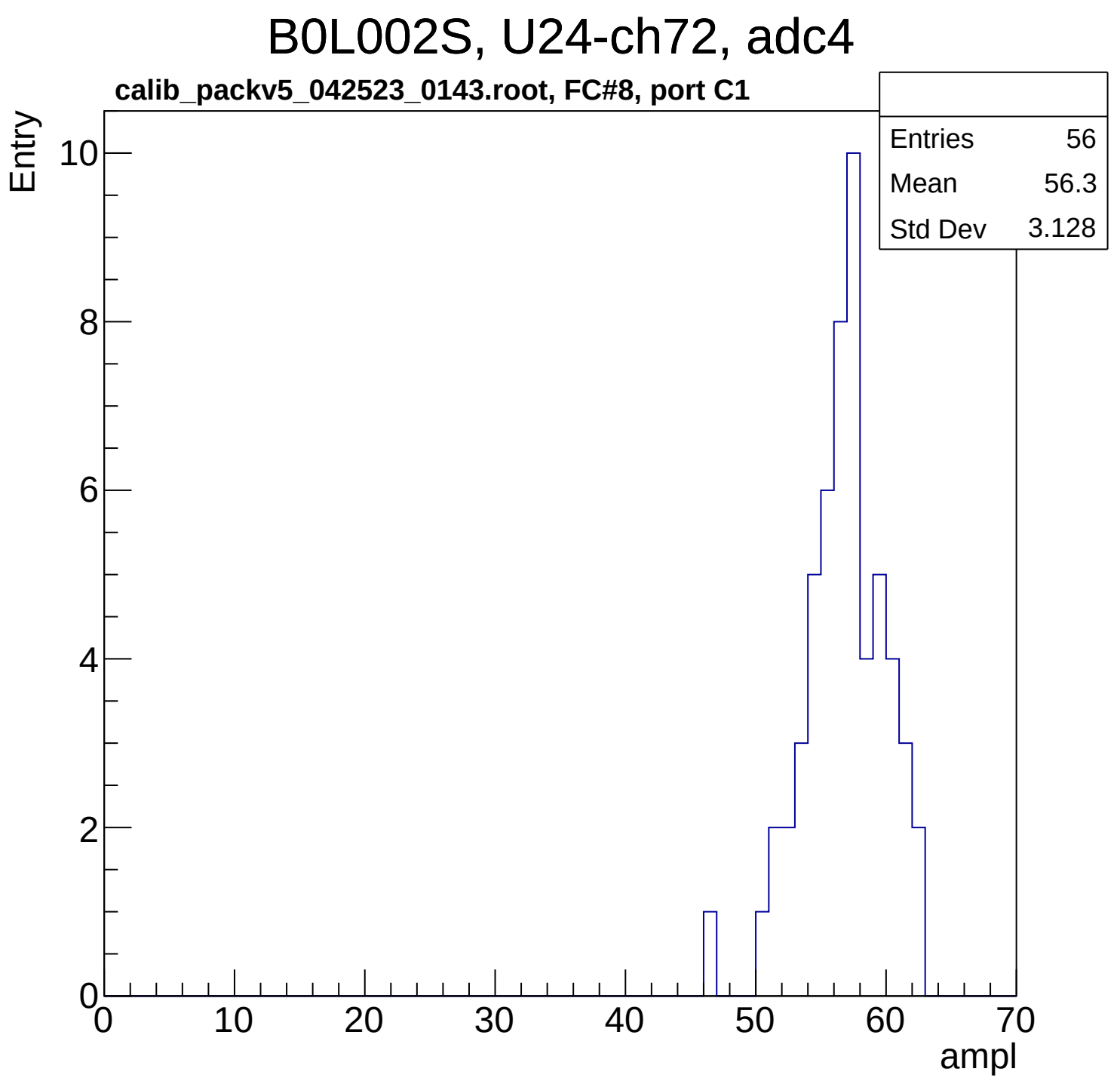
B0L002S, U24-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.3
Std Dev 3.128
Entry
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6
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10
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10
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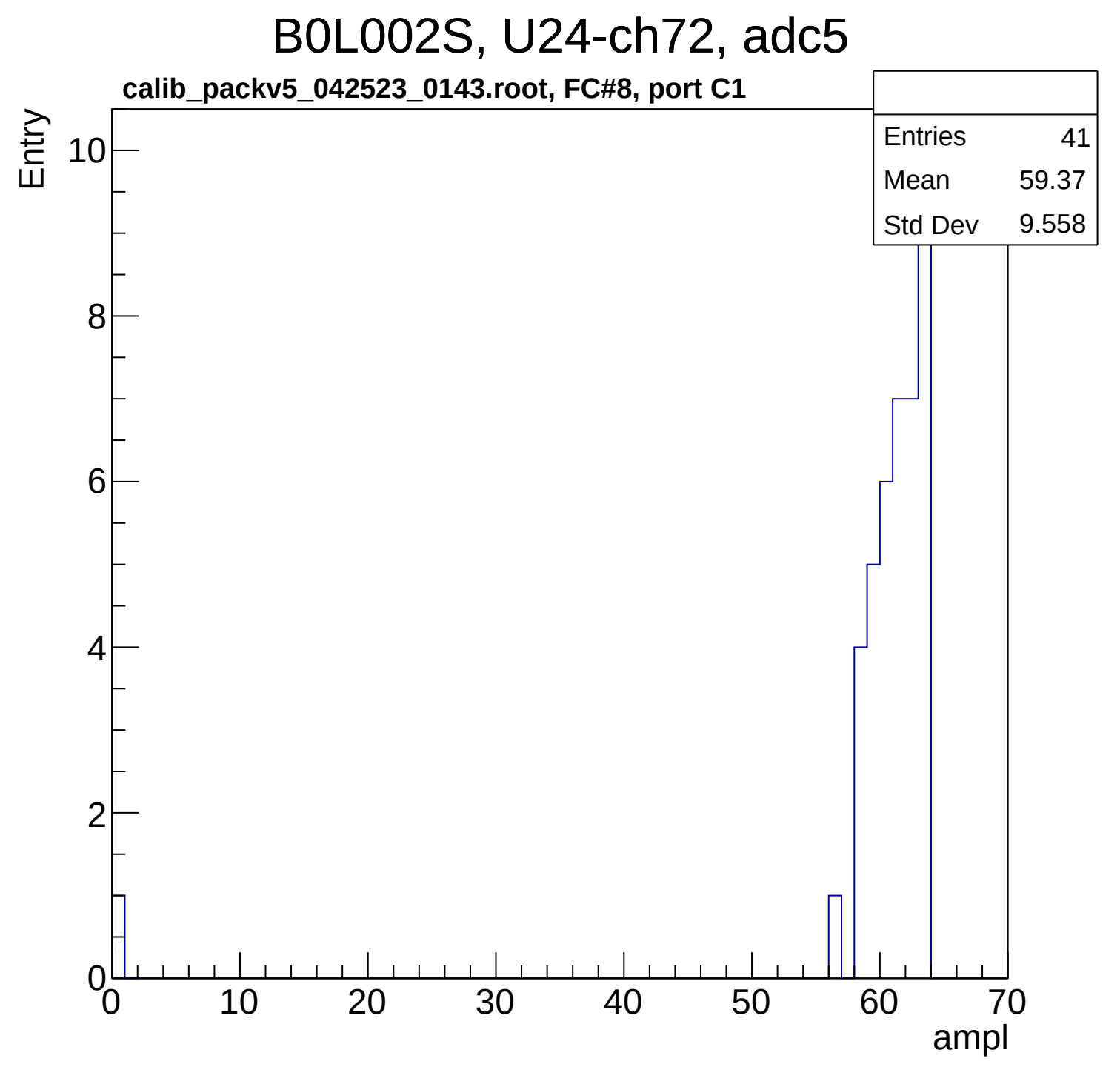
50
60
70
ampl


B0L002S, U24-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.37
Std Dev 9.558
Entry
0
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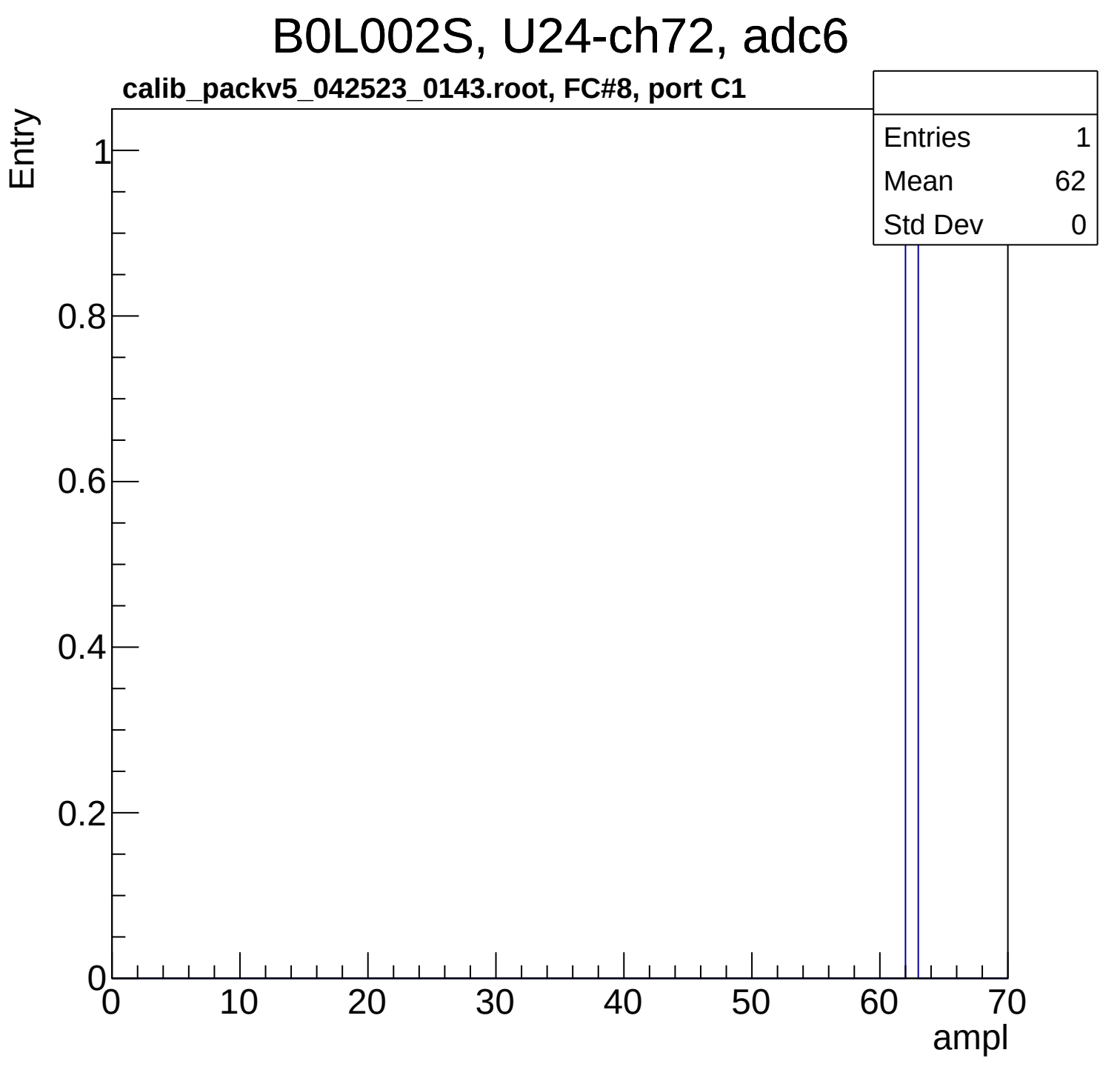
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
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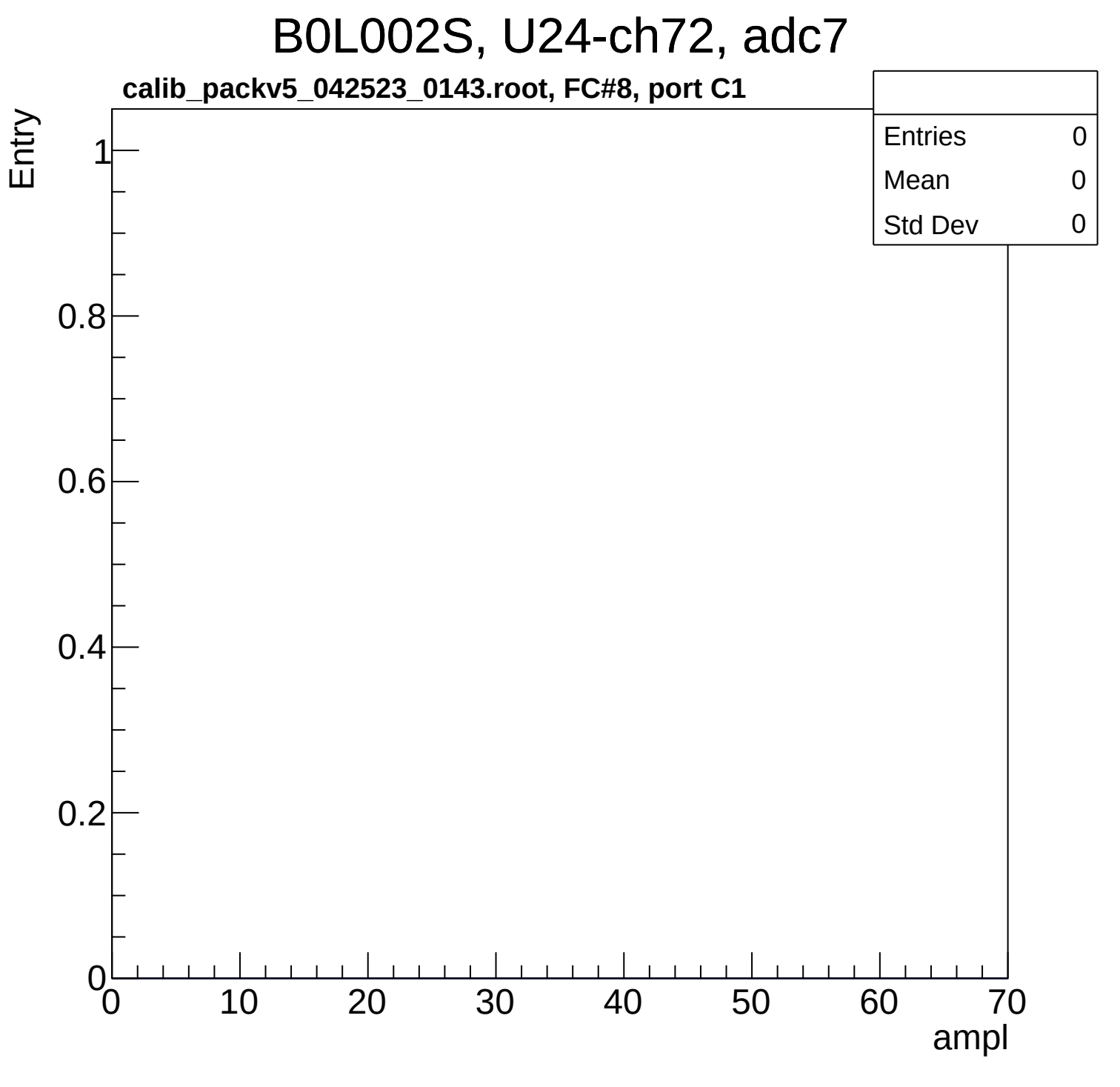
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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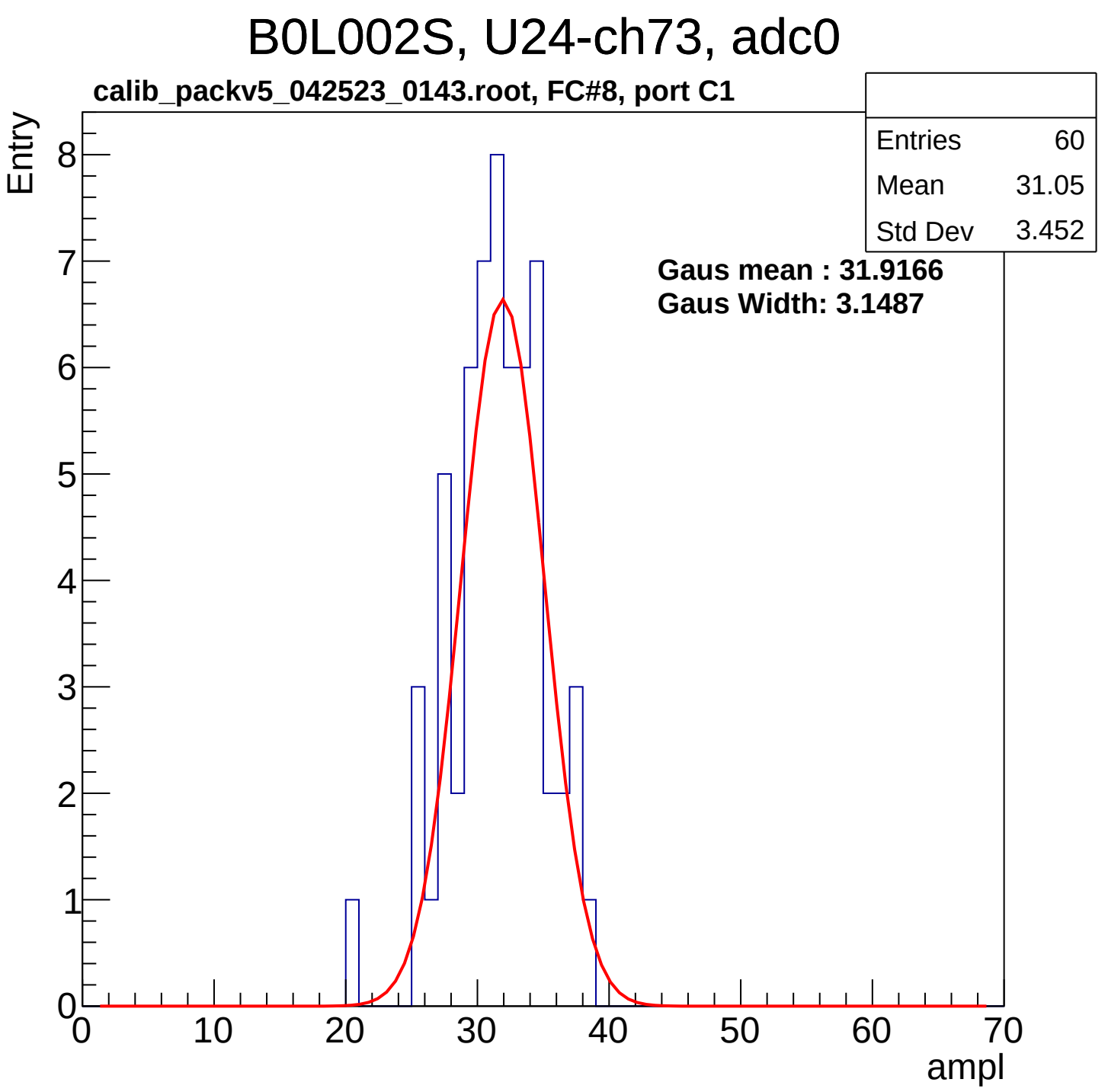
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.05
Std Dev 3.452
Gaus mean : 31.9166
Gaus Width: 3.1487
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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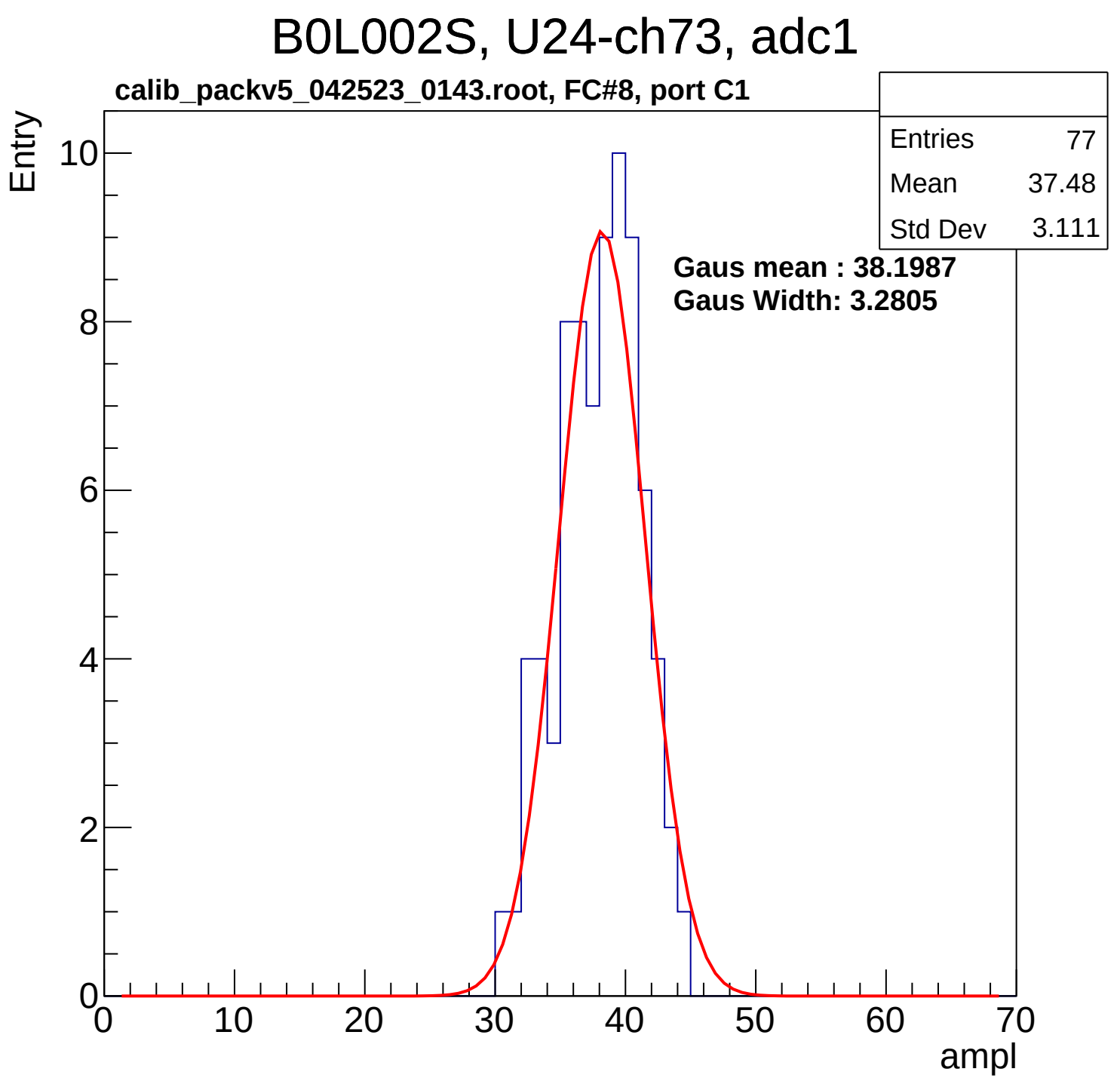
B0L002S, U24-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.48
Std Dev 3.111
Gaus mean : 38.1987
Gaus Width: 3.2805
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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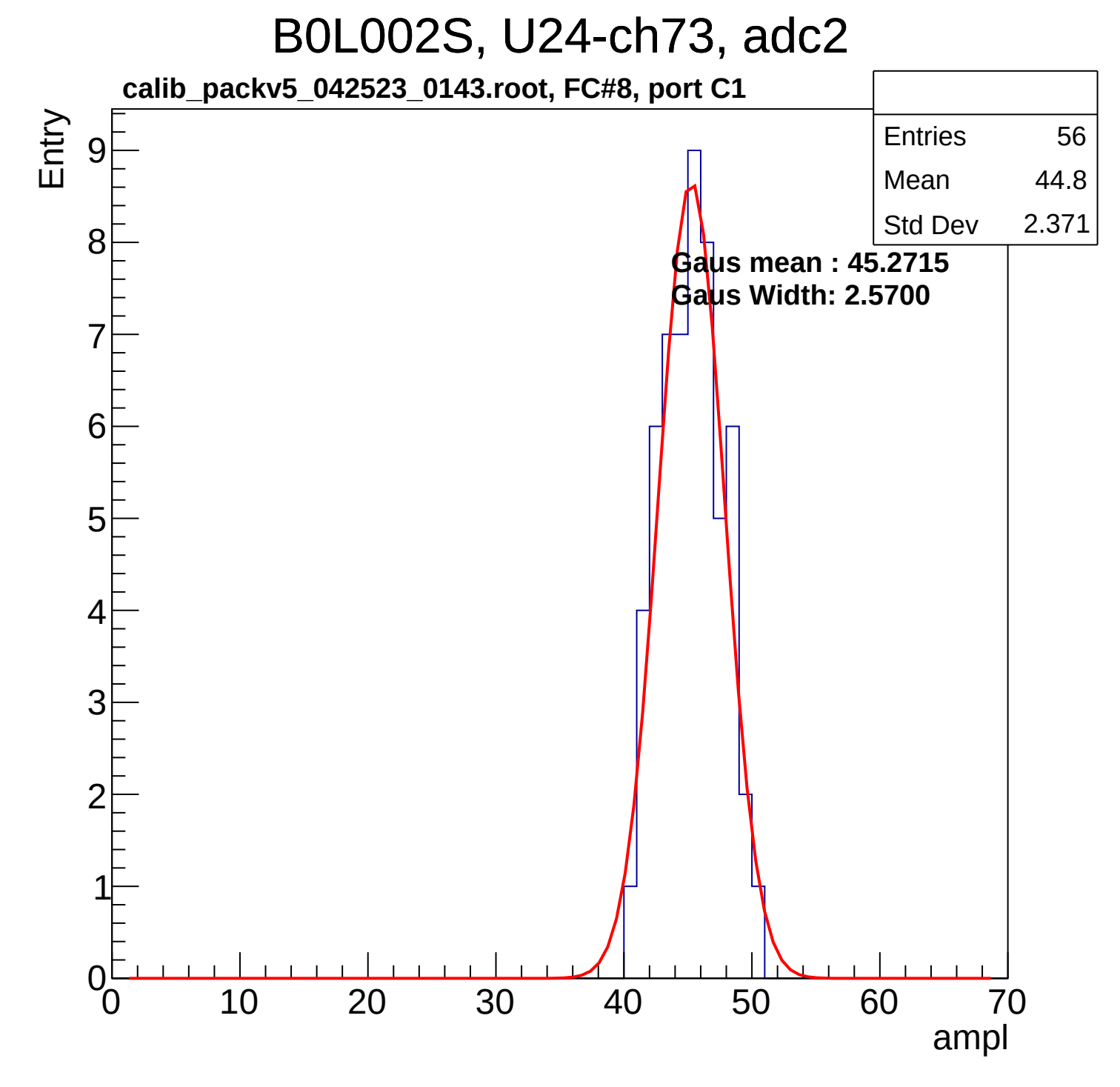
B0L002S, U24-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.8
Std Dev 2.371
Gaus mean : 45.2715
Gaus Width: 2.5700
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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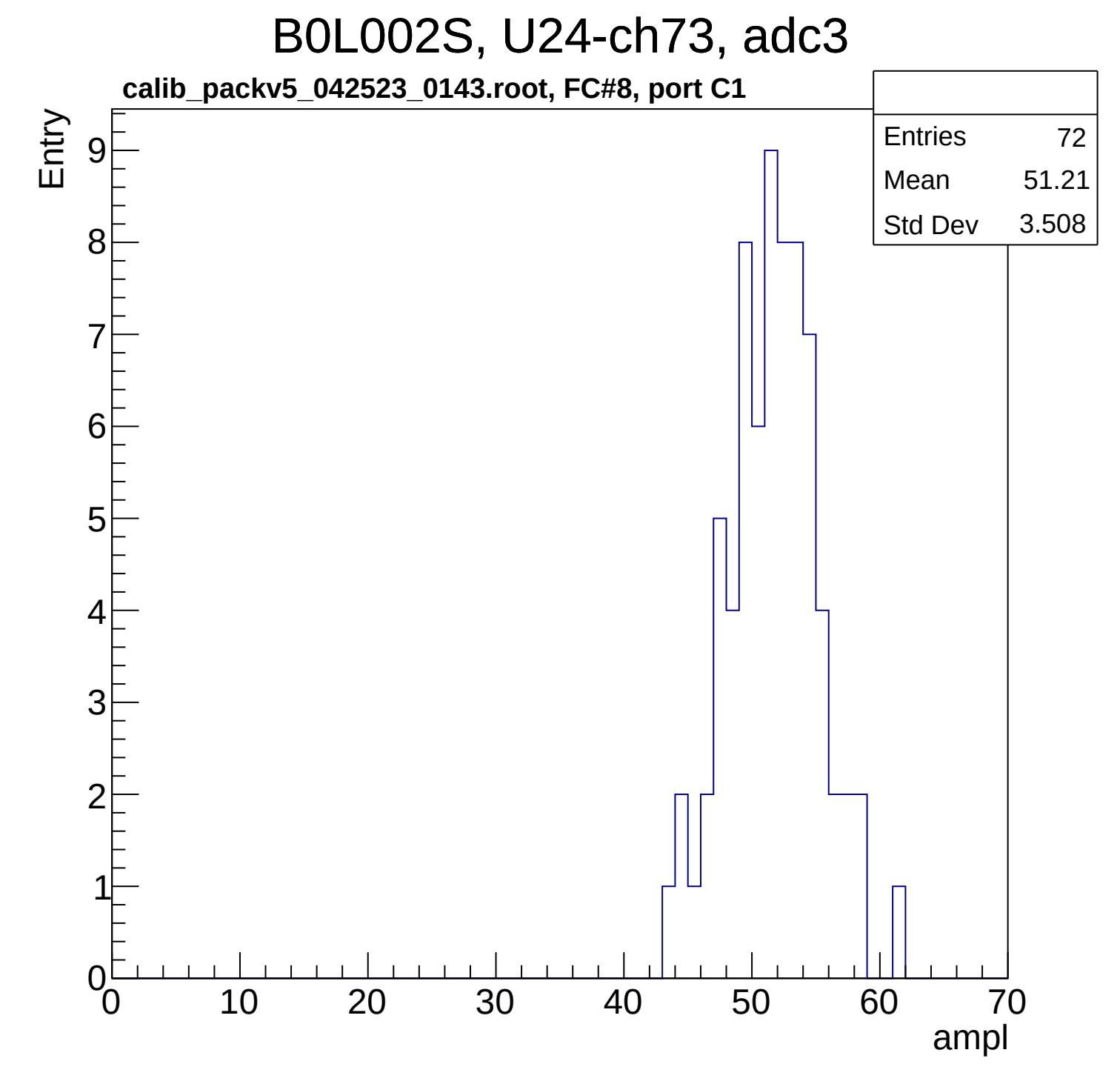
B0L002S, U24-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.21
Std Dev 3.508
Entry
ampl
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3
4
5
6
7
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0
10
20
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40
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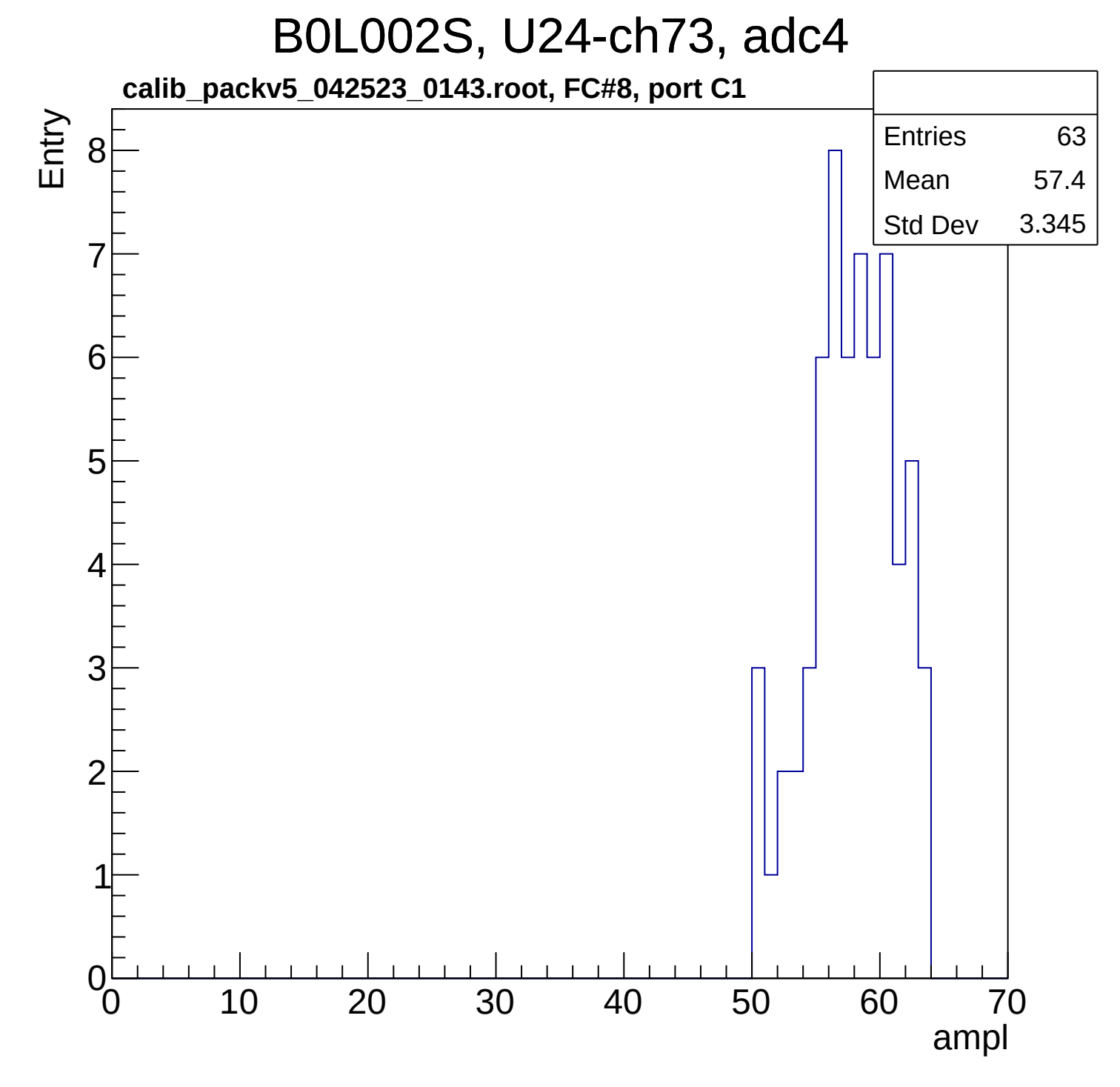
B0L002S, U24-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.4
Std Dev 3.345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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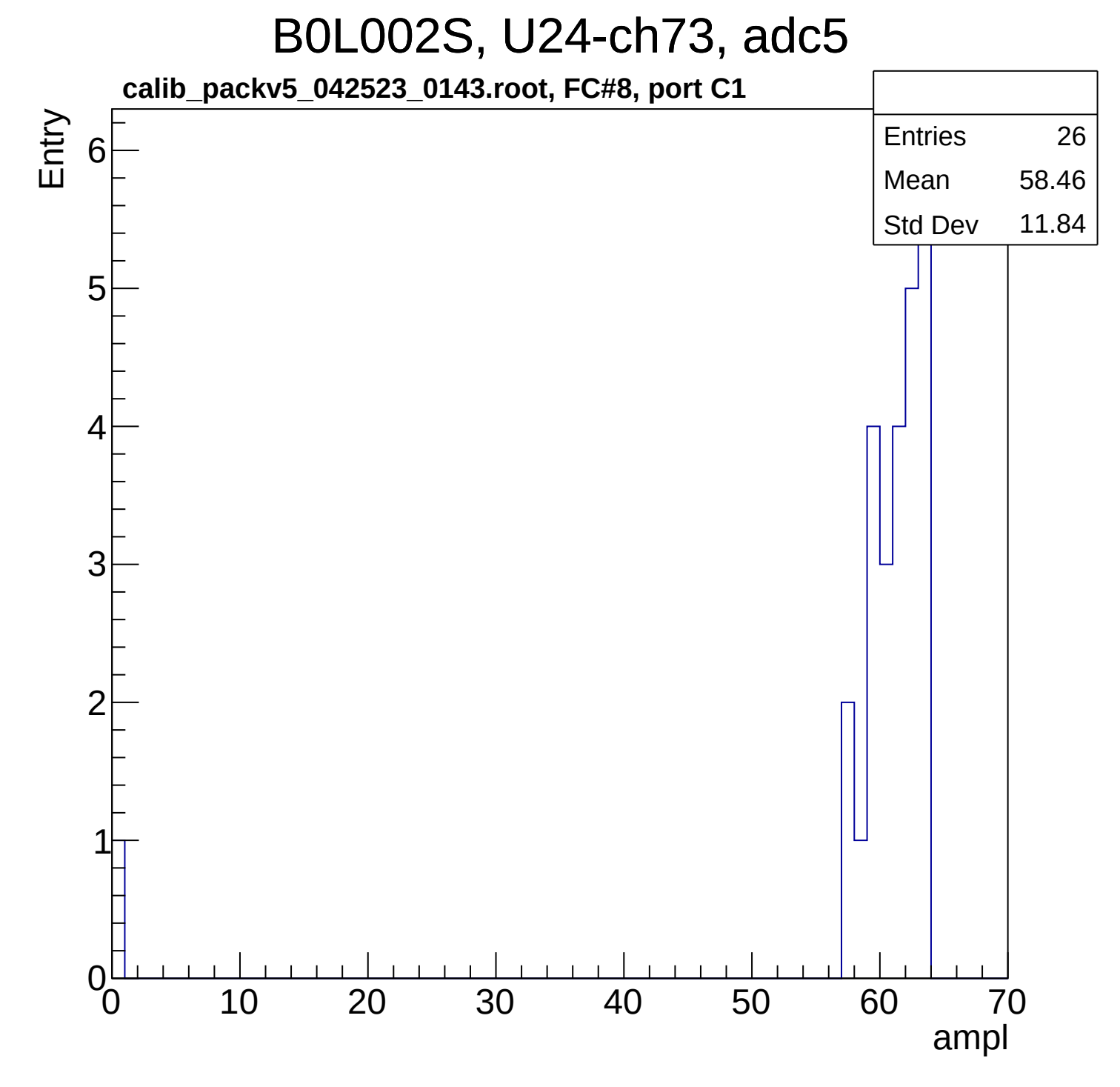
B0L002S, U24-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.46
Std Dev 11.84
Entry
0
1
2
3
4
5
6
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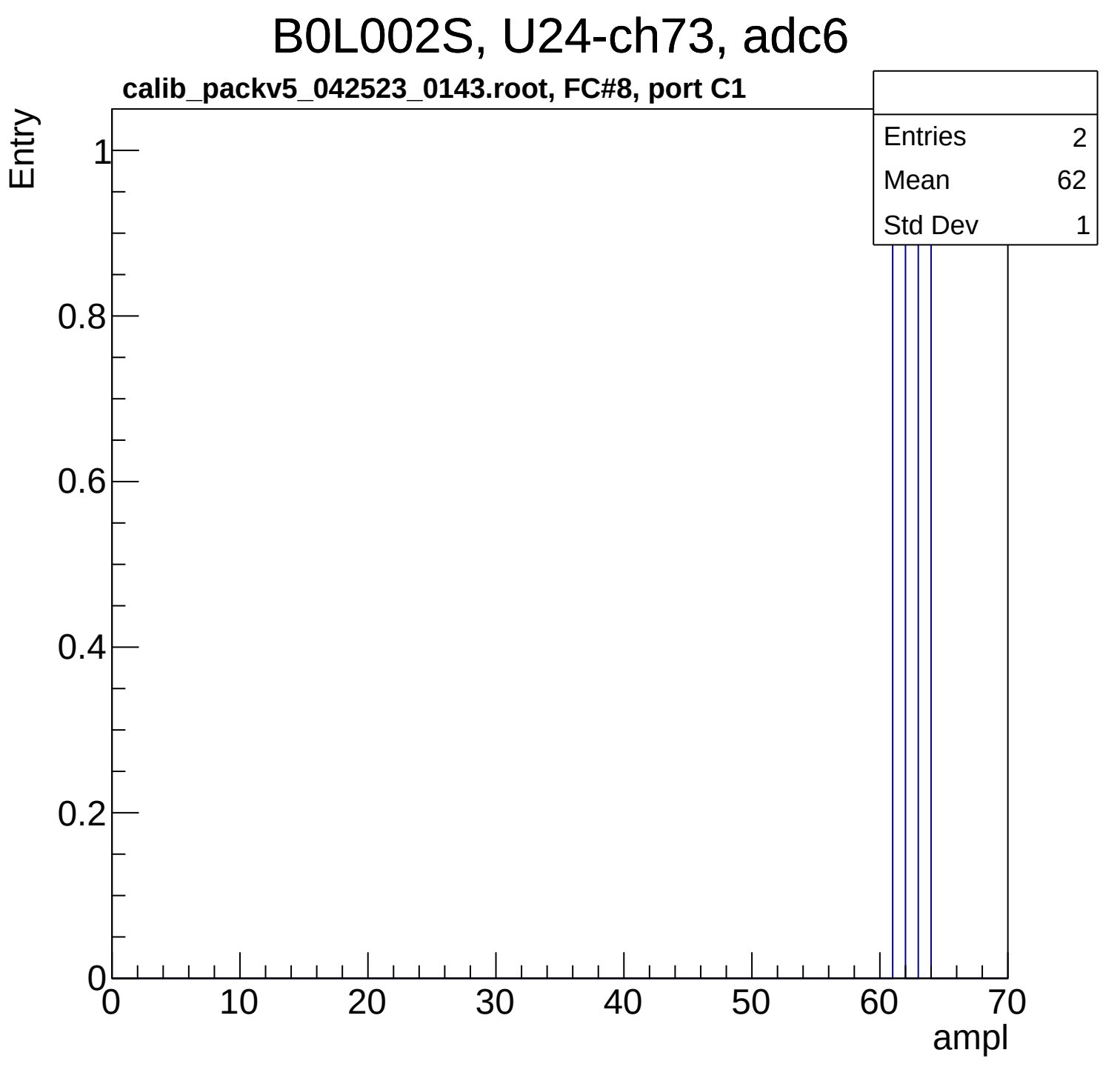
ampl


B0L002S, U24-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
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0.6
0.4
0.2
0
0
10
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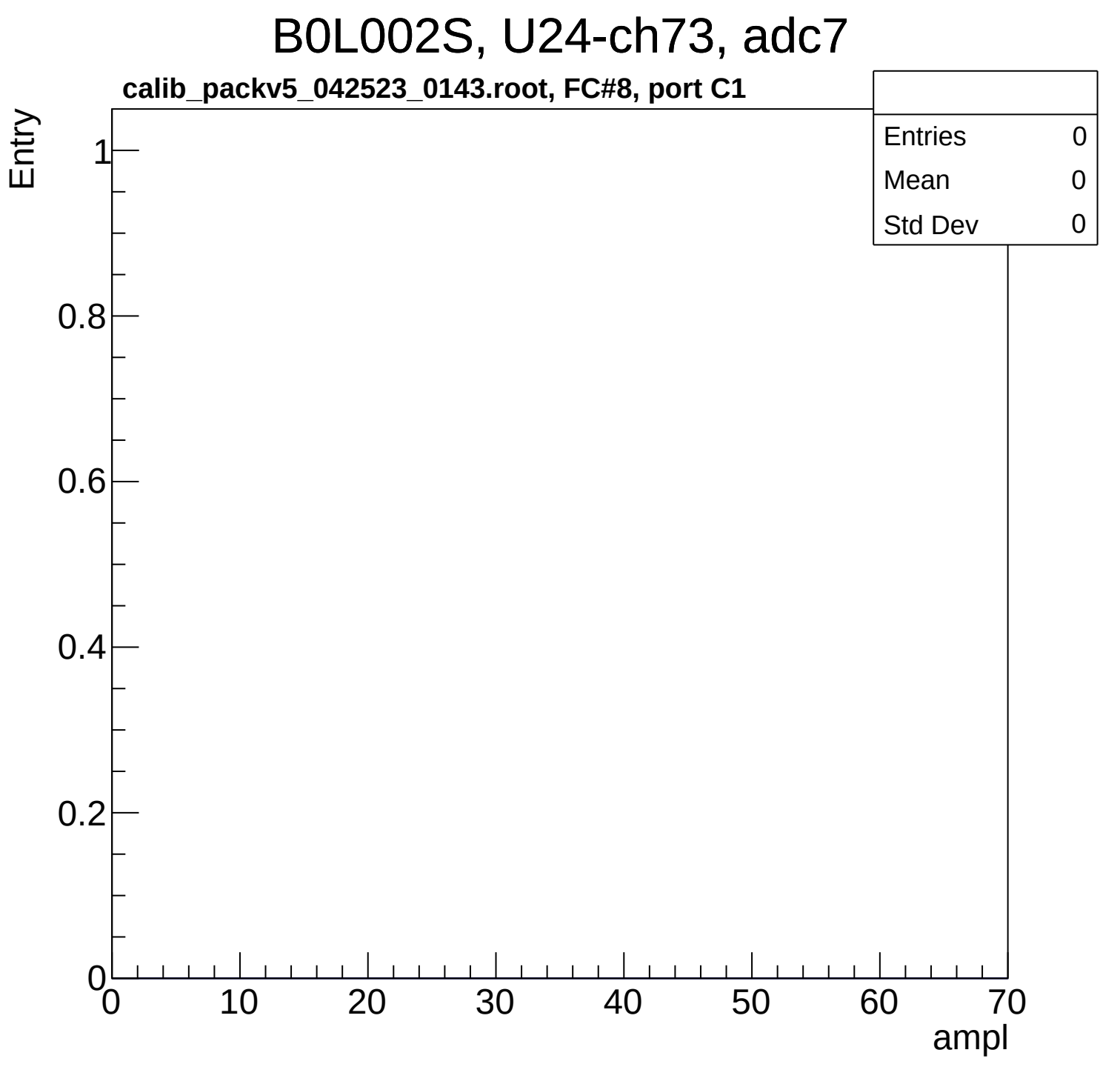
50
60
70
ampl


B0L002S, U24-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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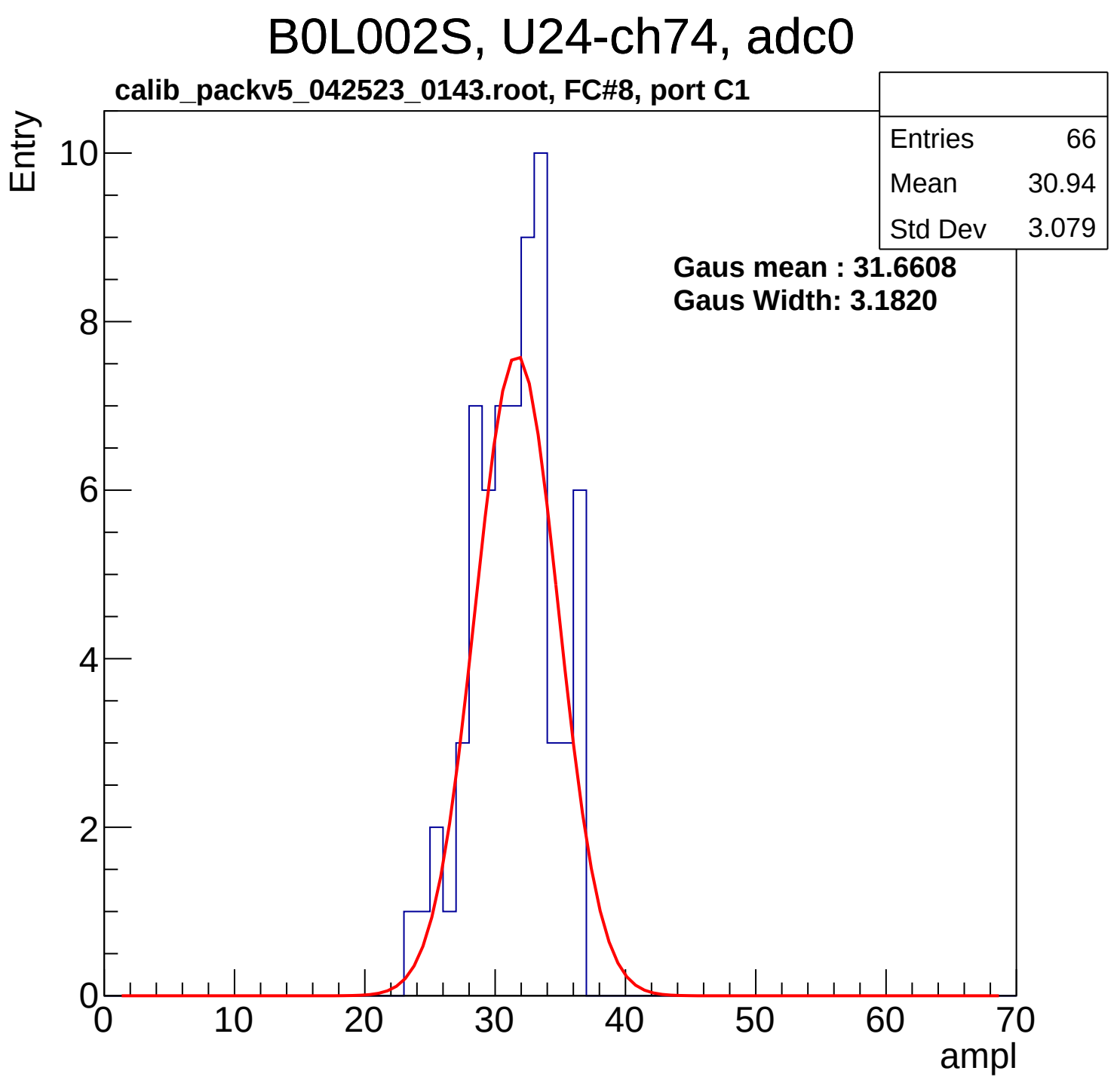
30
40
50
60
70
ampl


B0L002S, U24-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.94
Std Dev 3.079
Gaus mean : 31.6608
Gaus Width: 3.1820
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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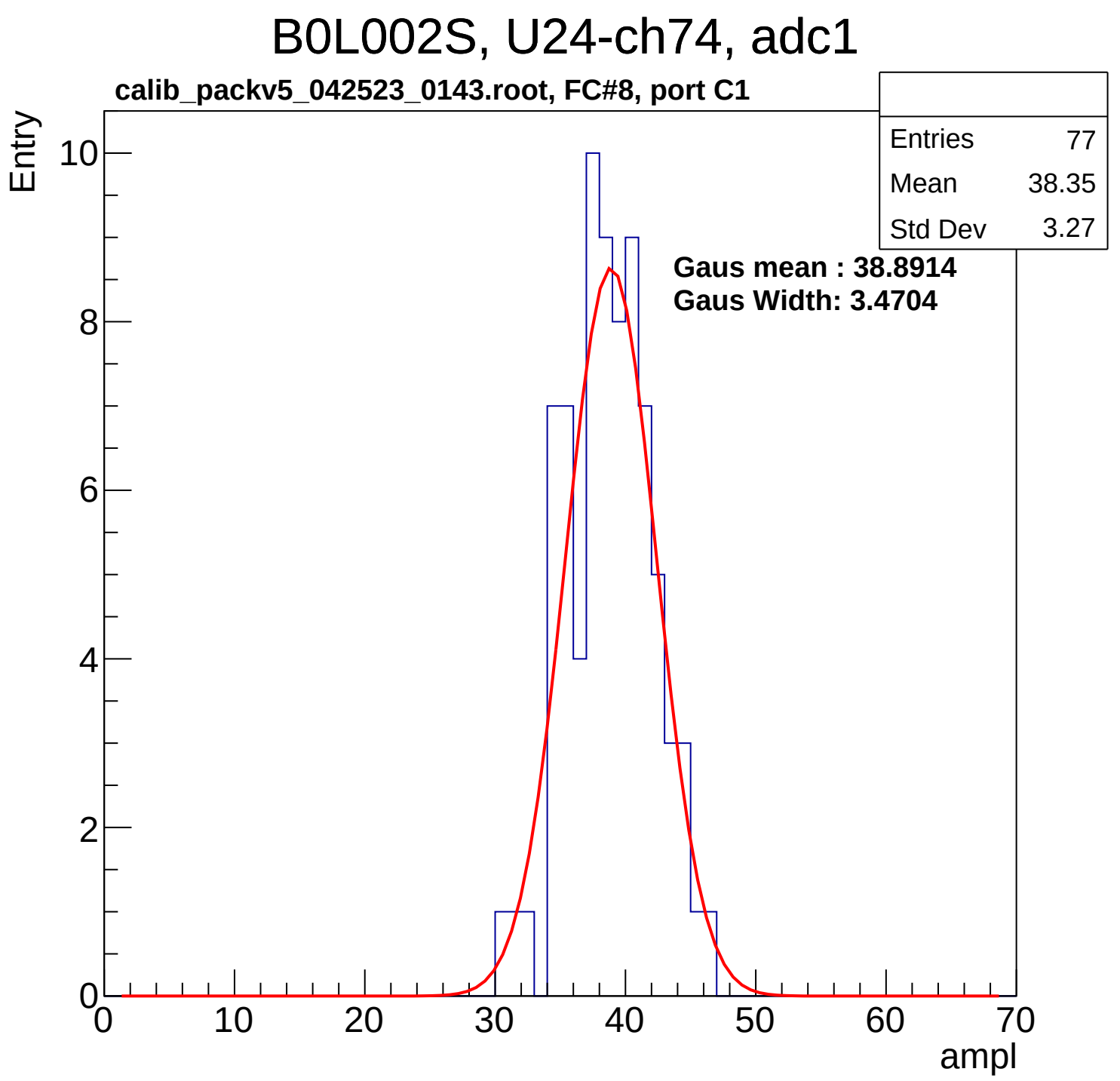
B0L002S, U24-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.35
Std Dev 3.27
Gaus mean : 38.8914
Gaus Width: 3.4704
Entry
ampl
0
2
4
6
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0
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40
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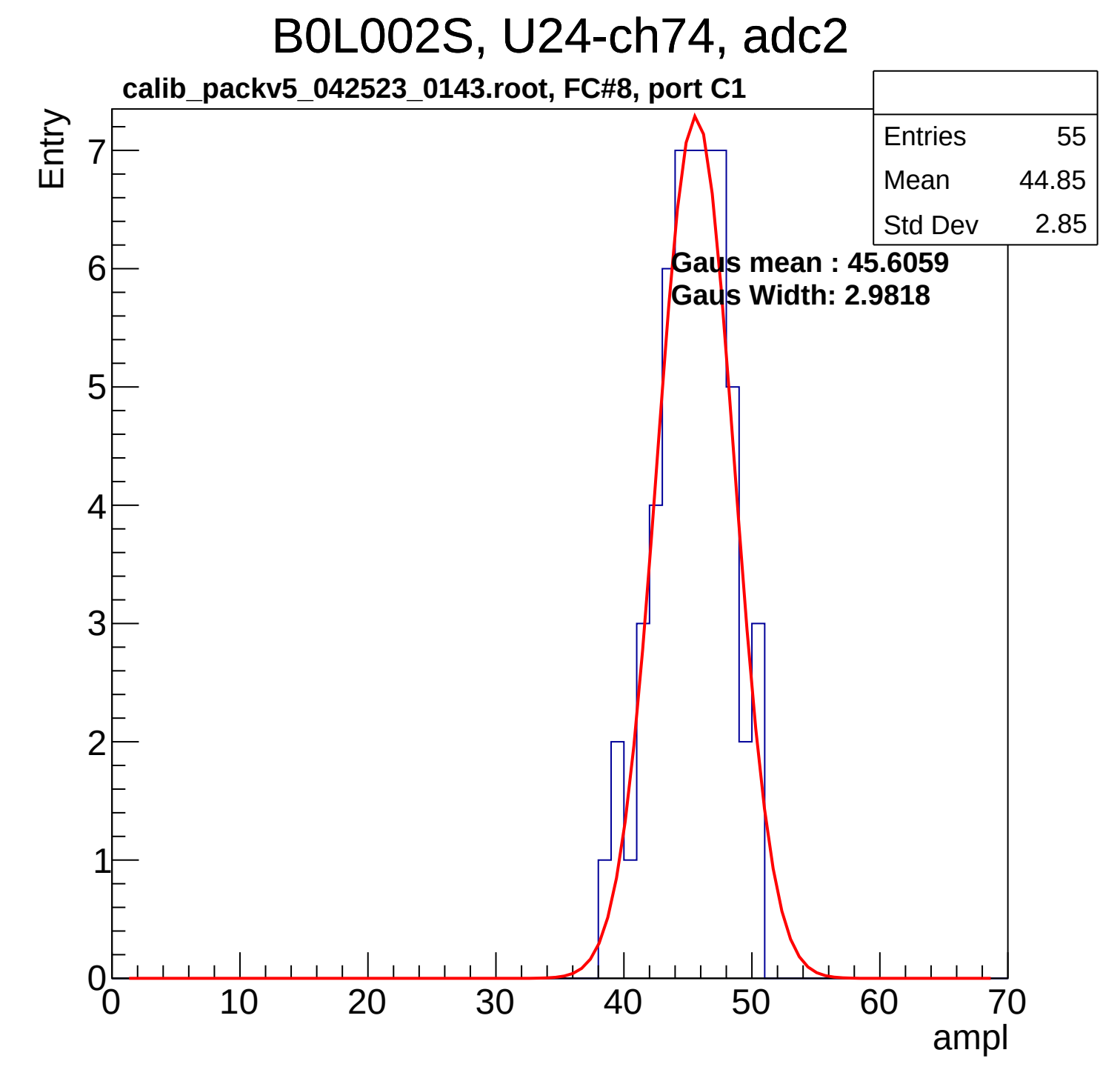
B0L002S, U24-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.85
Std Dev 2.85
Gaus mean : 45.6059
Gaus Width: 2.9818
Entry
ampl
0
1
2
3
4
5
6
7
0
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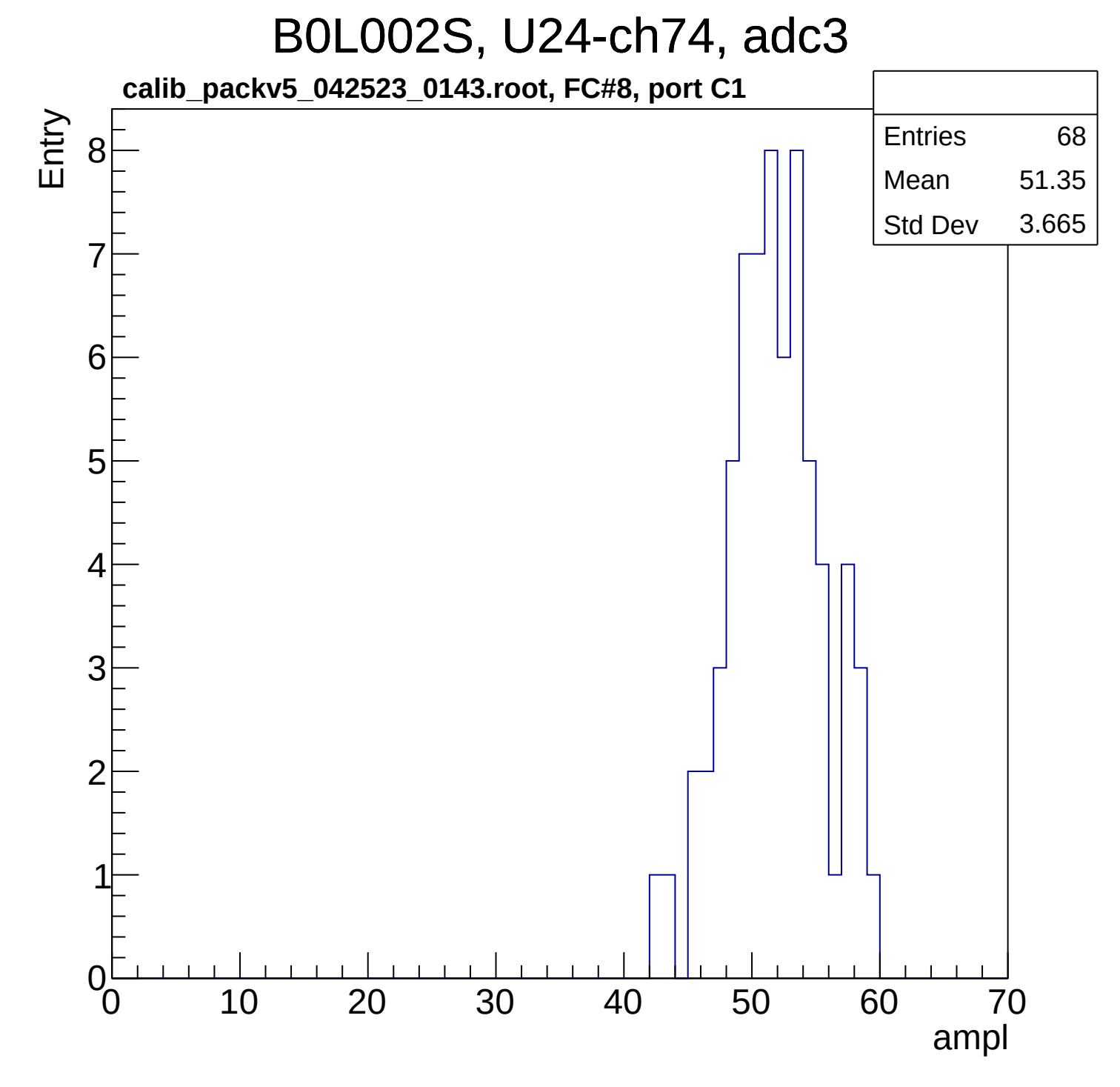
B0L002S, U24-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.35
Std Dev 3.665
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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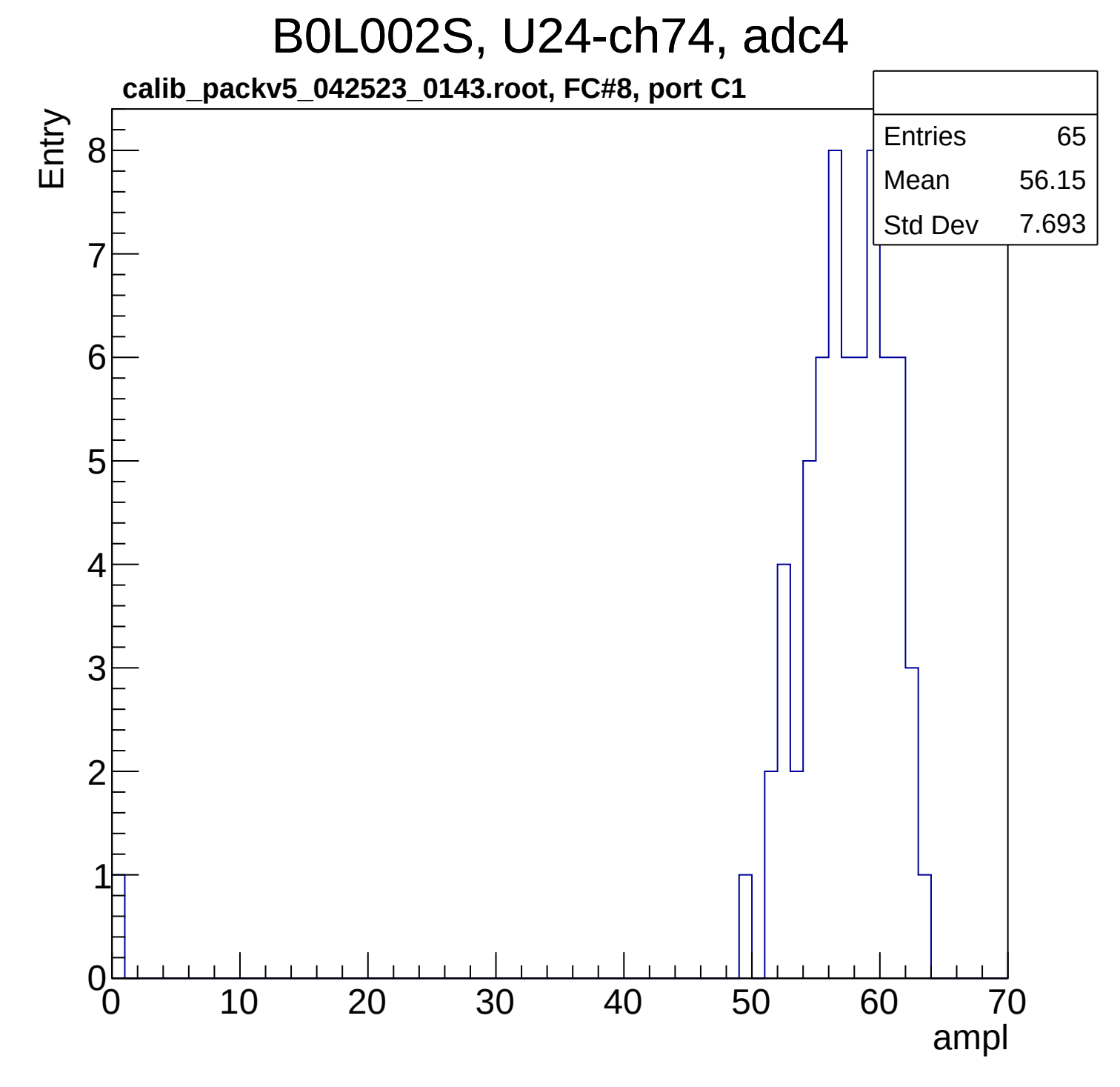
B0L002S, U24-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.15
Std Dev 7.693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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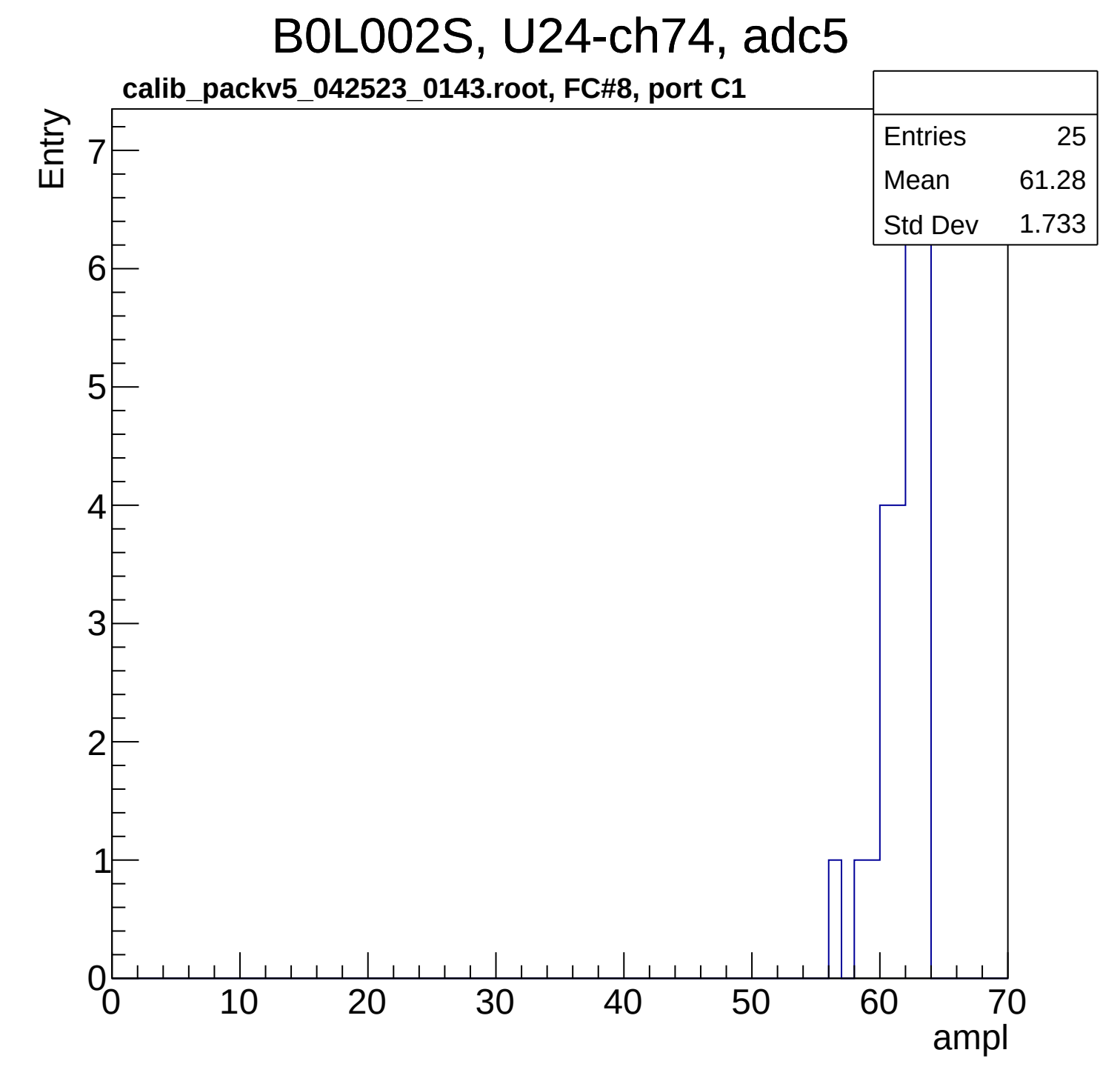
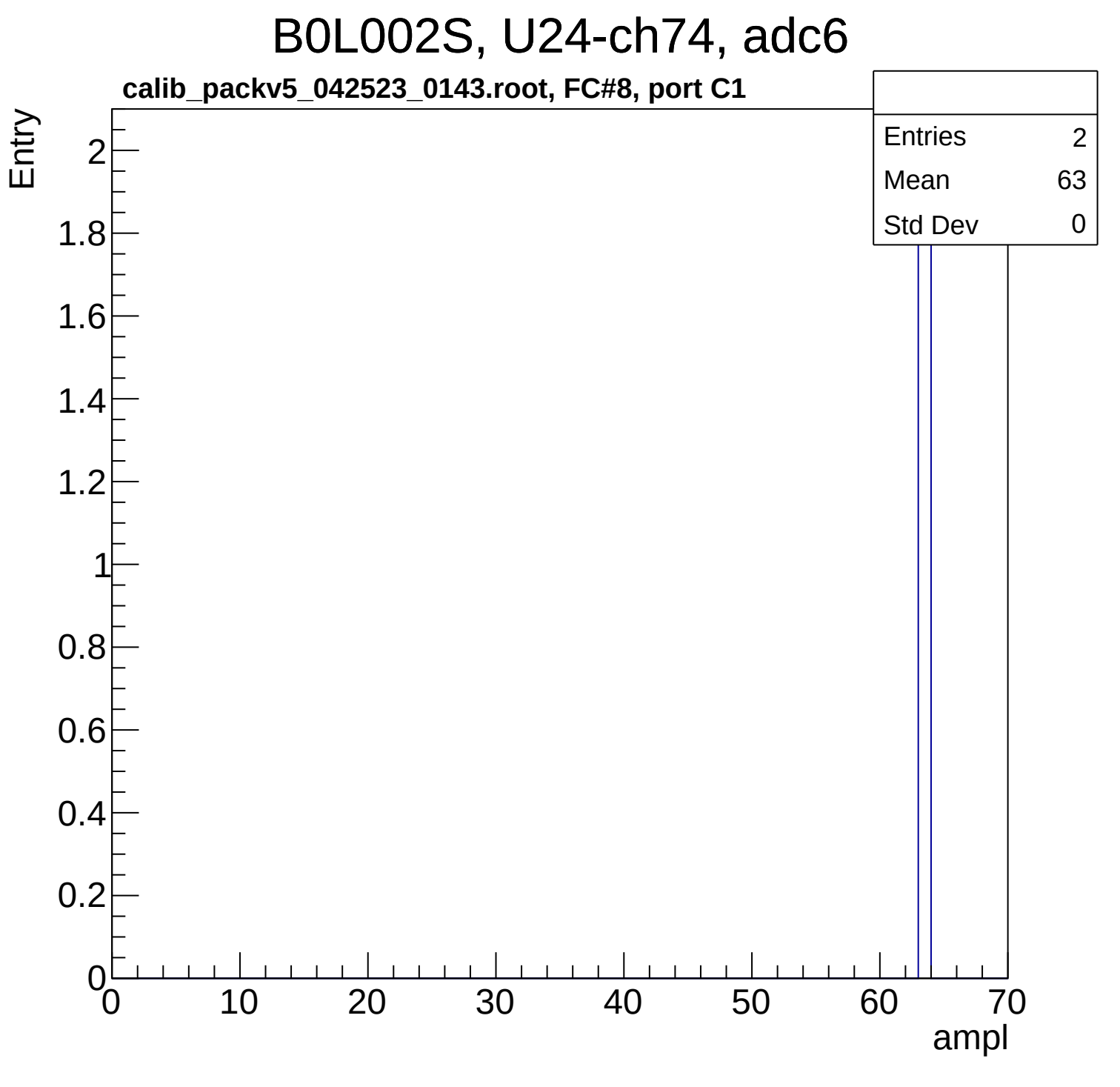
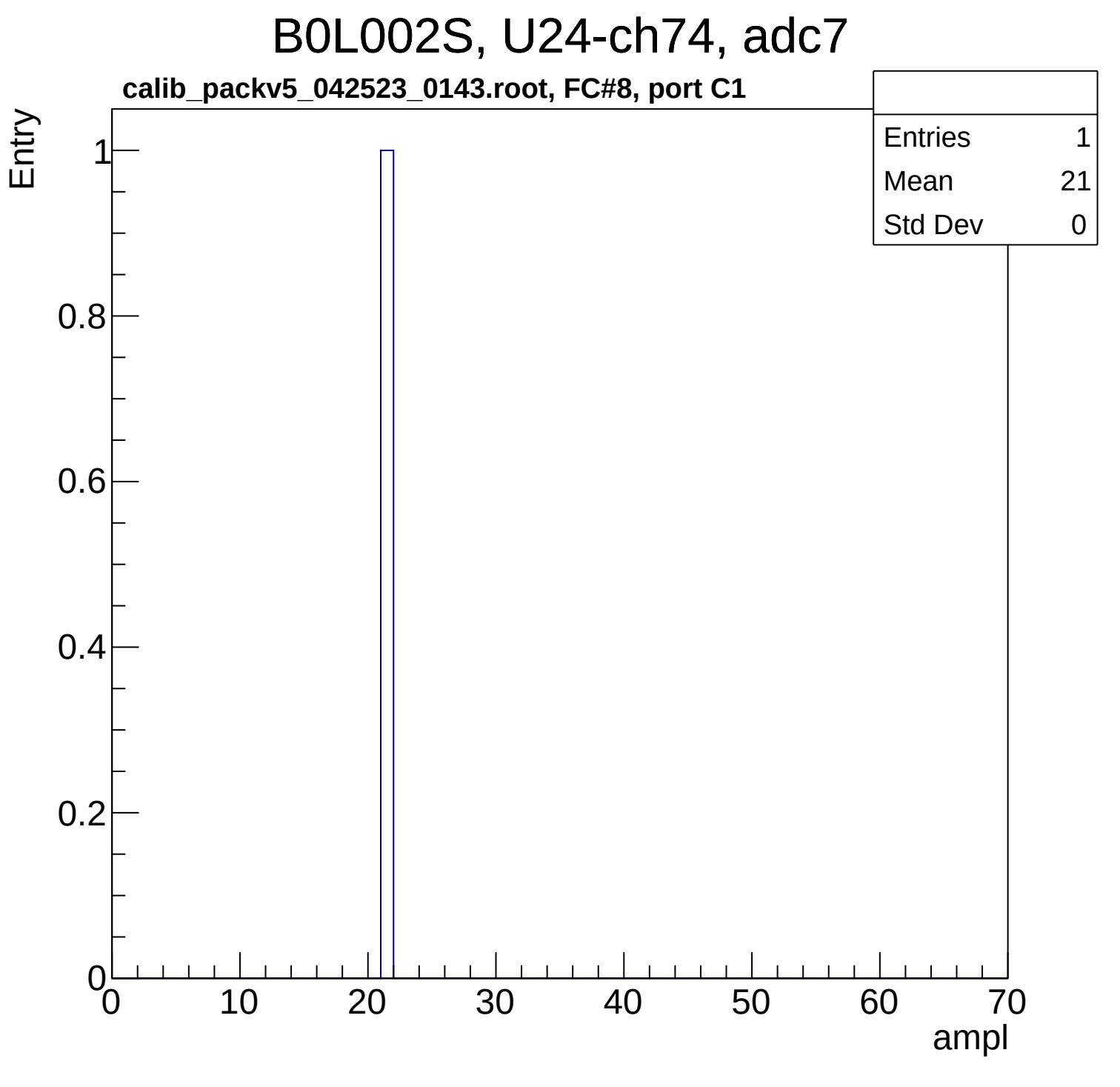

B0L002S, U24-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61.28
Std Dev 1.733
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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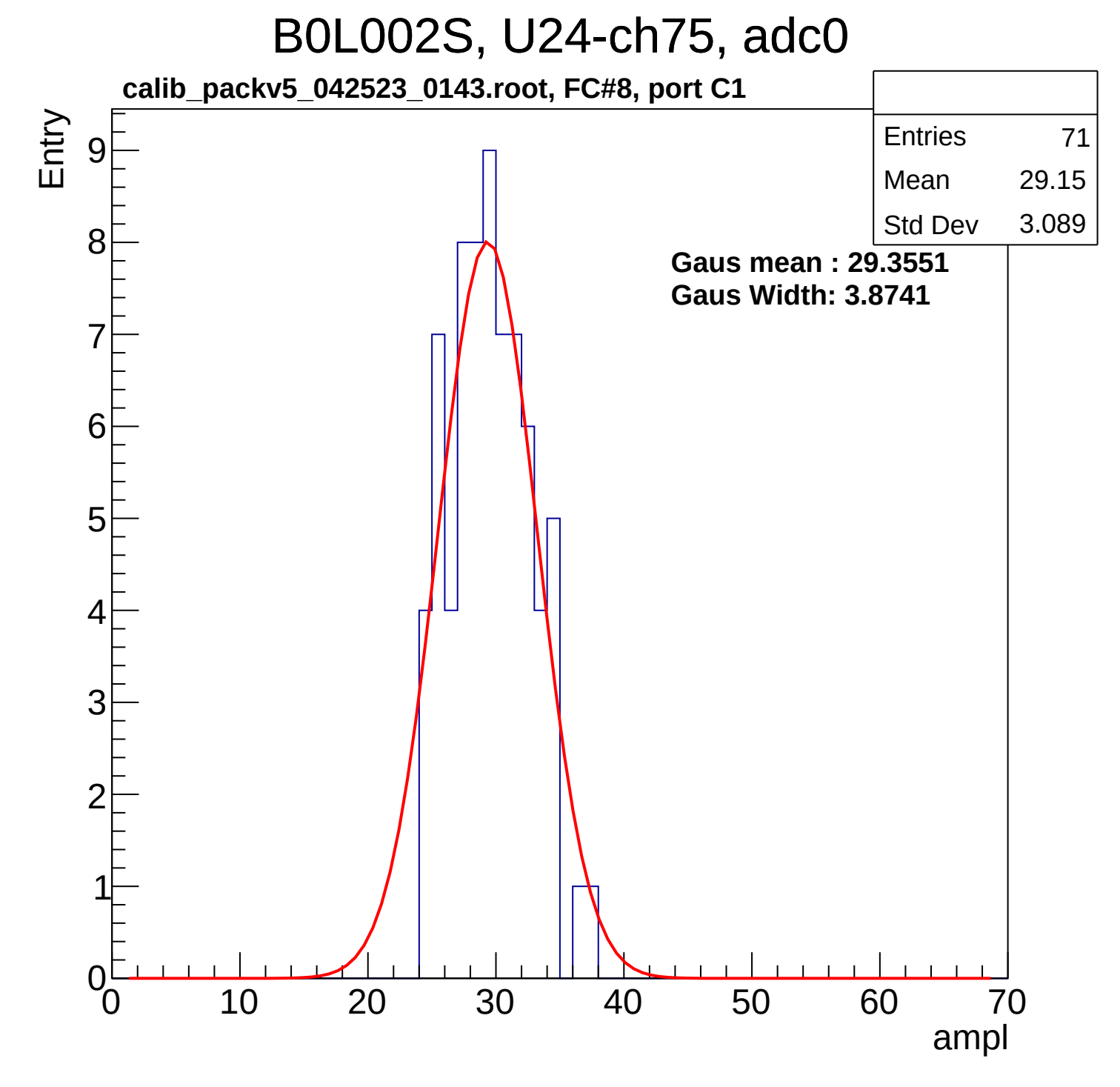
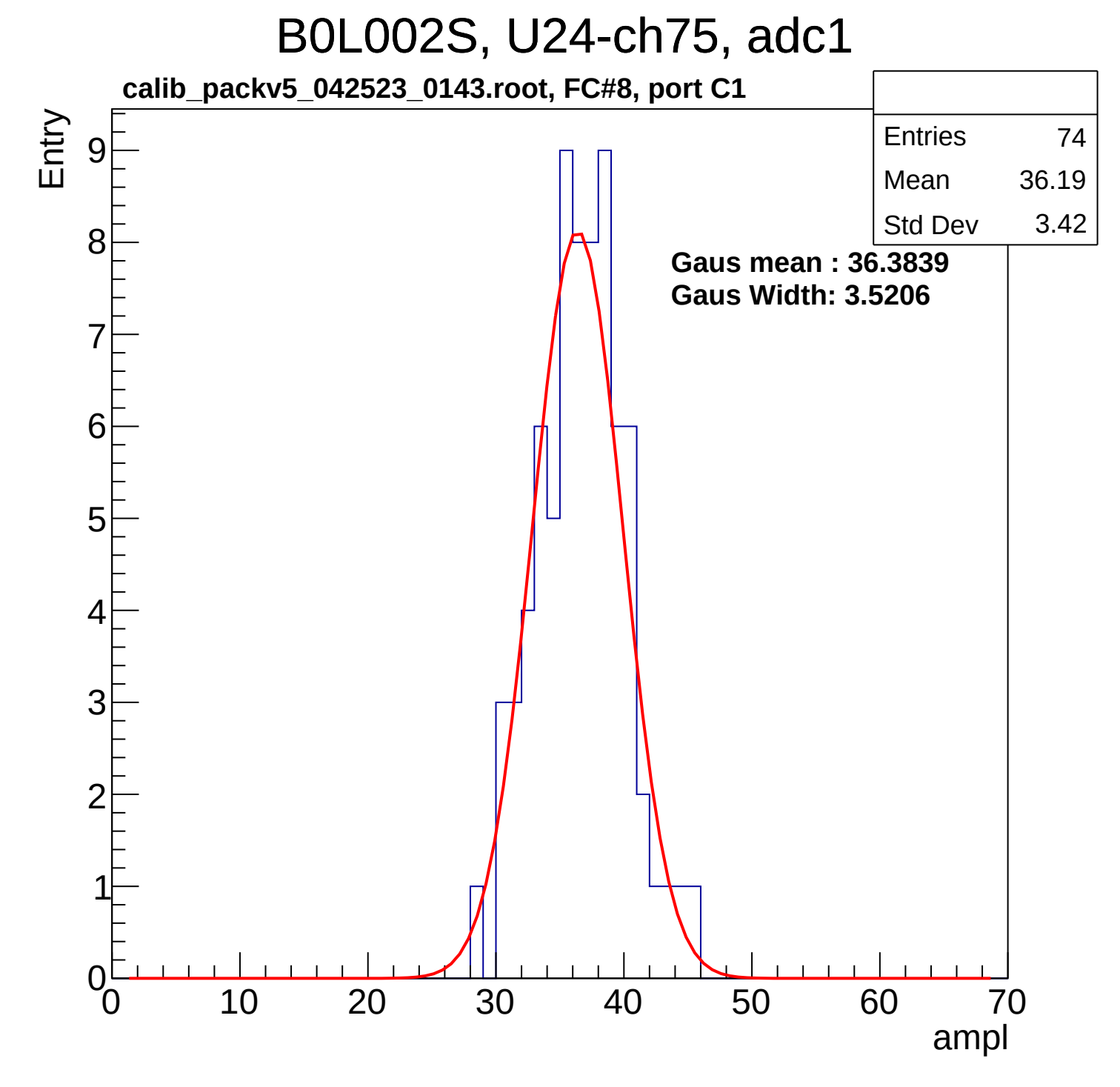
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.15
Std Dev 3.089
Gaus mean : 29.3551
Gaus Width: 3.8741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.19
Std Dev 3.42
Gaus mean : 36.3839
Gaus Width: 3.5206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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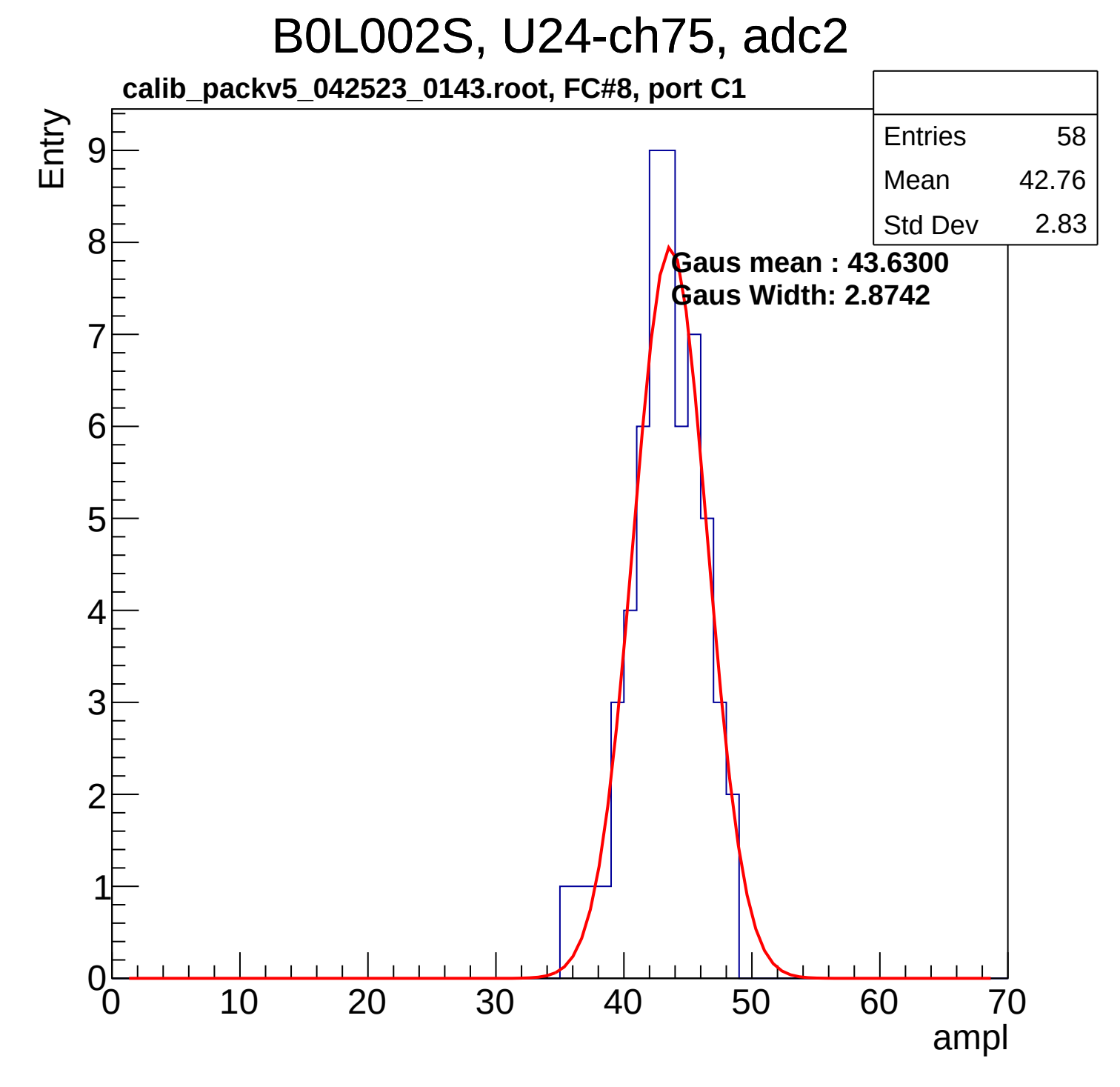
B0L002S, U24-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 42.76
Std Dev 2.83
Gaus mean : 43.6300
Gaus Width: 2.8742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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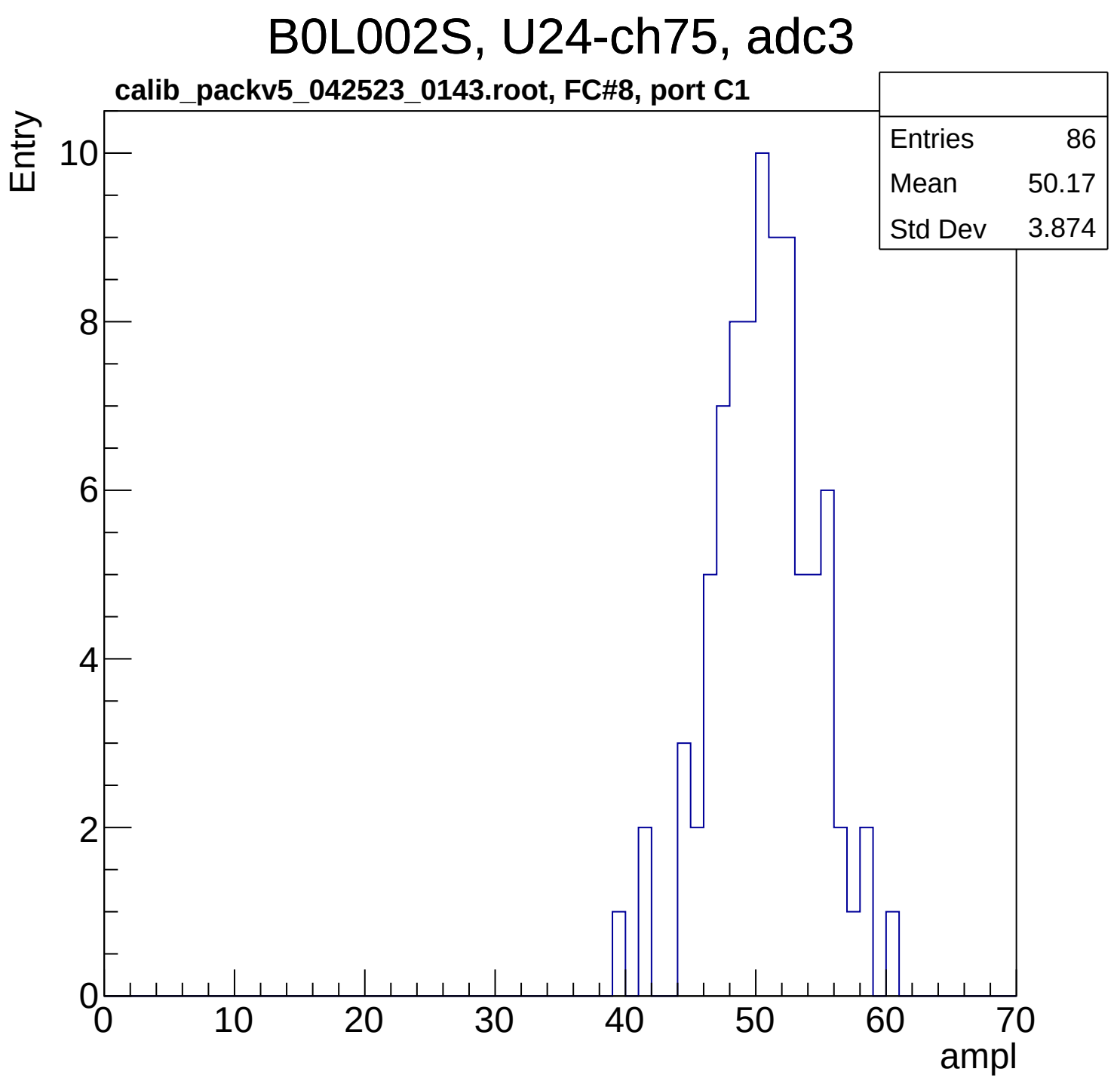
B0L002S, U24-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 50.17
Std Dev 3.874
Entry
ampl
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4
6
8
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0
10
20
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40
50
60
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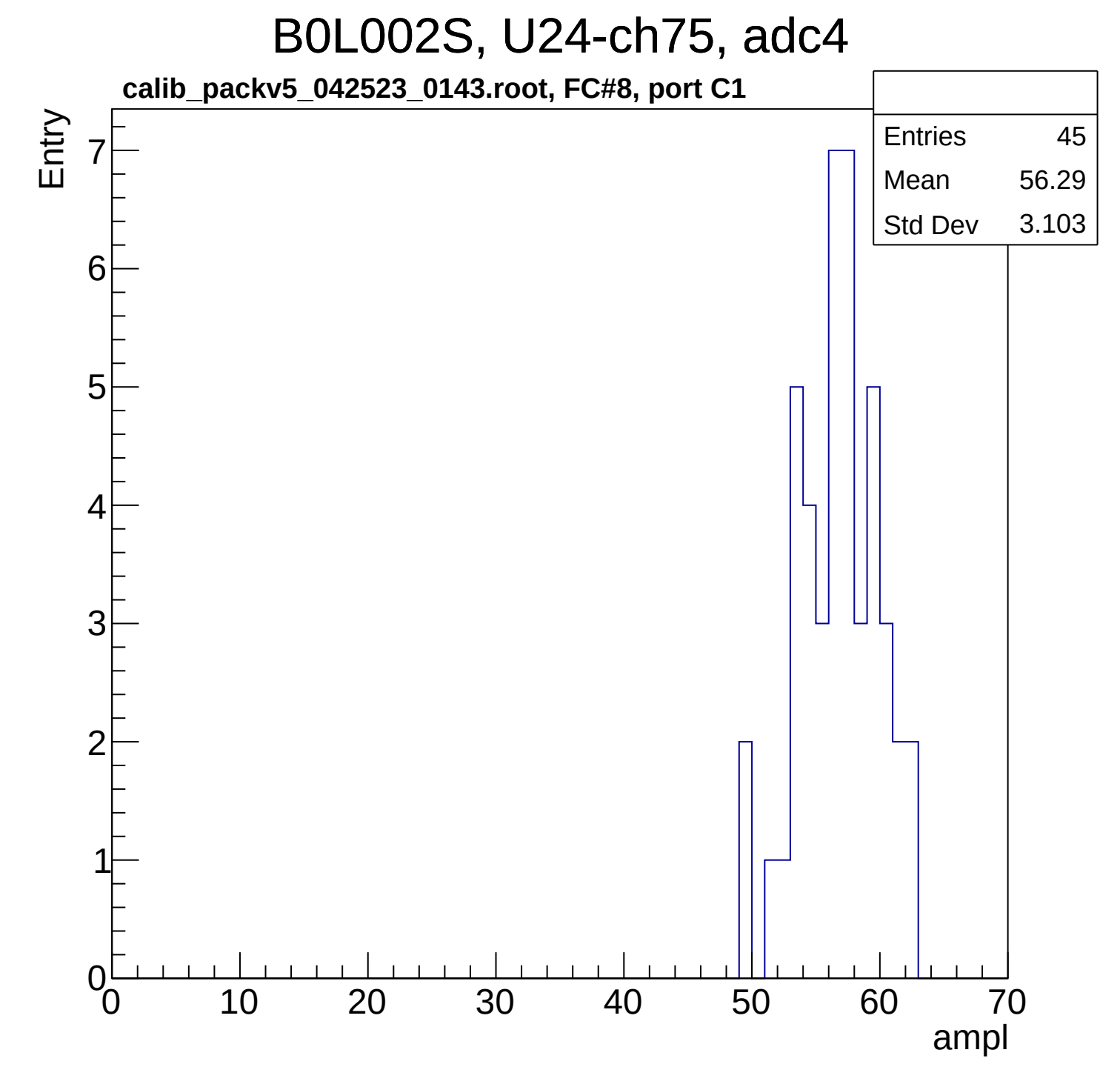
B0L002S, U24-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.29
Std Dev 3.103
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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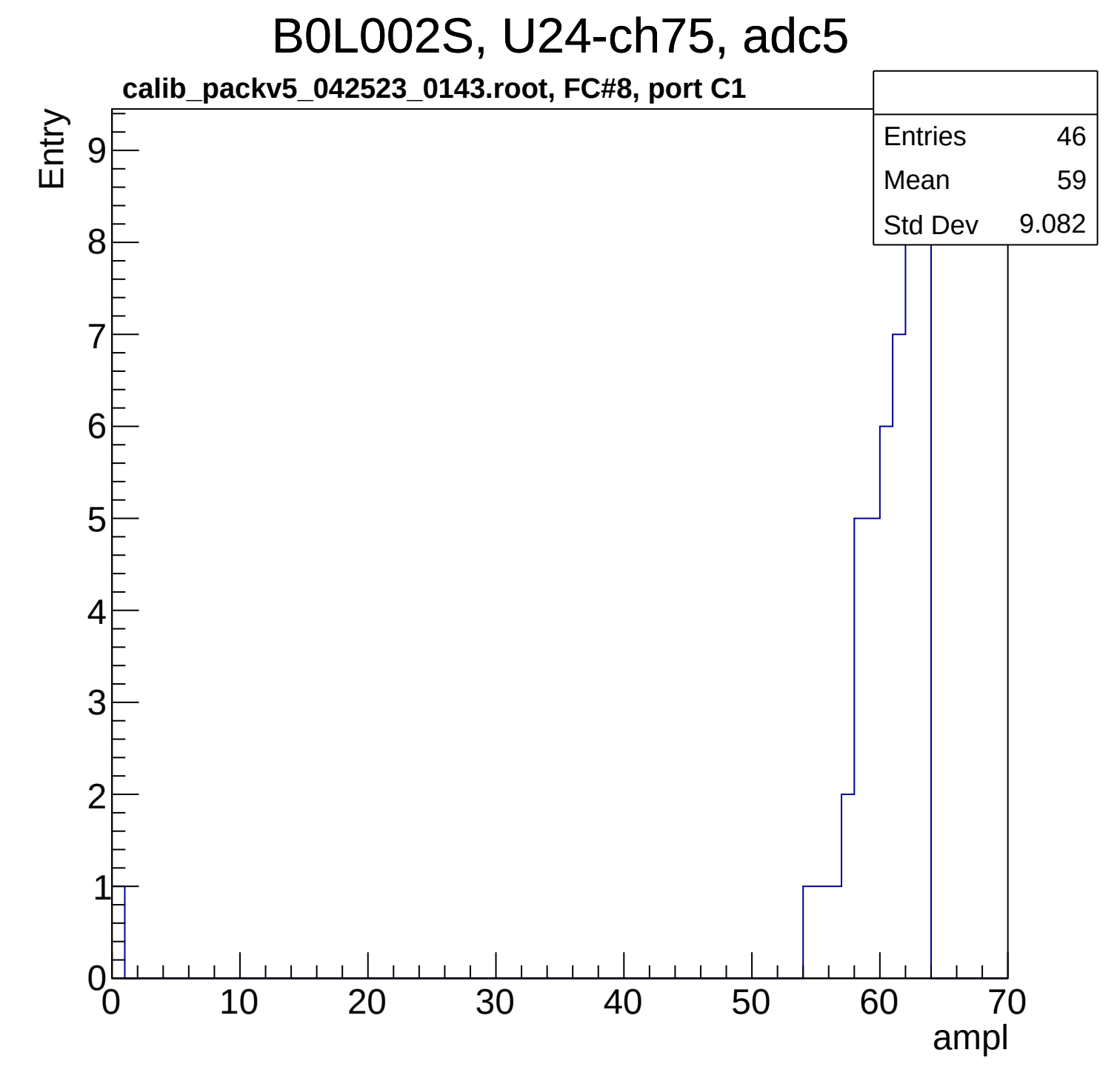
B0L002S, U24-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59
Std Dev 9.082
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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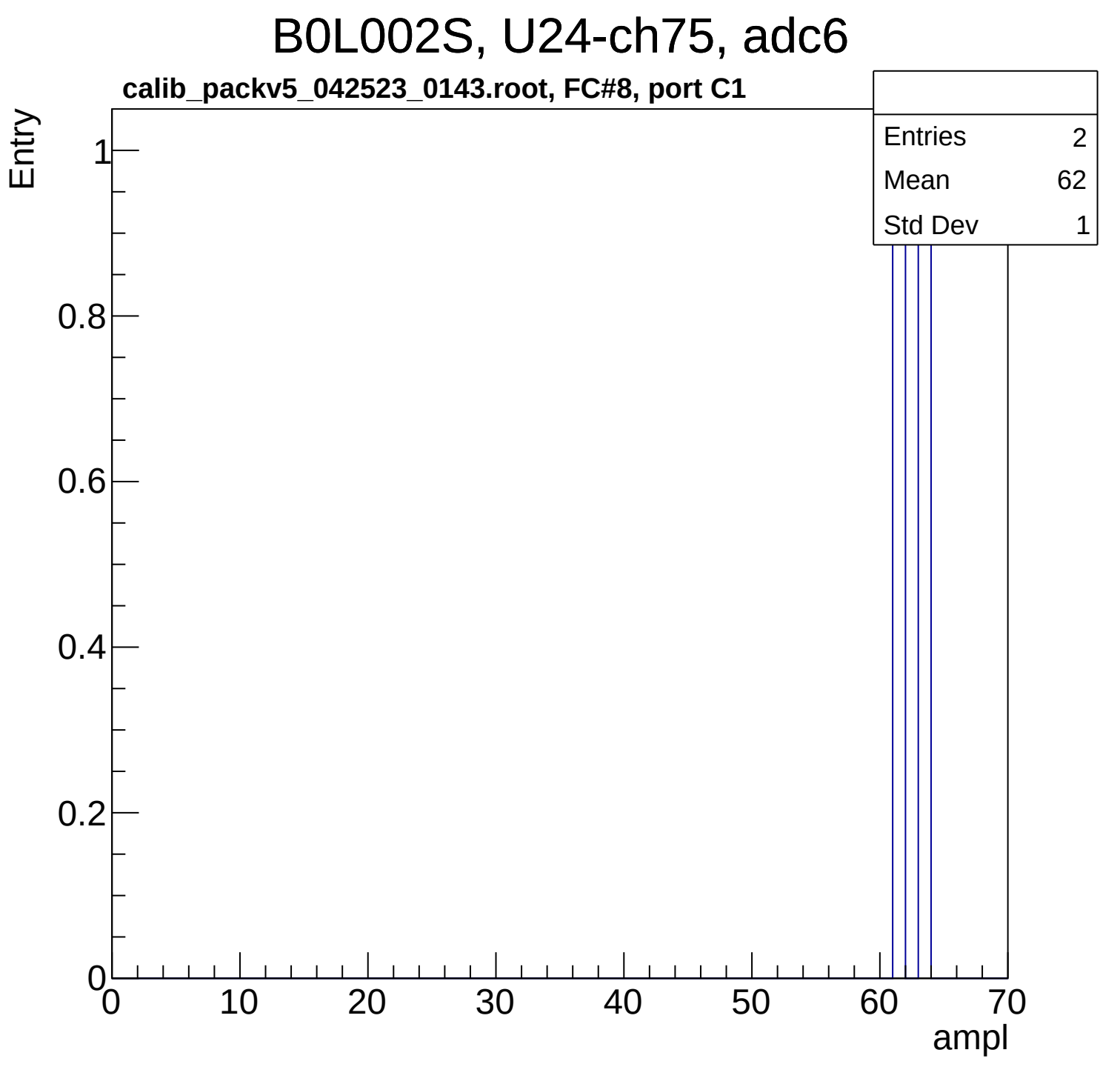
B0L002S, U24-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
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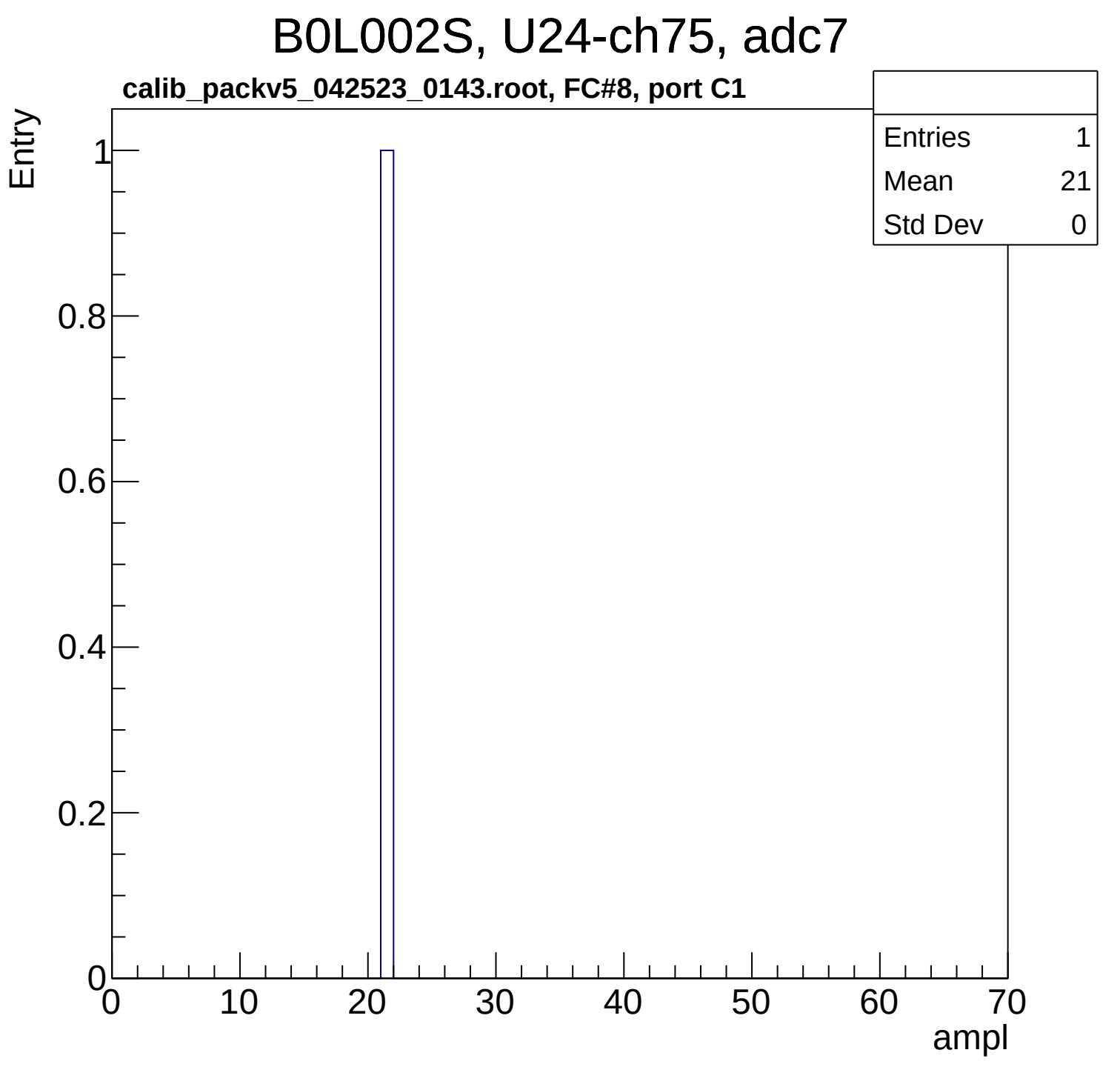
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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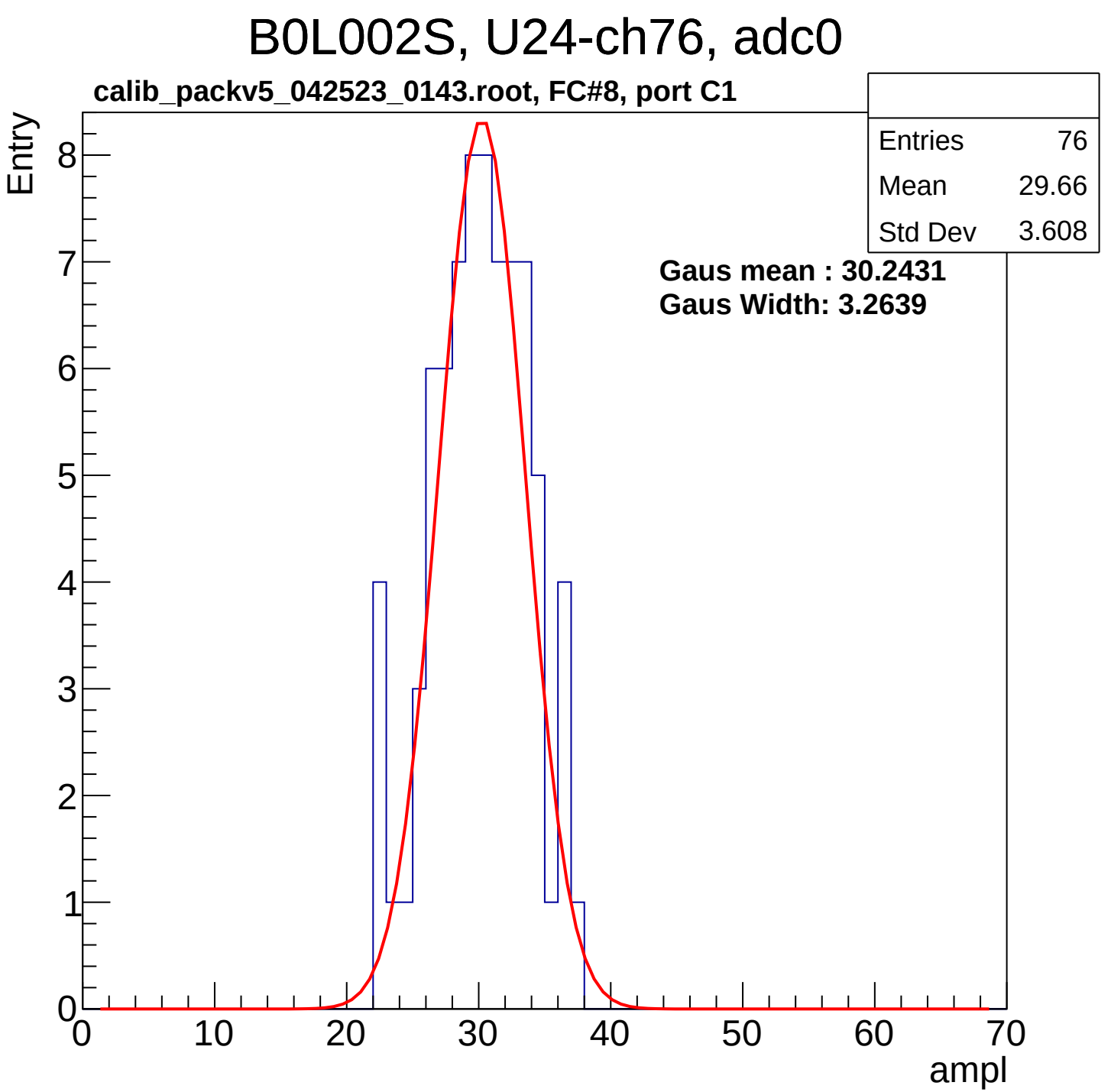
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 29.66
Std Dev 3.608
Gaus mean : 30.2431
Gaus Width: 3.2639
Entry
ampl
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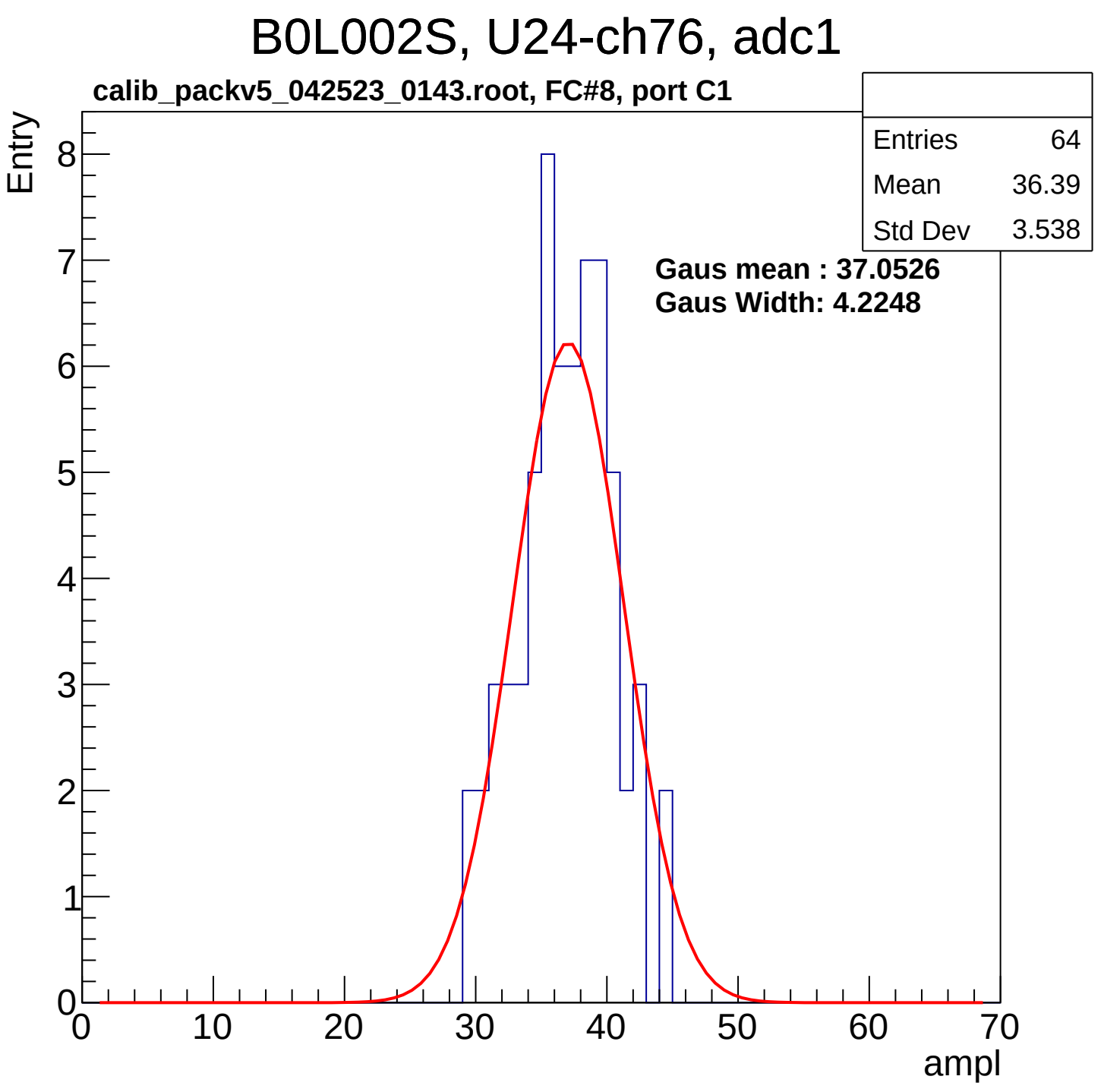
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 36.39
Std Dev 3.538
Gaus mean : 37.0526
Gaus Width: 4.2248
Entry
ampl
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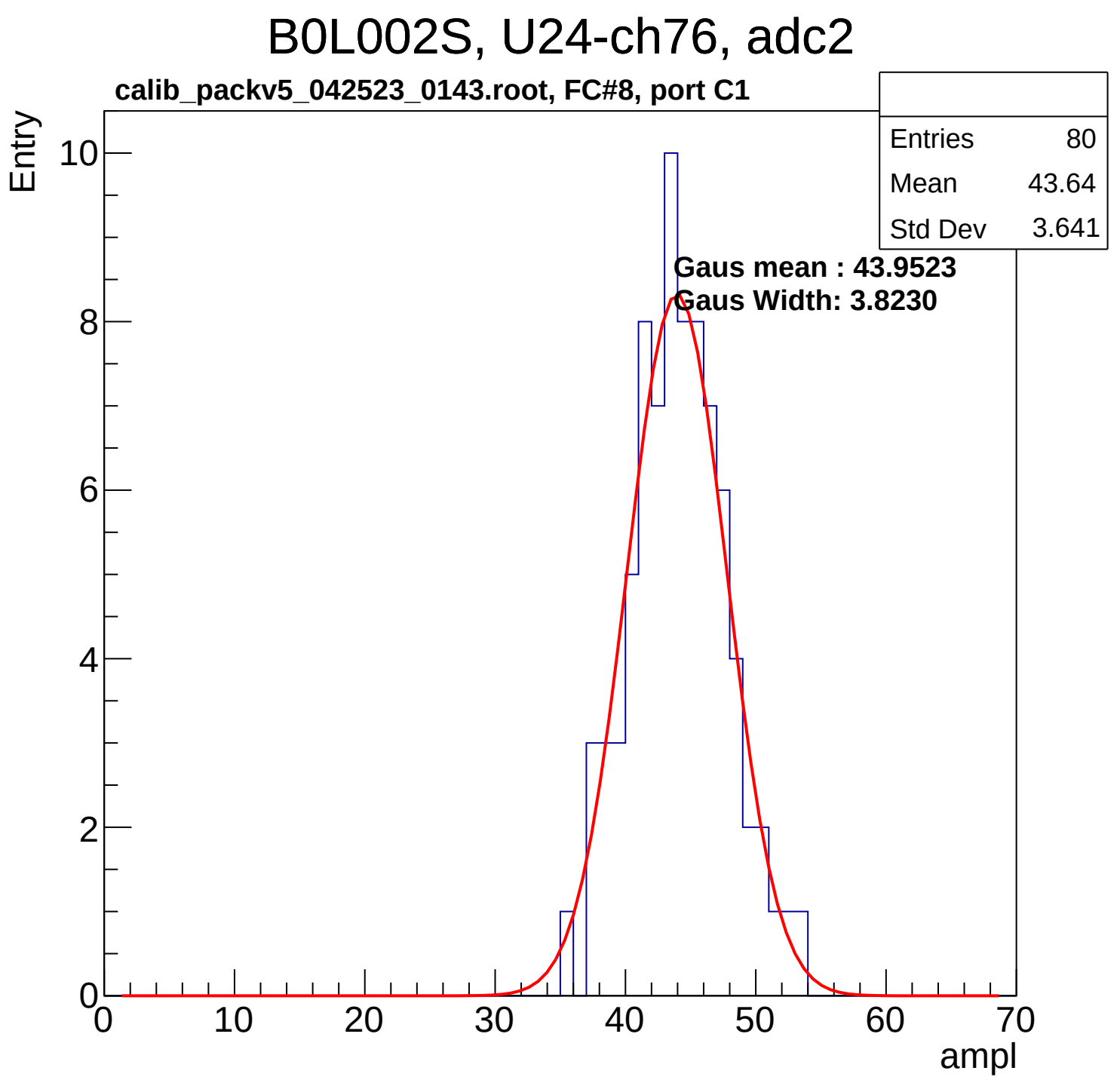
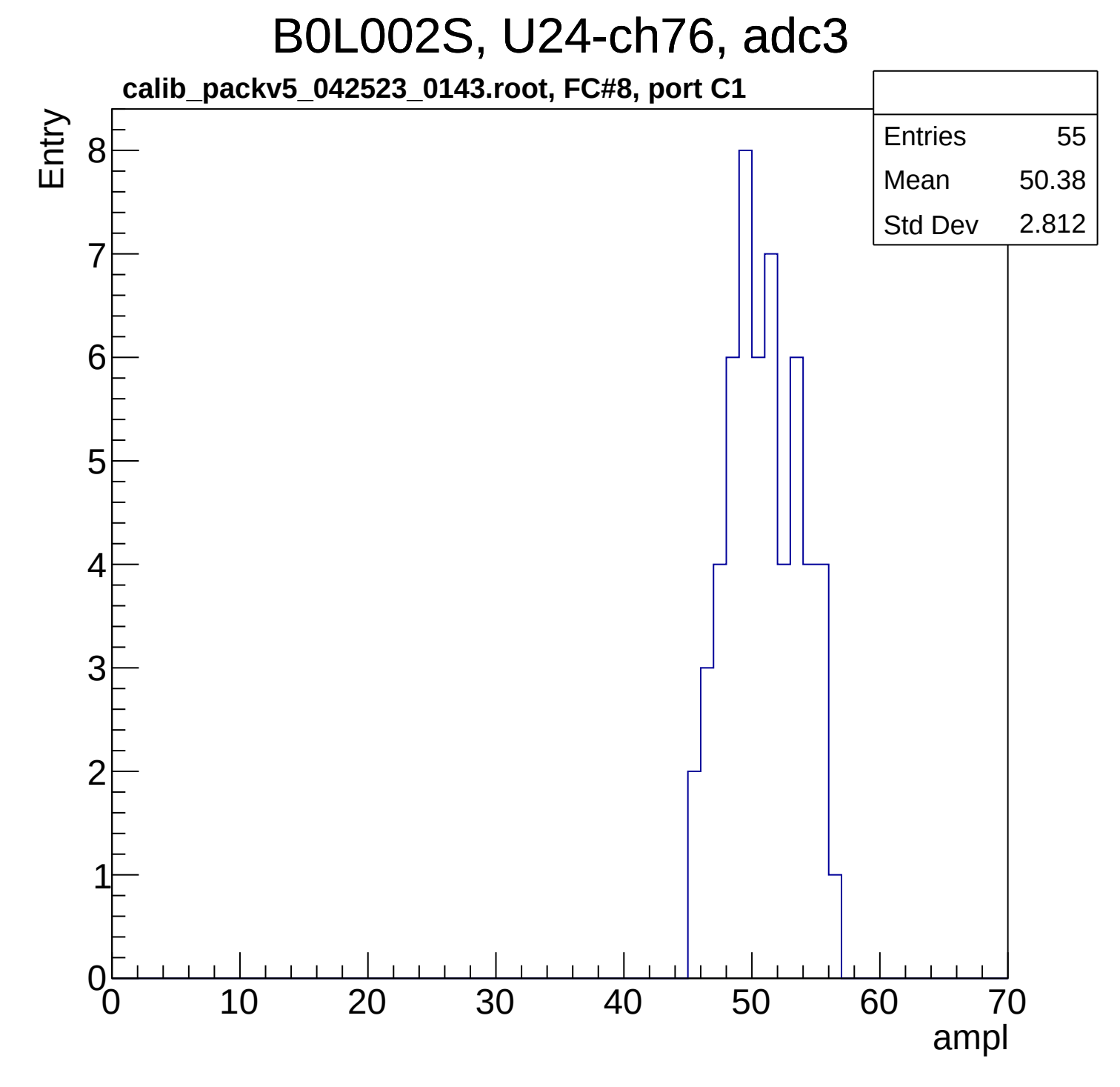
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.64
Std Dev 3.641
Gaus mean : 43.9523
Gaus Width: 3.8230
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 50.38
Std Dev 2.812
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
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60
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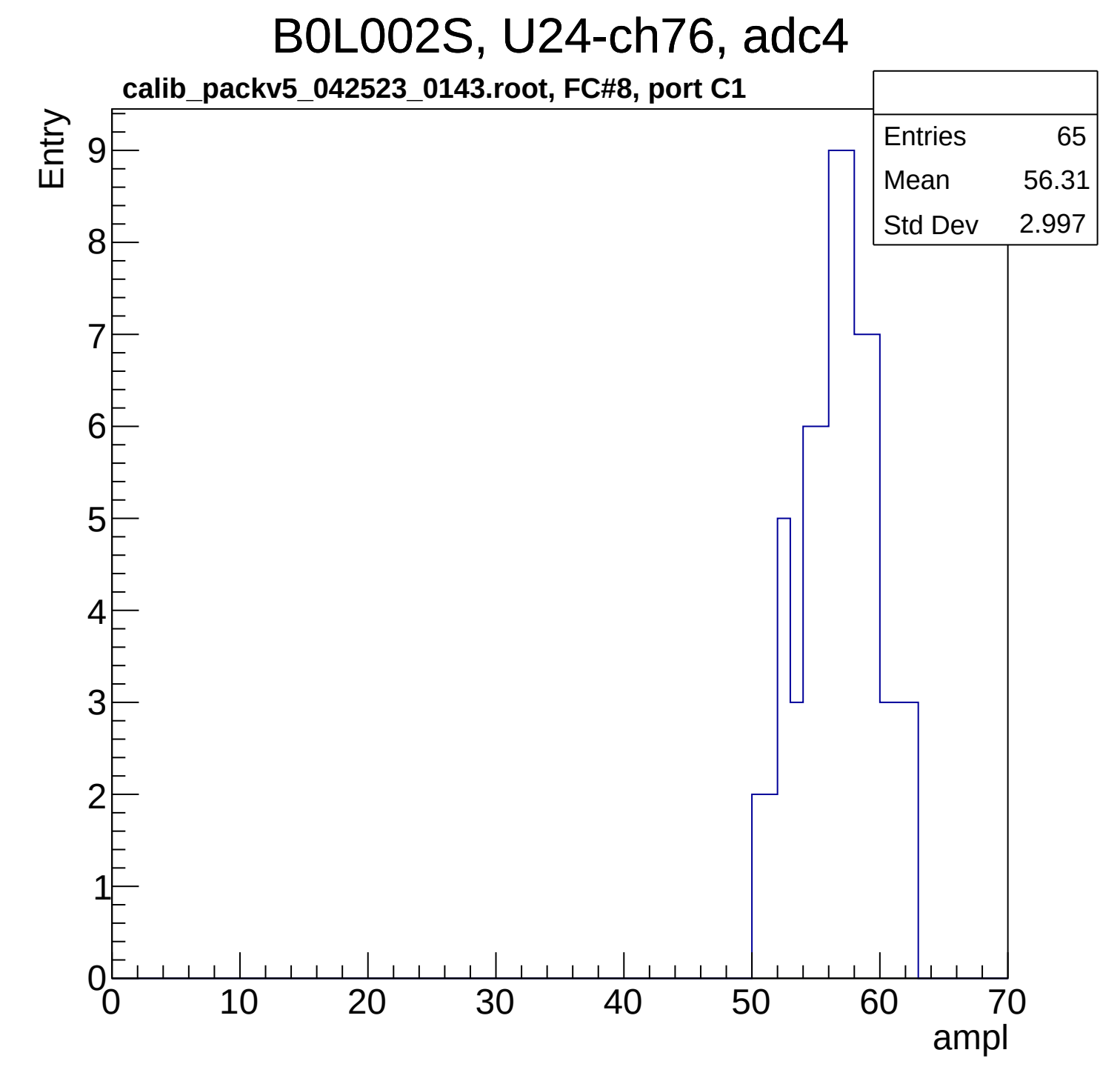
B0L002S, U24-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.31
Std Dev 2.997
Entry
ampl
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3
4
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6
7
8
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0
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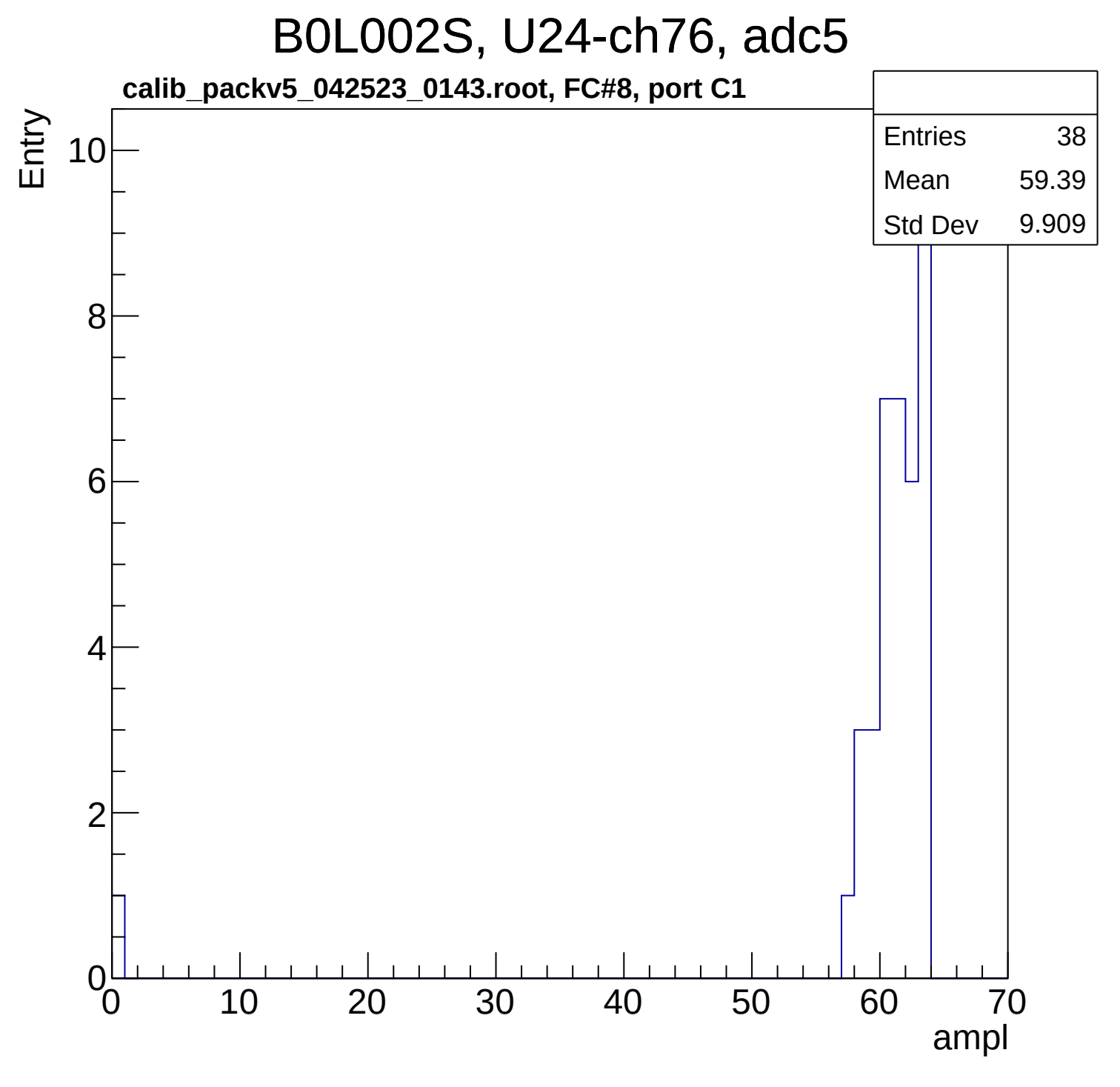
B0L002S, U24-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.39
Std Dev 9.909
Entry
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6
8
10
0
10
20
30
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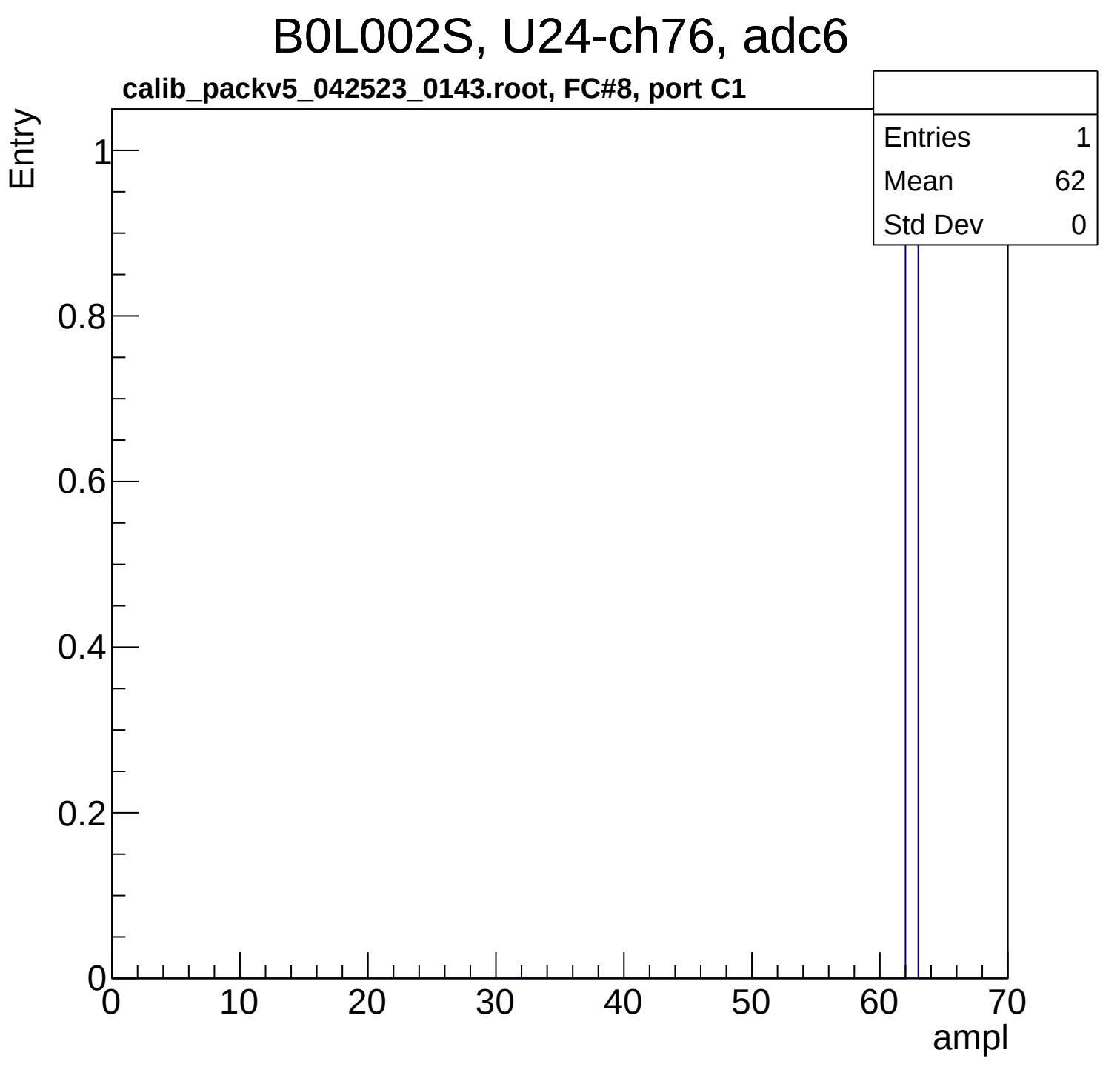
50
60
70
ampl


B0L002S, U24-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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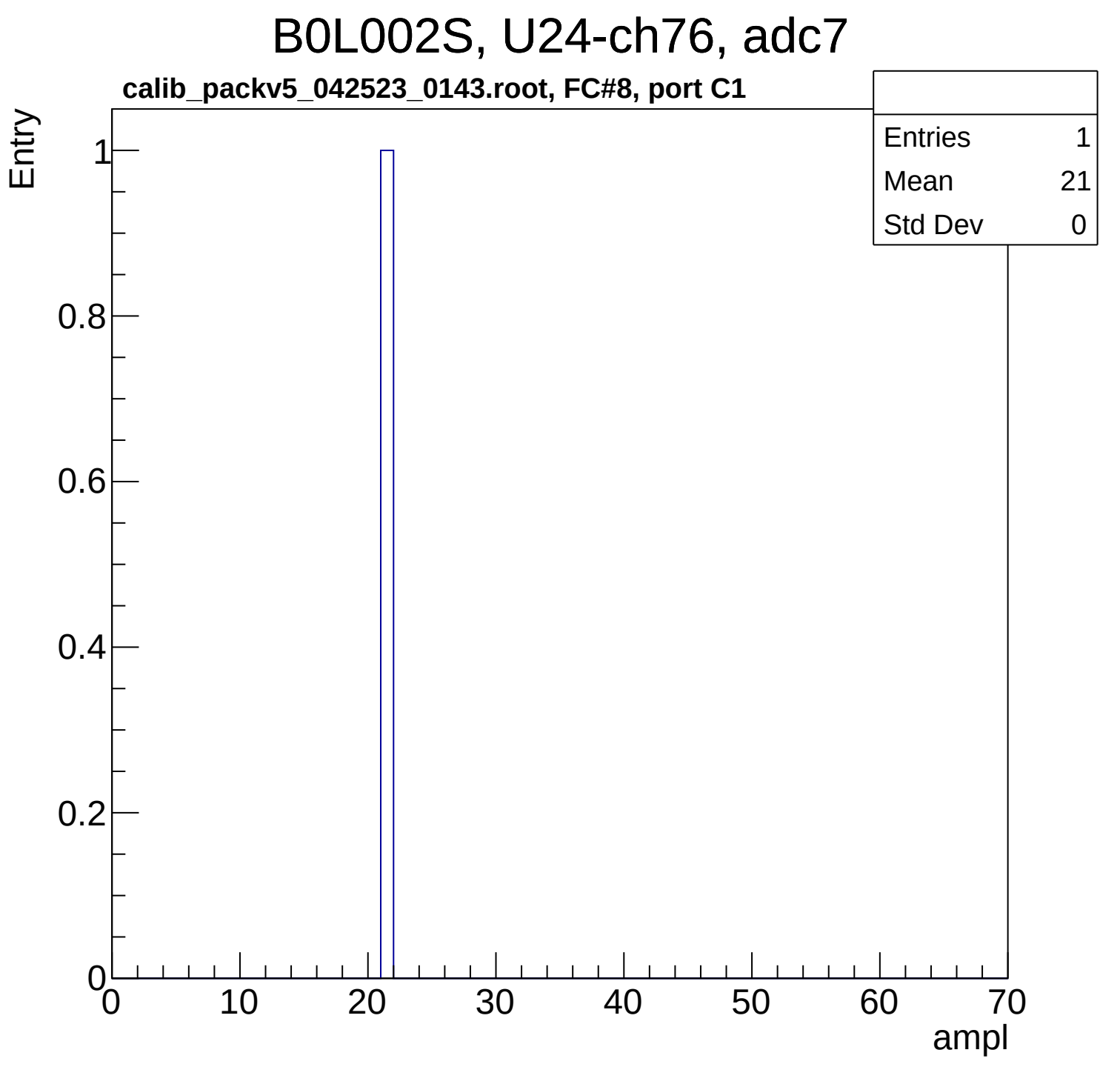
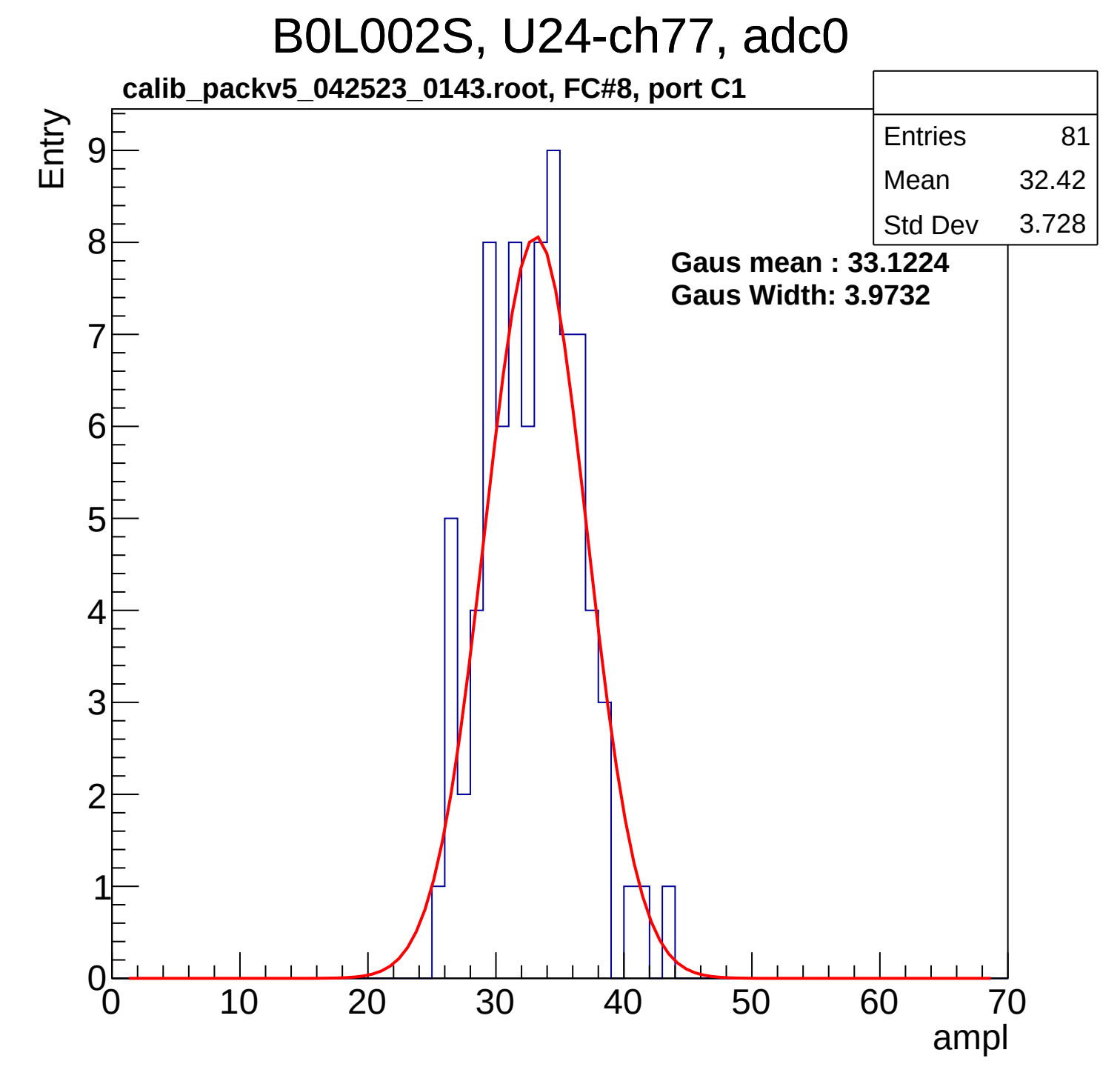
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 32.42
Std Dev 3.728
Gaus mean : 33.1224
Gaus Width: 3.9732
Entry
ampl
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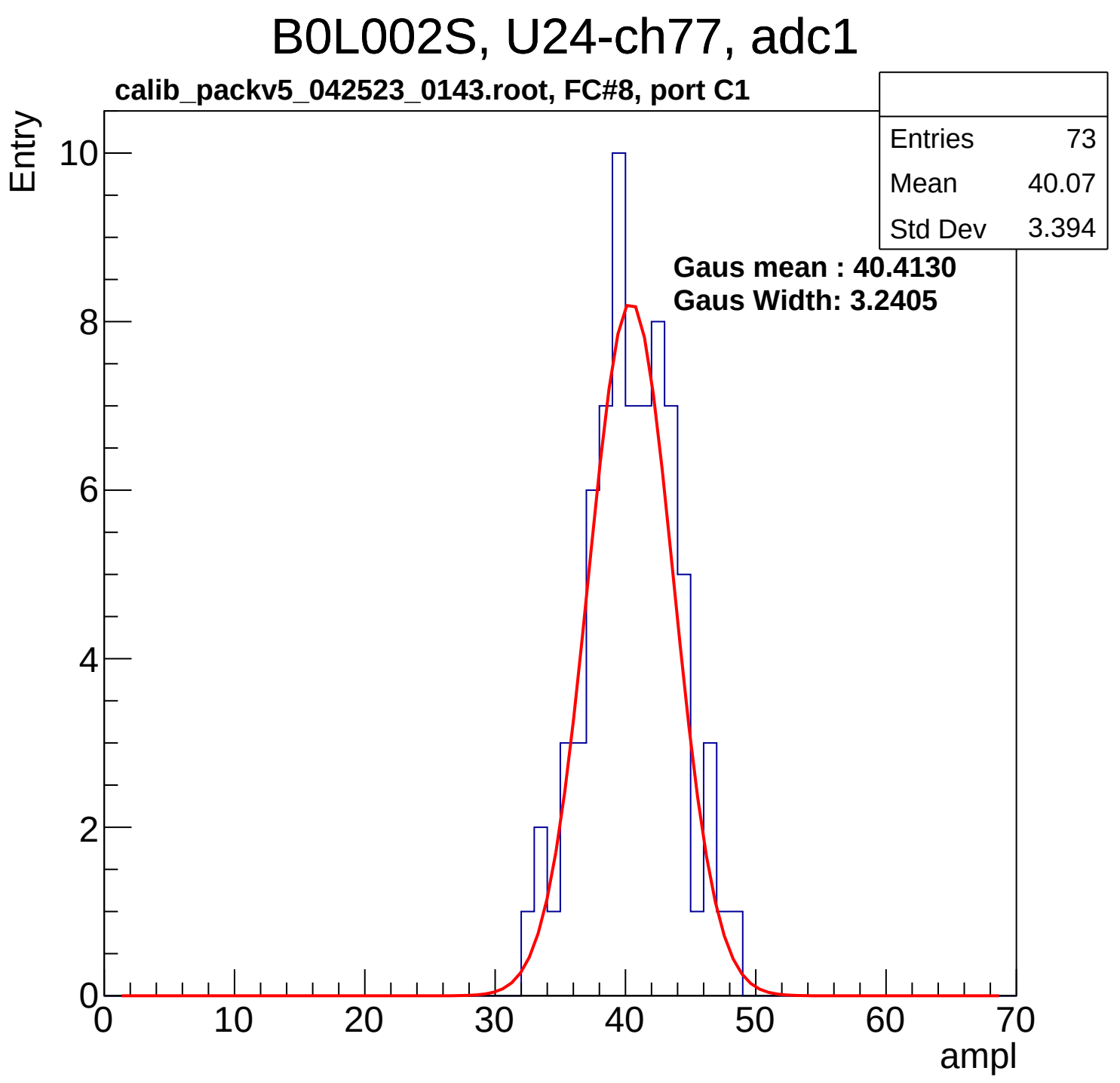
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 40.07
Std Dev 3.394
Gaus mean : 40.4130
Gaus Width: 3.2405
Entry
ampl
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6
8
10
0
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20
30
40
50
60
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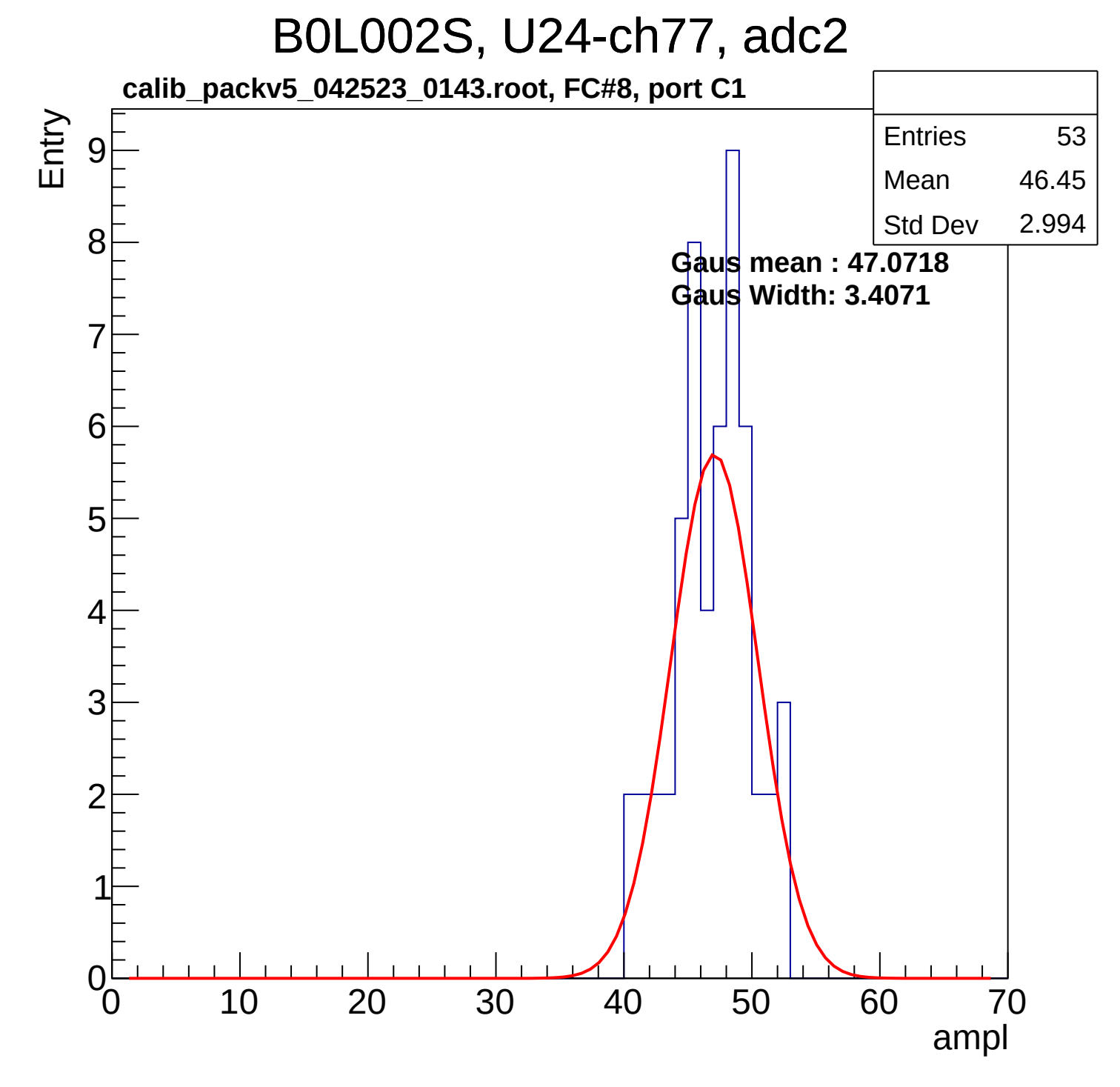
B0L002S, U24-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 46.45
Std Dev 2.994
Gaus mean : 47.0718
Gaus Width: 3.4071
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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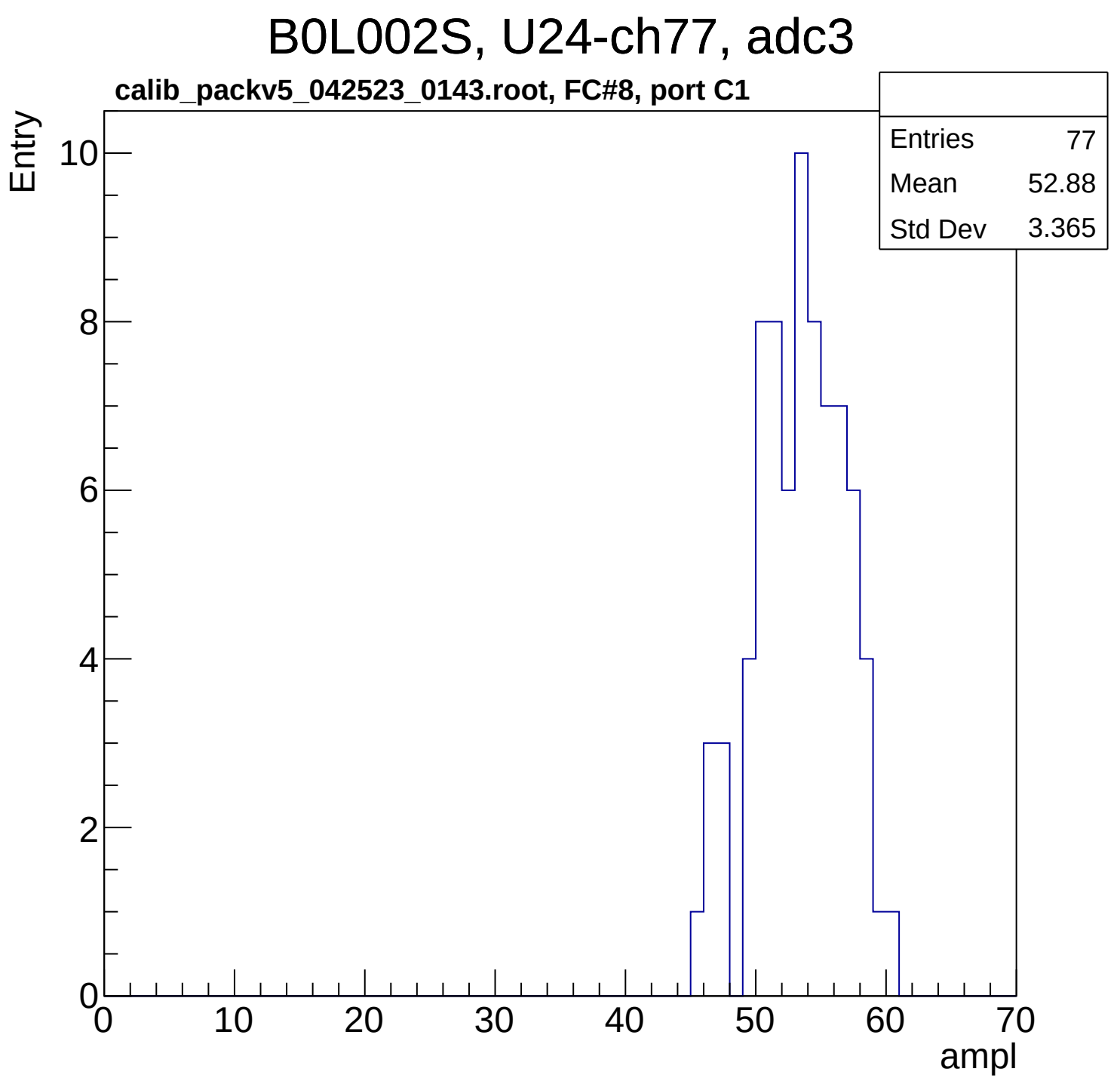
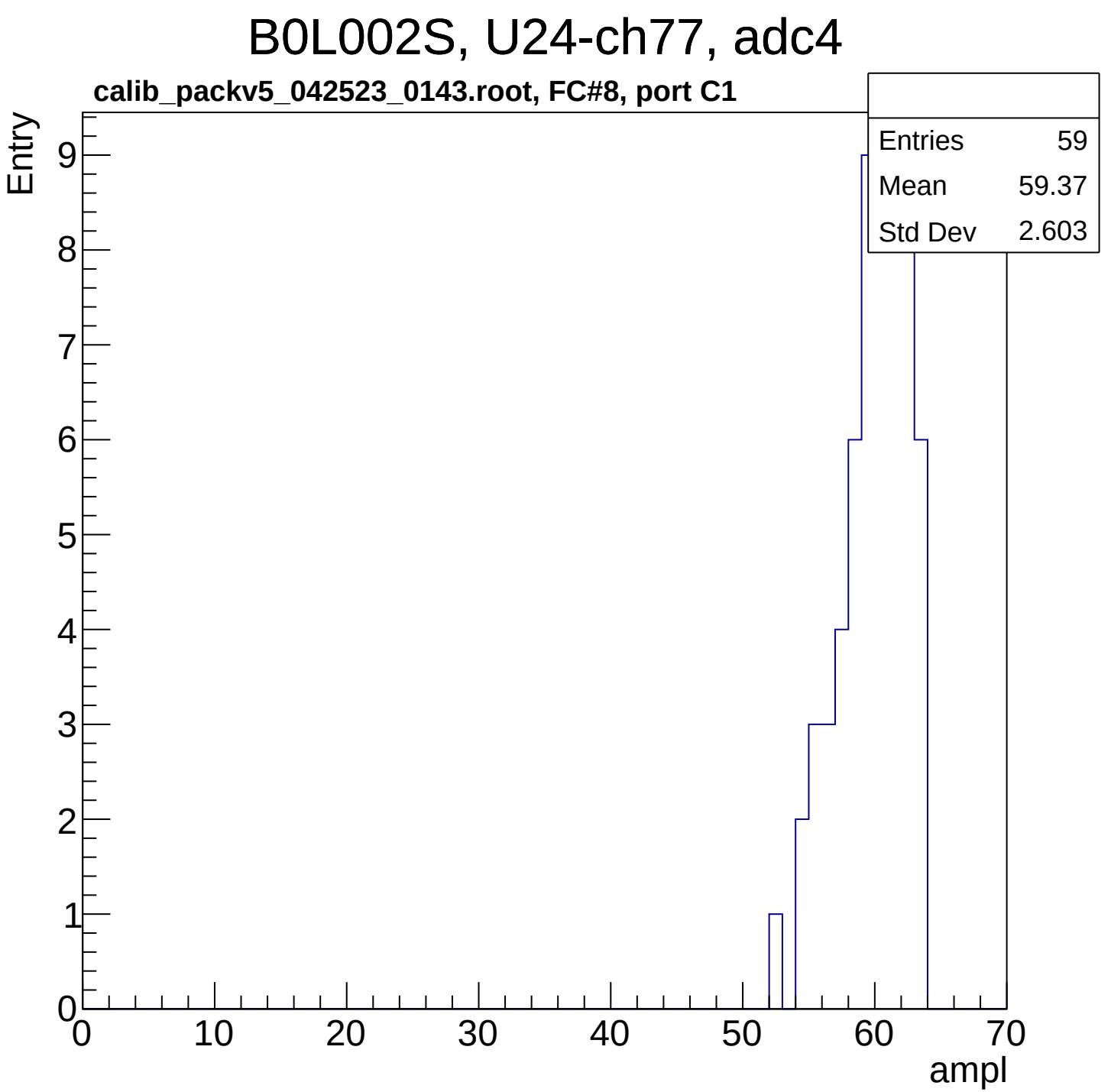

B0L002S, U24-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 52.88
Std Dev 3.365
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 59.37
Std Dev 2.603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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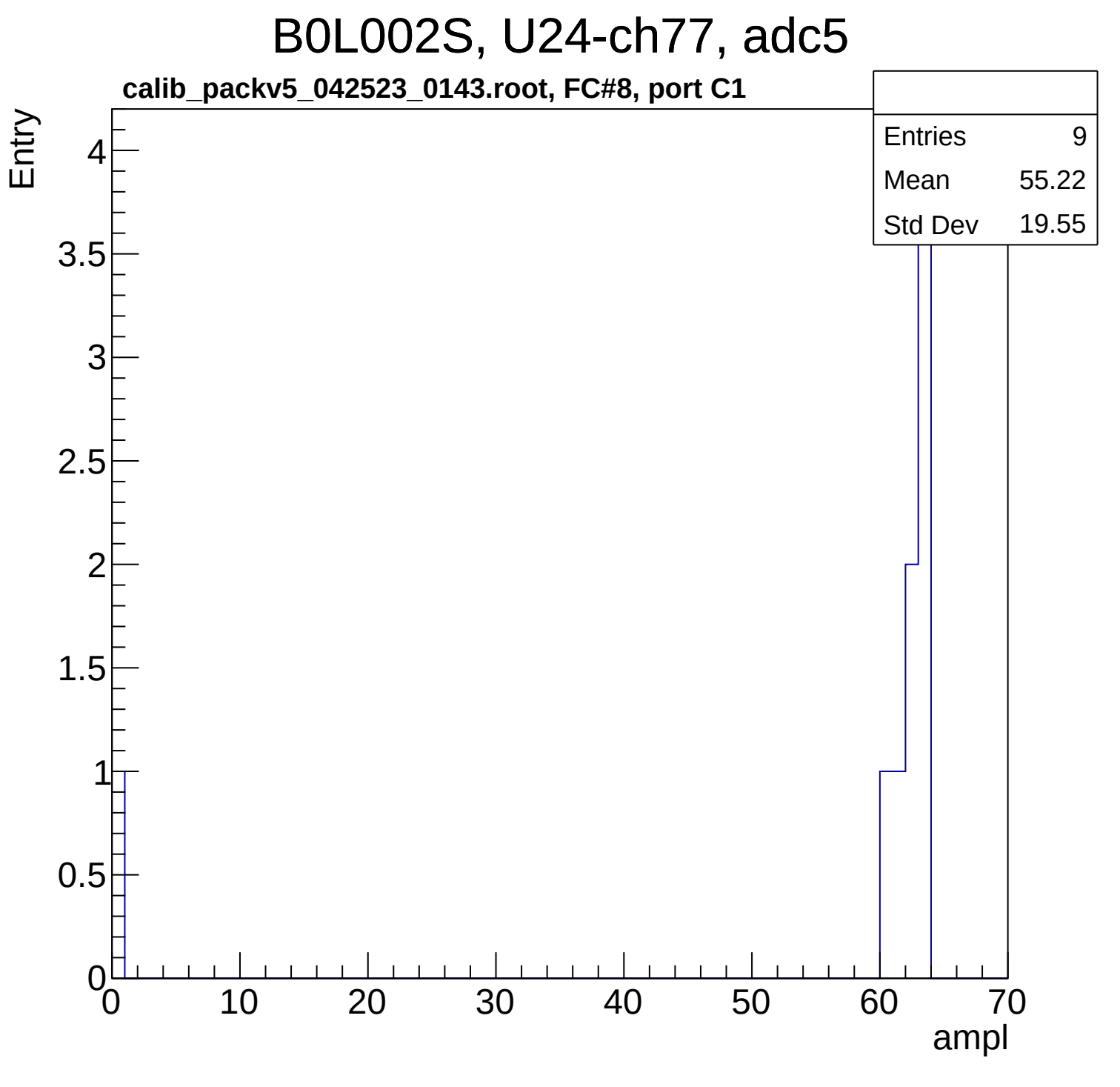
B0L002S, U24-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
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0
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20
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60
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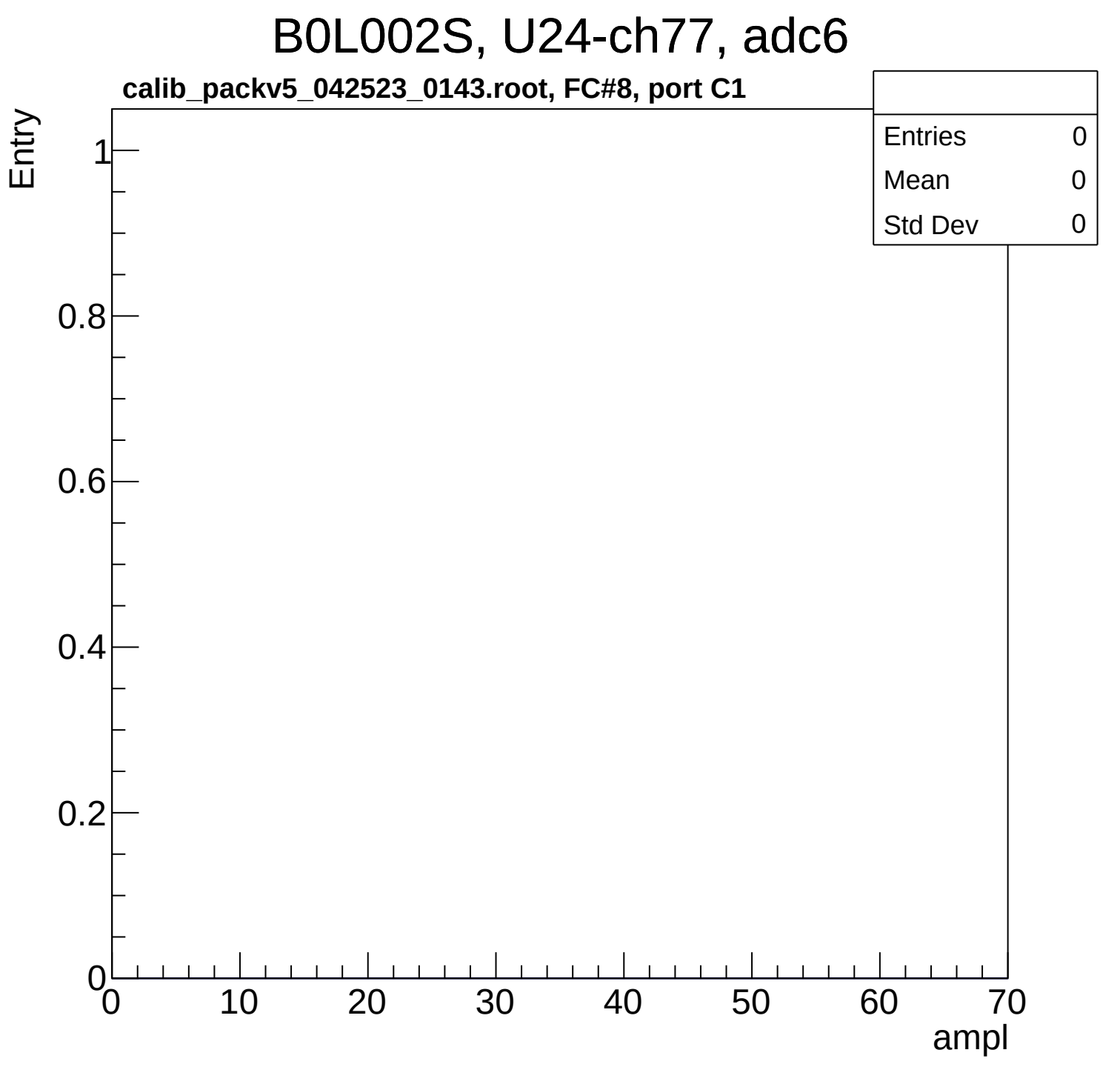
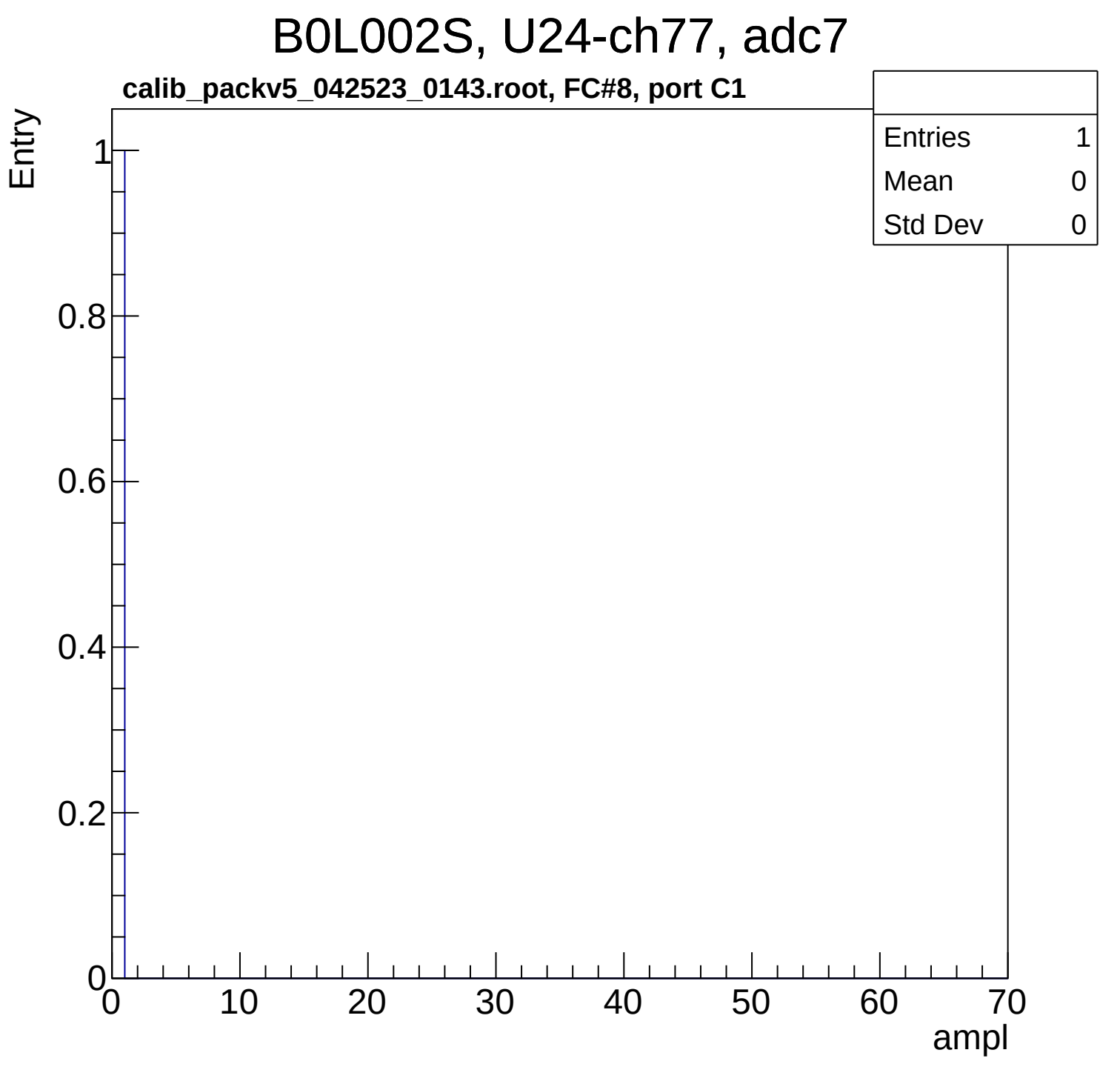
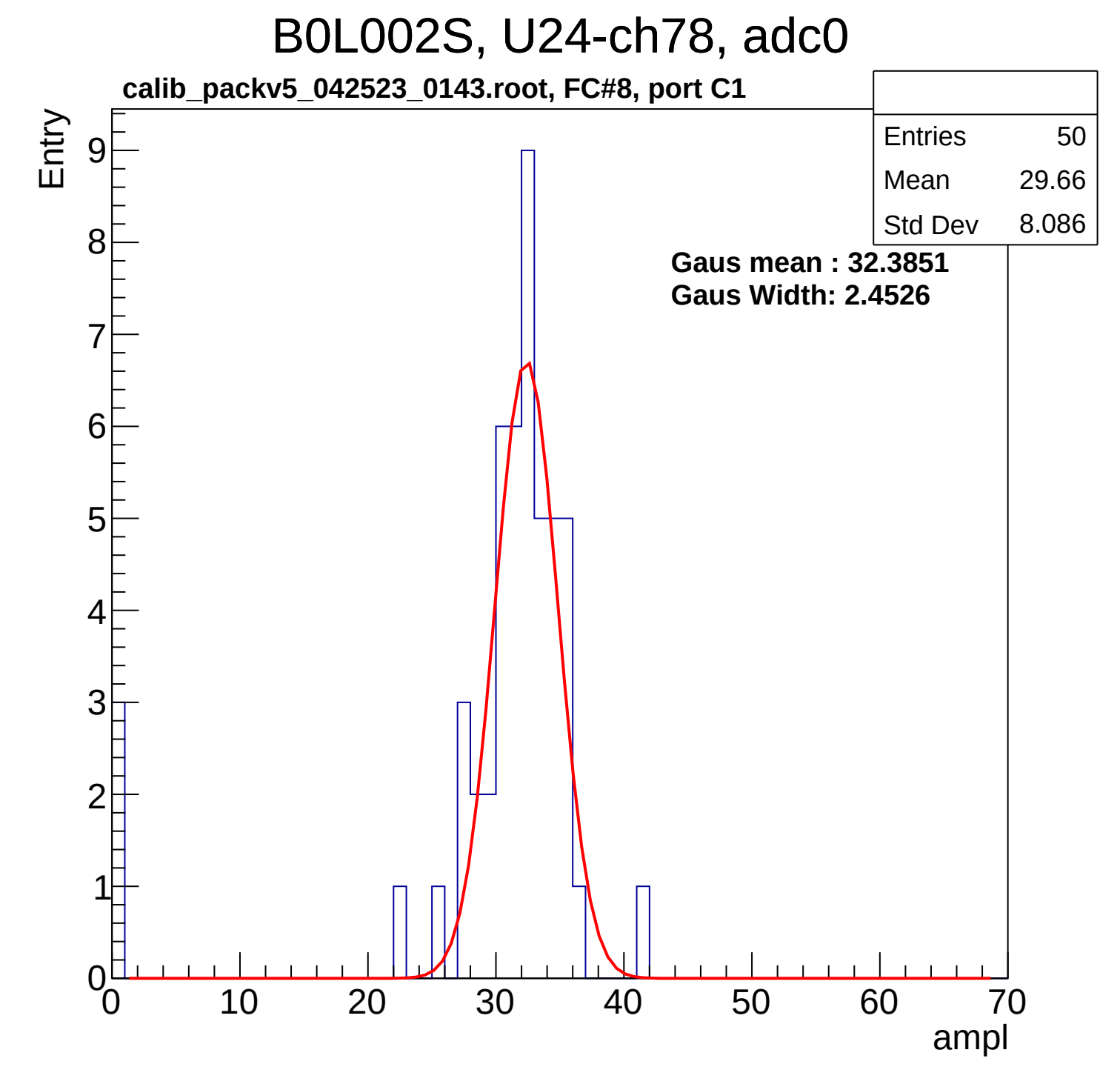

B0L002S, U24-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 29.66
Std Dev 8.086
Gaus mean : 32.3851
Gaus Width: 2.4526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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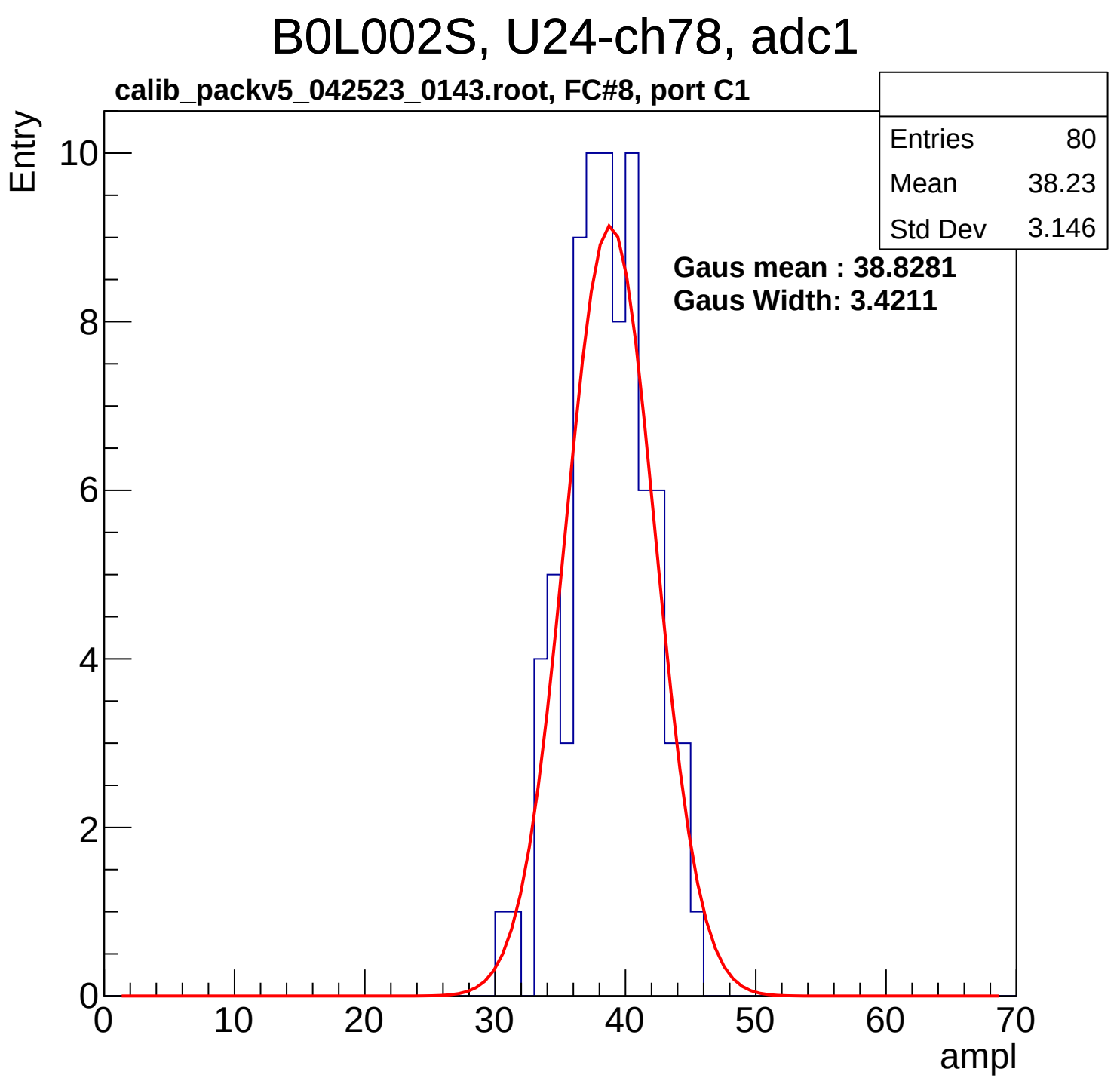
B0L002S, U24-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.23
Std Dev 3.146
Gaus mean : 38.8281
Gaus Width: 3.4211
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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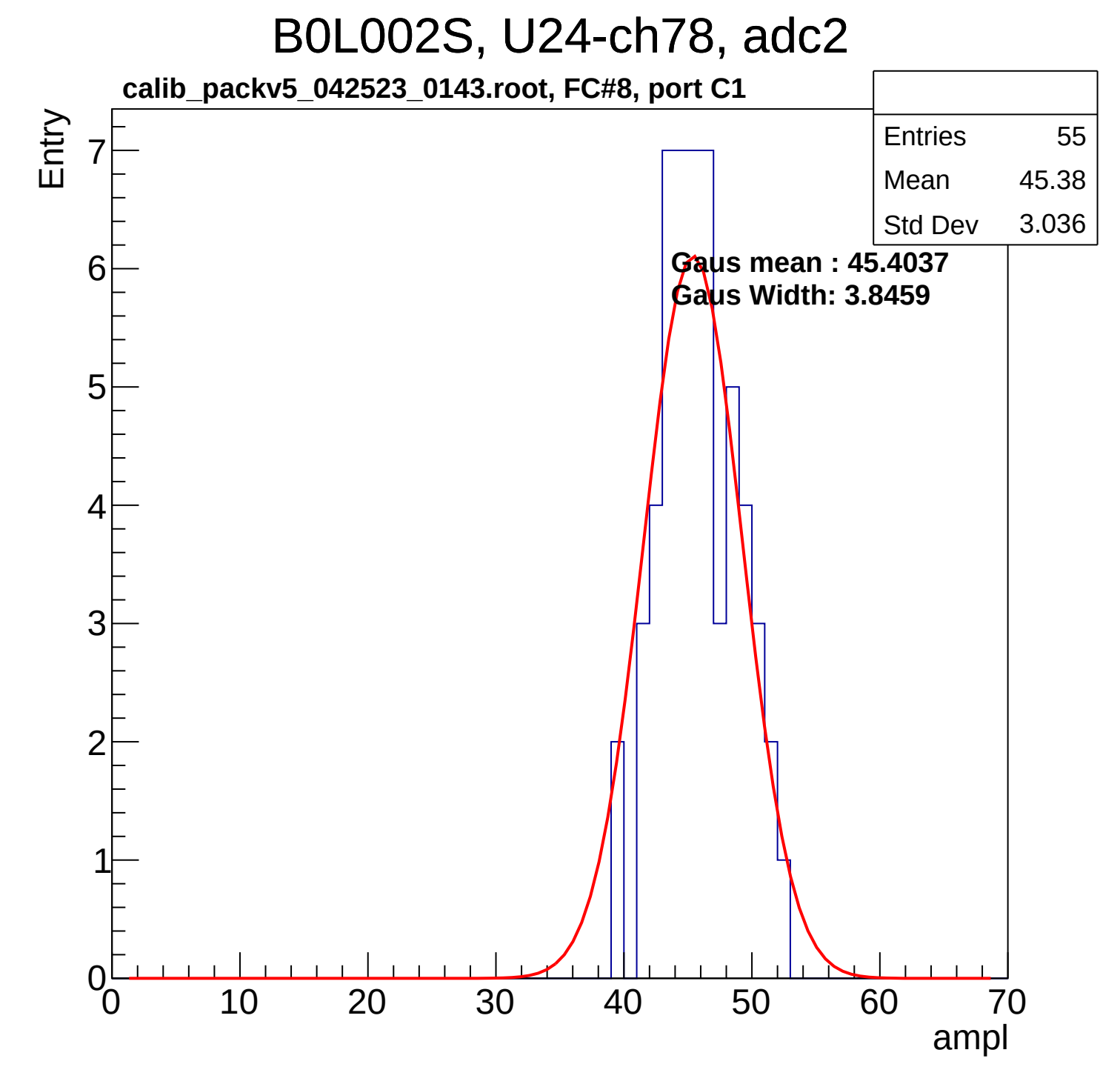
B0L002S, U24-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.38
Std Dev 3.036
Gaus mean : 45.4037
Gaus Width: 3.8459
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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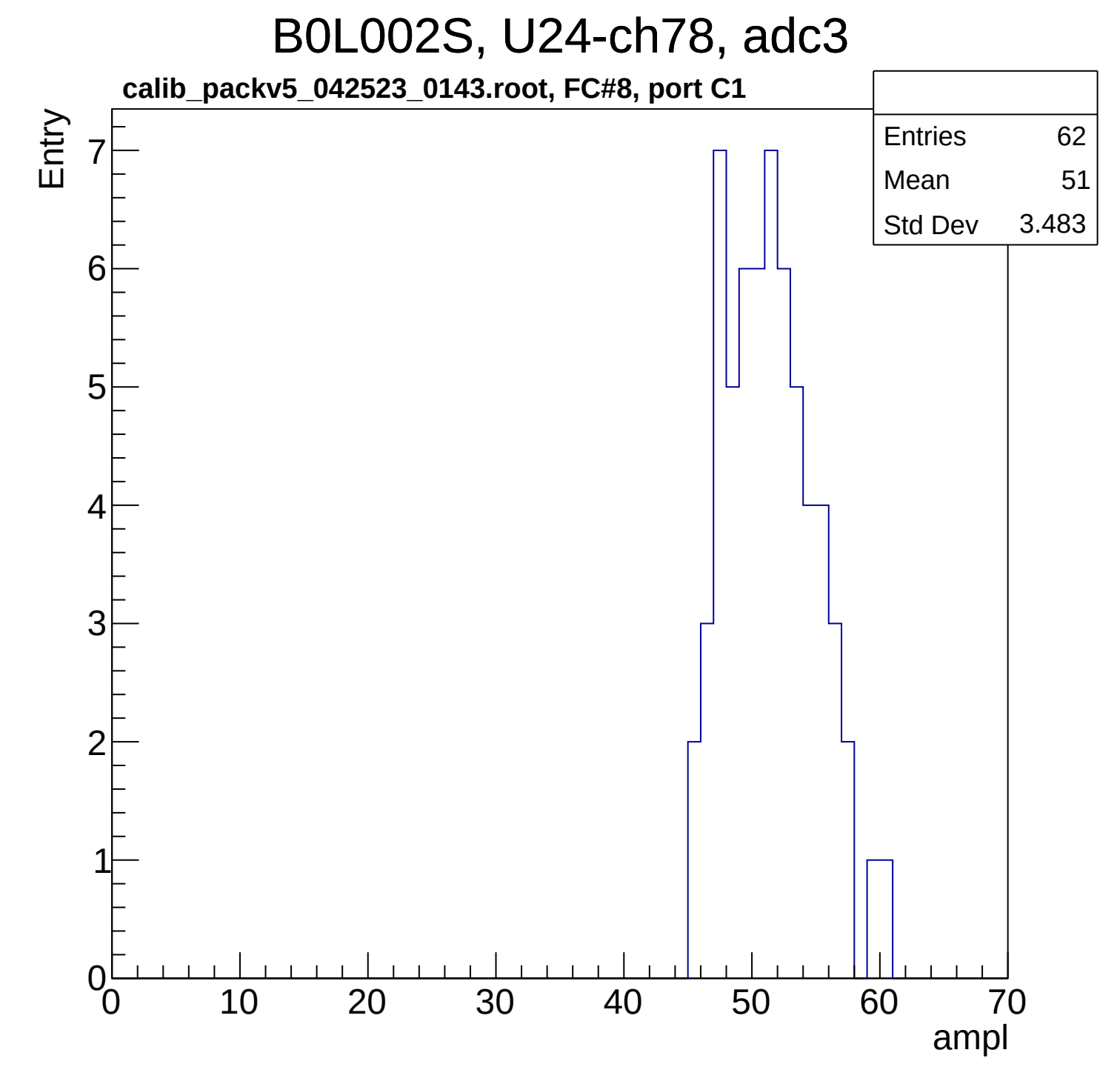
B0L002S, U24-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51
Std Dev 3.483
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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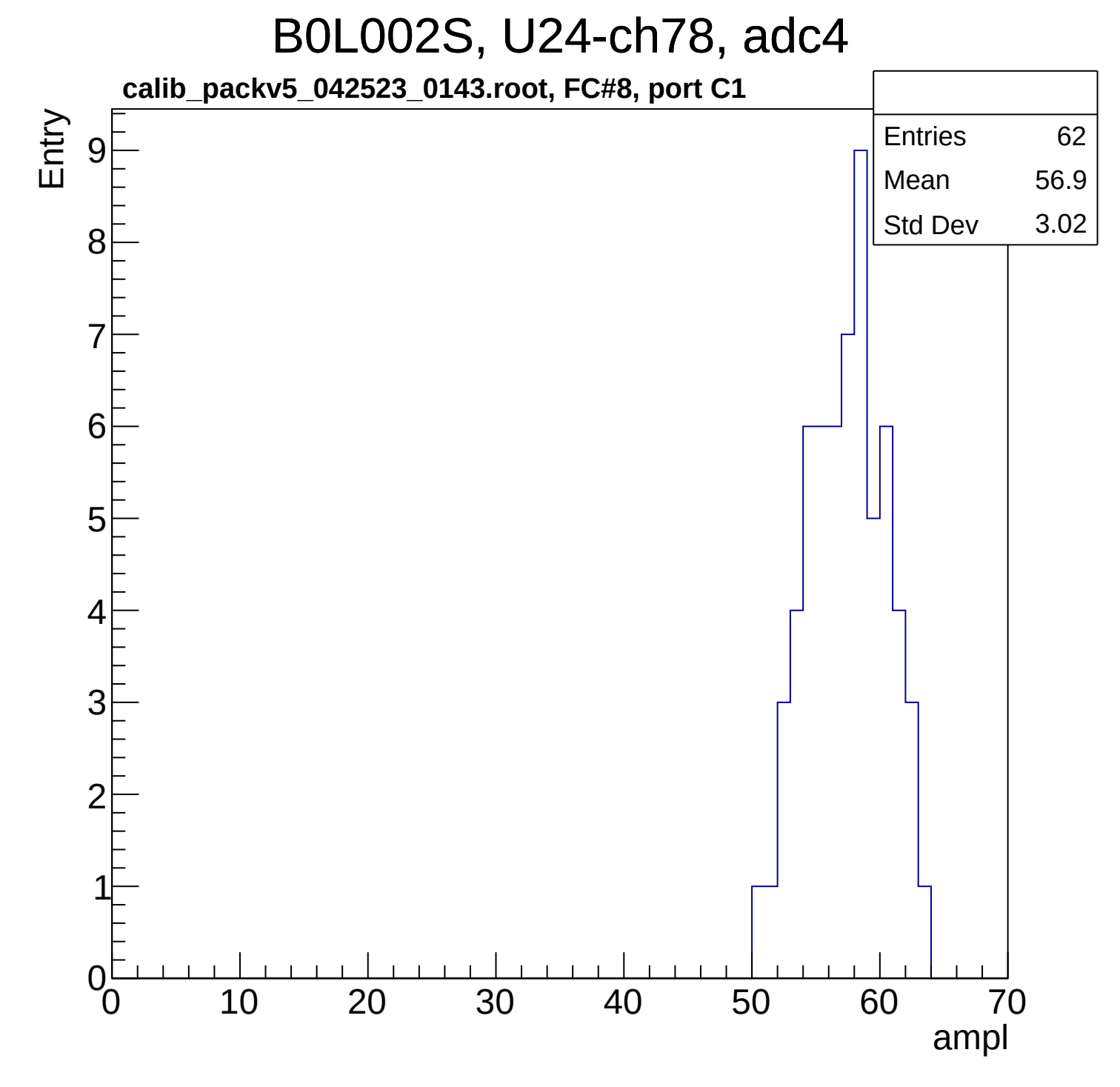
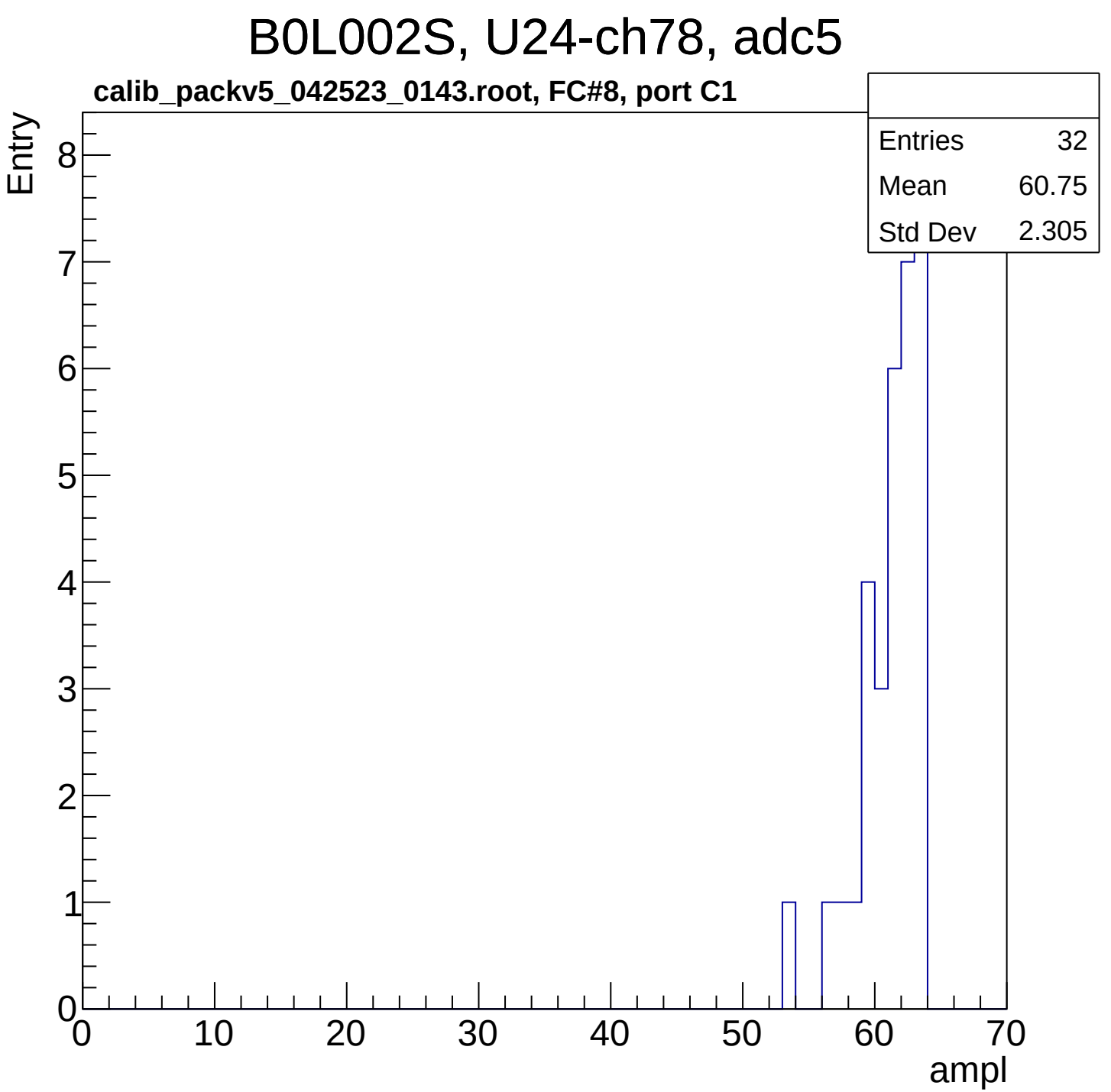

B0L002S, U24-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.9
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 60.75
Std Dev 2.305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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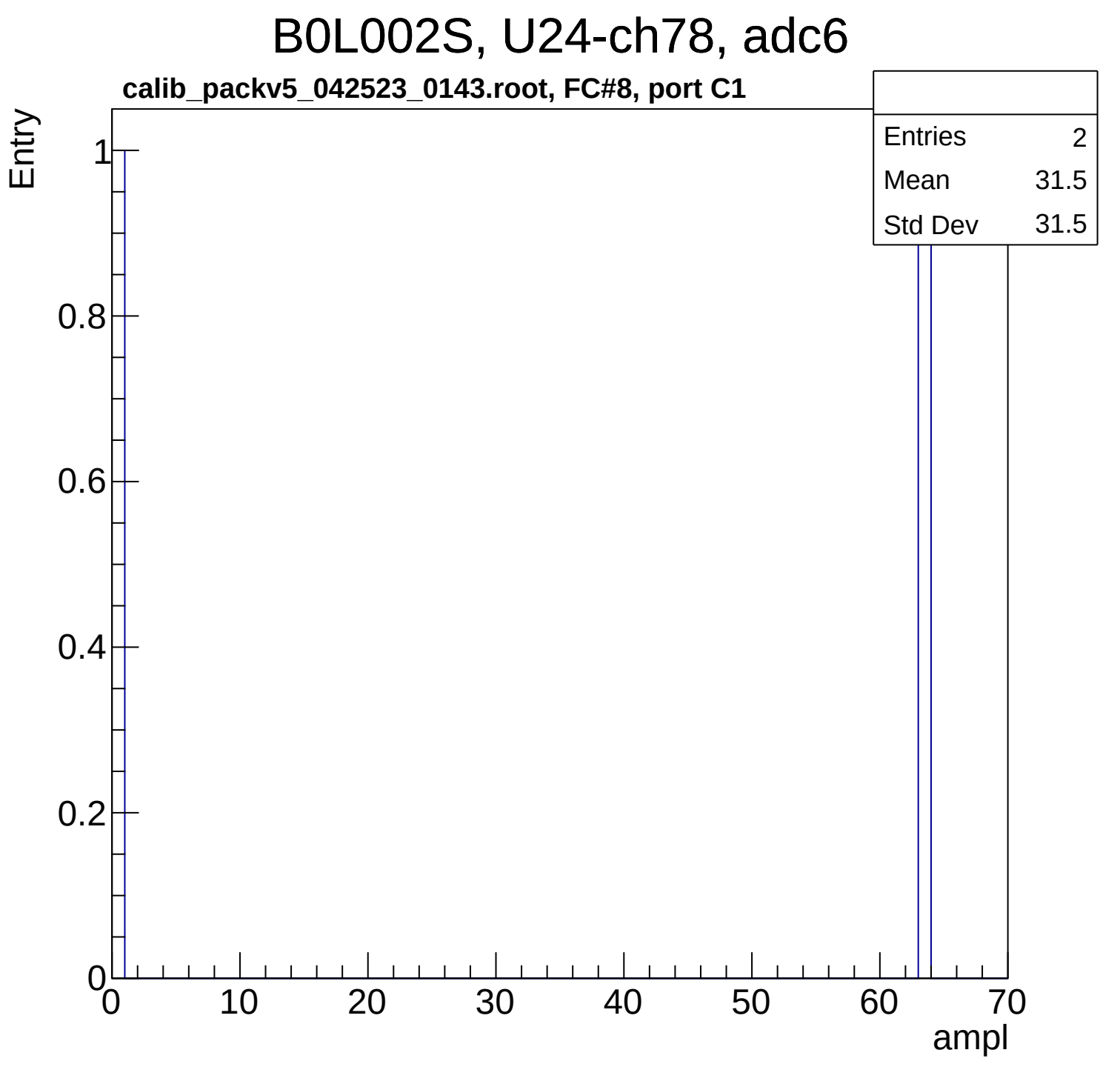
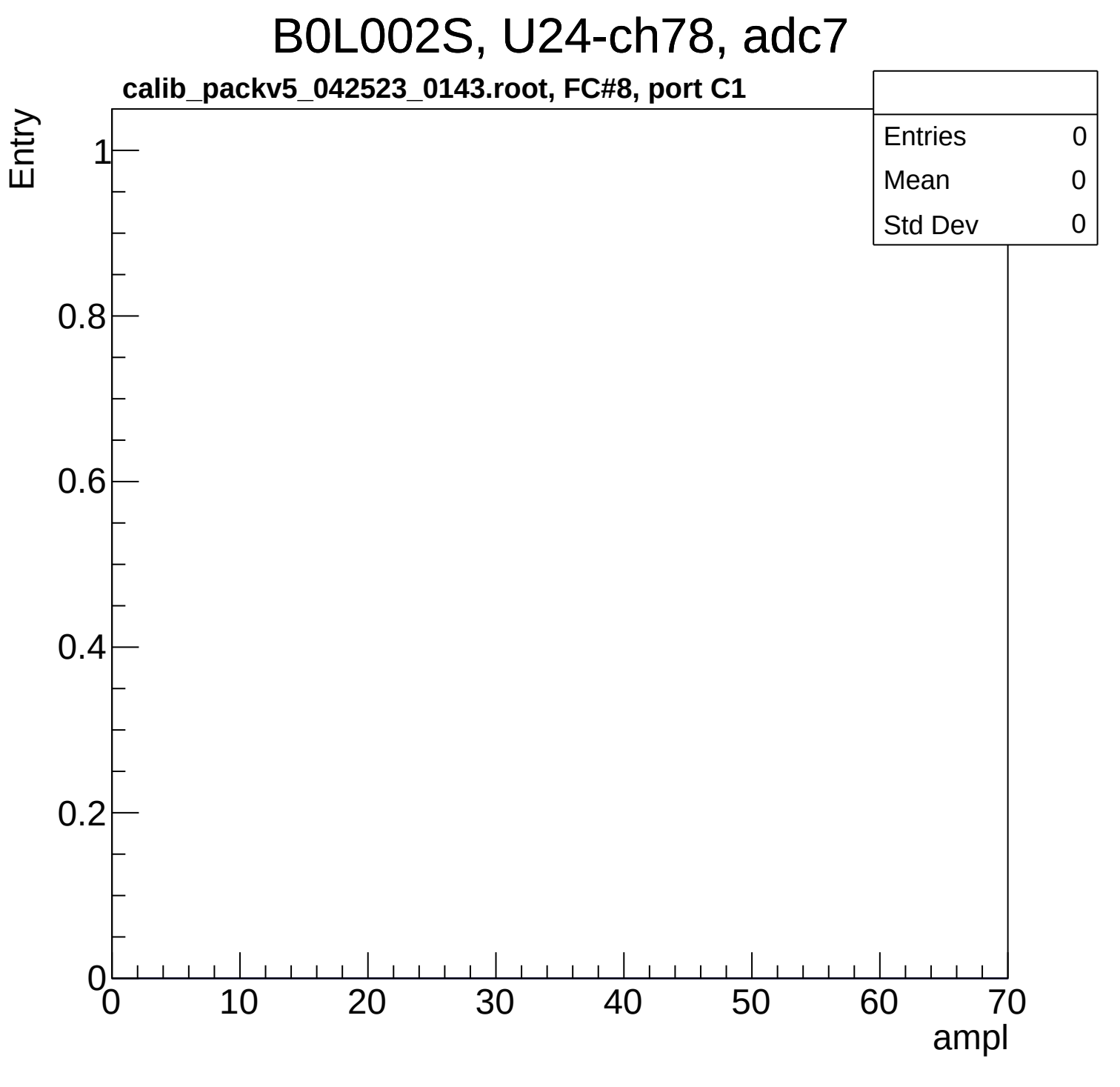
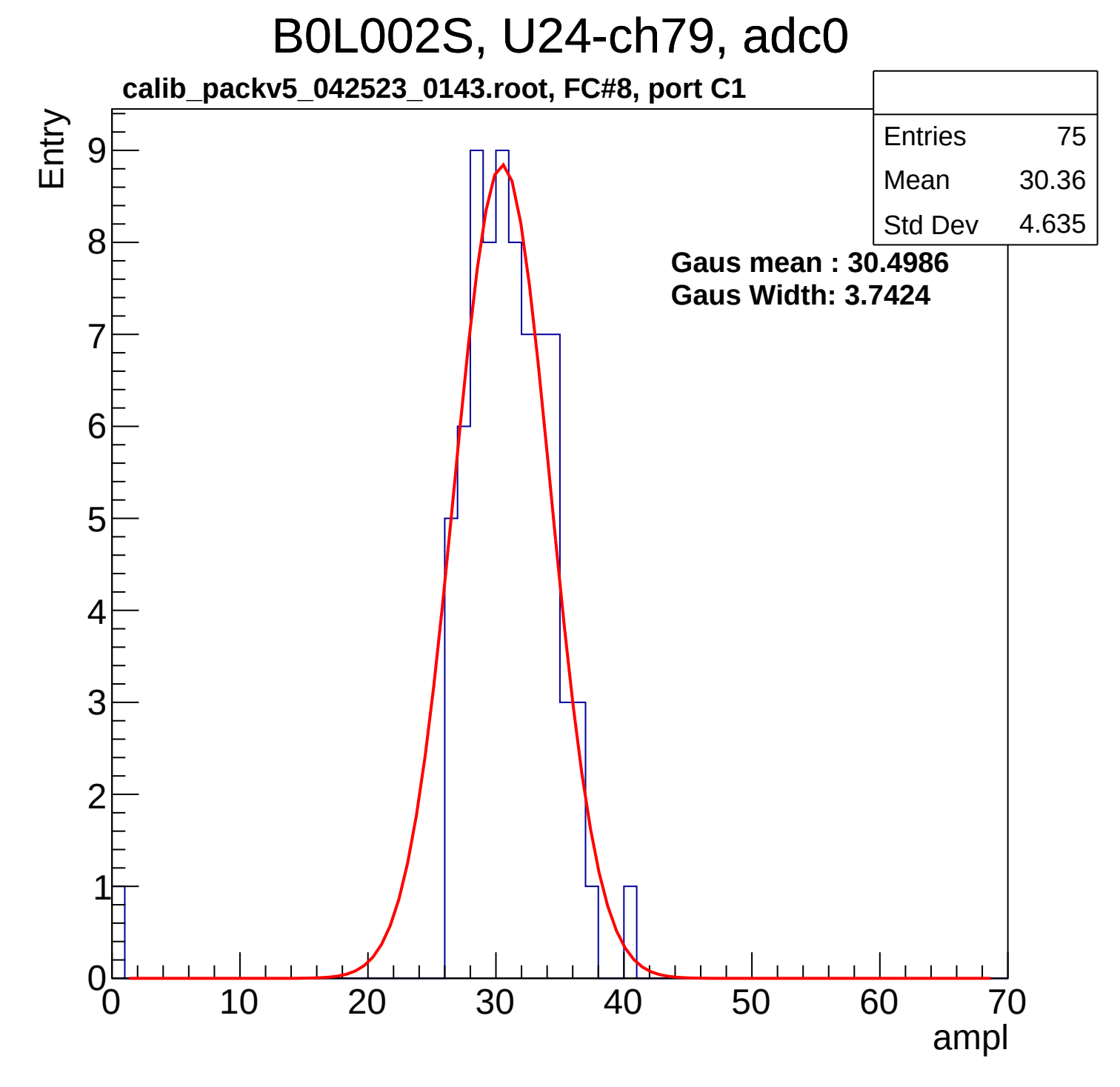
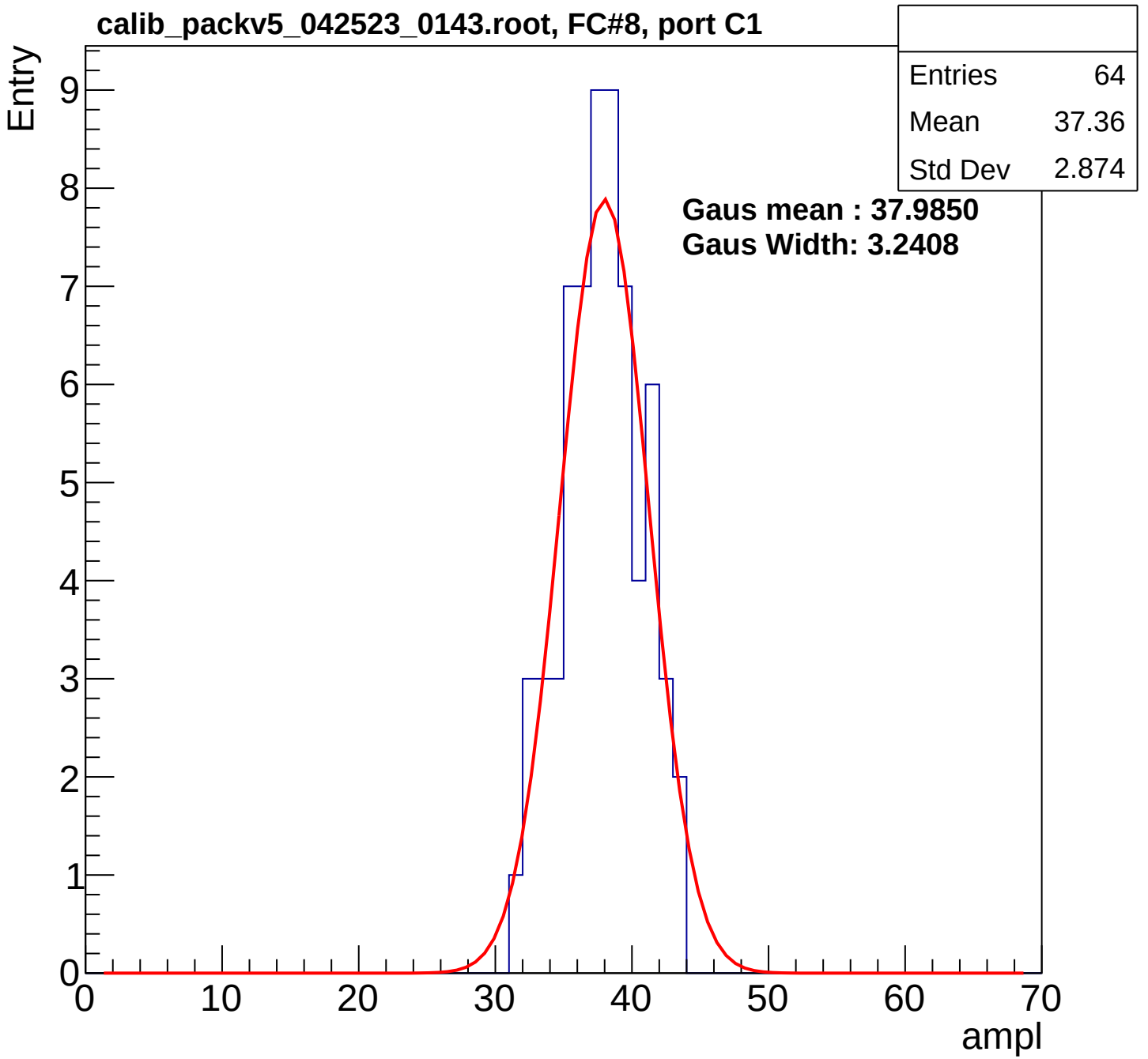

B0L002S, U24-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30.36
Std Dev 4.635
Gaus mean : 30.4986
Gaus Width: 3.7424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.36
Std Dev 2.874
Gaus mean : 37.9850
Gaus Width: 3.2408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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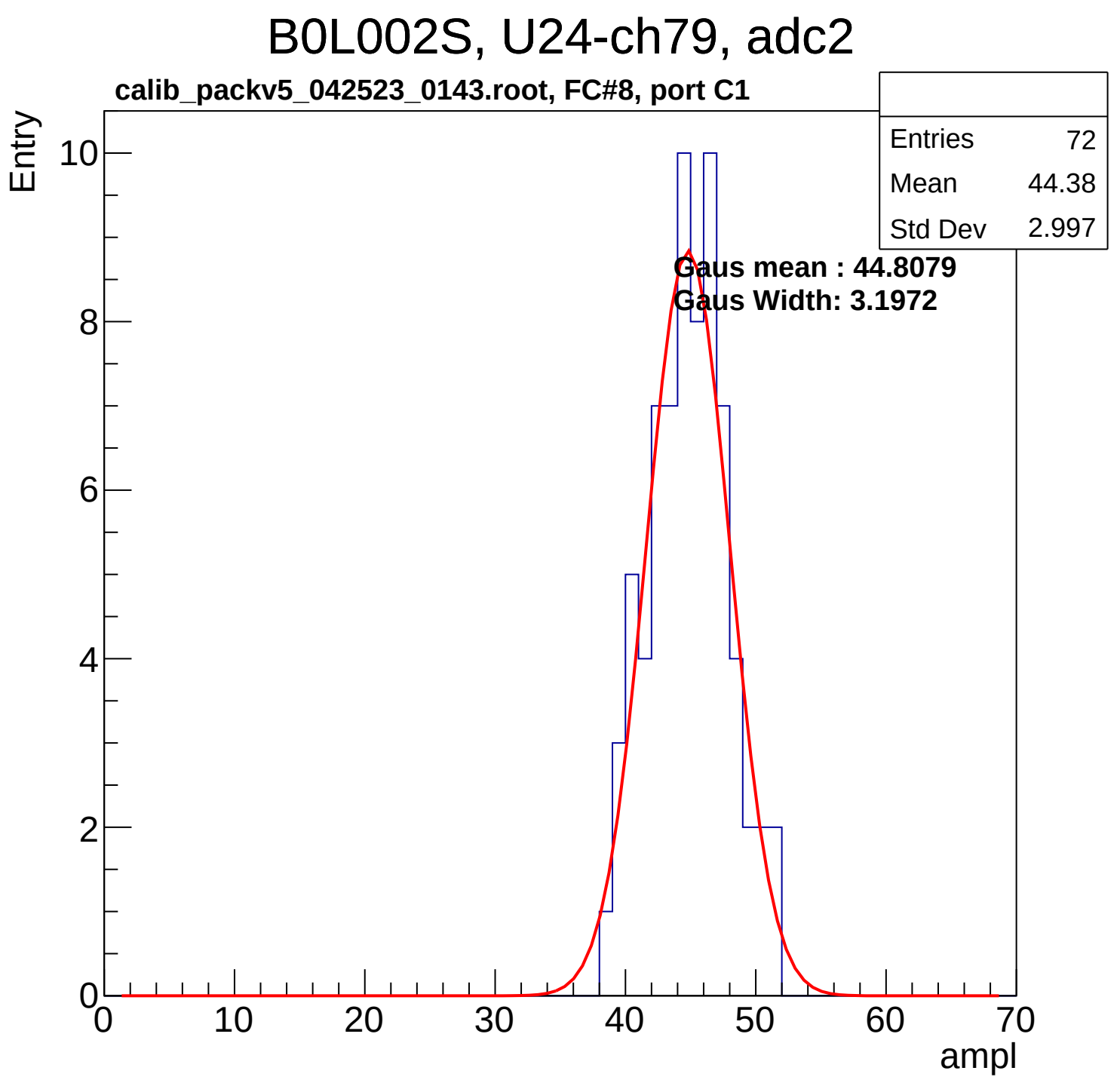
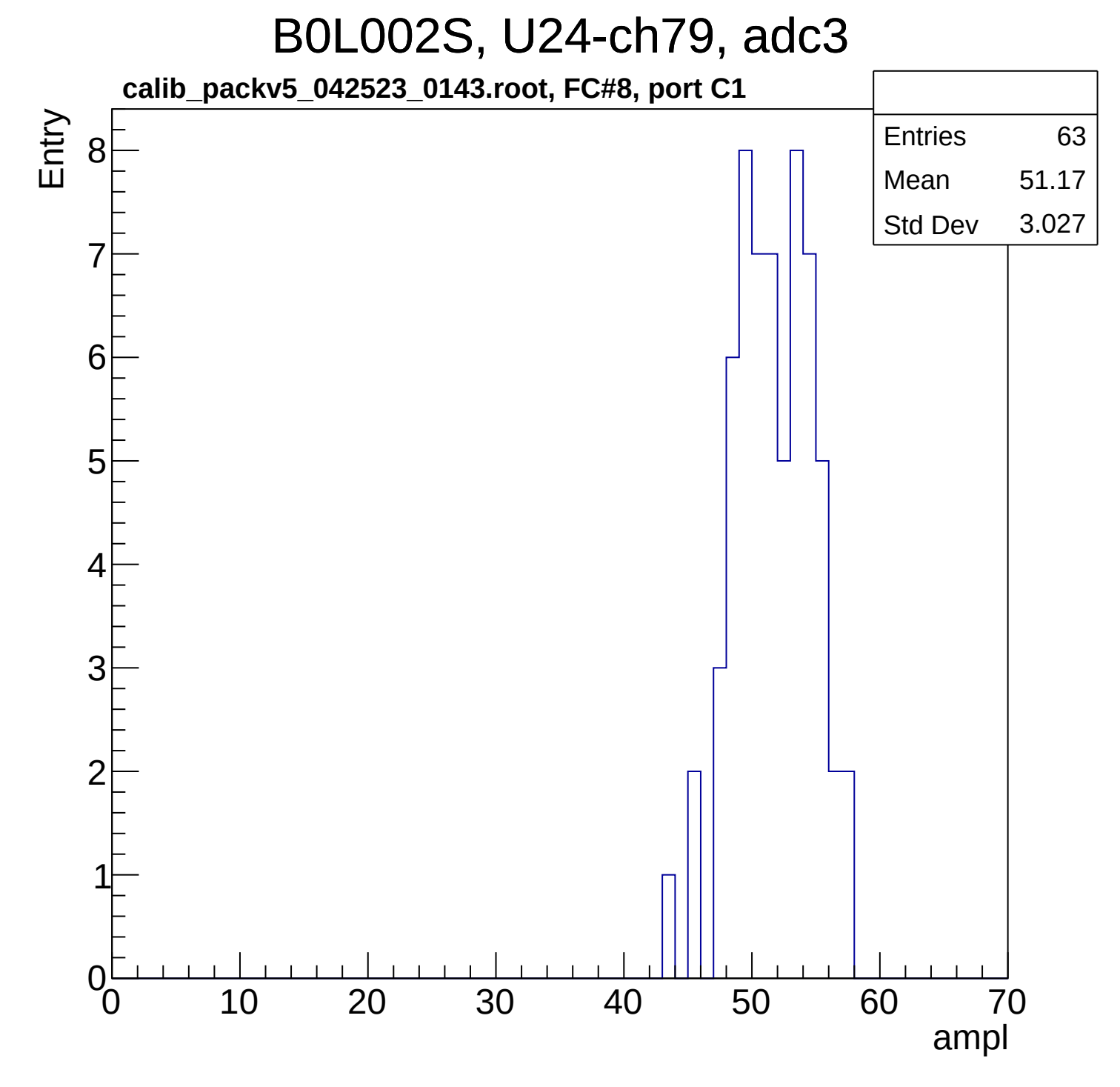

B0L002S, U24-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.38
Std Dev 2.997
Gaus mean : 44.8079
Gaus Width: 3.1972
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.17
Std Dev 3.027
Entry
ampl
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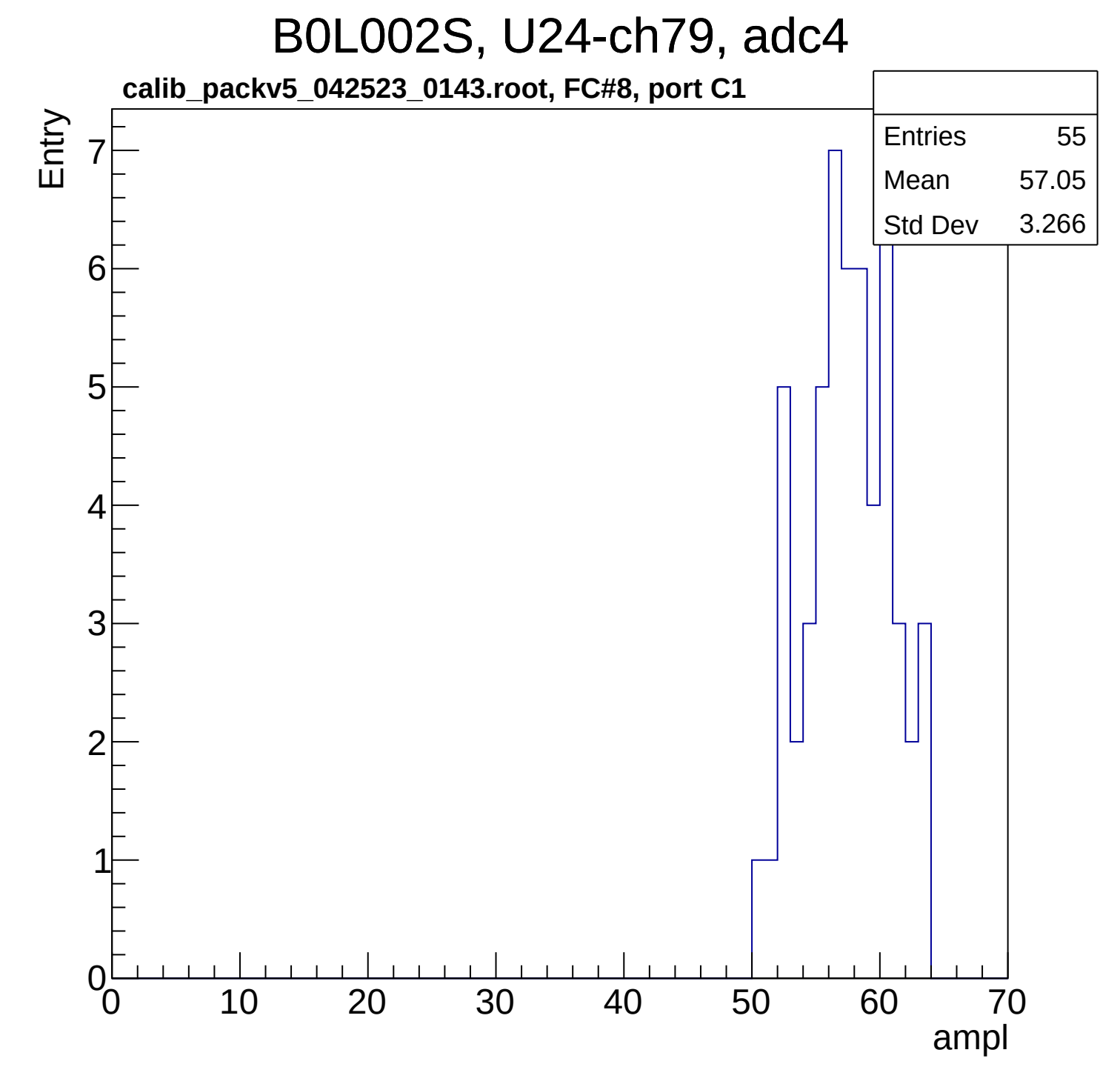
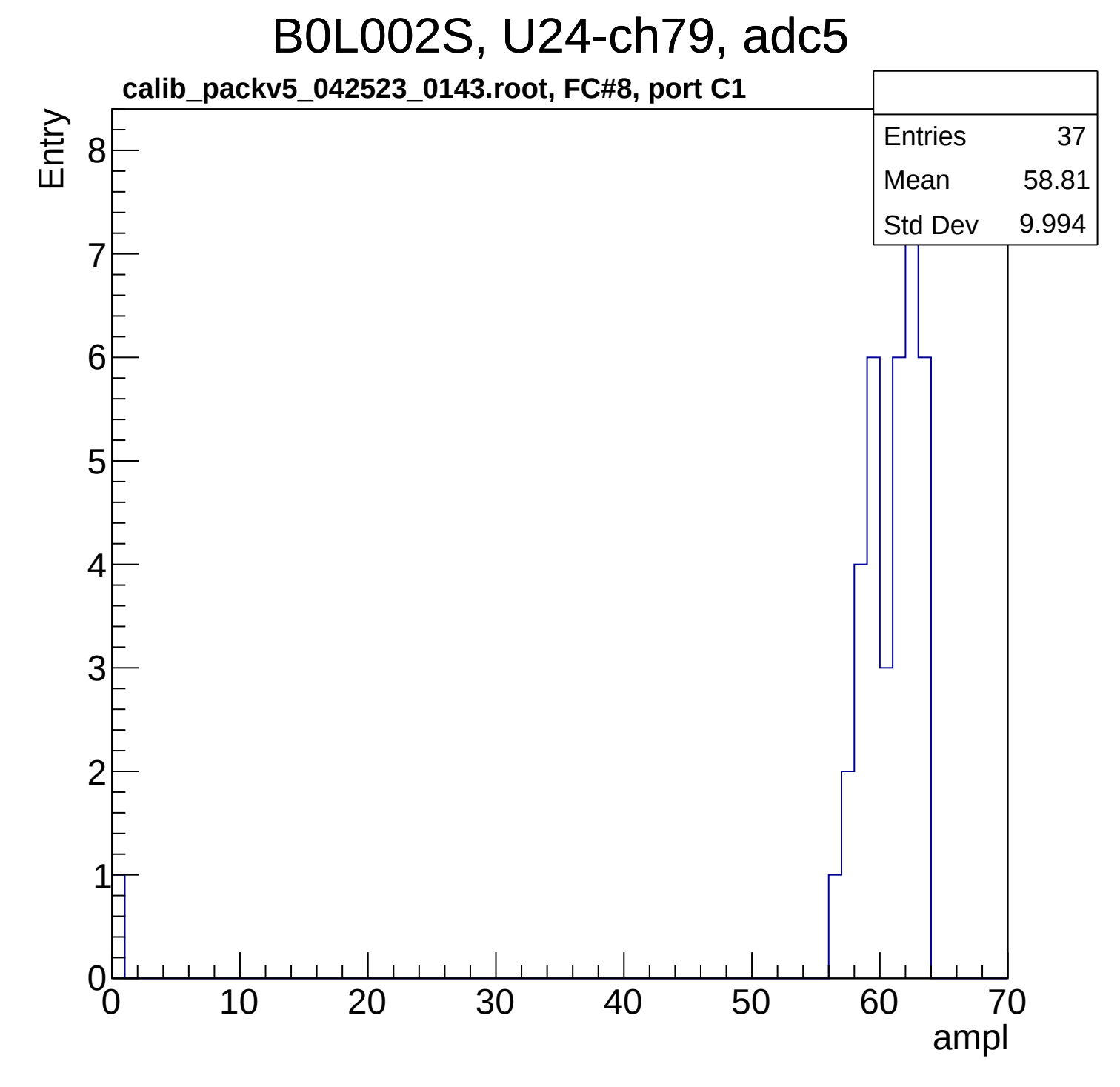
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.05
Std Dev 3.266
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.81
Std Dev 9.994
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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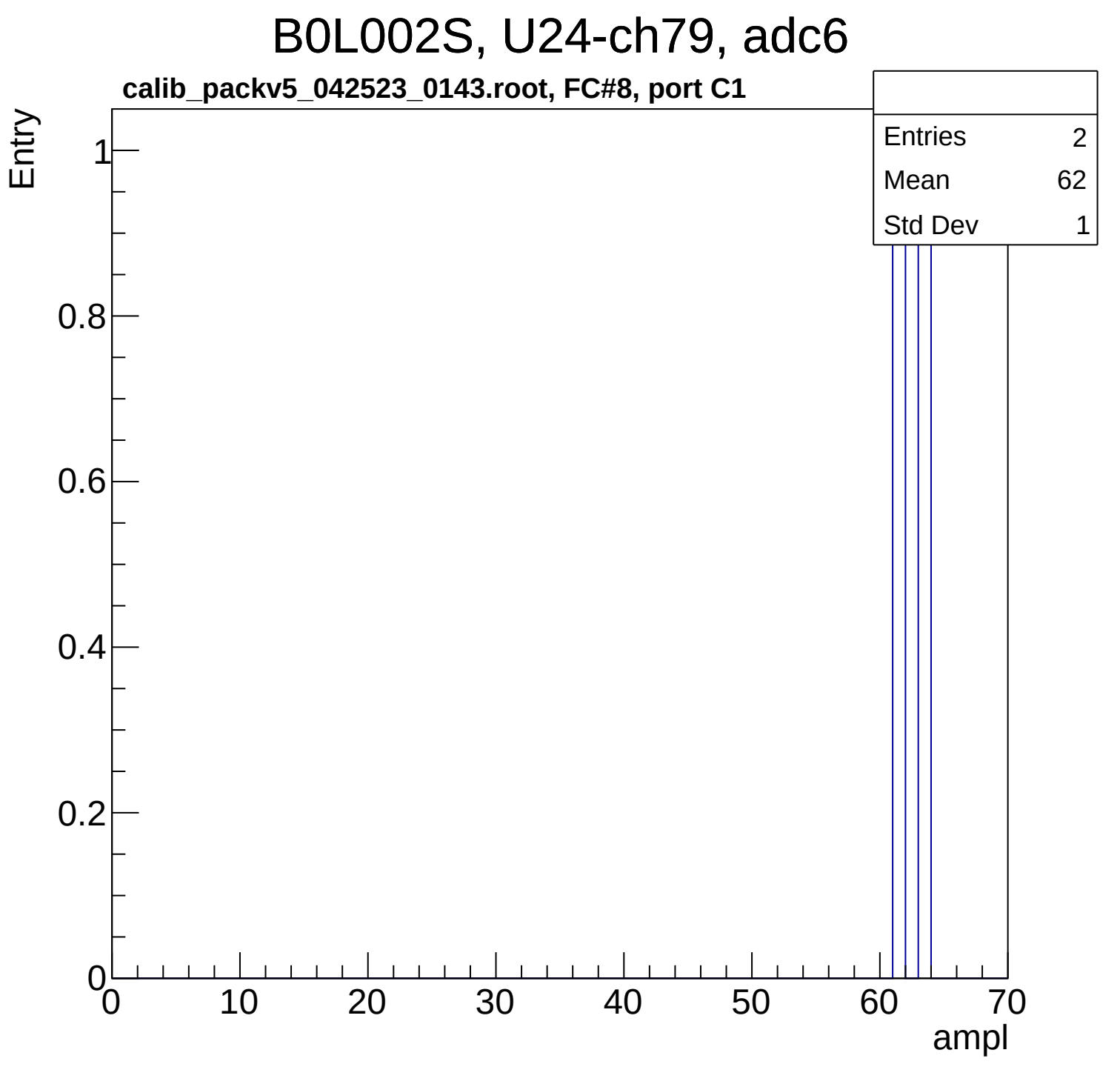
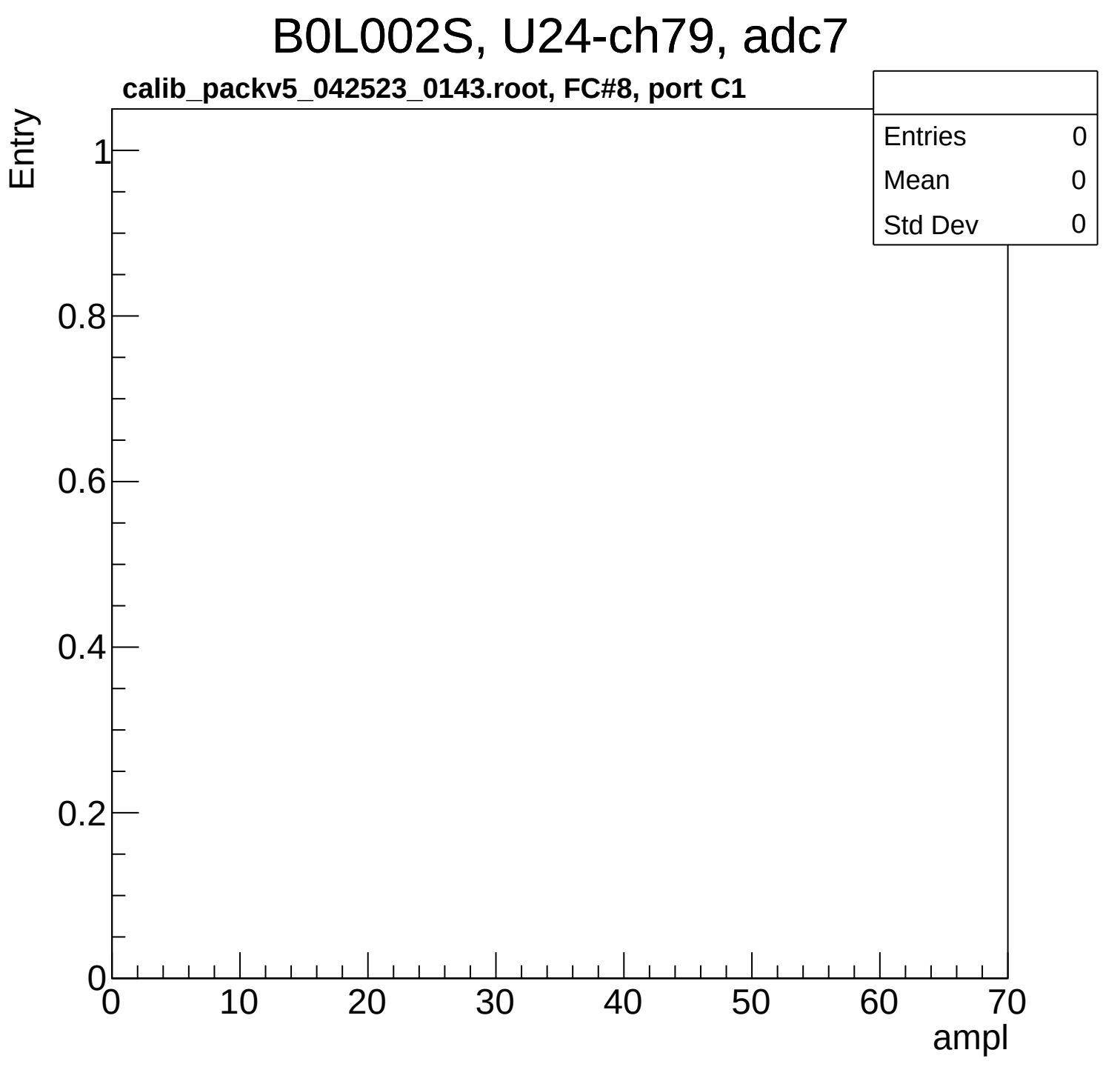

B0L002S, U24-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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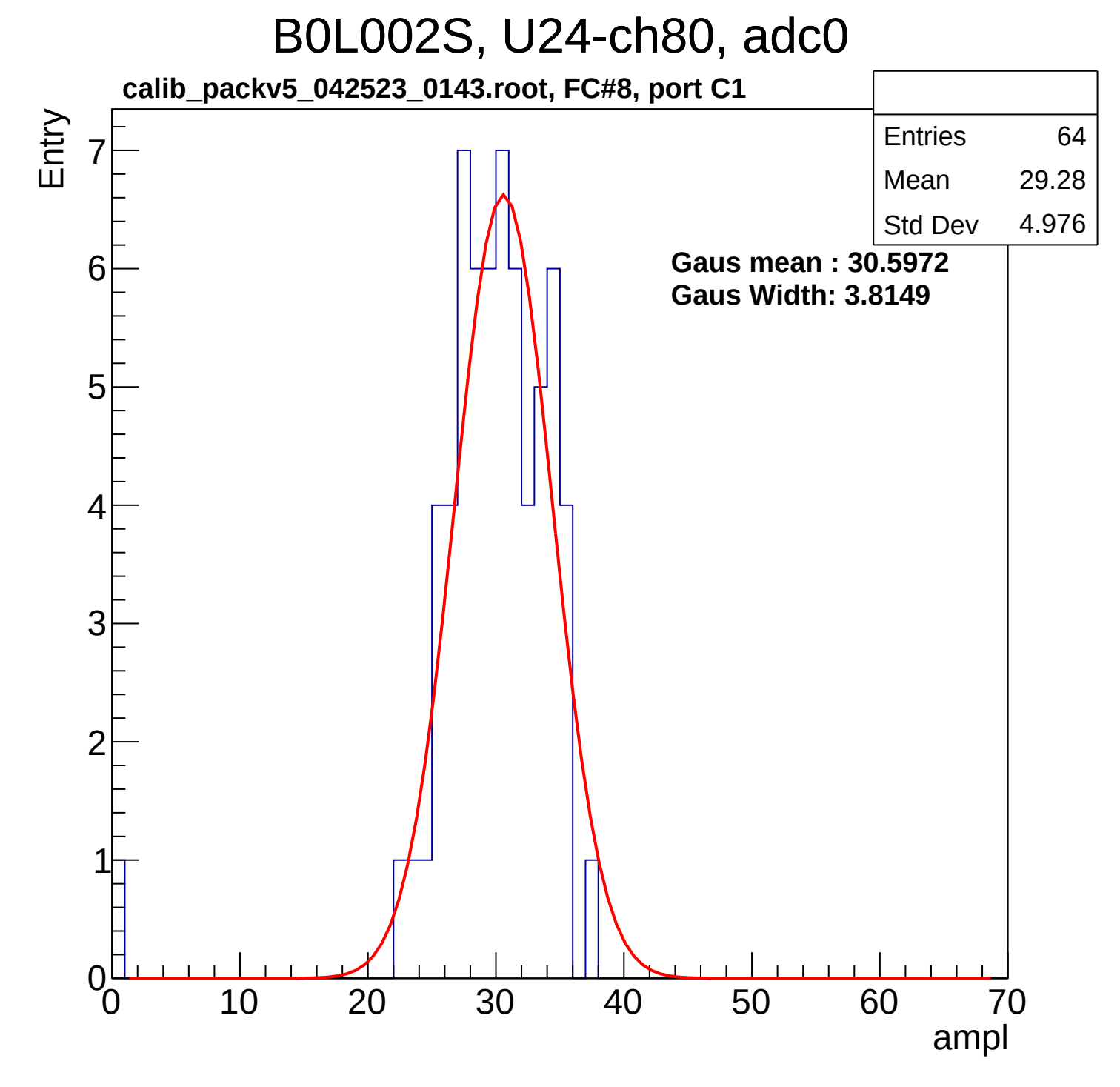
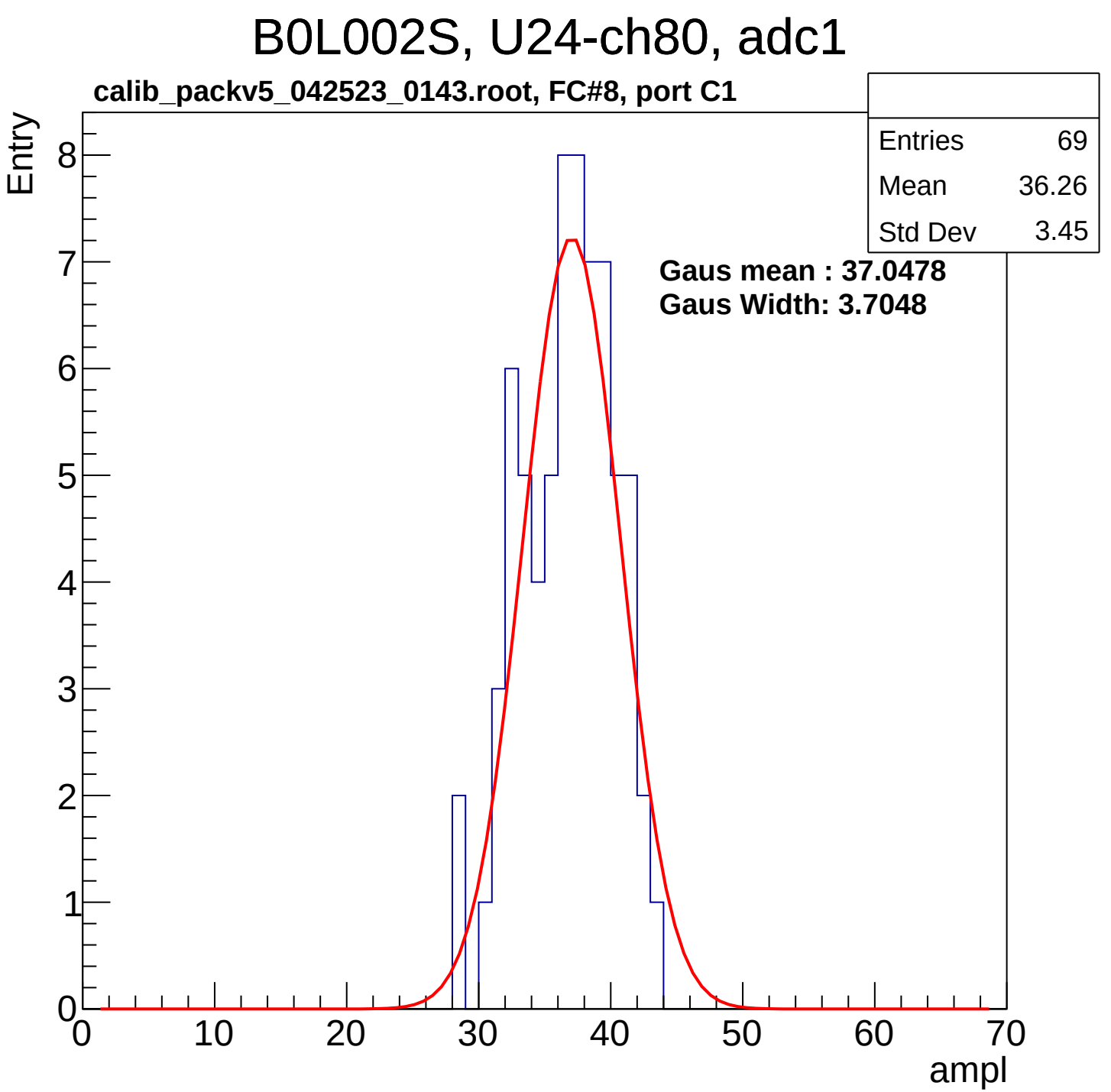
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.28
Std Dev 4.976
Gaus mean : 30.5972
Gaus Width: 3.8149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.26
Std Dev 3.45
Gaus mean : 37.0478
Gaus Width: 3.7048
Entry
ampl
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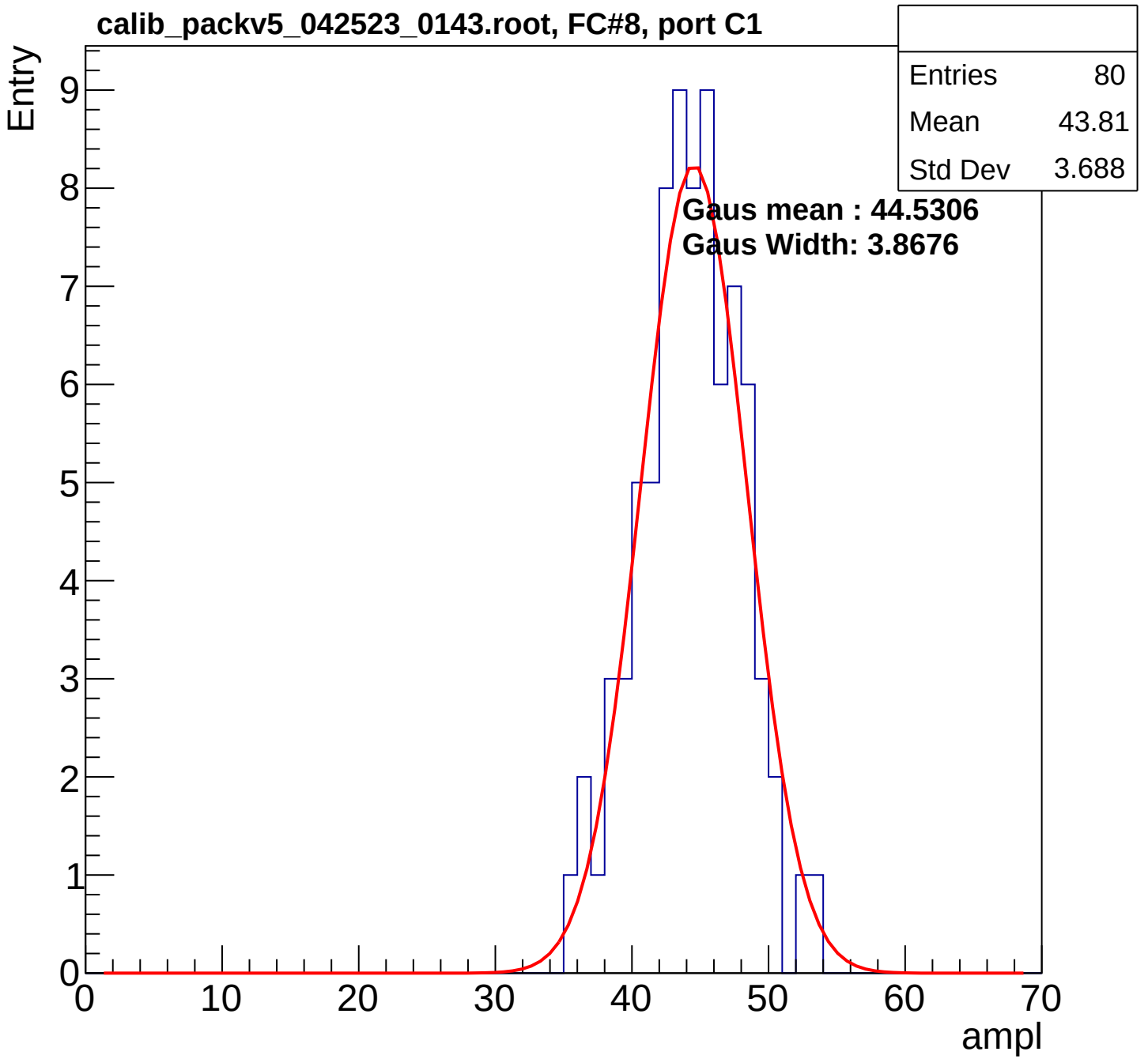
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.81
Std Dev 3.688
Gaus mean : 44.5306
Gaus Width: 3.8676
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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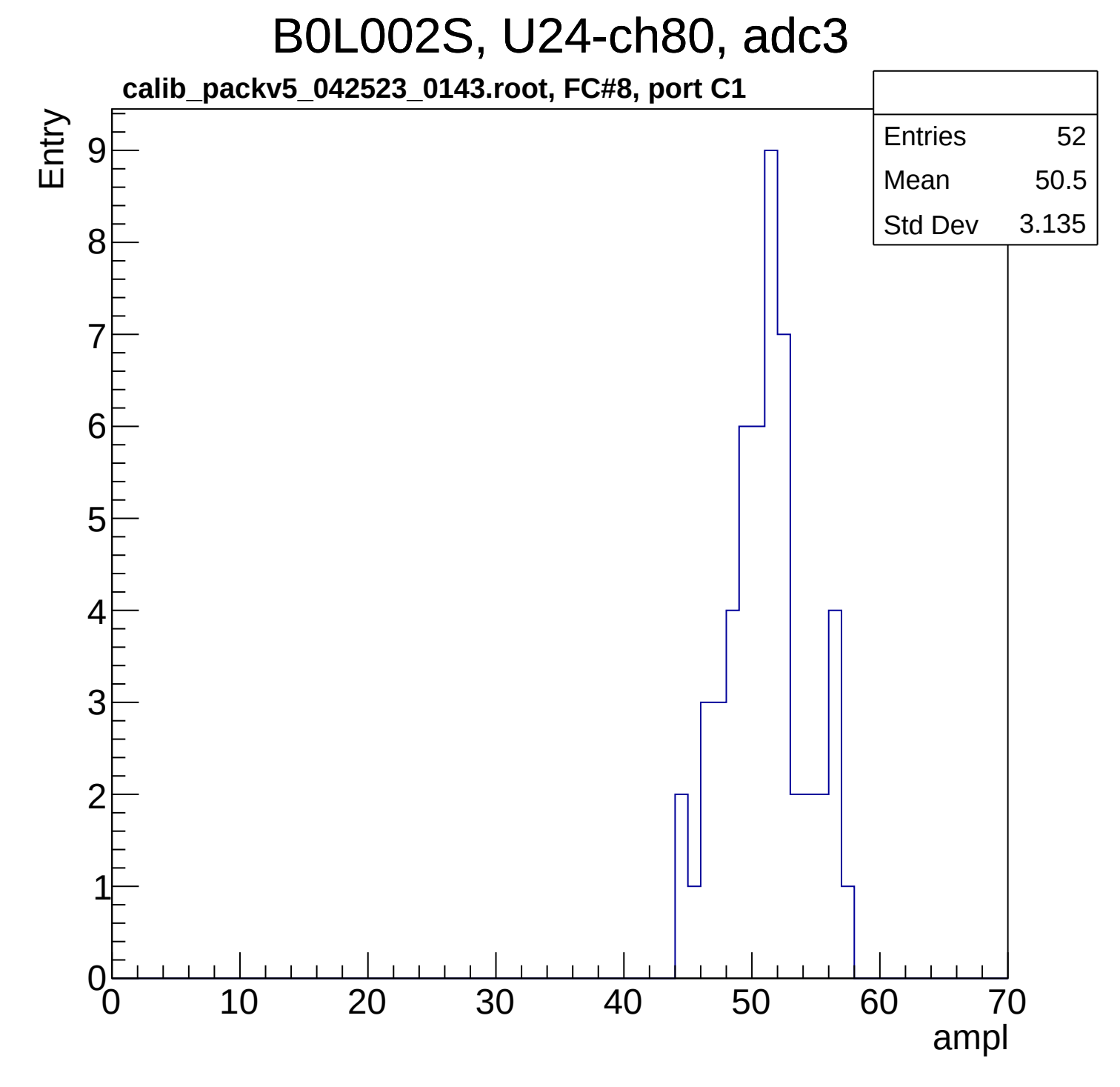
40
50
60
70


B0L002S, U24-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.5
Std Dev 3.135
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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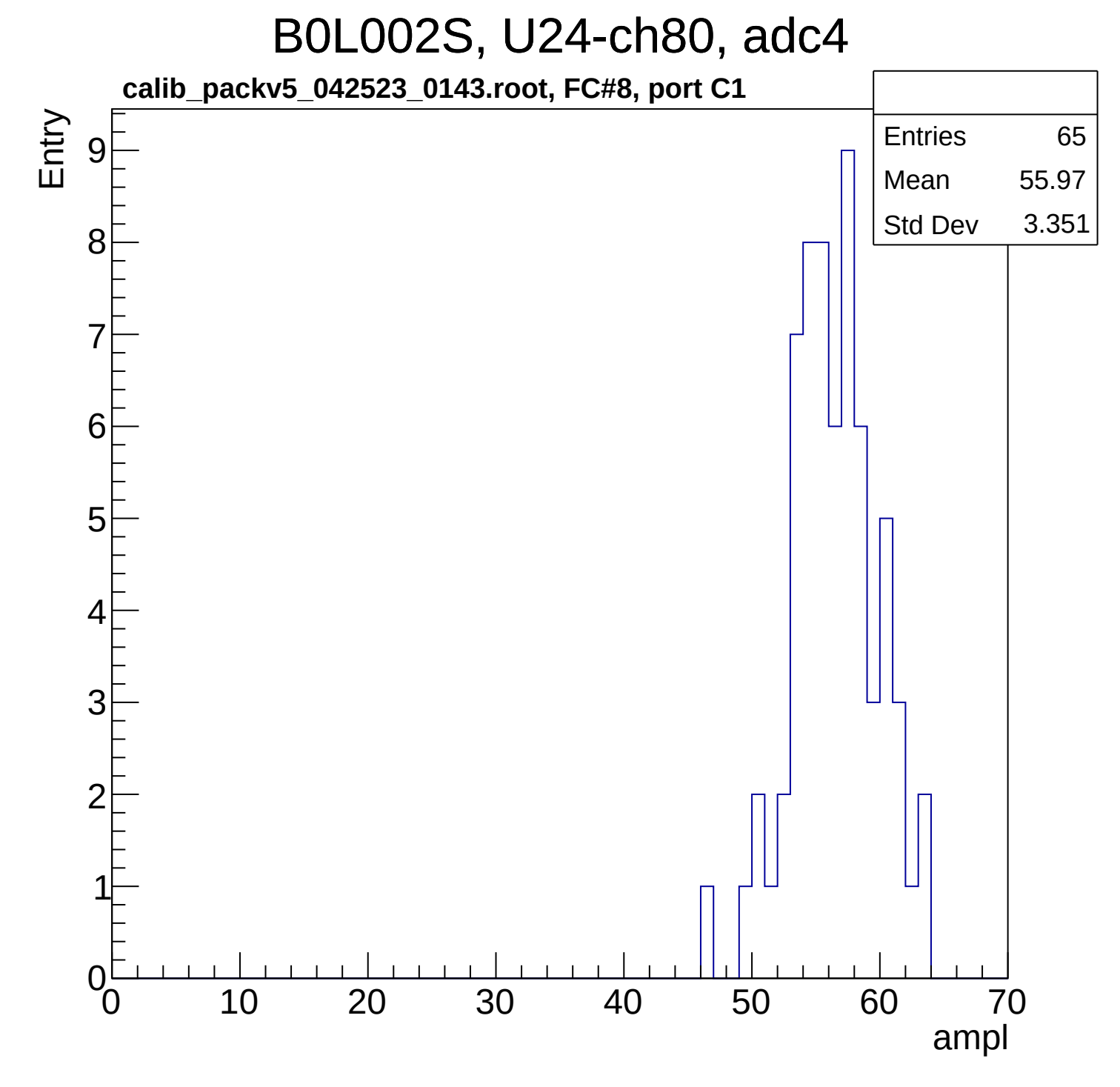
40
50
60
70


B0L002S, U24-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 55.97
Std Dev 3.351
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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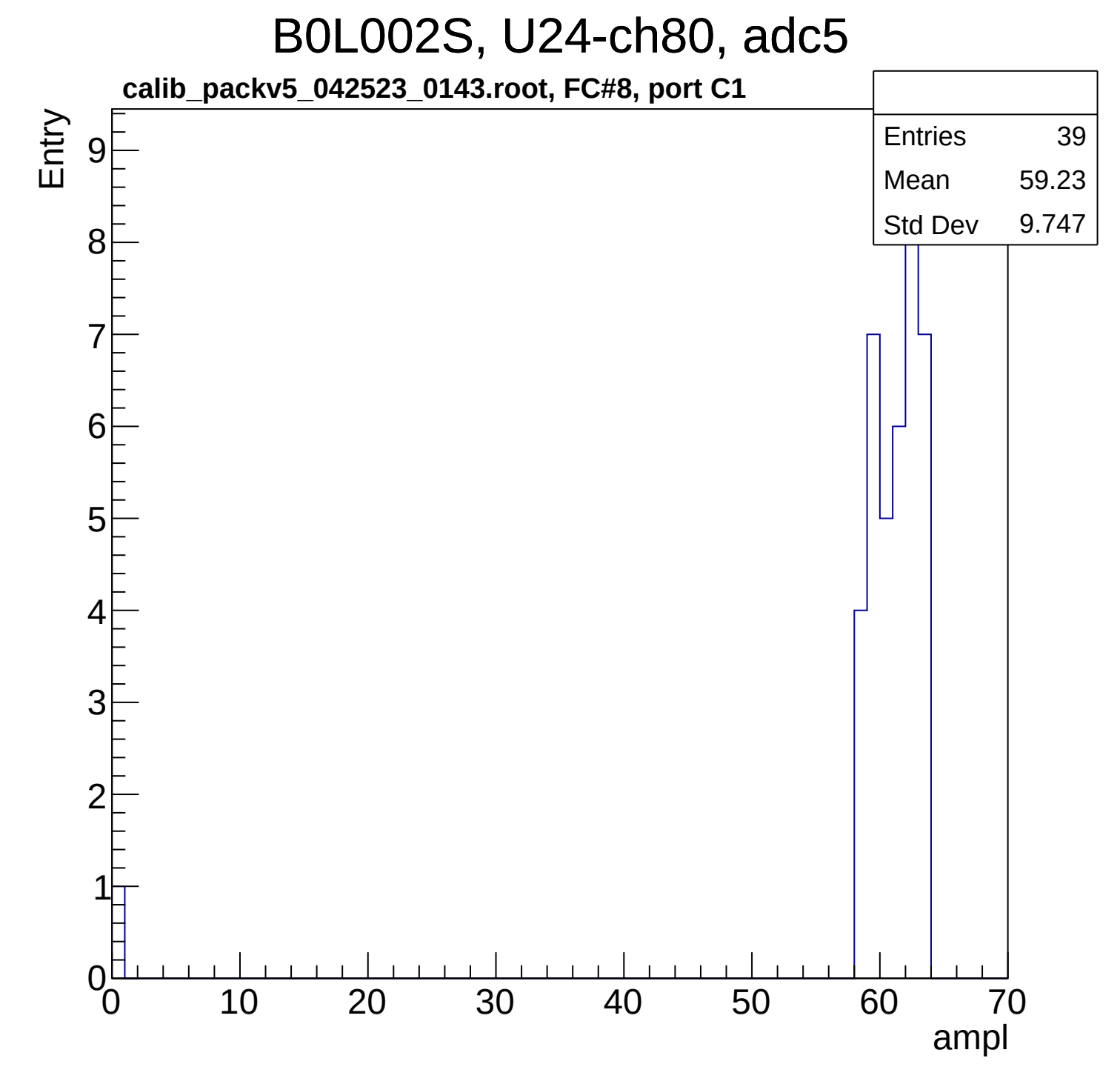
B0L002S, U24-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.23
Std Dev 9.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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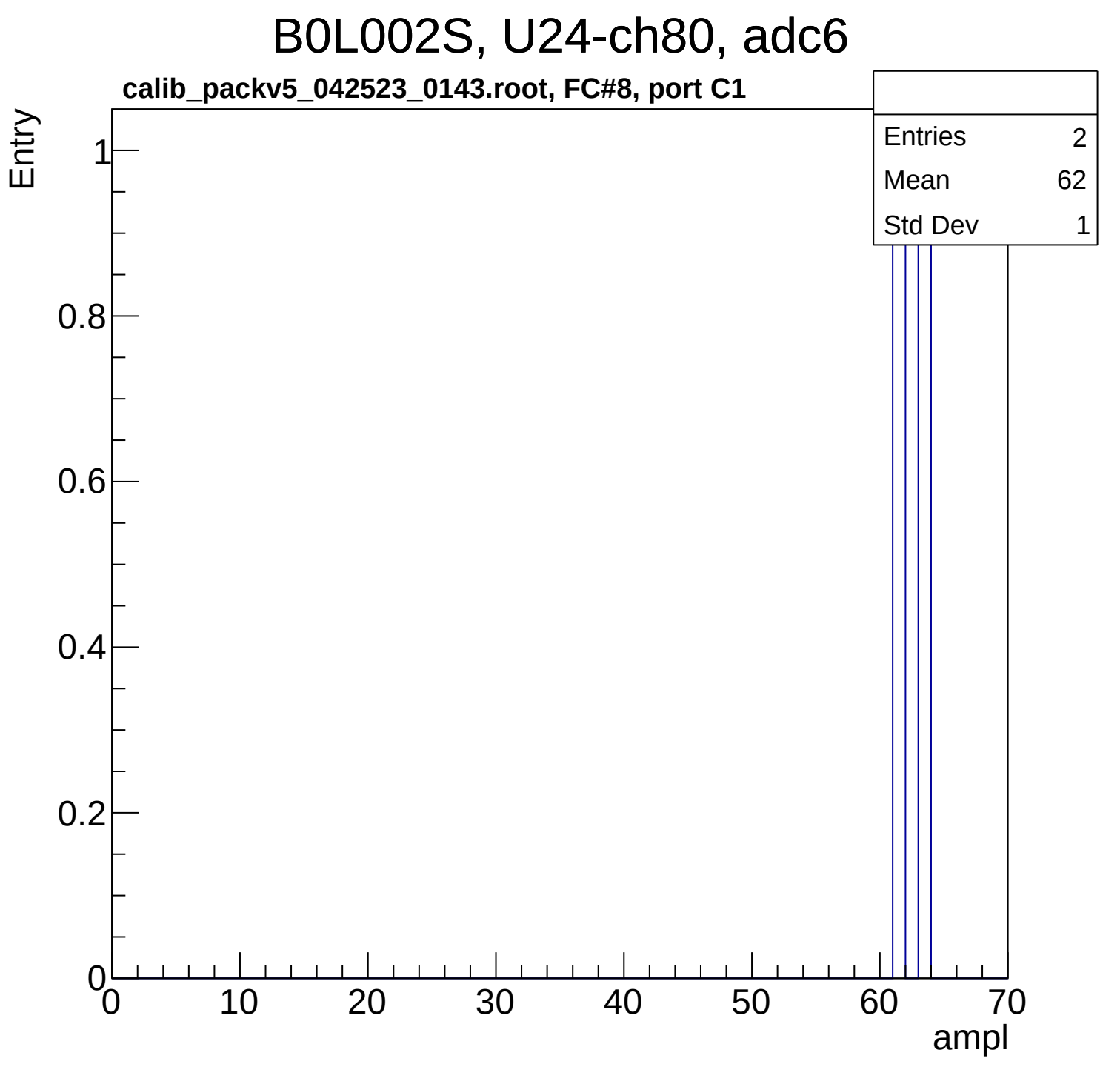
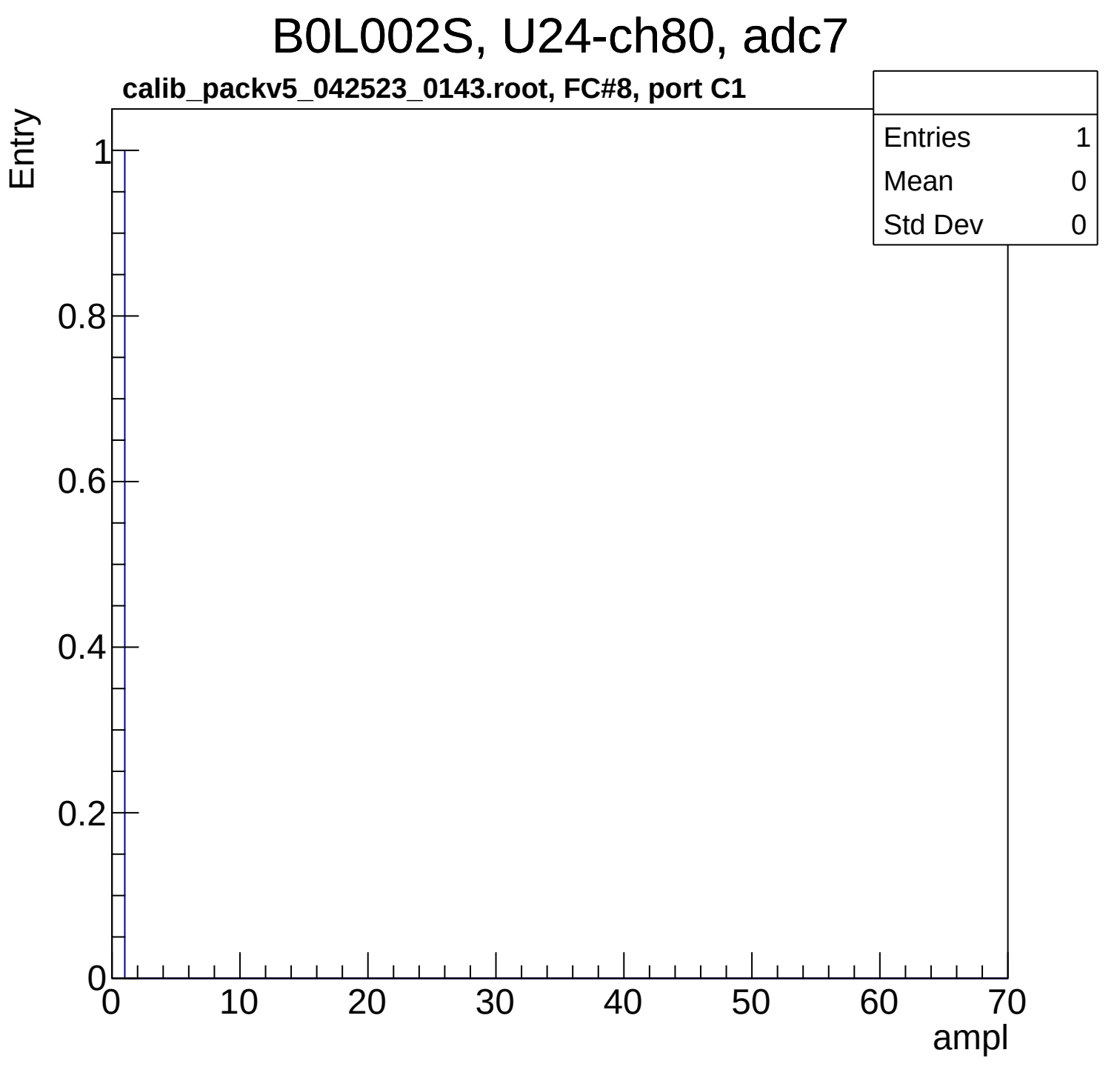
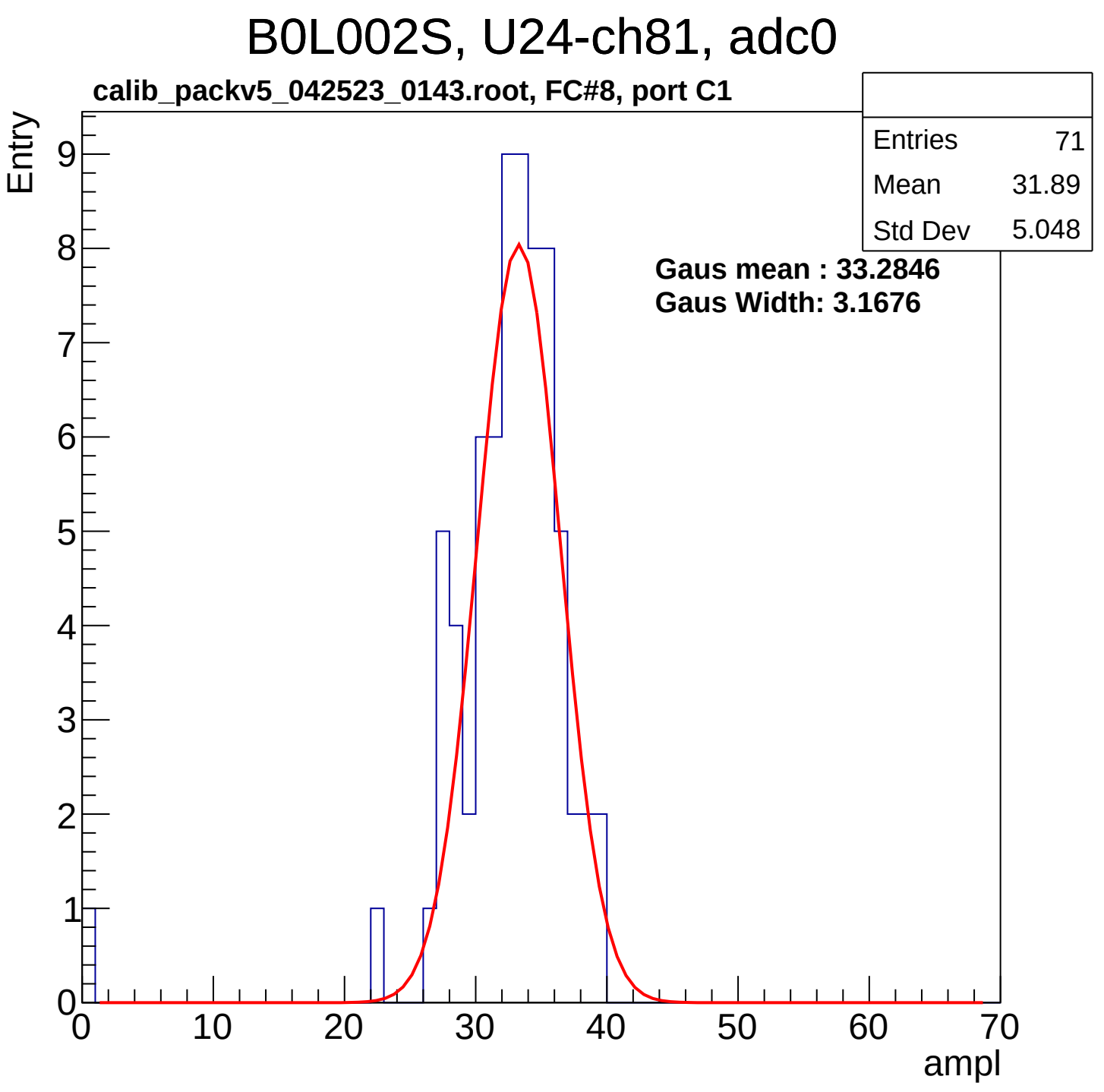

B0L002S, U24-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.89
Std Dev 5.048
Gaus mean : 33.2846
Gaus Width: 3.1676
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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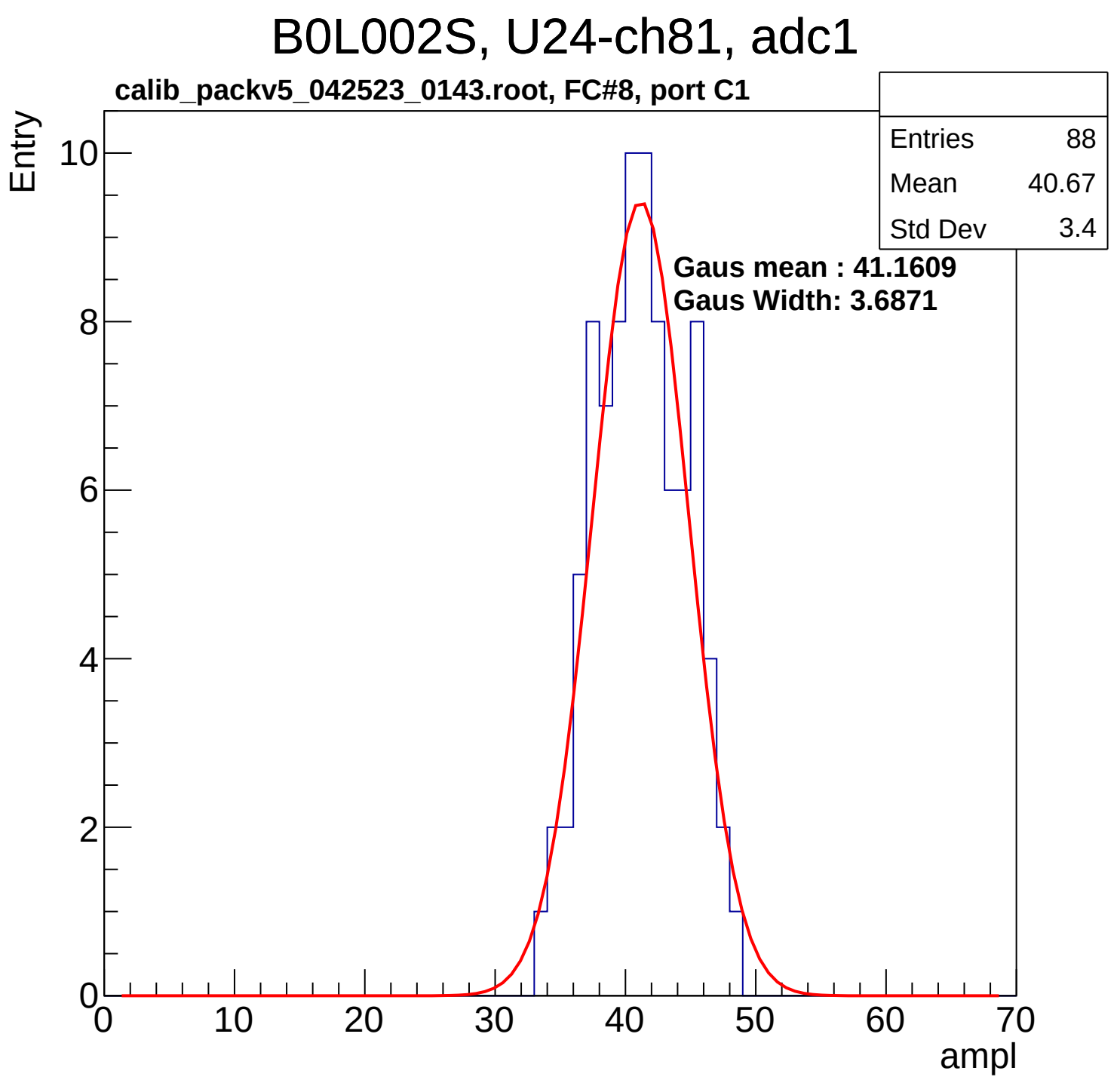
B0L002S, U24-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 40.67
Std Dev 3.4
Gaus mean : 41.1609
Gaus Width: 3.6871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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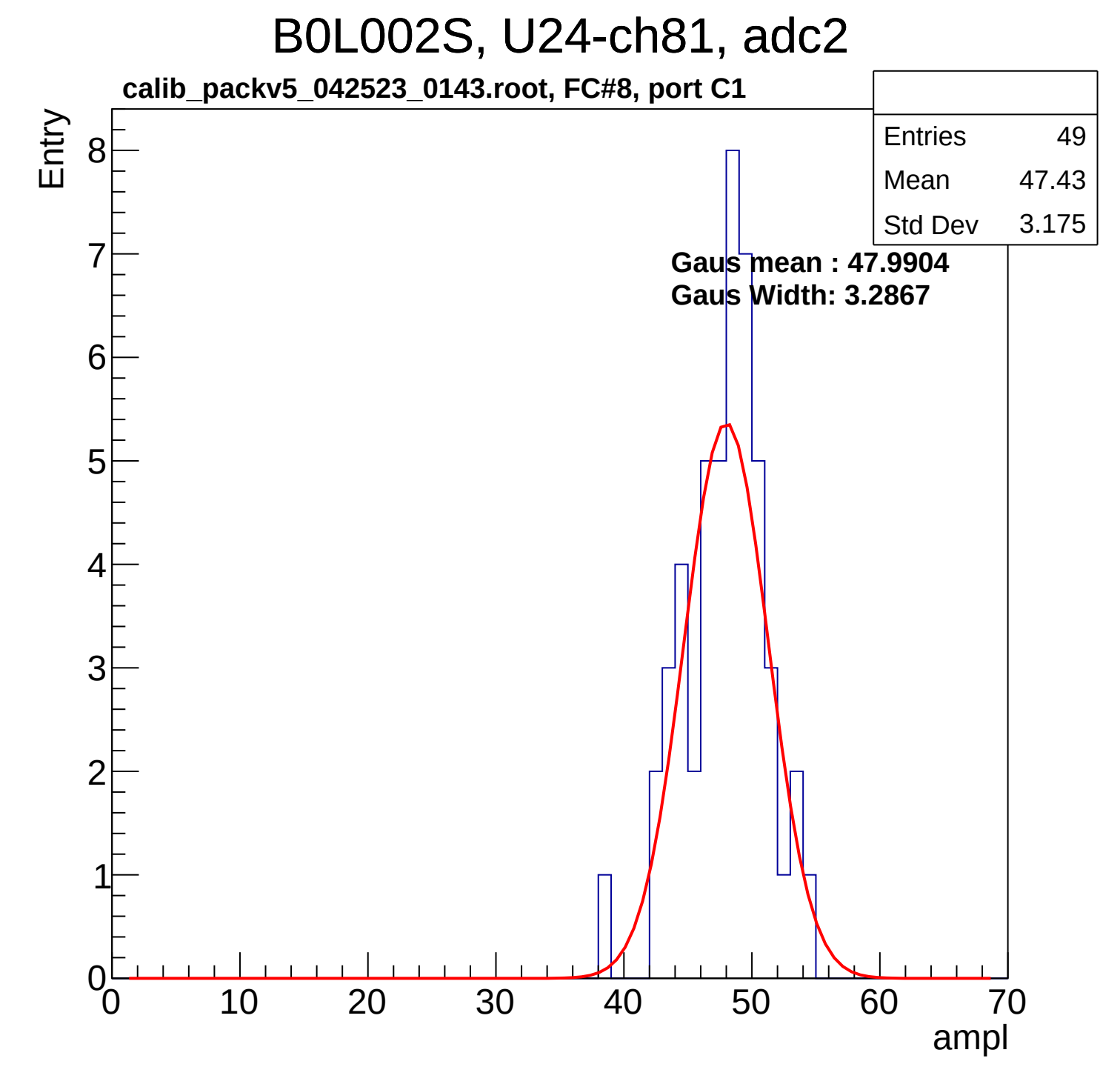
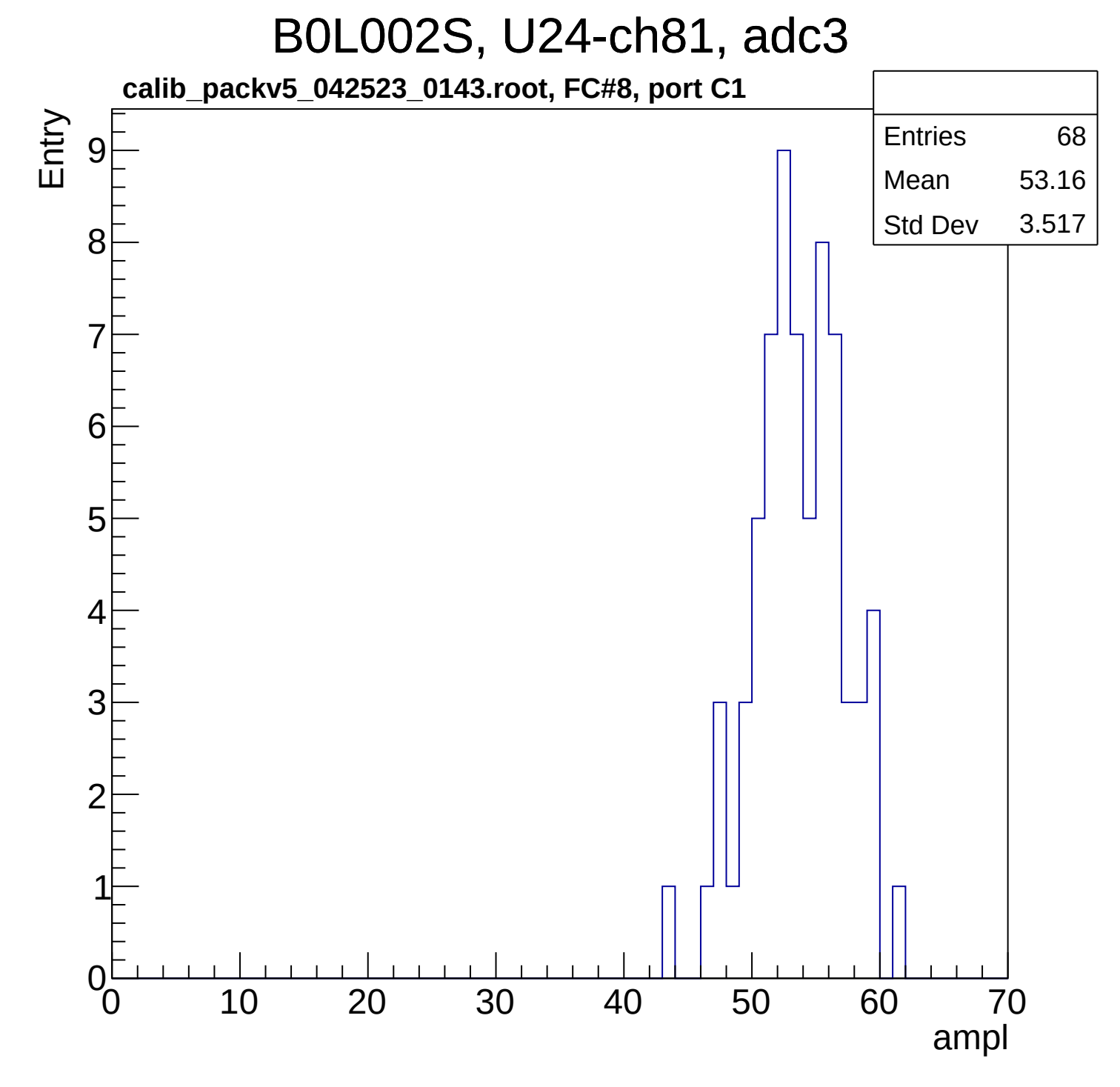
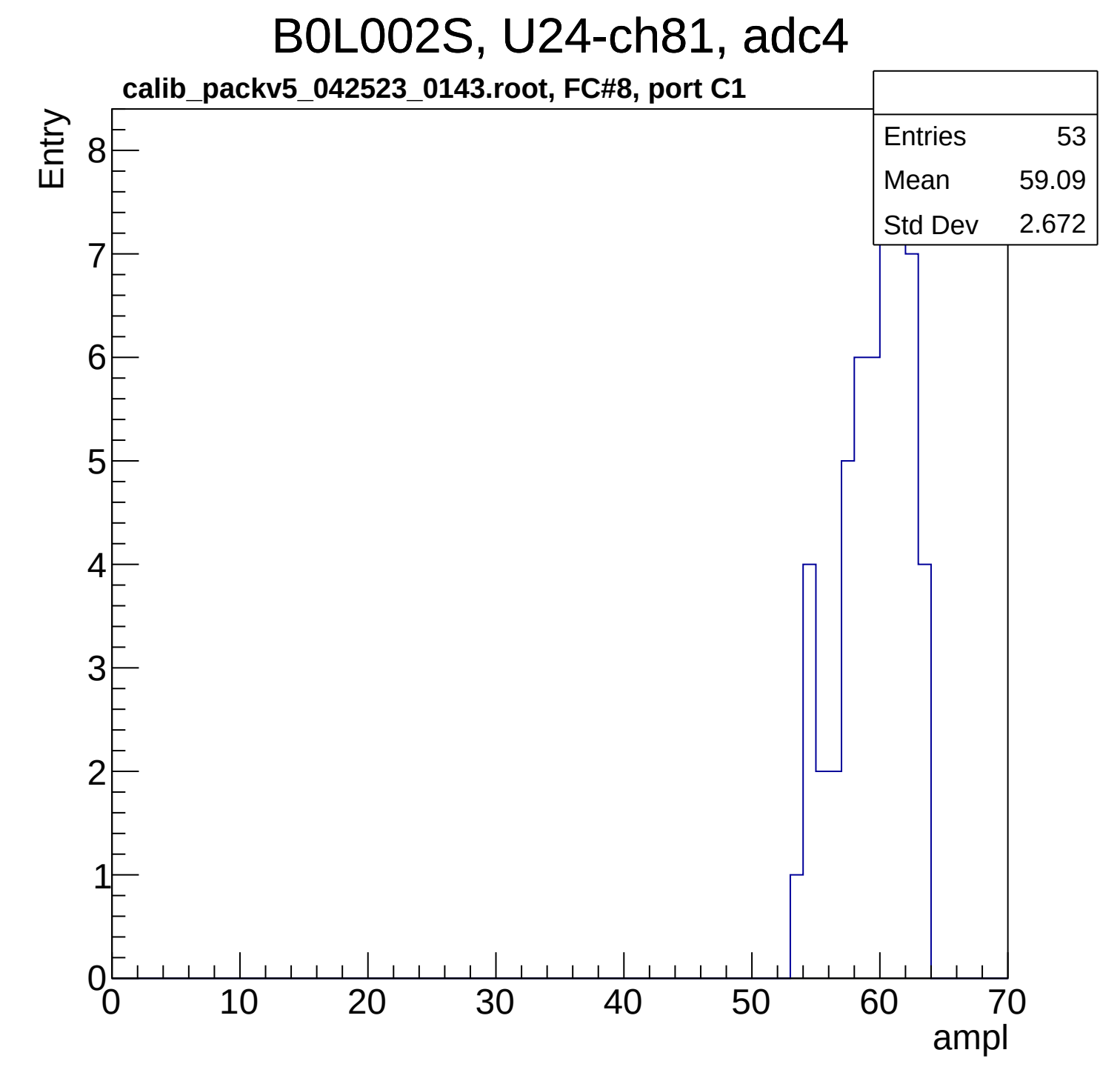
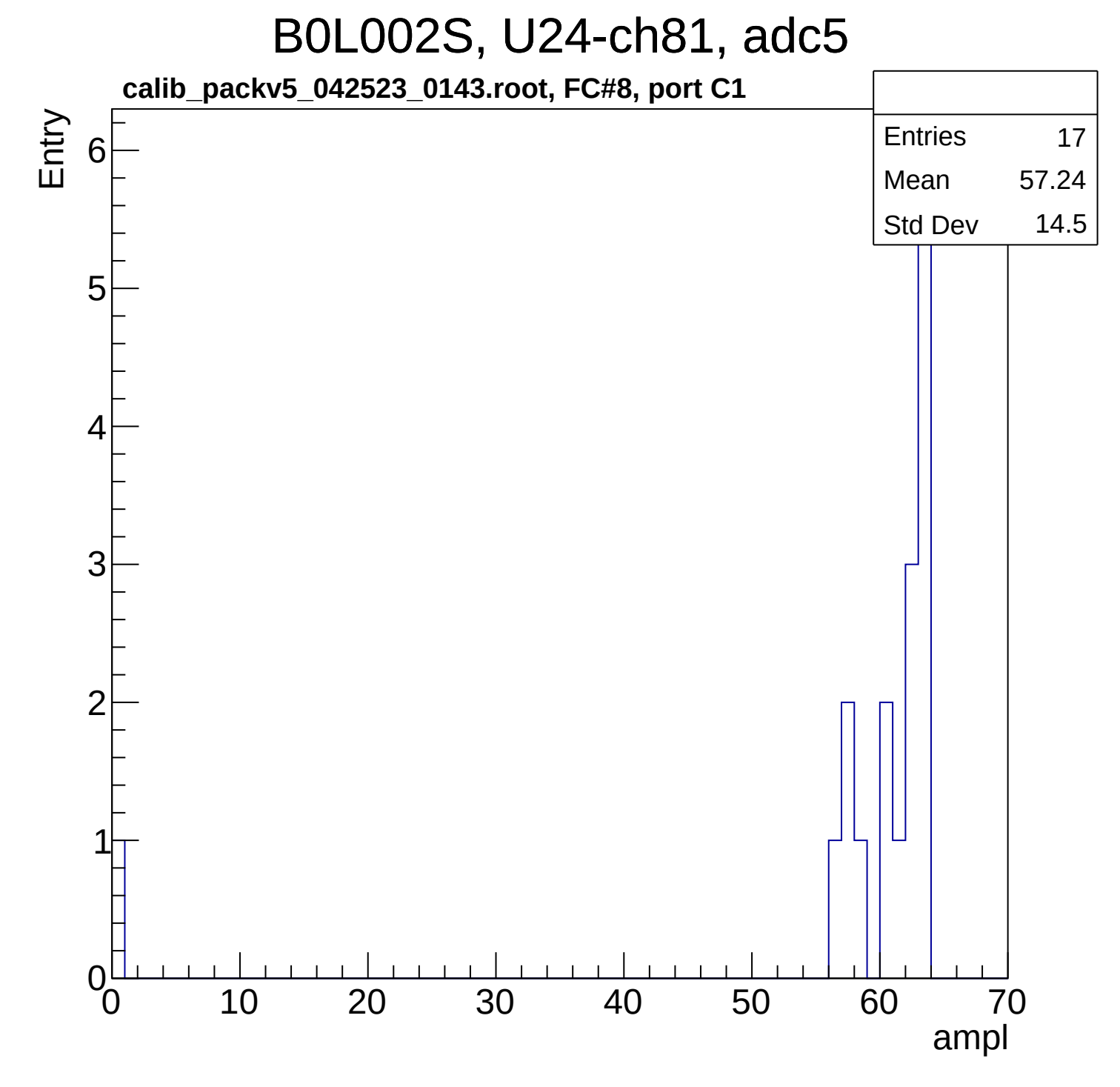
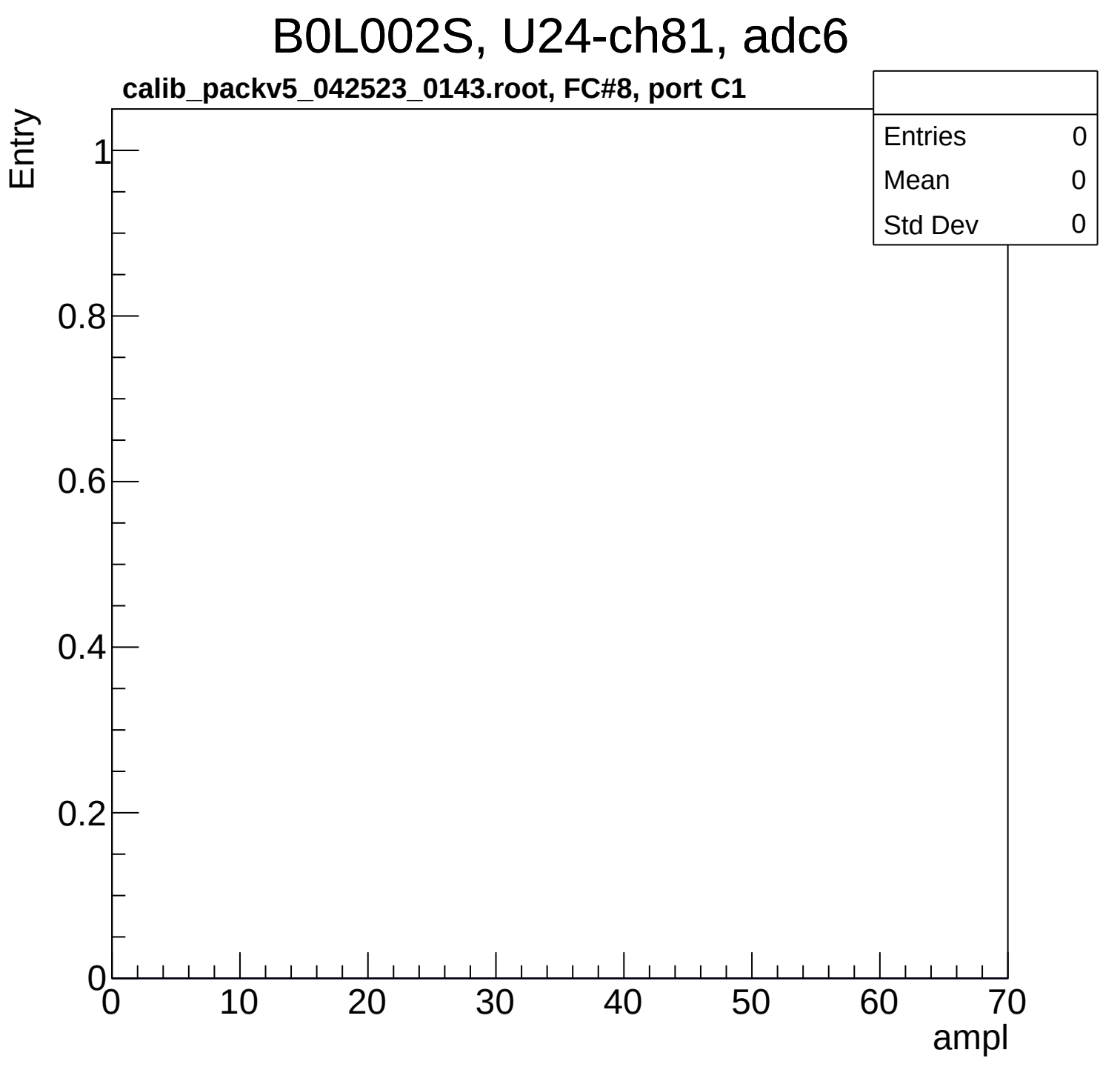
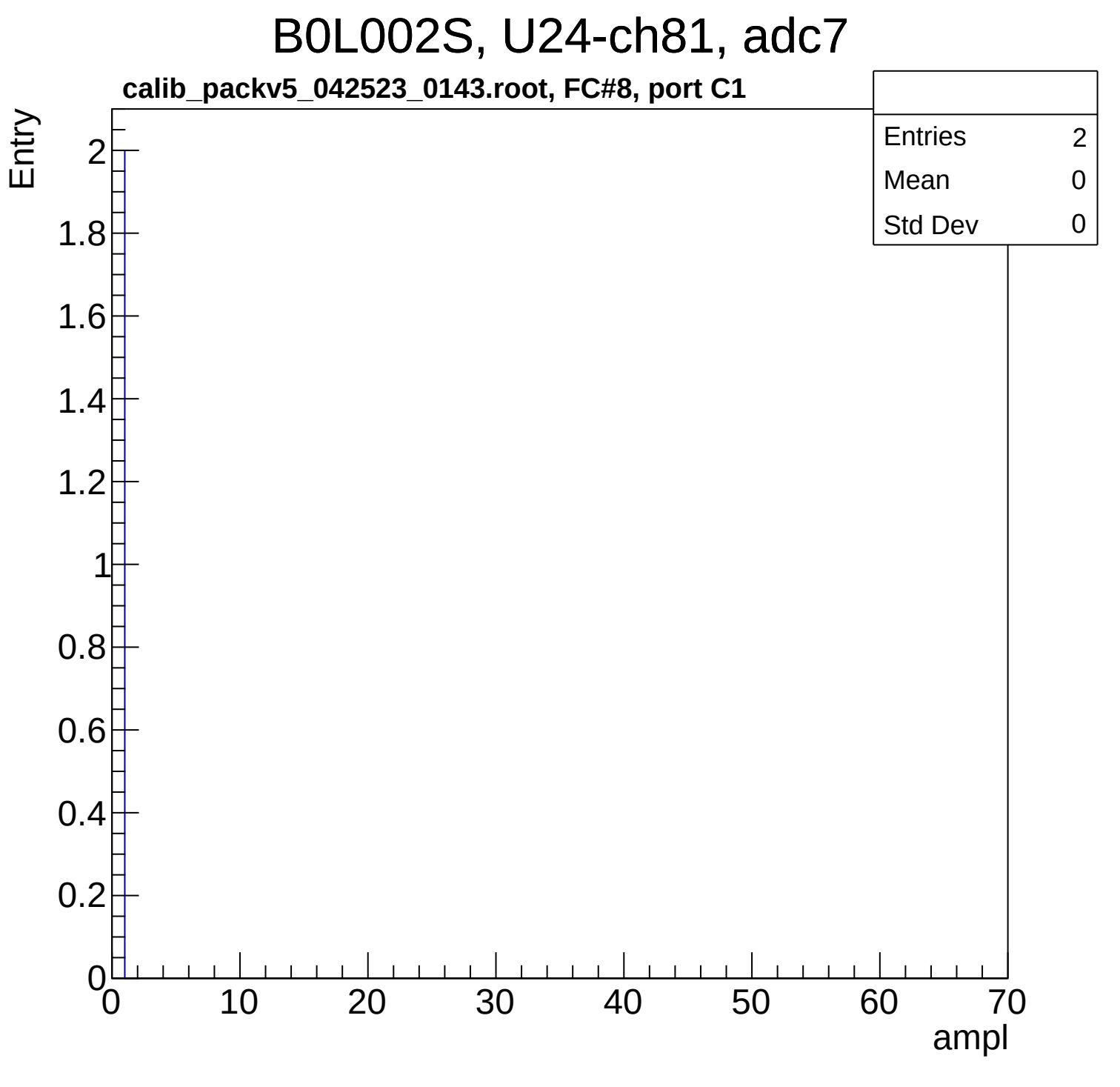

B0L002S, U24-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 47.43
Std Dev 3.175
Gaus mean : 47.9904
Gaus Width: 3.2867
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

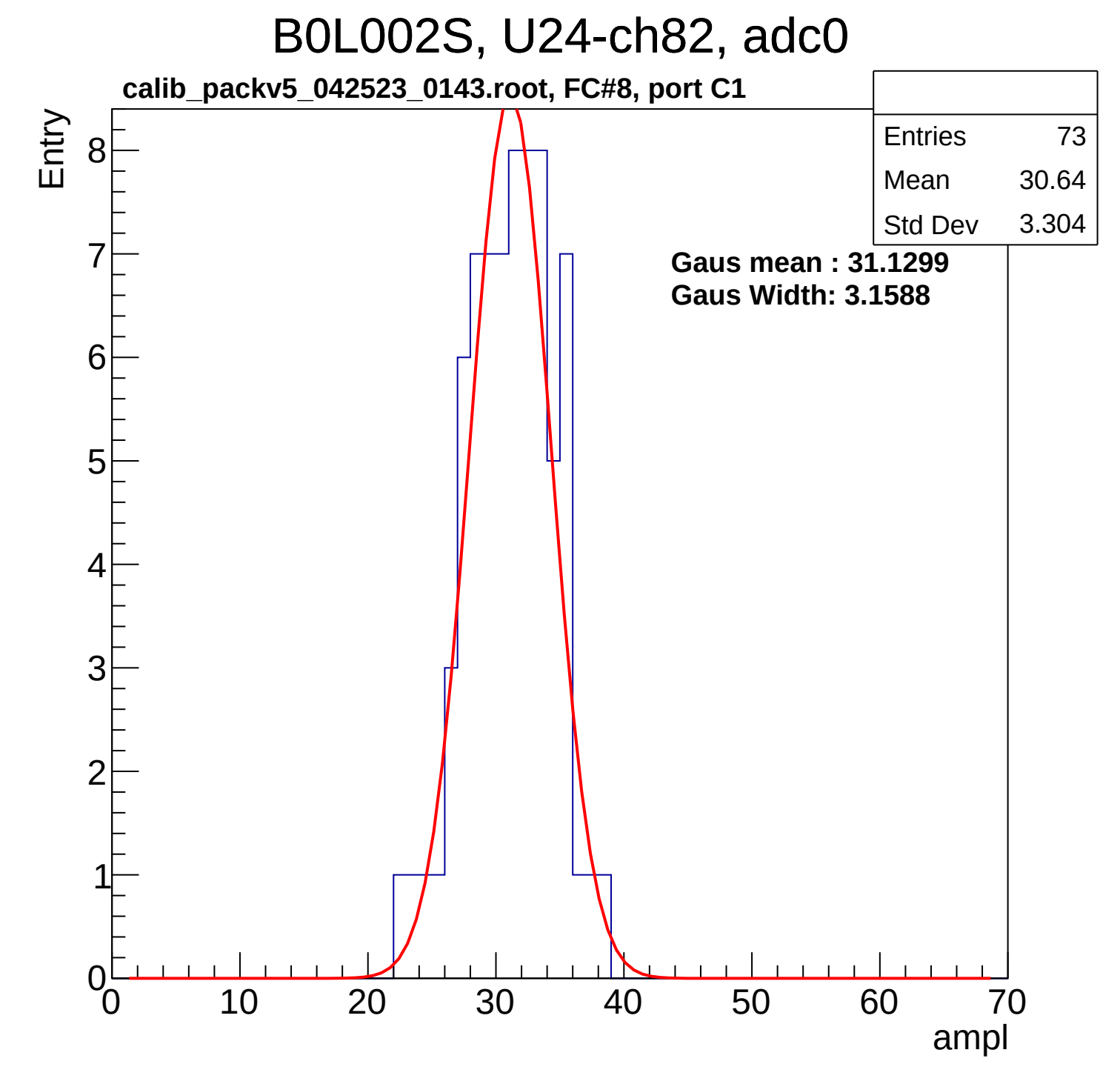

B0L002S, U24-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 53.16
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

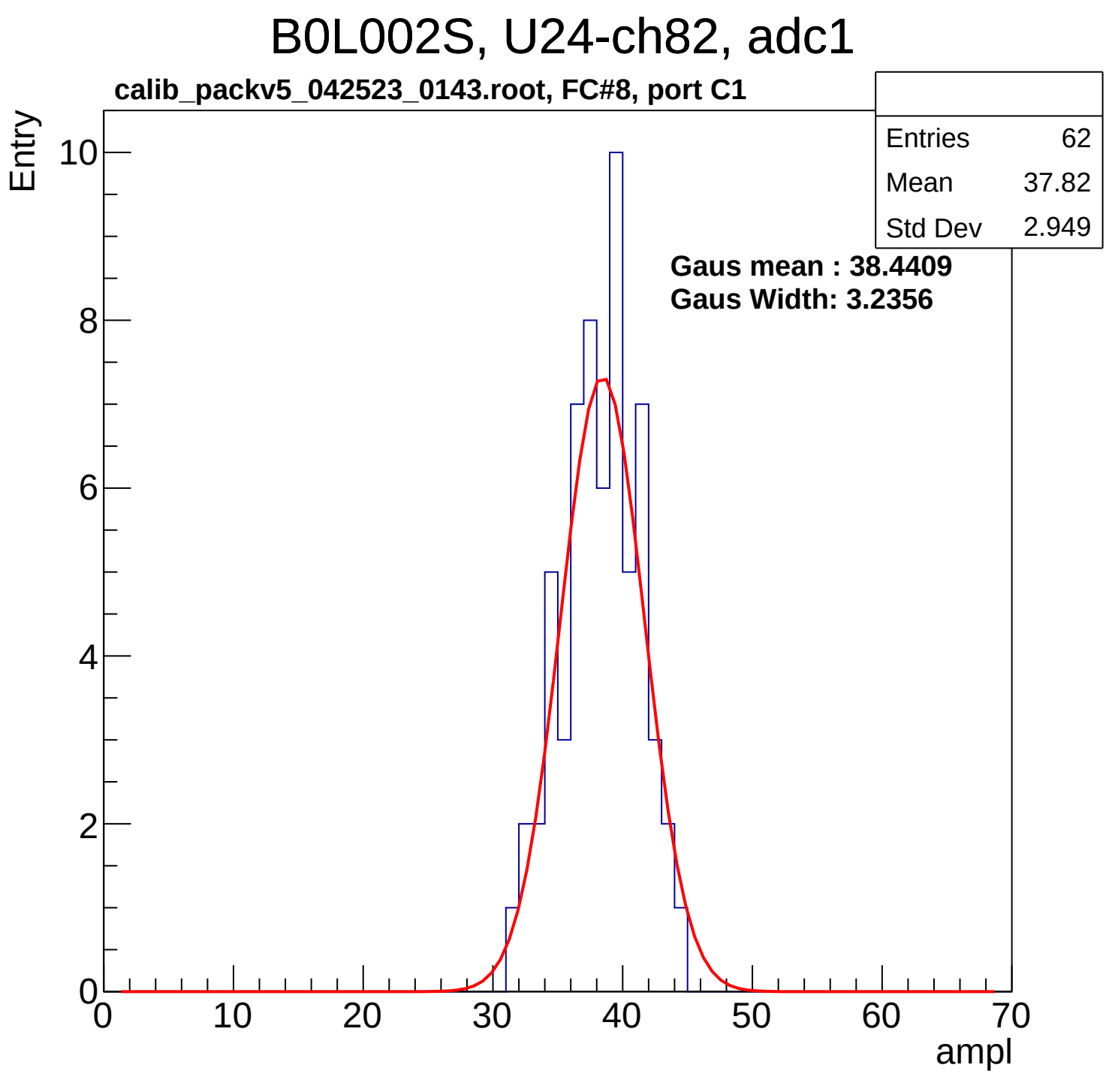

B0L002S, U24-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 59.09
Std Dev 2.672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

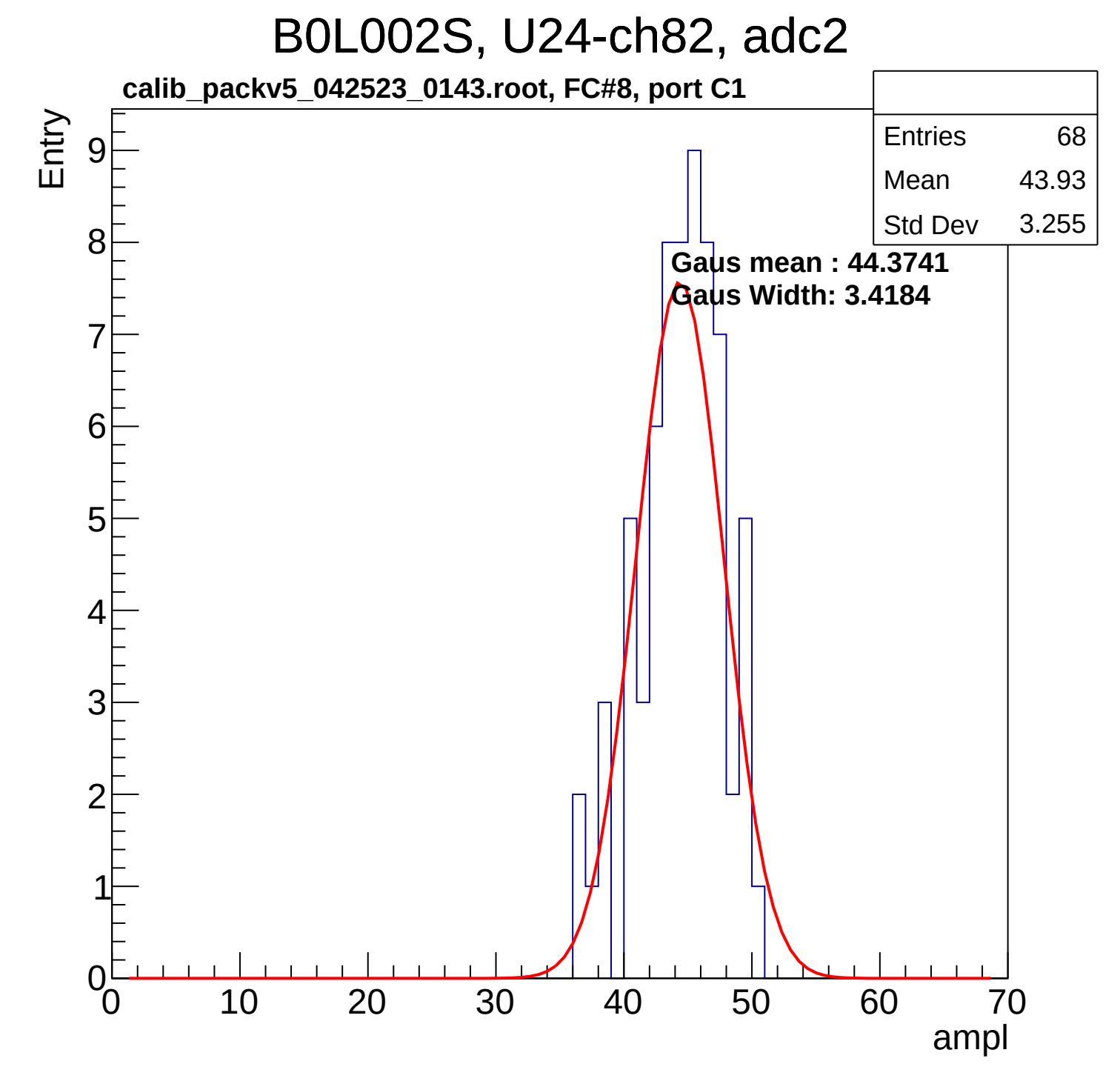

B0L002S, U24-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 57.24
Std Dev 14.5
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U24-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.64
Std Dev 3.304
Gaus mean : 31.1299
Gaus Width: 3.1588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.82
Std Dev 2.949
Gaus mean : 38.4409
Gaus Width: 3.2356
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.93
Std Dev 3.255
Gaus mean : 44.3741
Gaus Width: 3.4184
Entry
ampl
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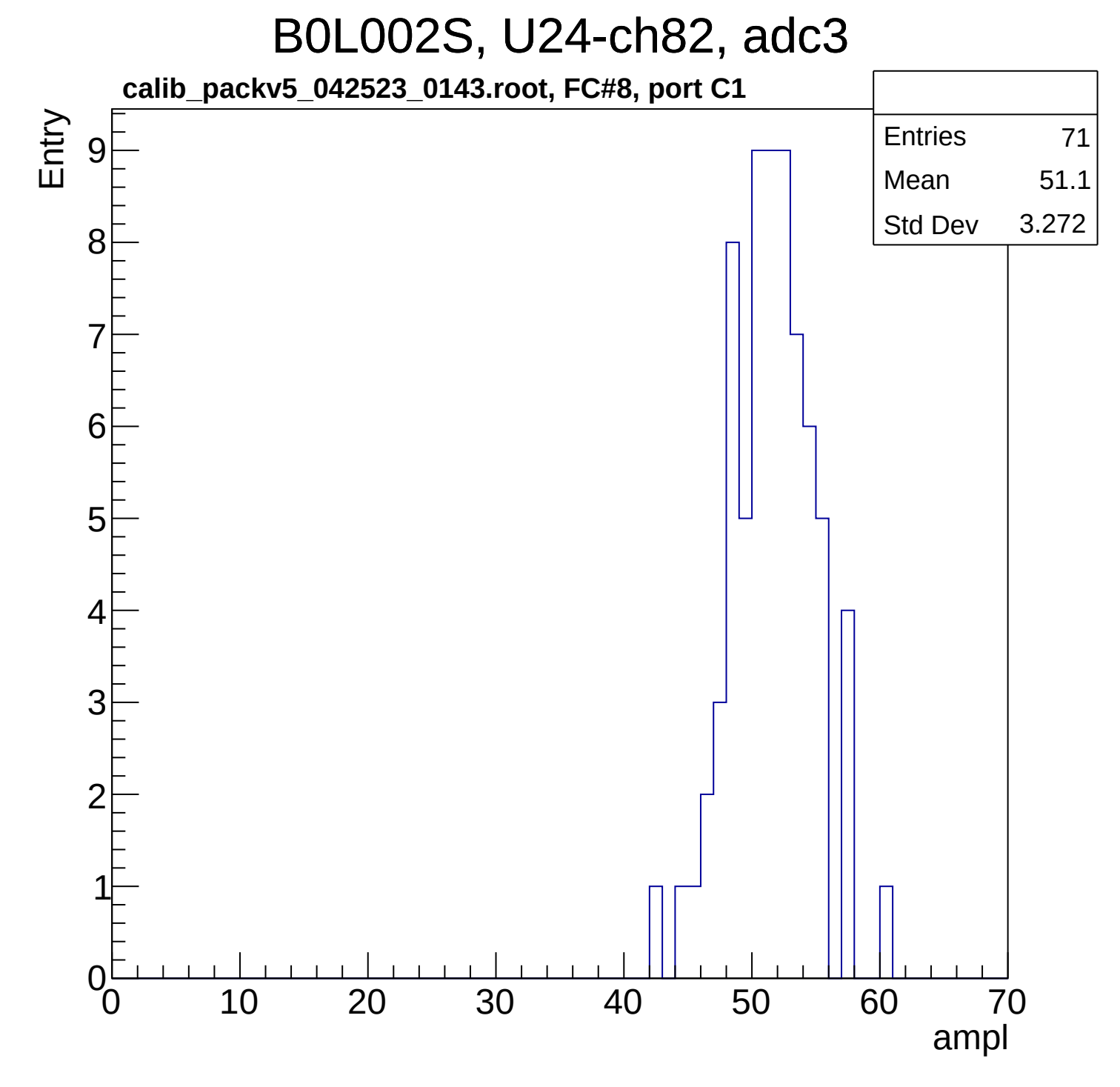
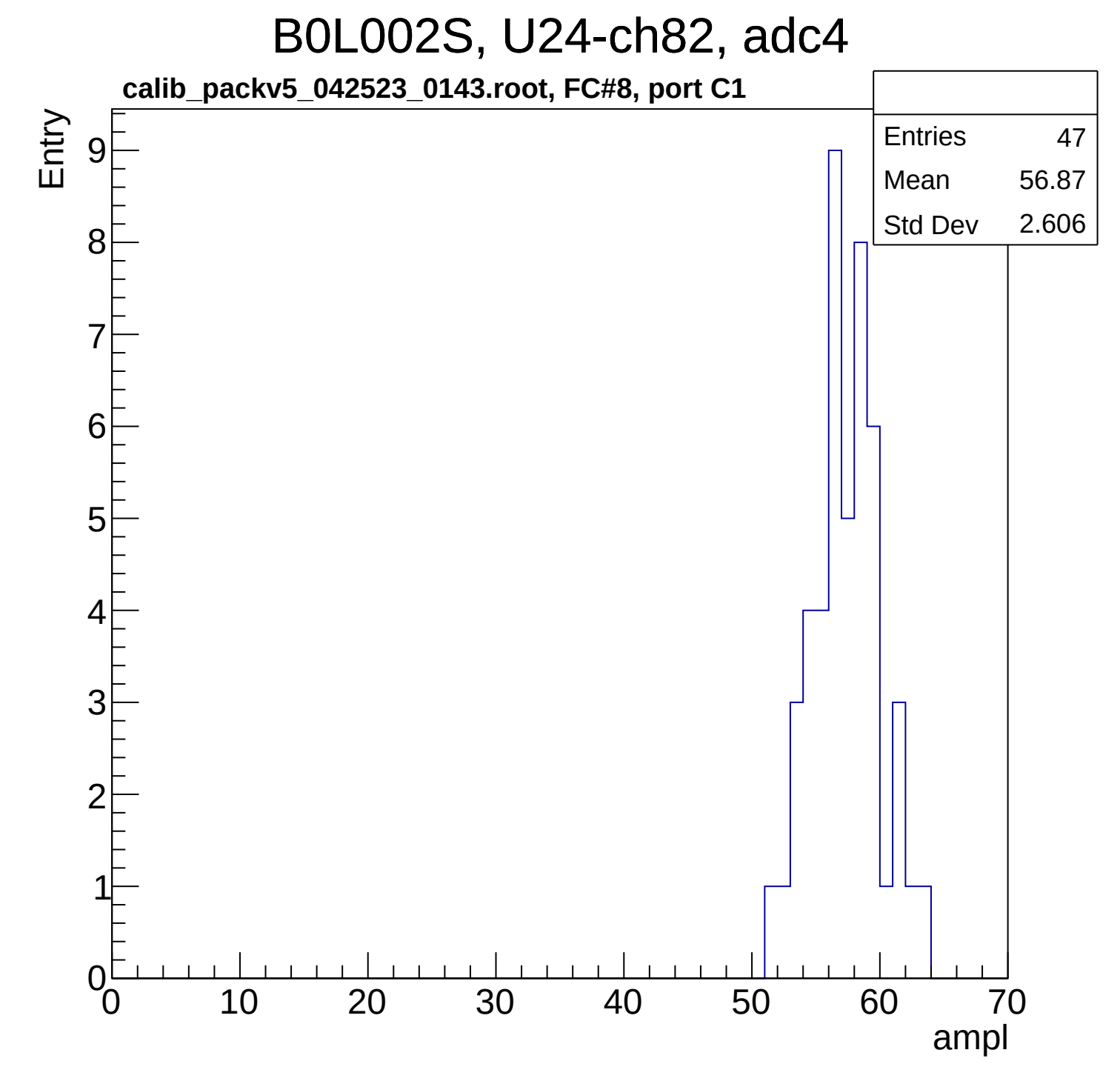
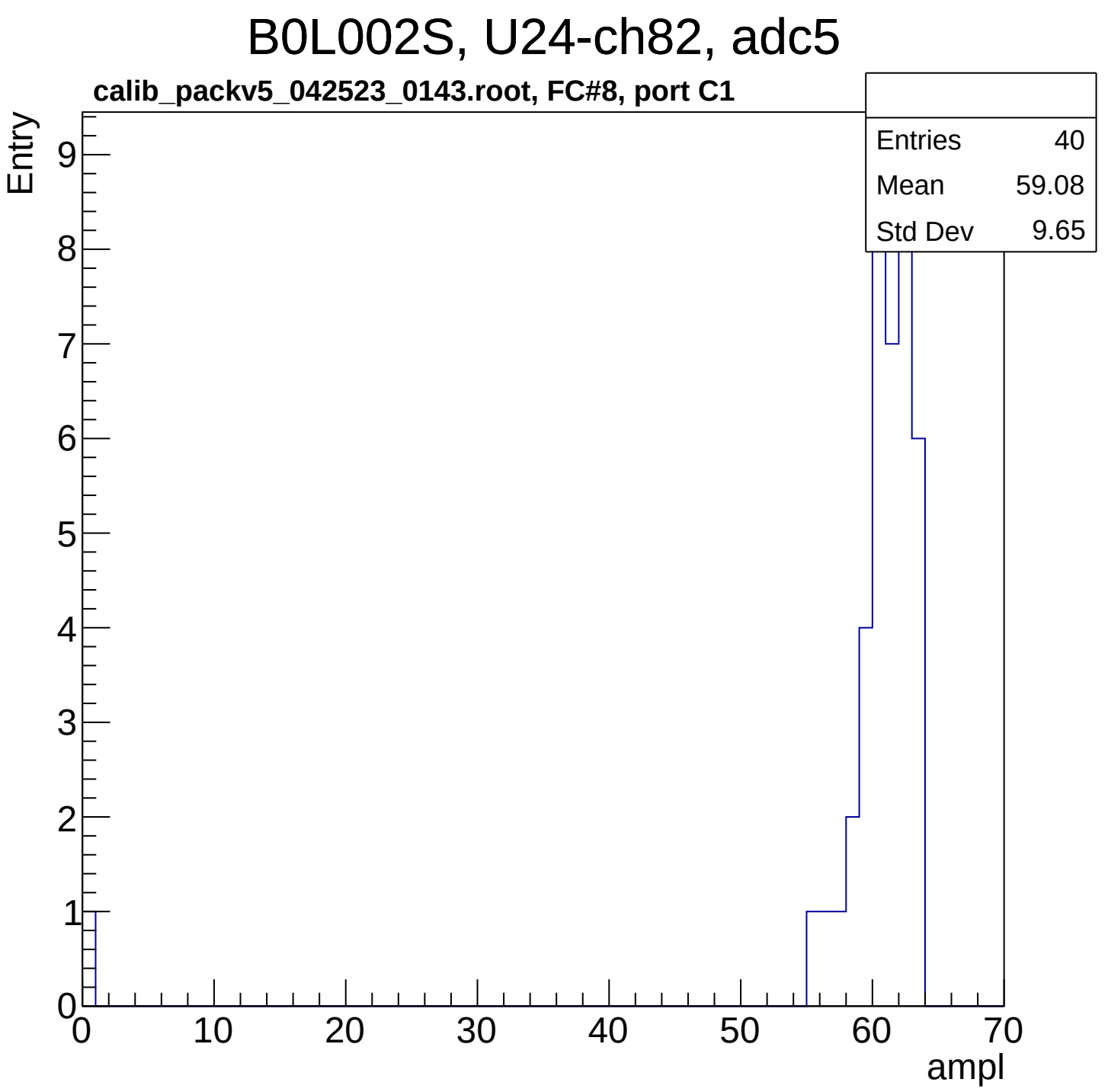
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.1
Std Dev 3.272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 56.87
Std Dev 2.606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.08
Std Dev 9.65
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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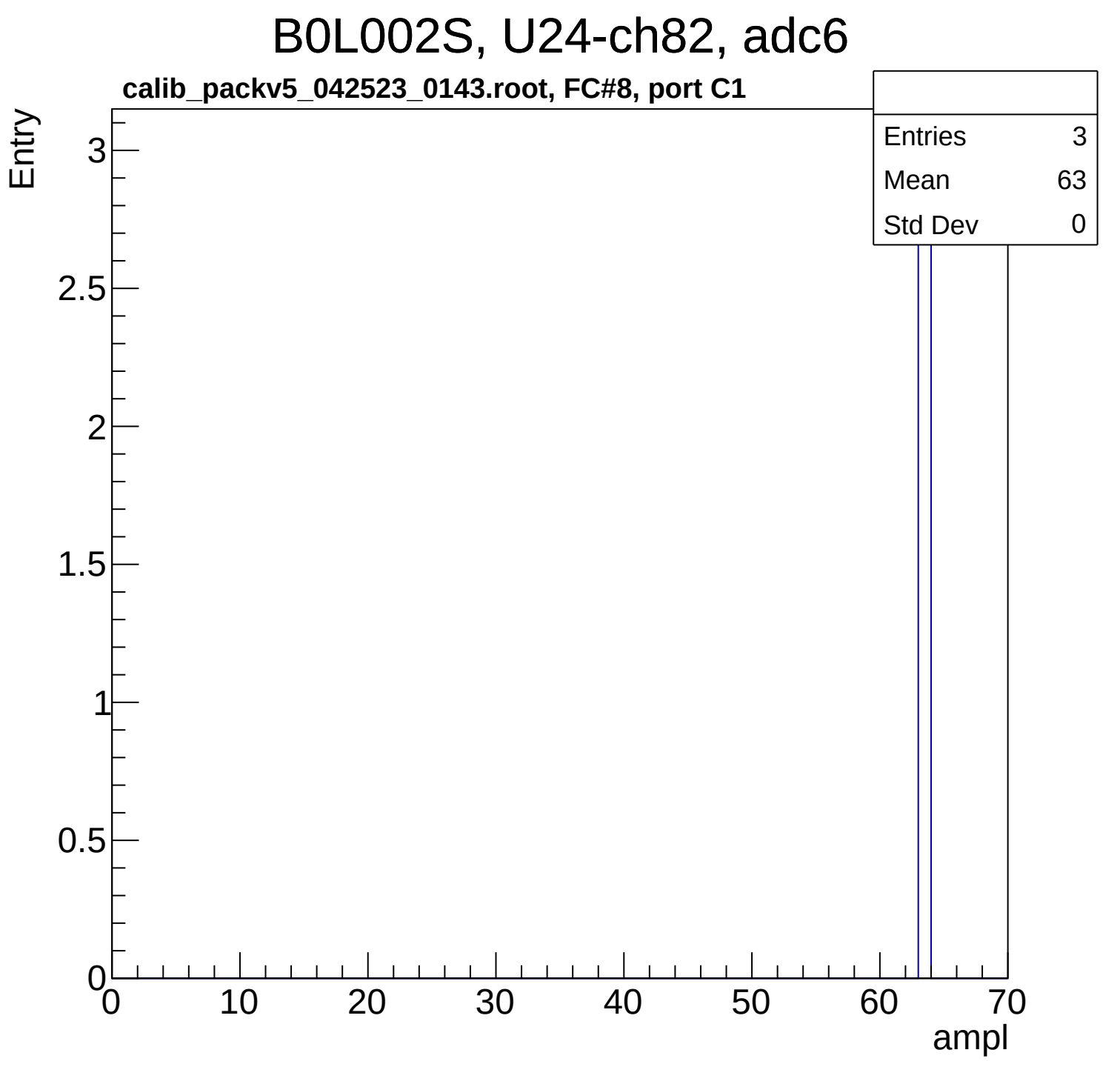
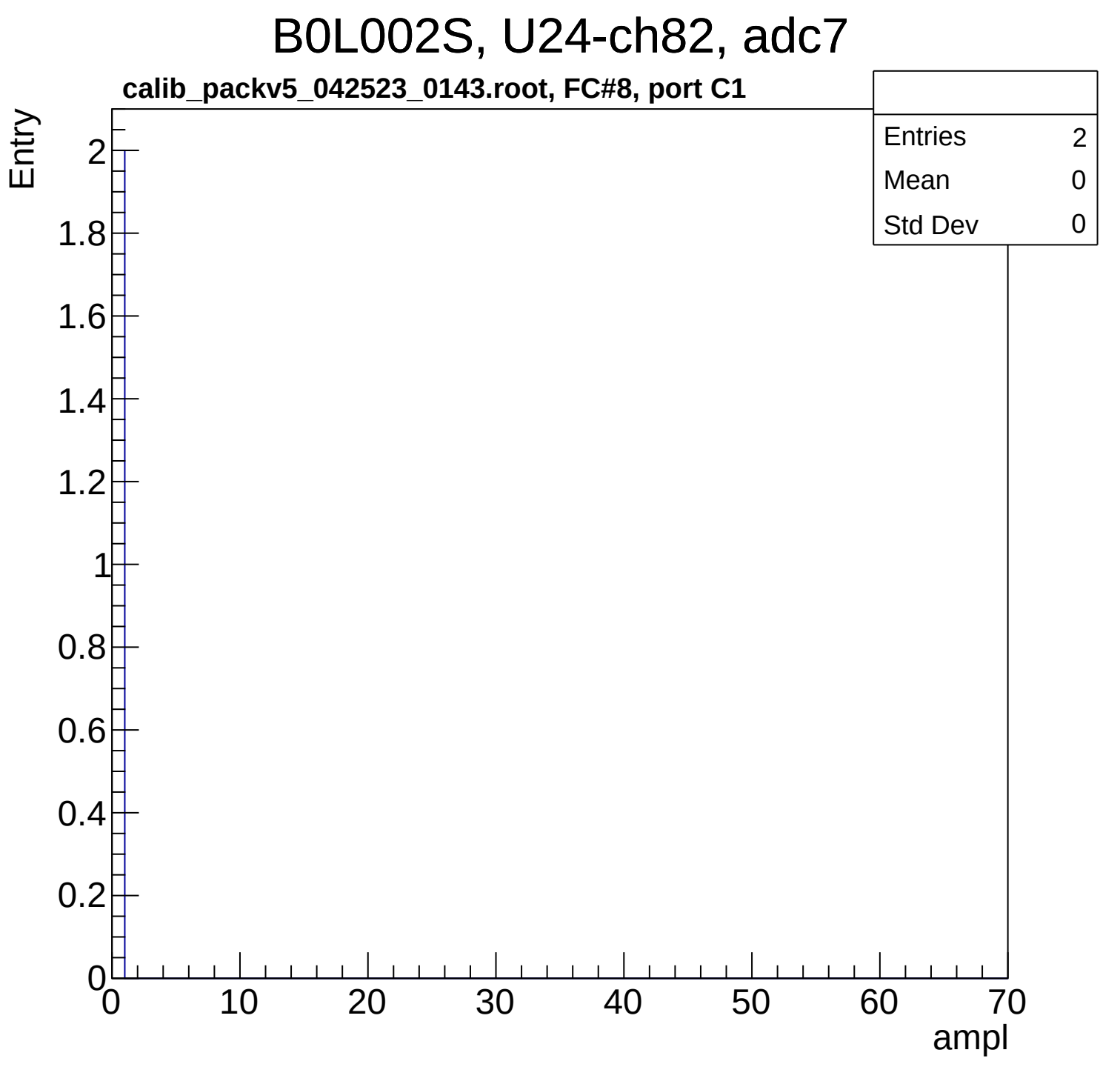
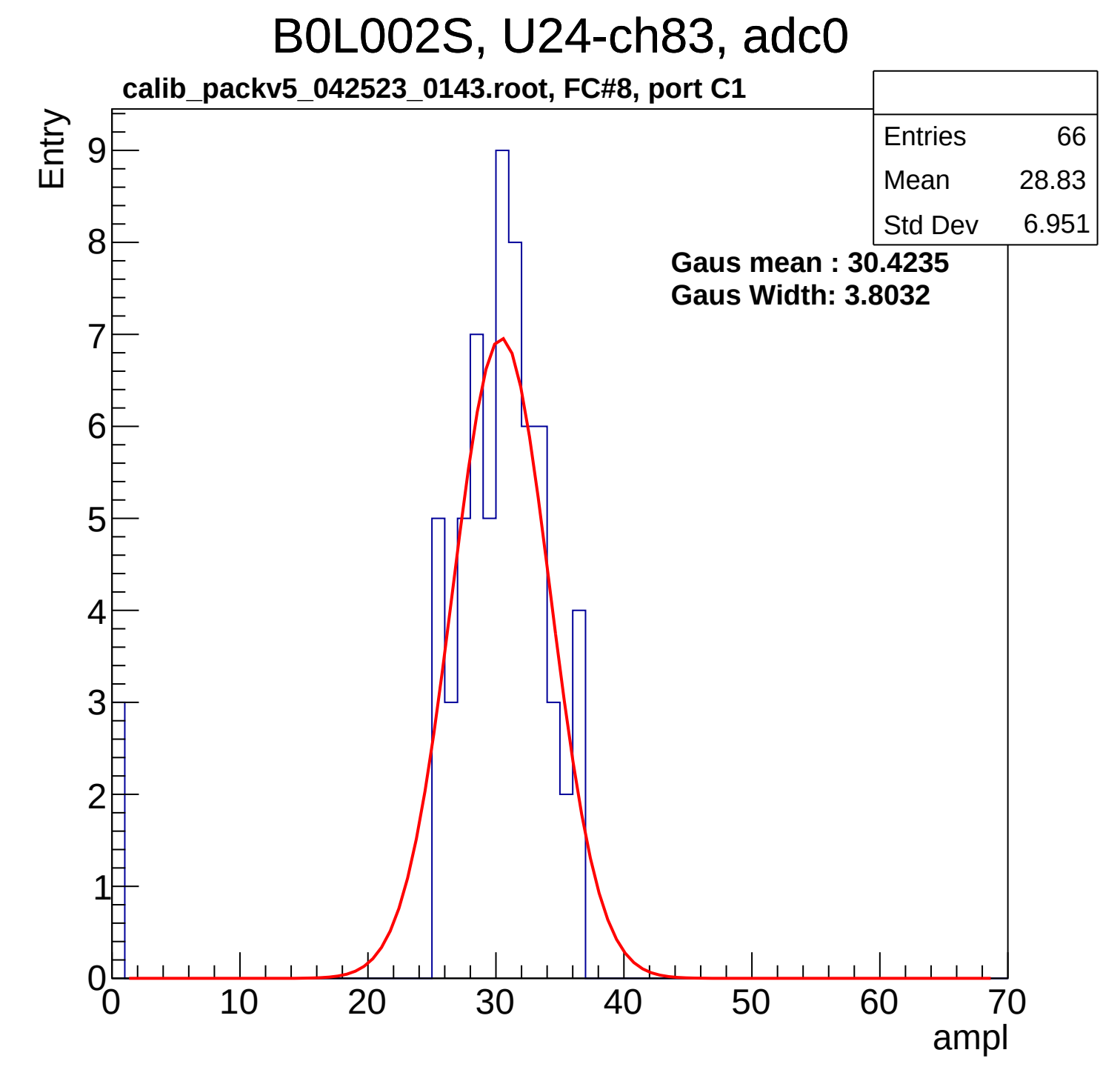
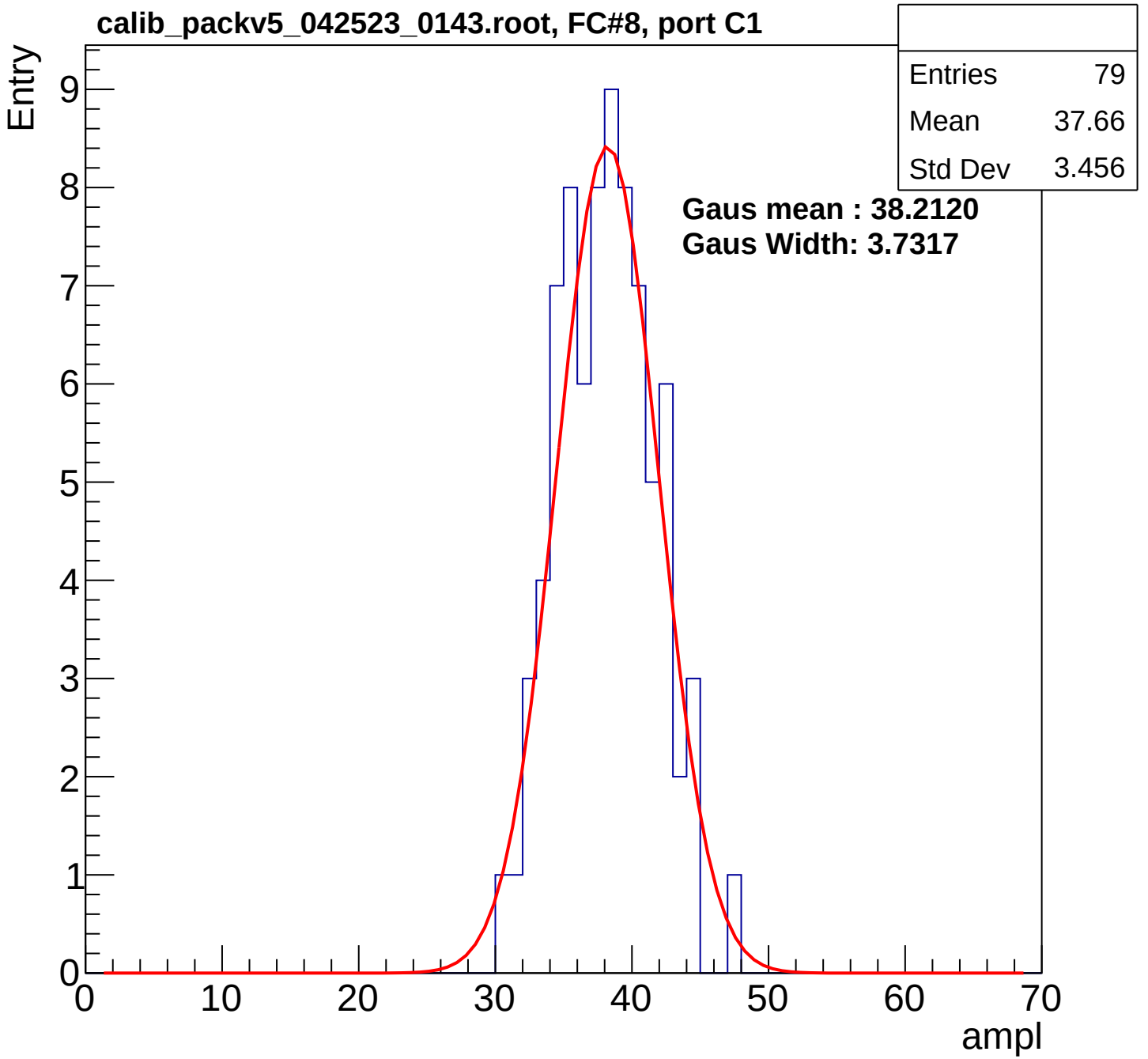
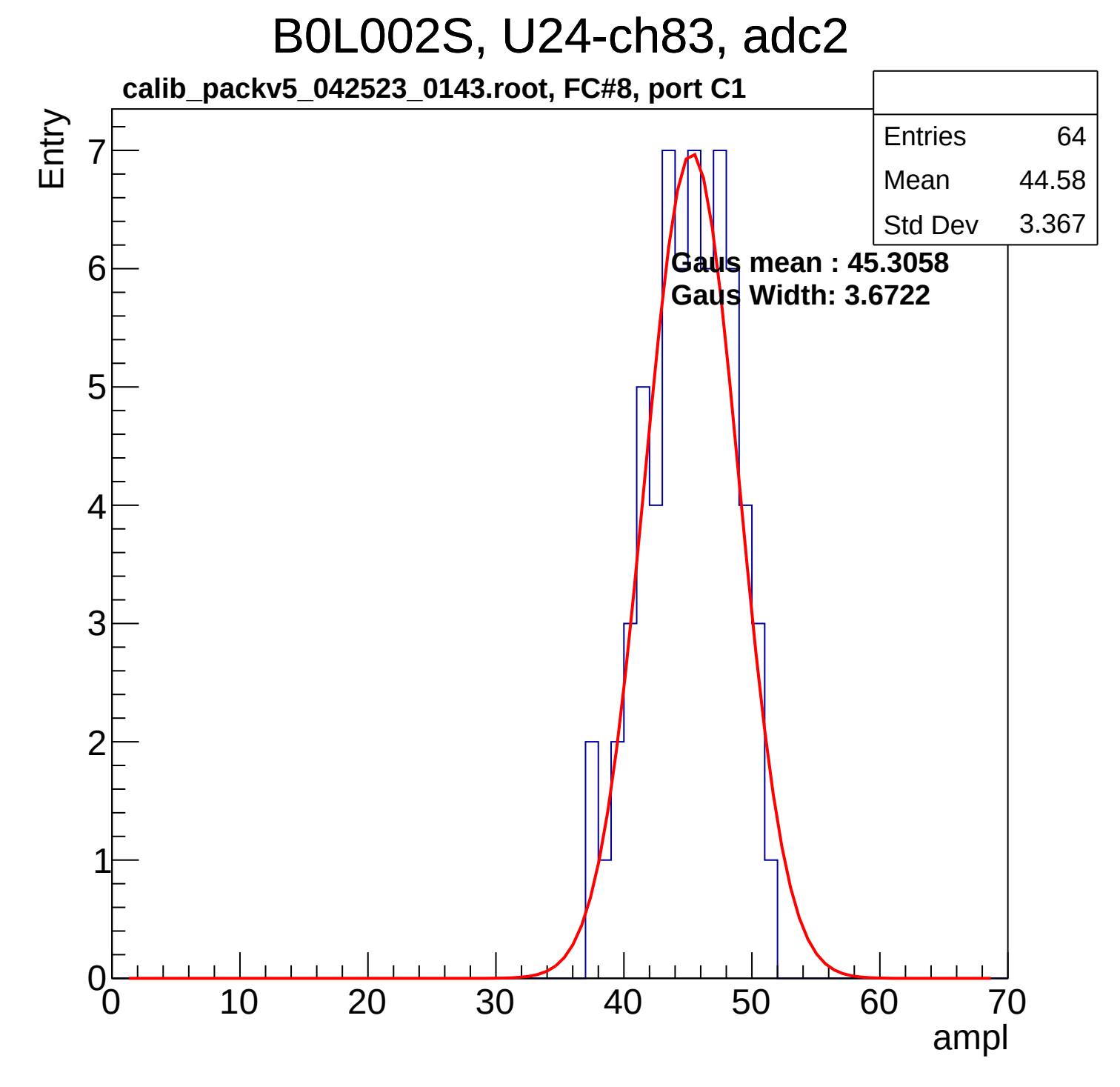

B0L002S, U24-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 28.83
Std Dev 6.951
Gaus mean : 30.4235
Gaus Width: 3.8032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 37.66
Std Dev 3.456
Gaus mean : 38.2120
Gaus Width: 3.7317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.58
Std Dev 3.367
Gaus mean : 45.3058
Gaus Width: 3.6722
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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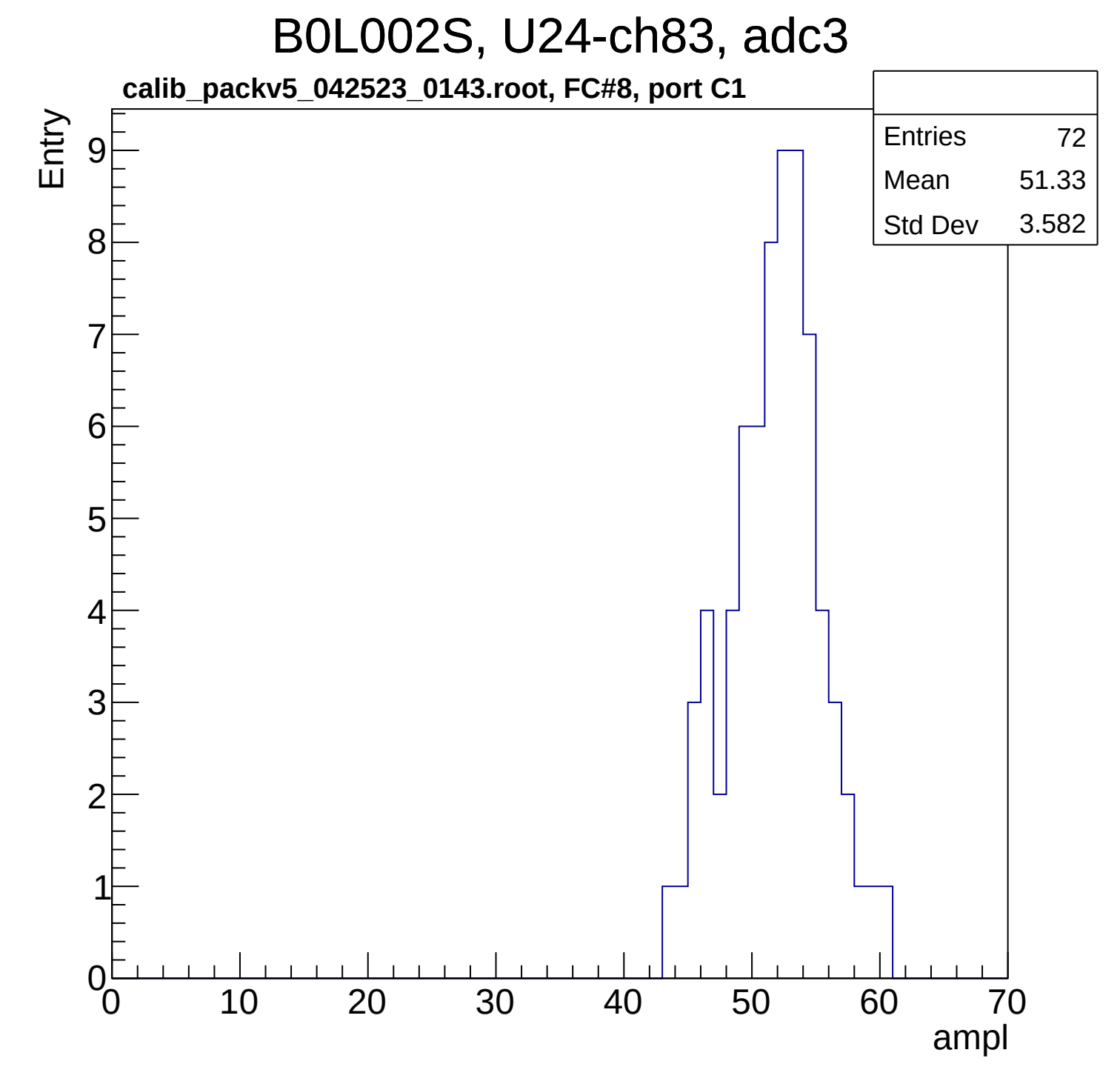
B0L002S, U24-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.33
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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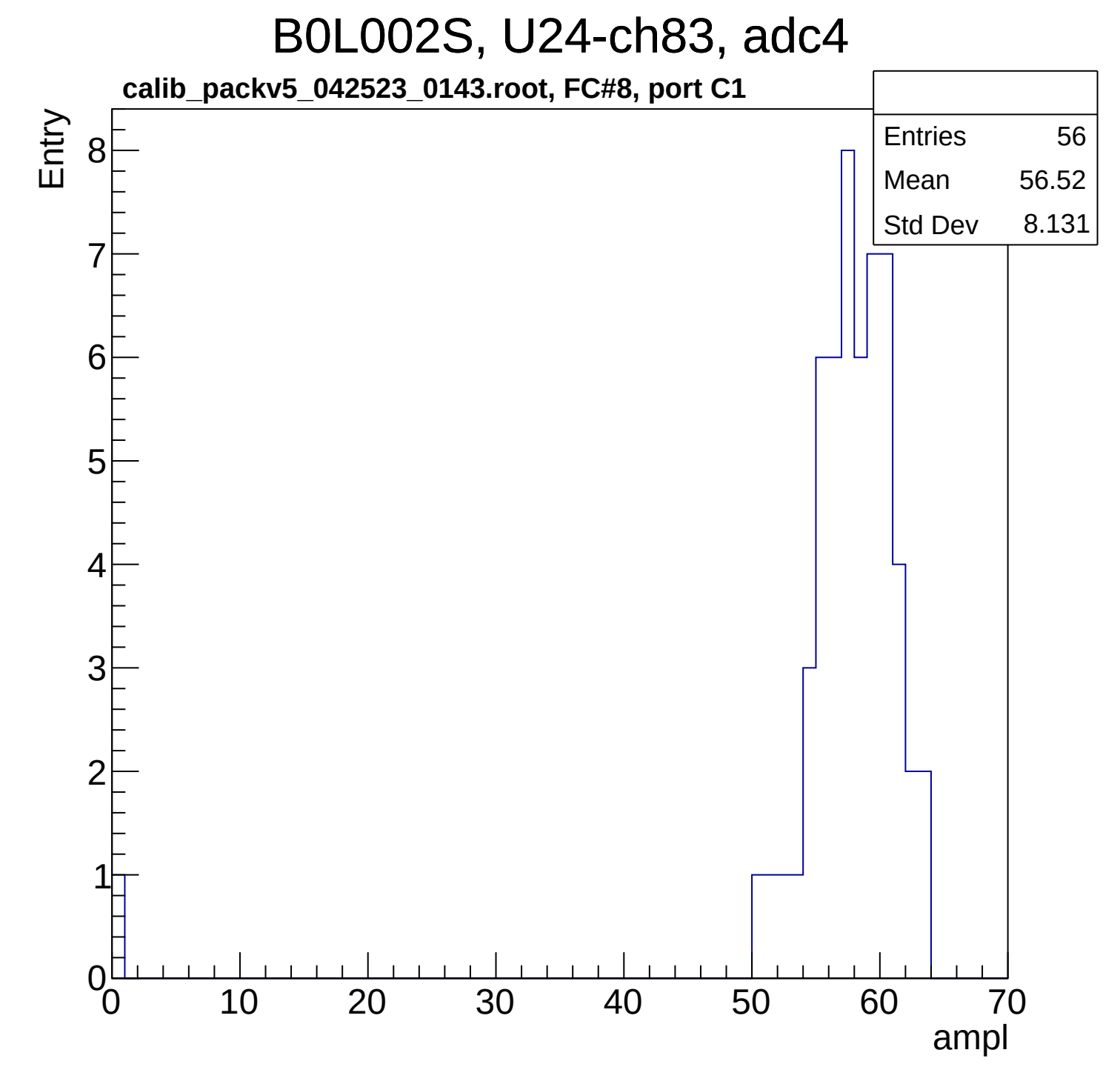
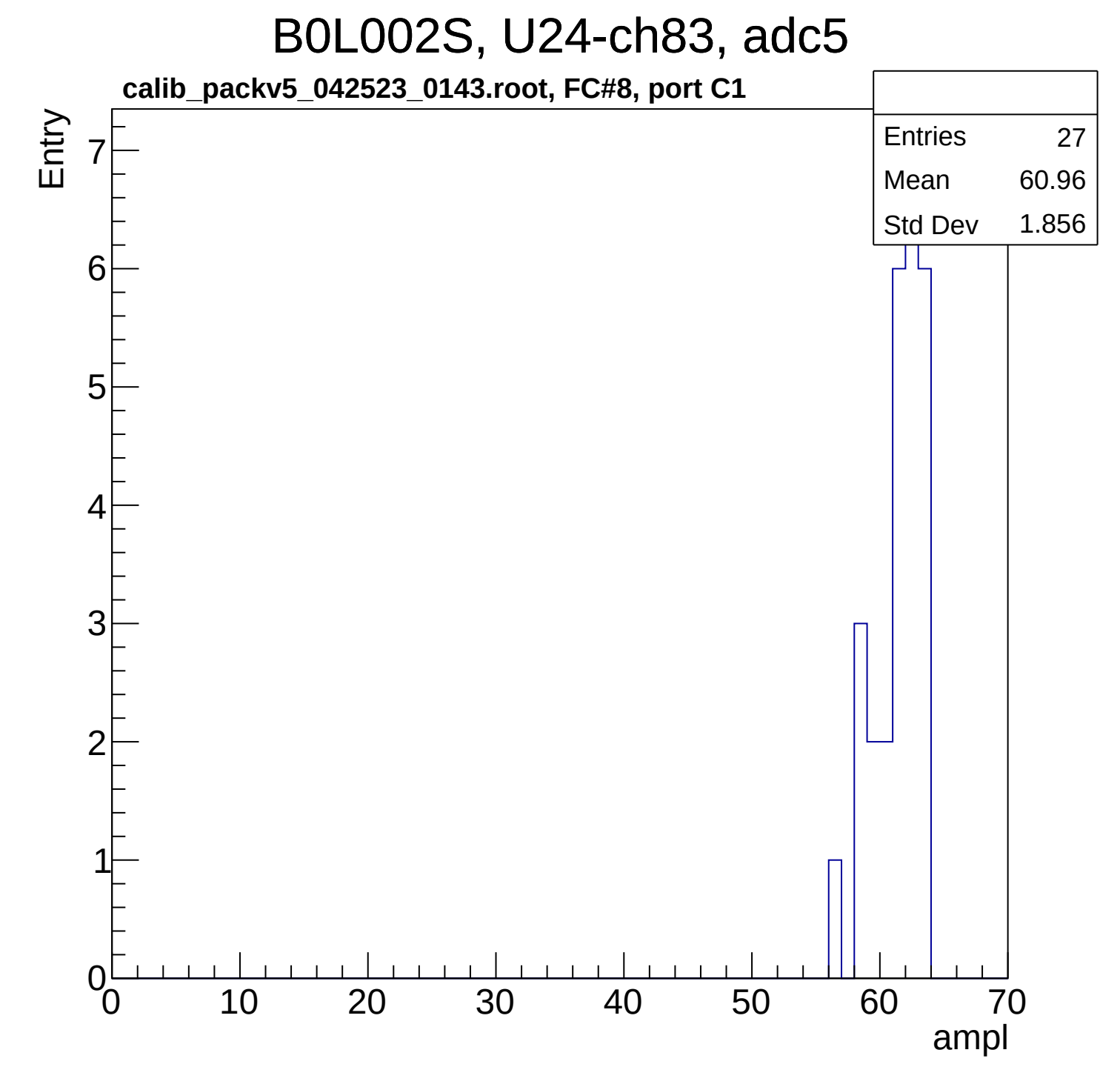
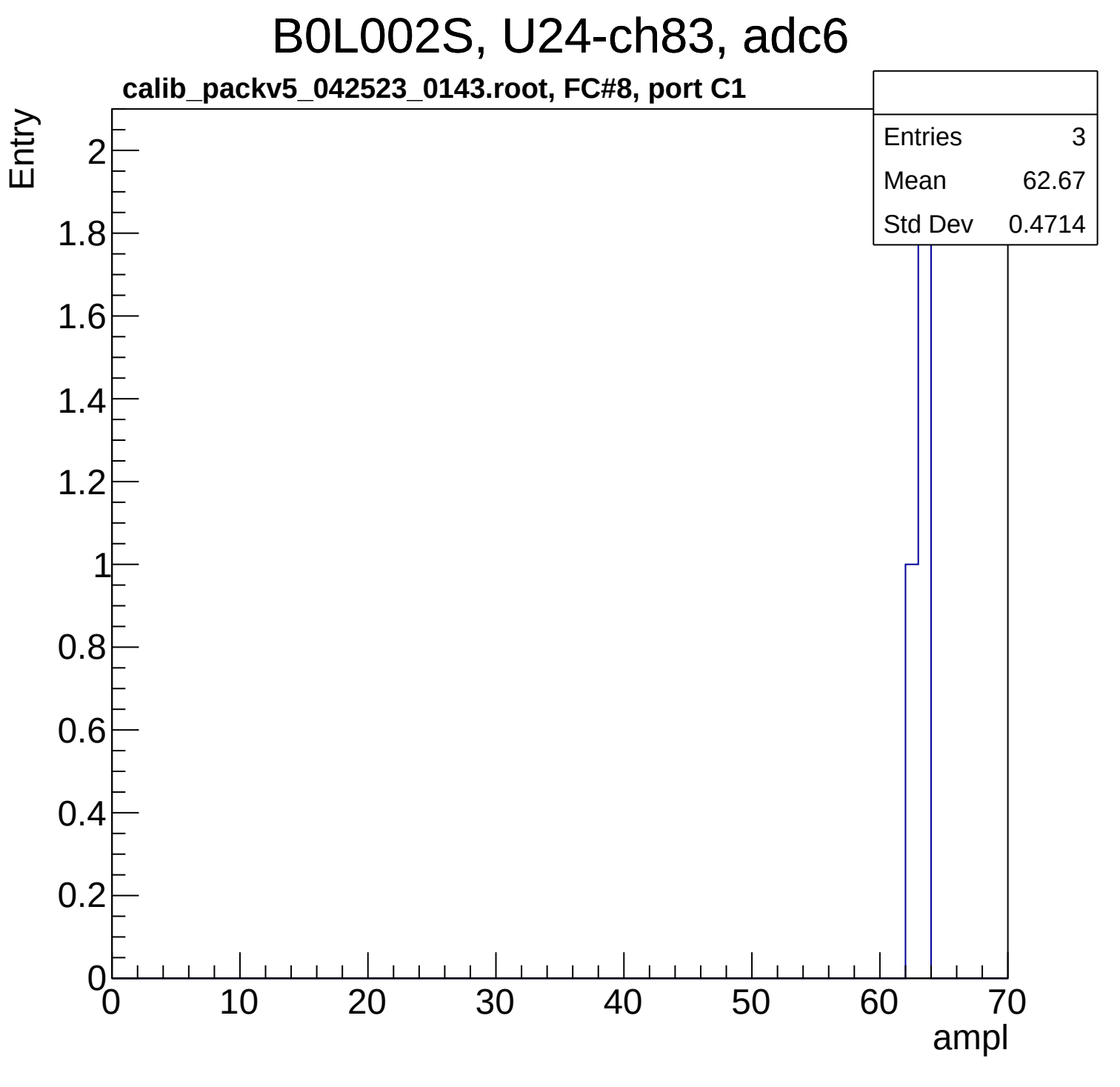
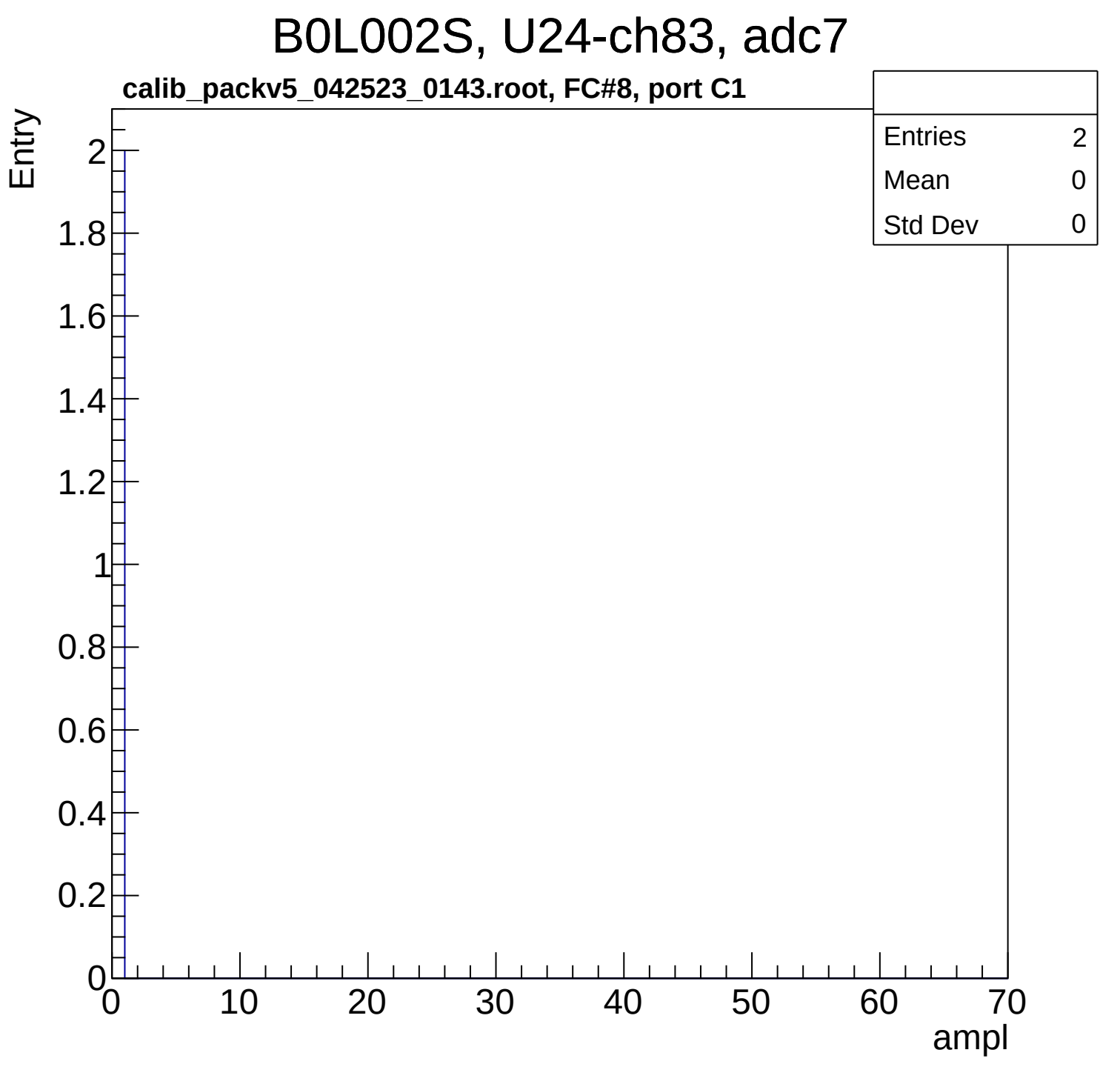
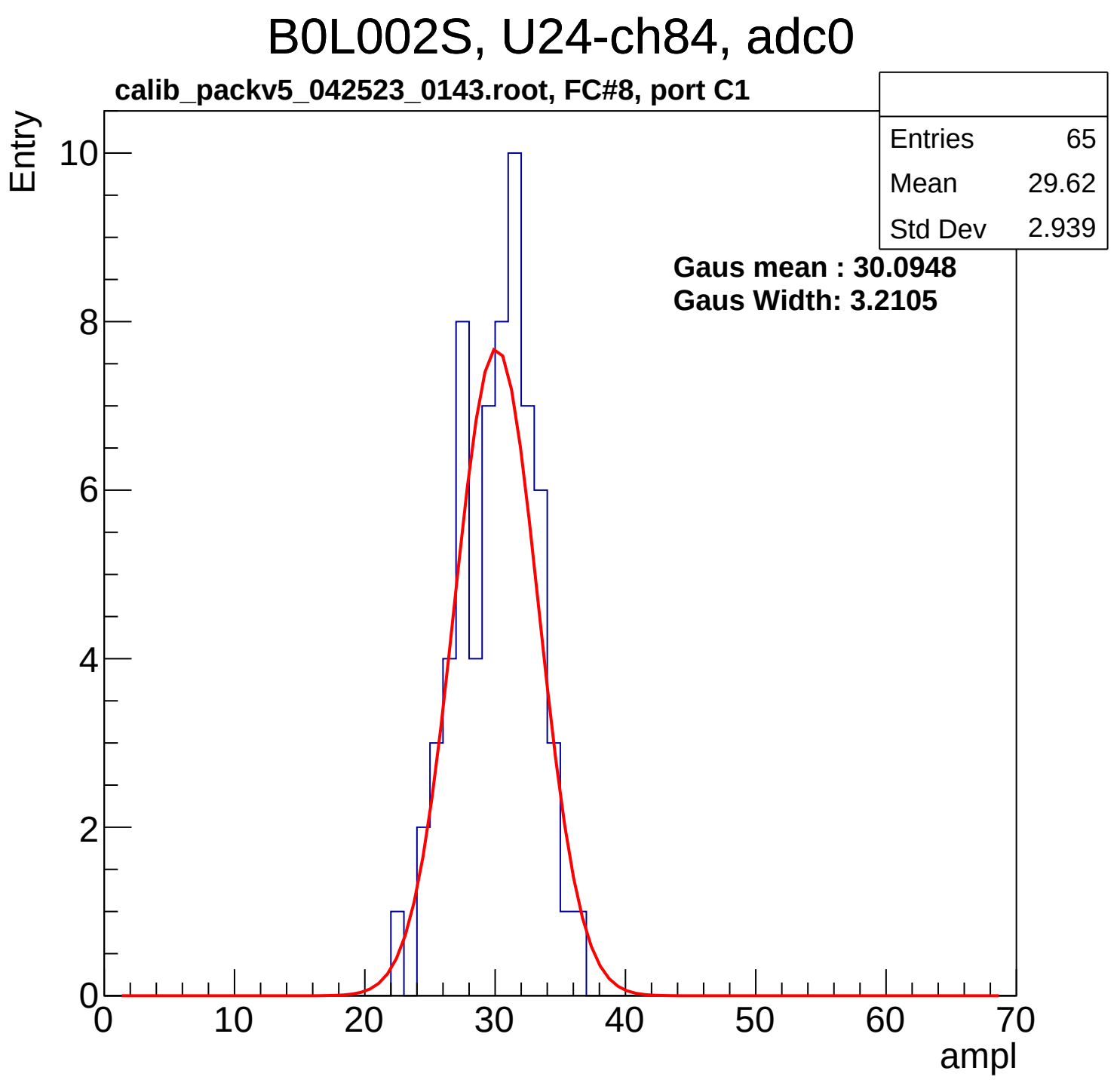
40
50
60
70

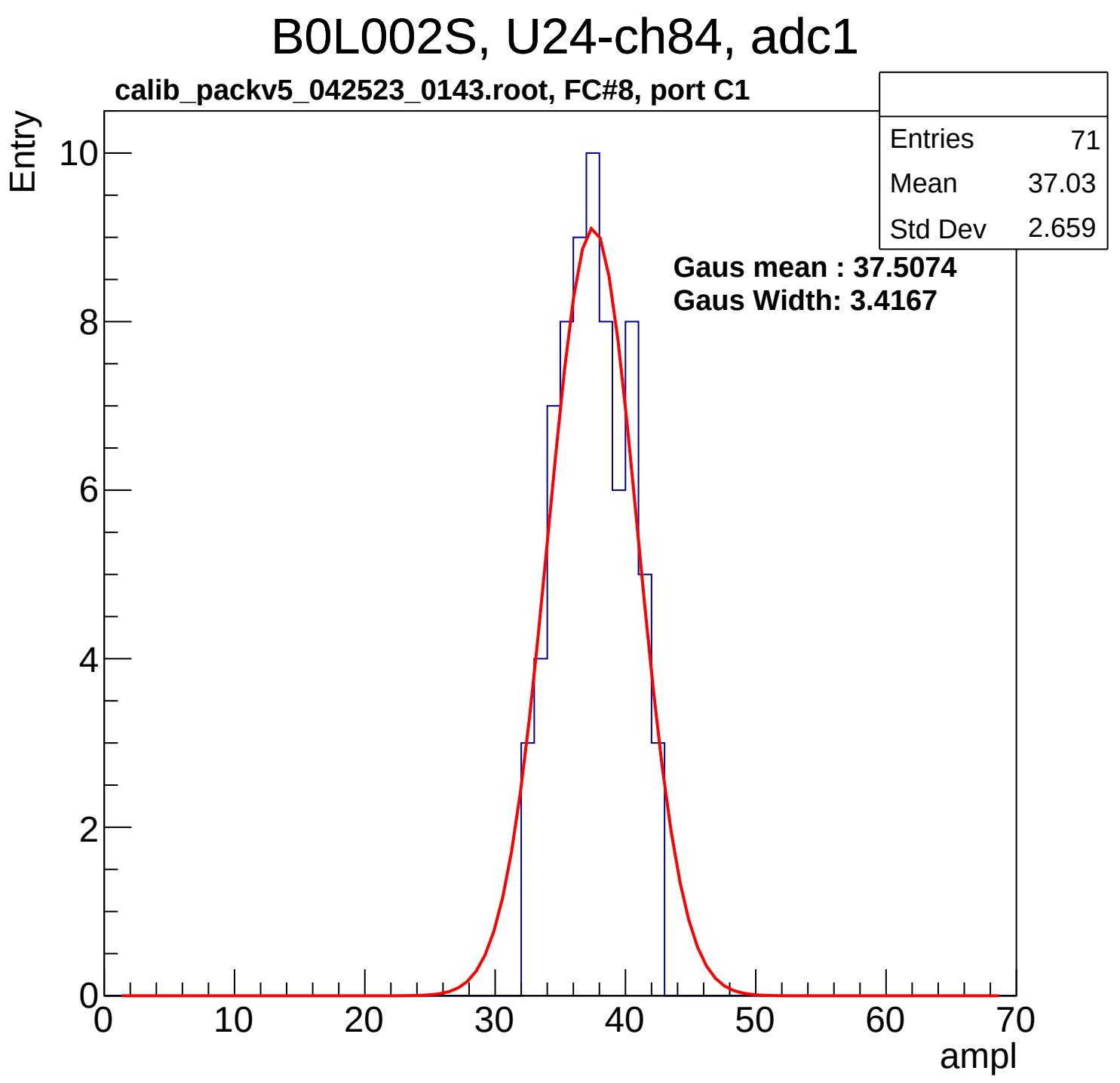

B0L002S, U24-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.52
Std Dev 8.131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

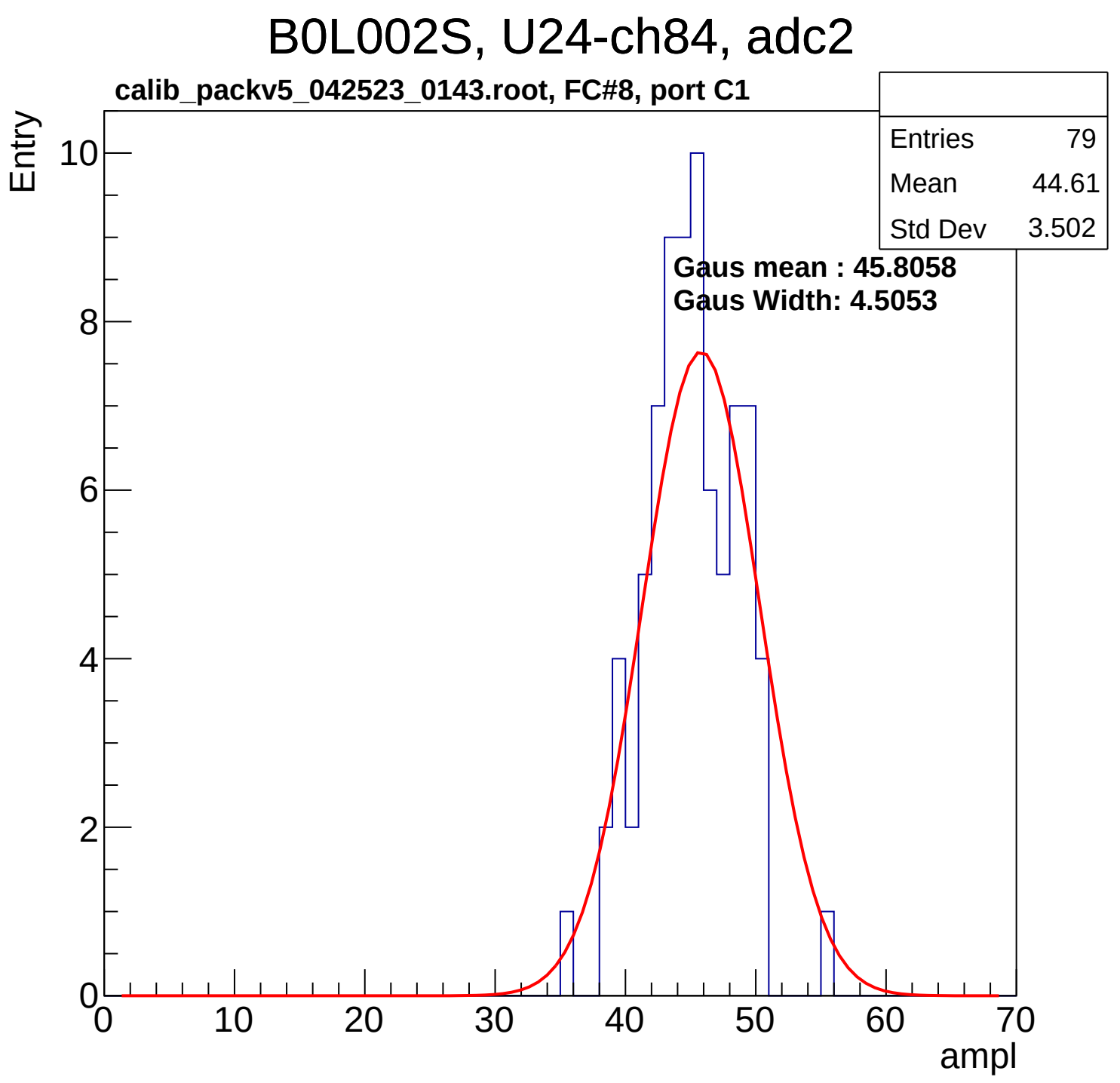

B0L002S, U24-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 60.96
Std Dev 1.856
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.62
Std Dev 2.939
Gaus mean : 30.0948
Gaus Width: 3.2105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.03
Std Dev 2.659
Gaus mean : 37.5074
Gaus Width: 3.4167
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 44.61
Std Dev 3.502
Gaus mean : 45.8058
Gaus Width: 4.5053
Entry
ampl
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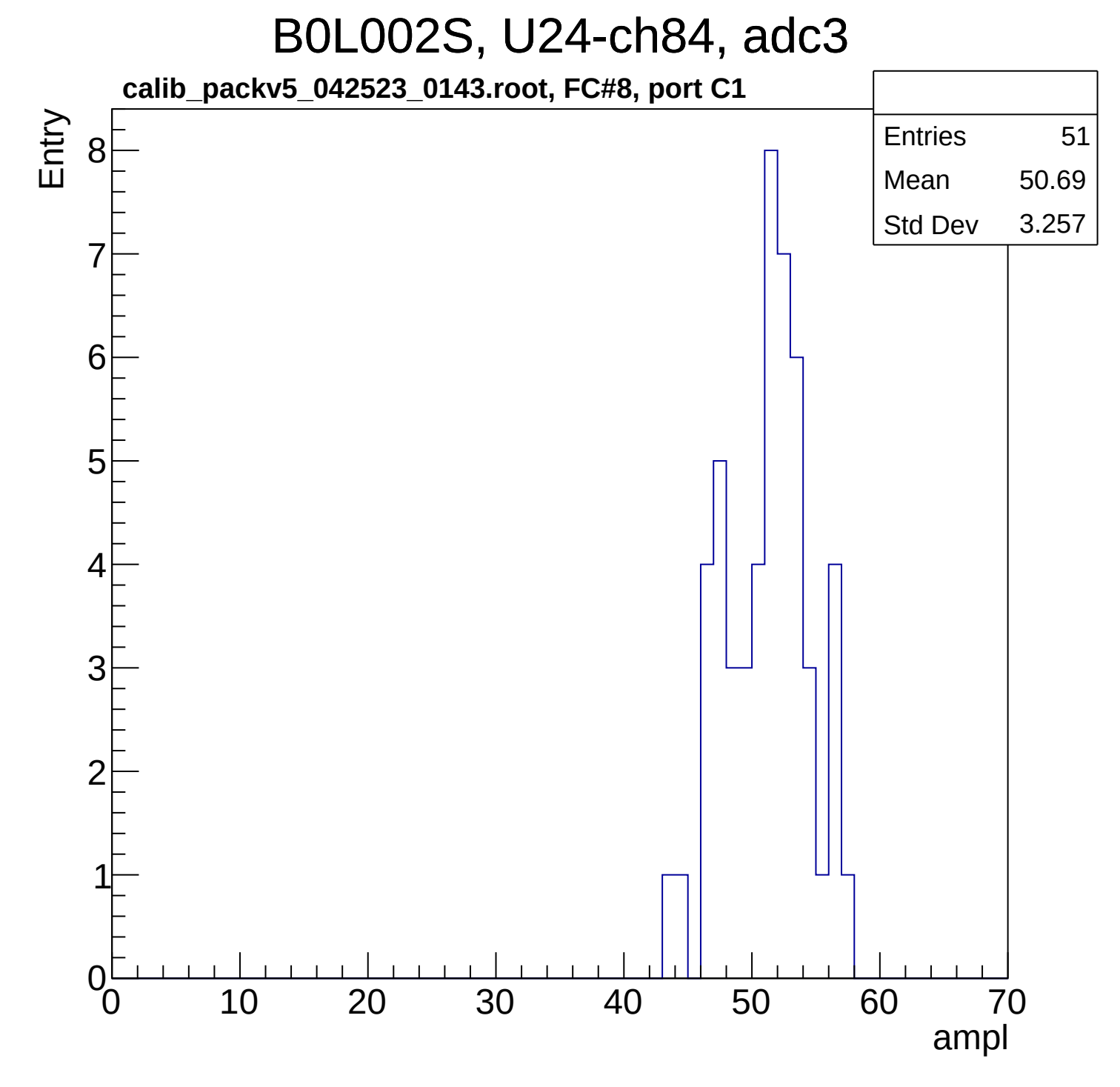
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 50.69
Std Dev 3.257
Entry
ampl
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1
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3
4
5
6
7
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0
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20
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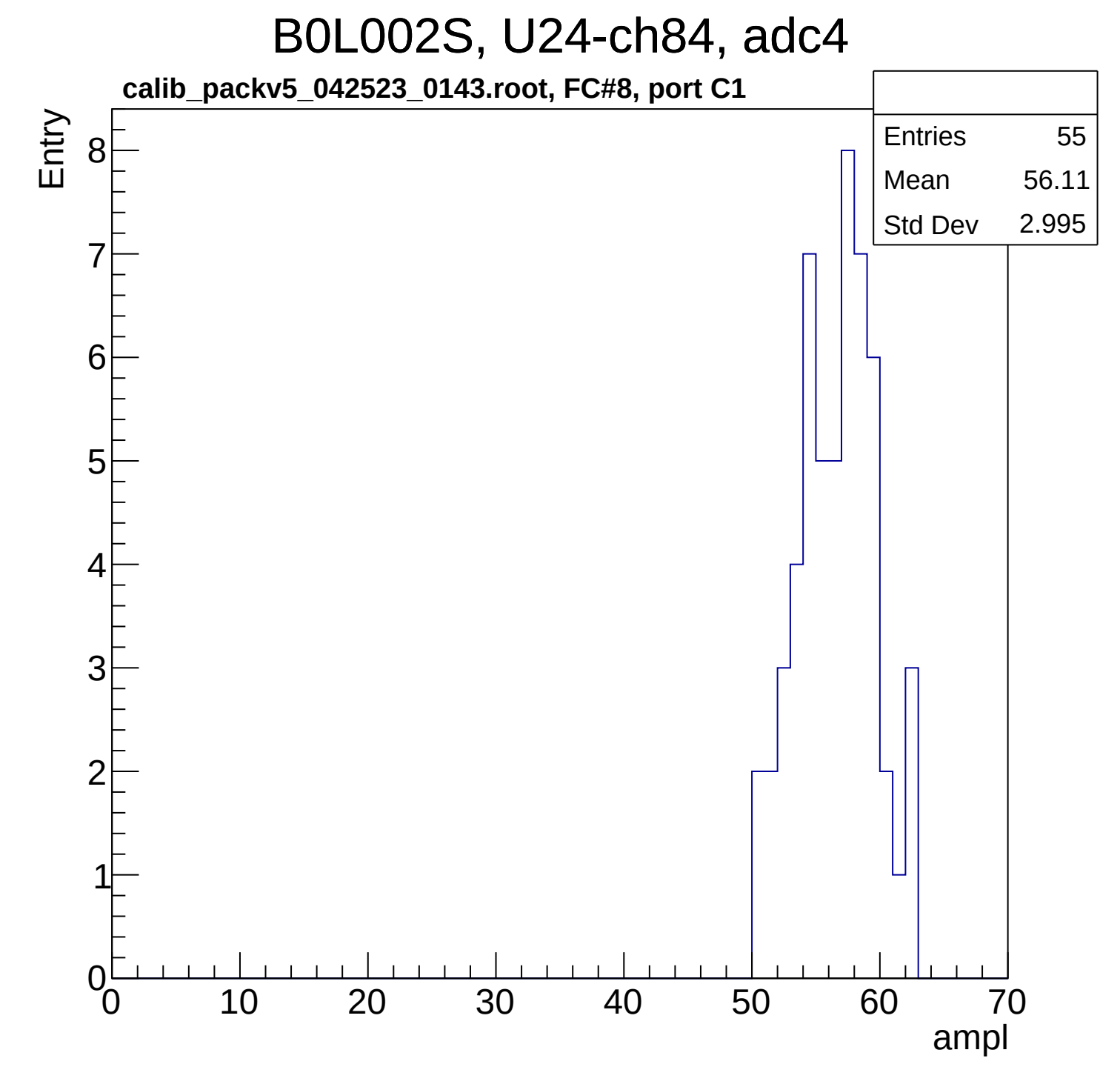
40
50
60
70


B0L002S, U24-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.11
Std Dev 2.995
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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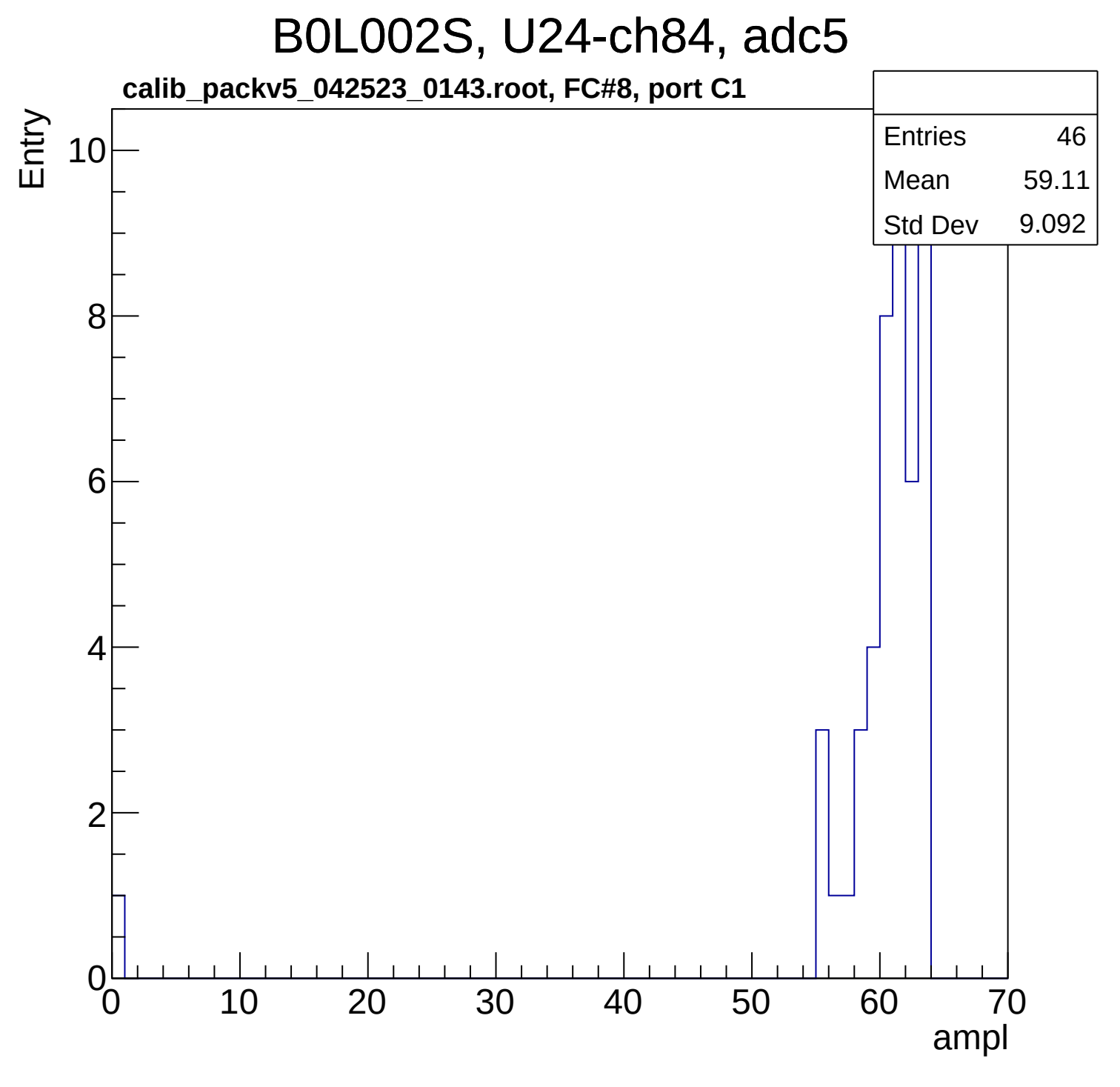
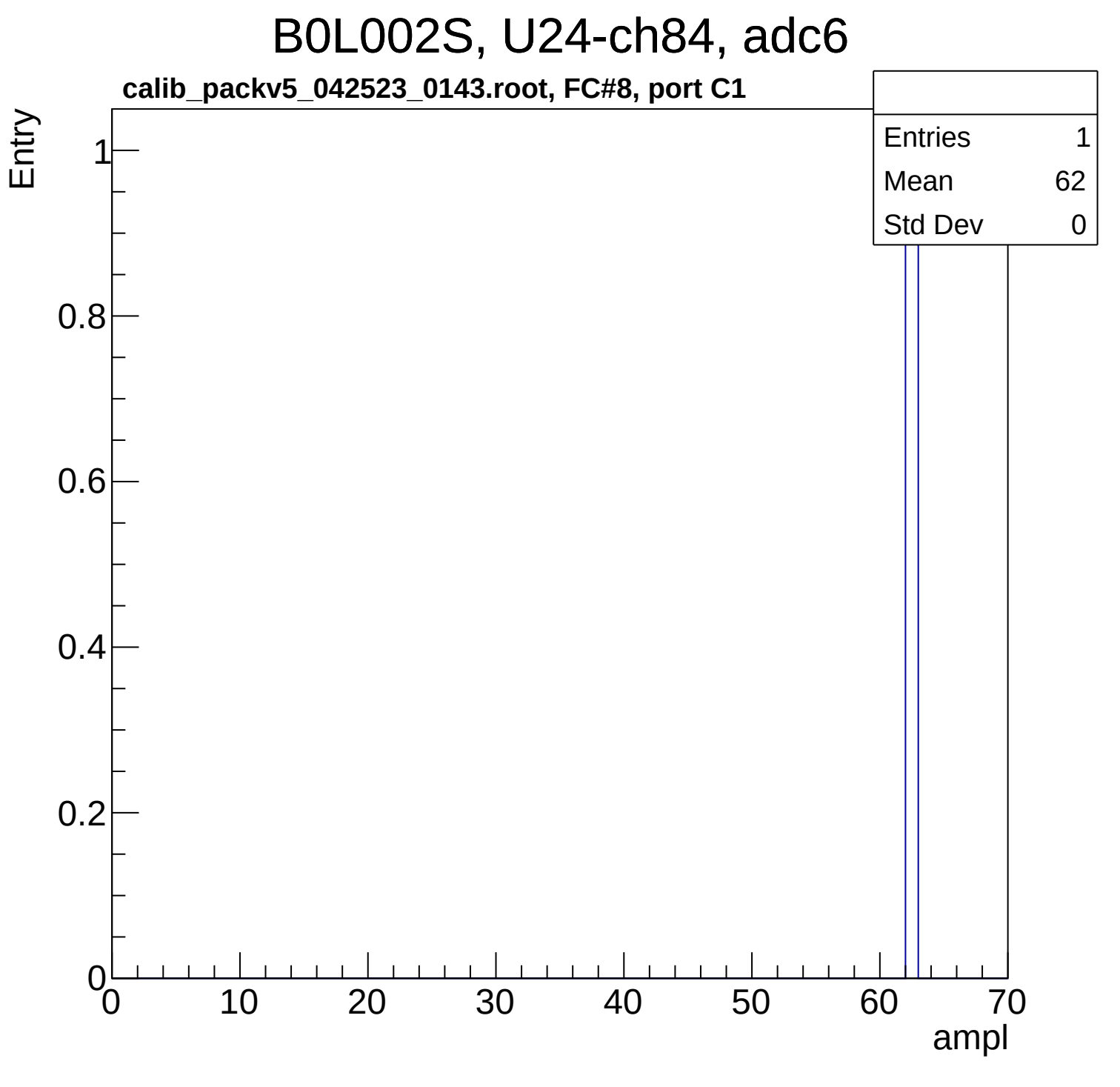
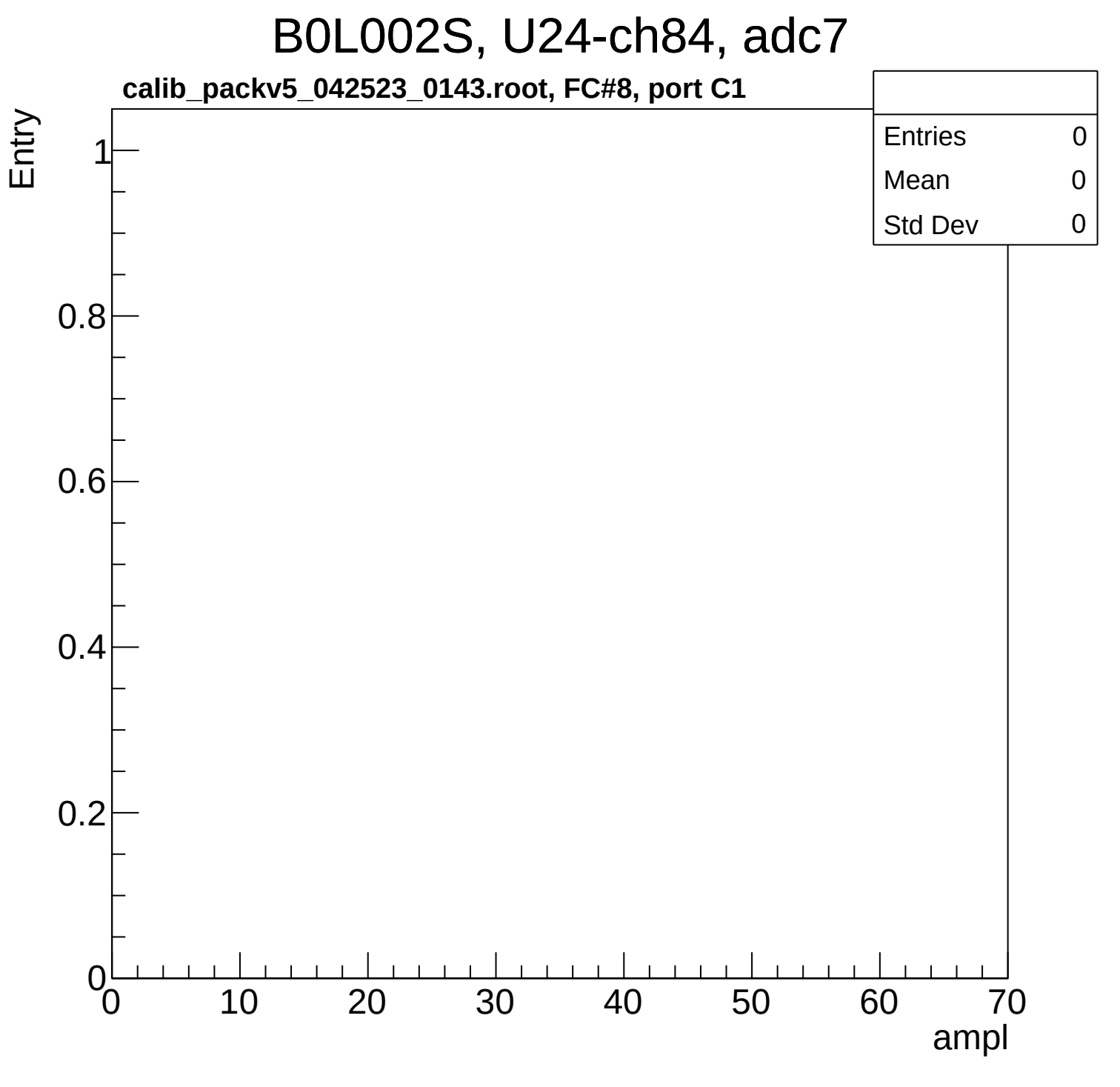
40
50
60
70


B0L002S, U24-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.11
Std Dev 9.092
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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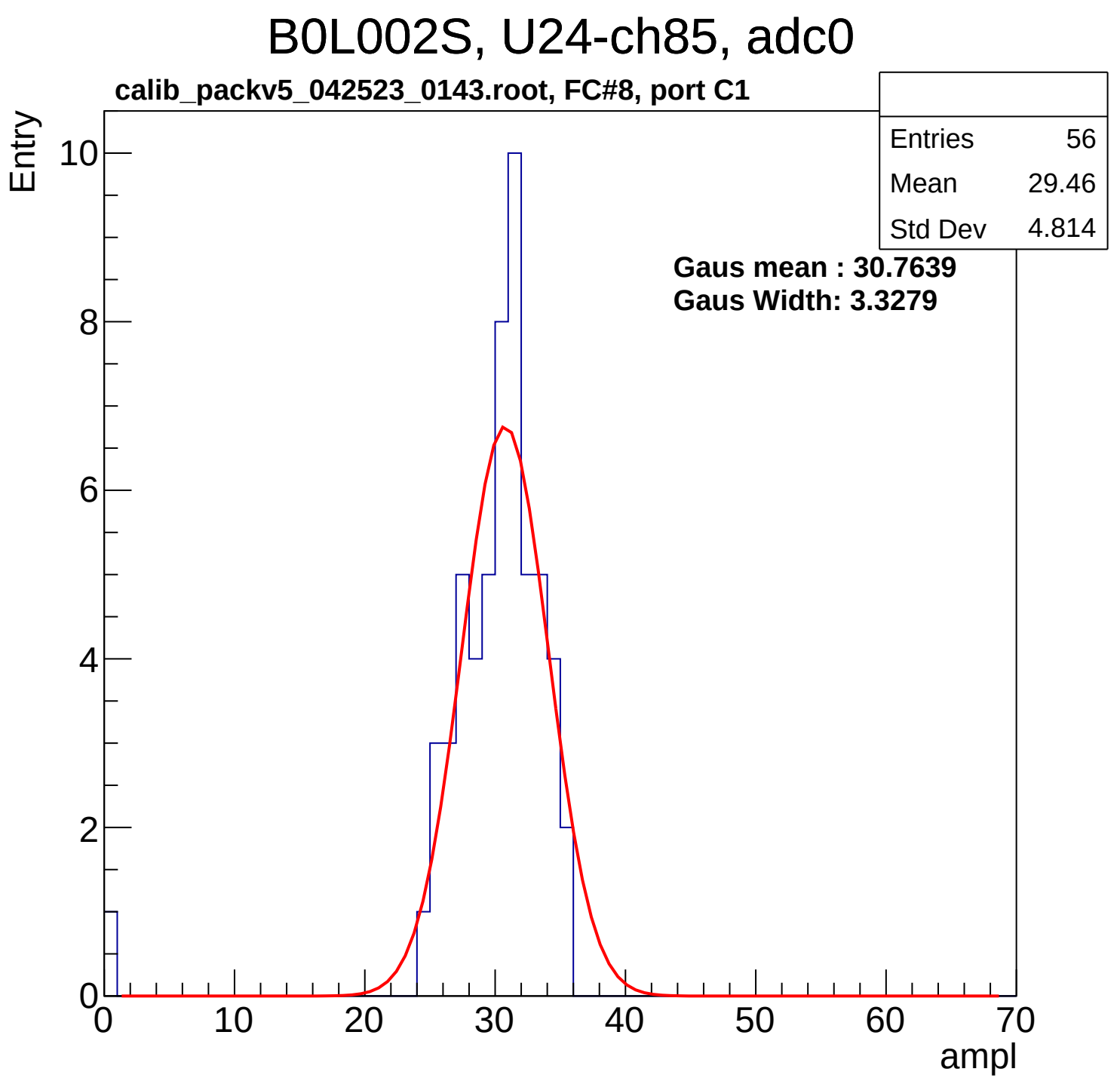
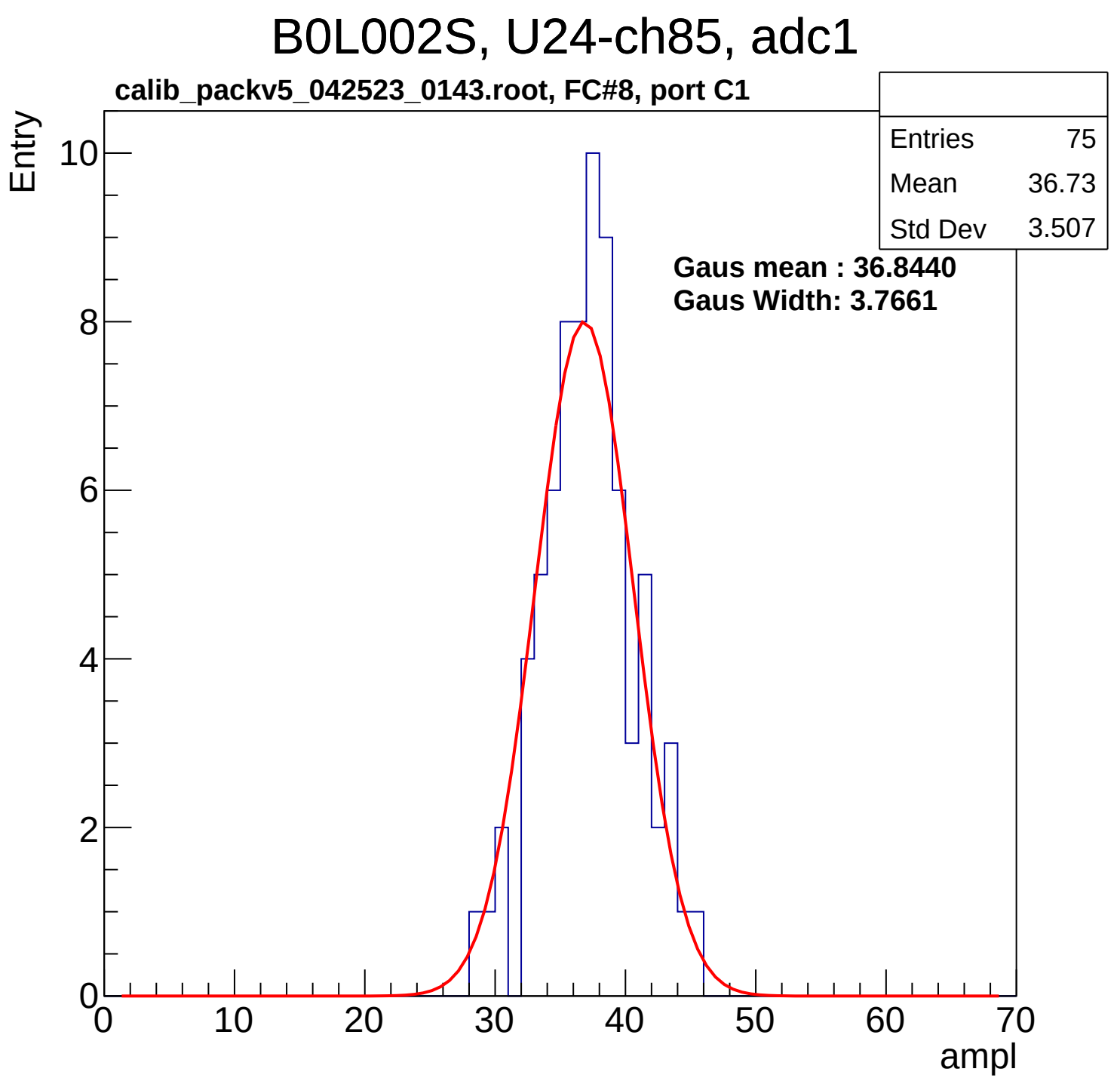
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 29.46
Std Dev 4.814
Gaus mean : 30.7639
Gaus Width: 3.3279
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 36.73
Std Dev 3.507
Gaus mean : 36.8440
Gaus Width: 3.7661
Entry
ampl
0
2
4
6
8
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60
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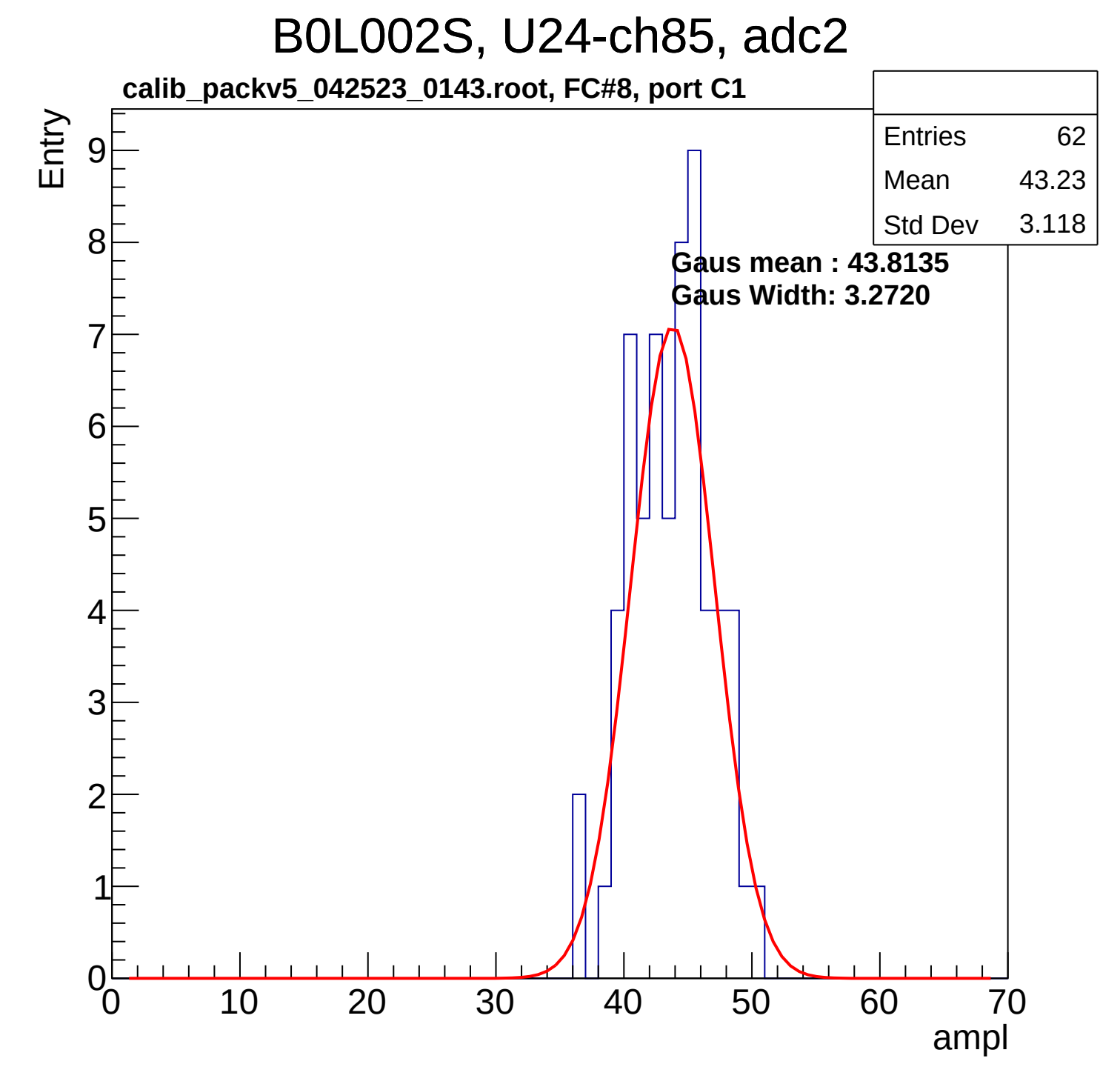
B0L002S, U24-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 43.23
Std Dev 3.118
Gaus mean : 43.8135
Gaus Width: 3.2720
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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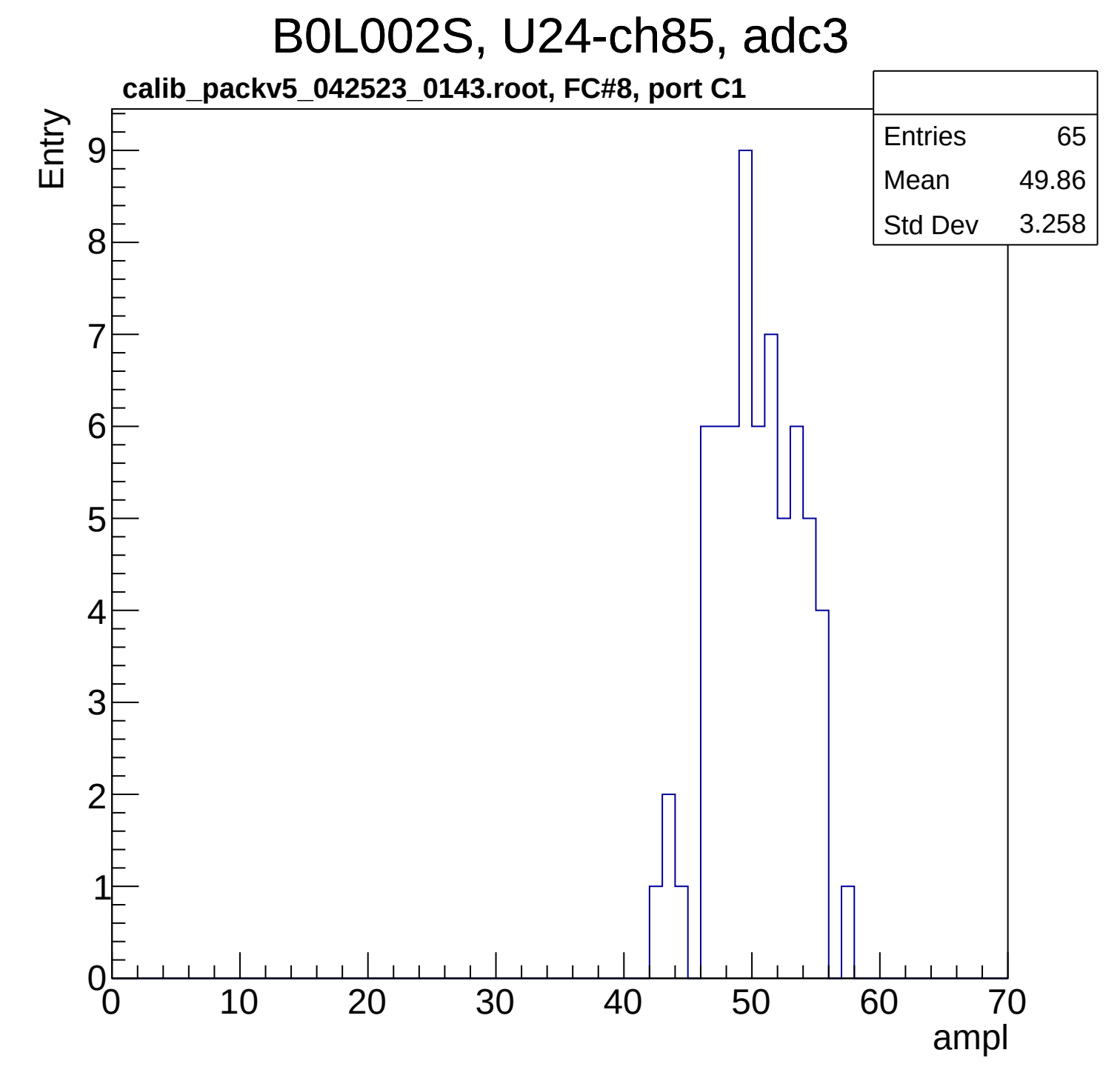
B0L002S, U24-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.86
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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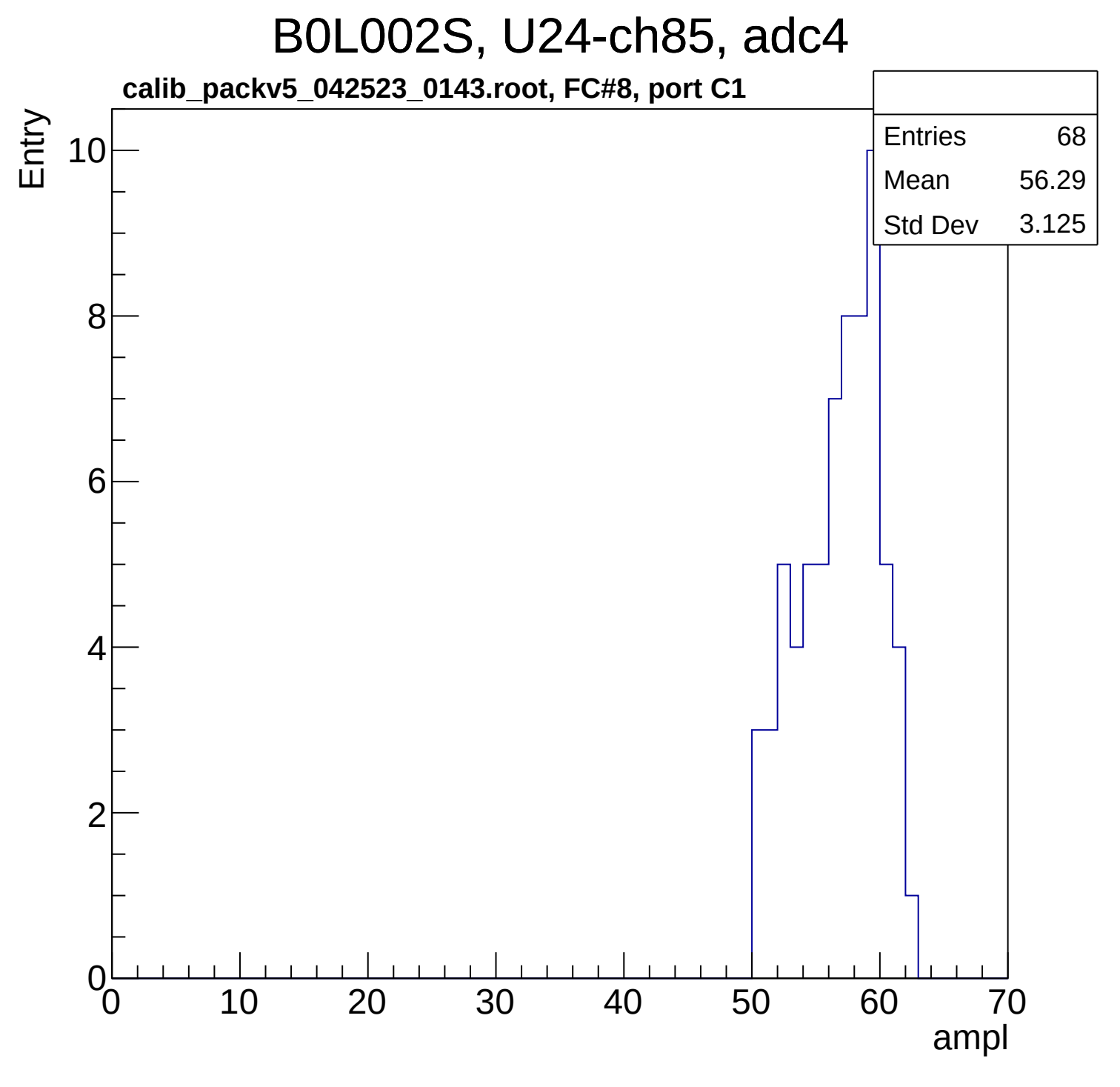
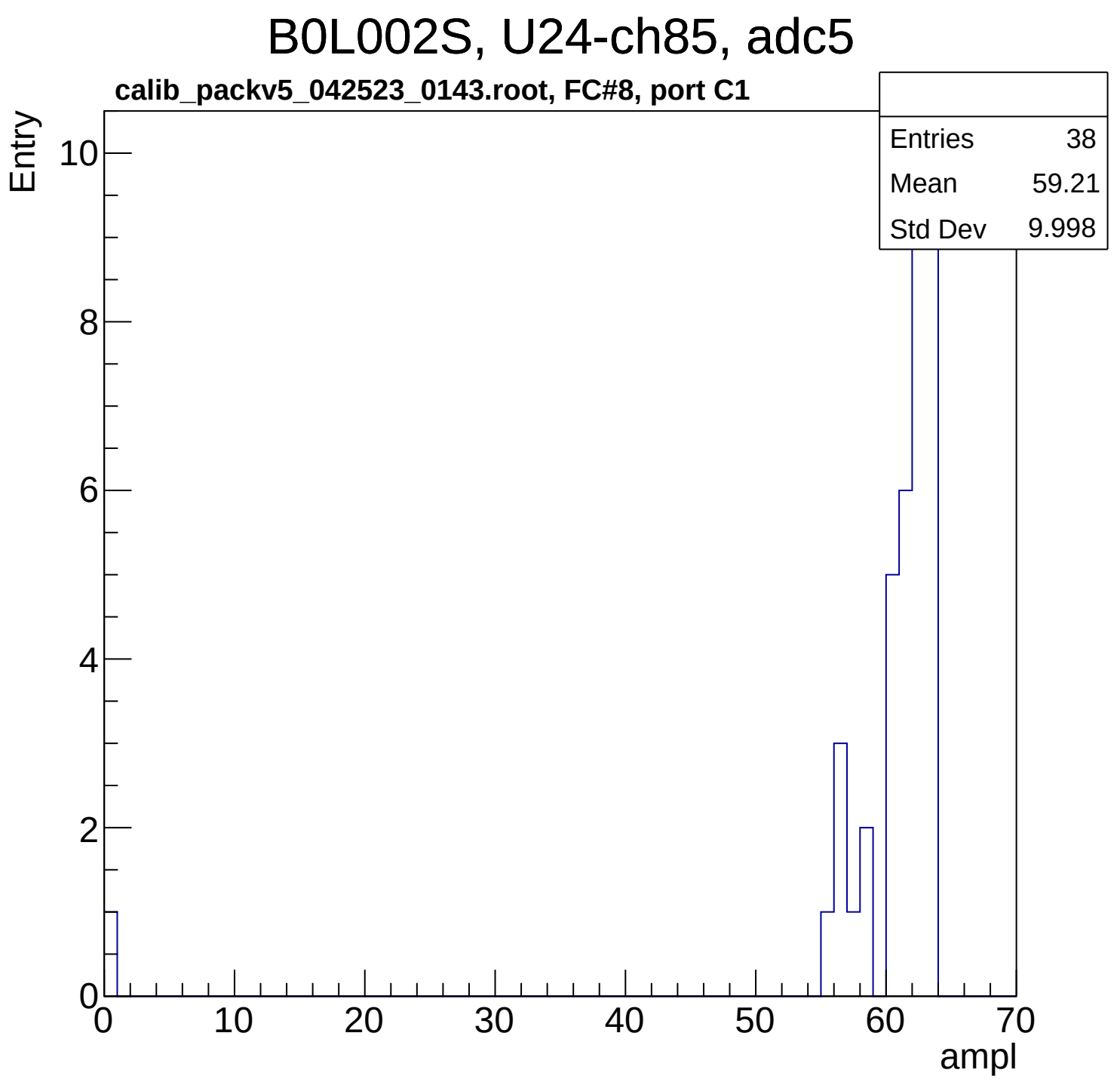
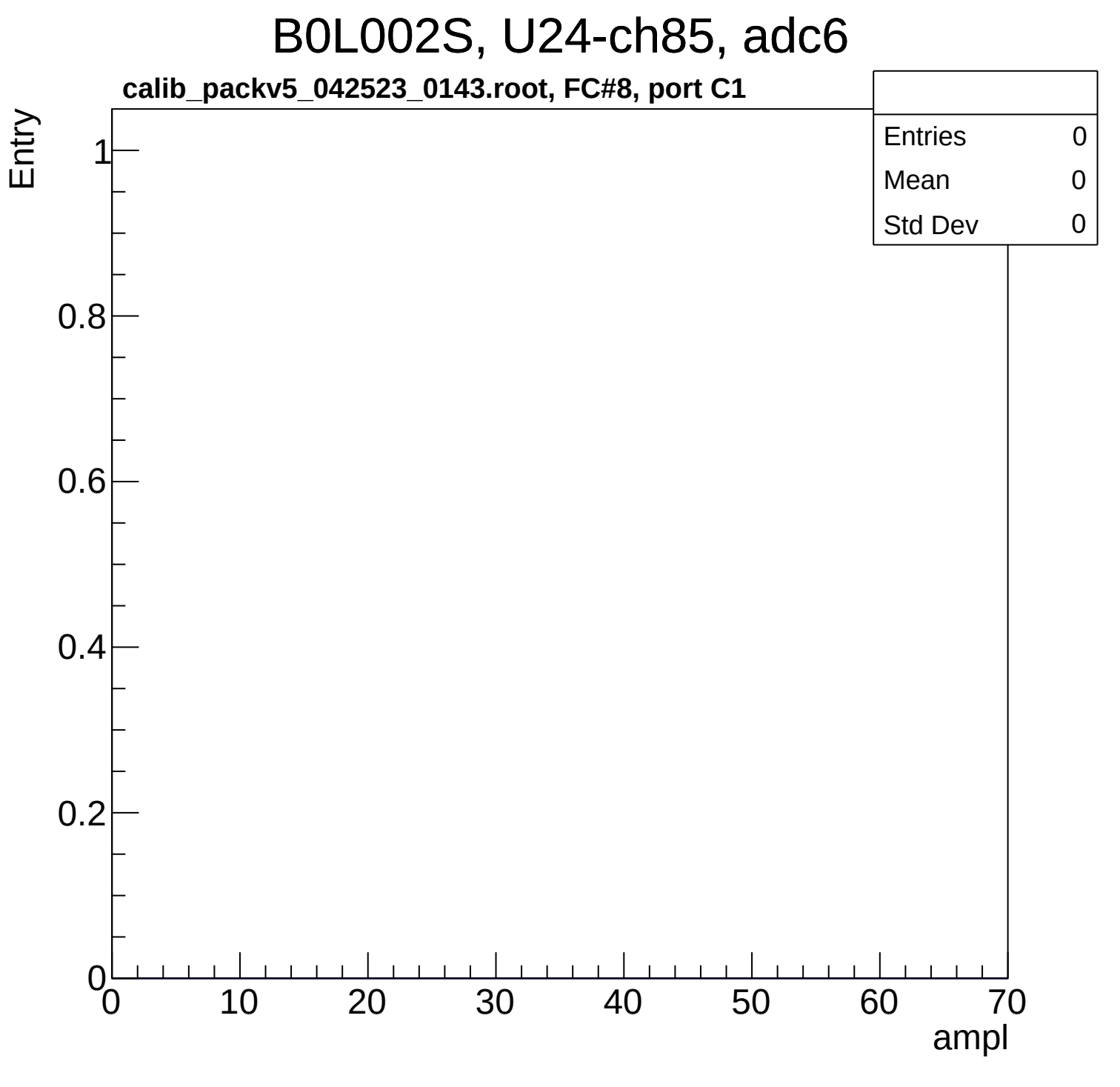
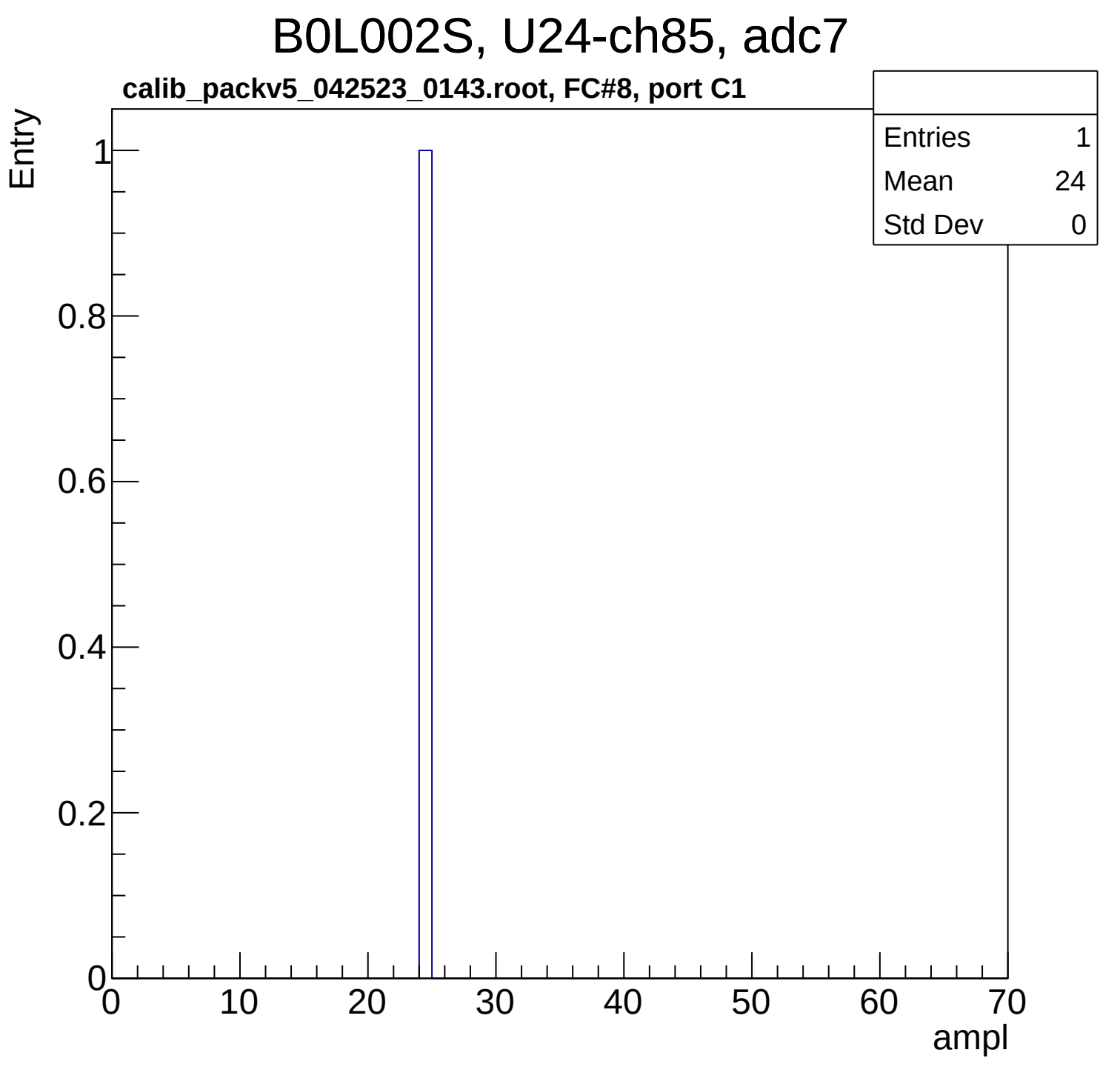
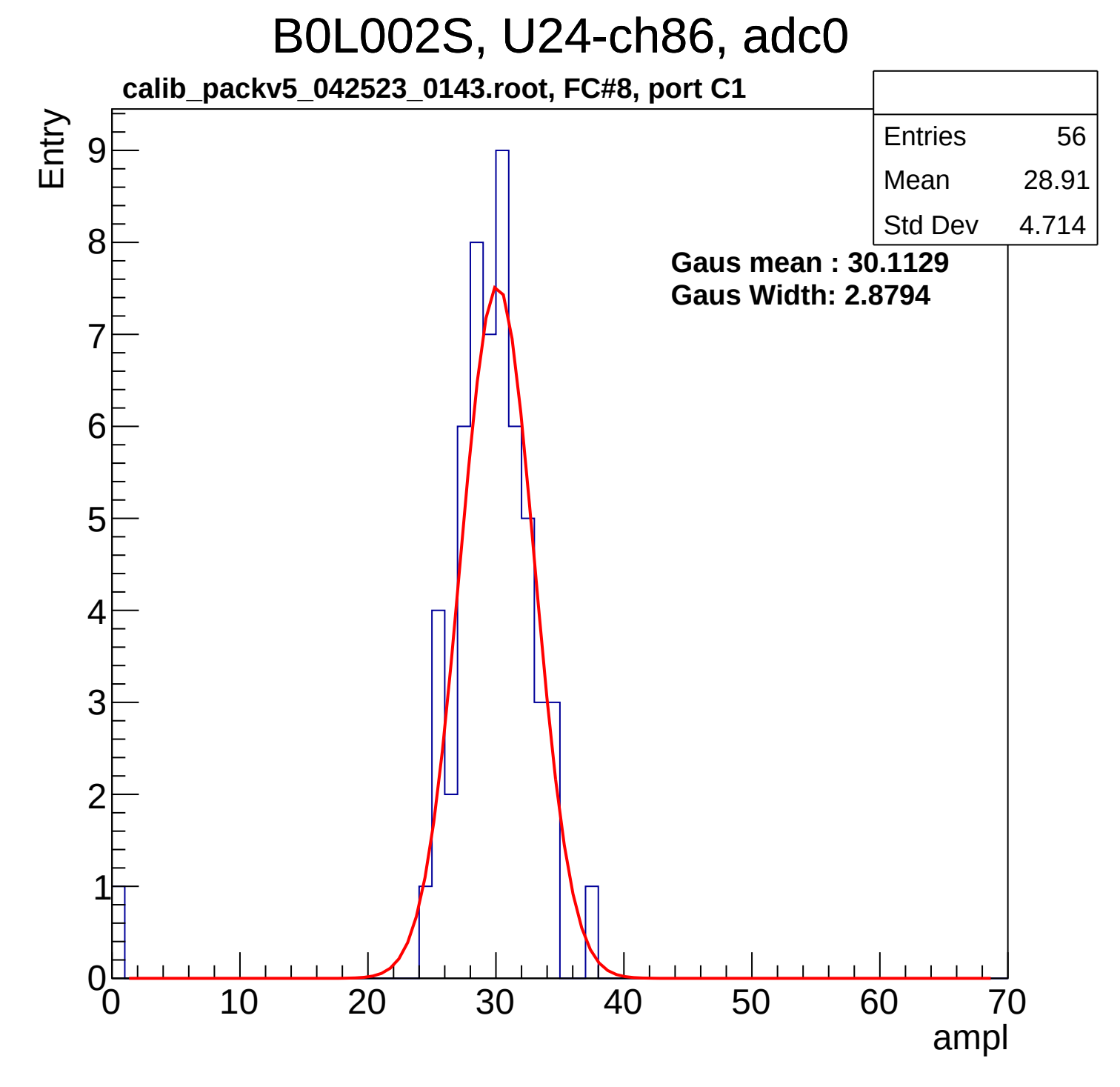
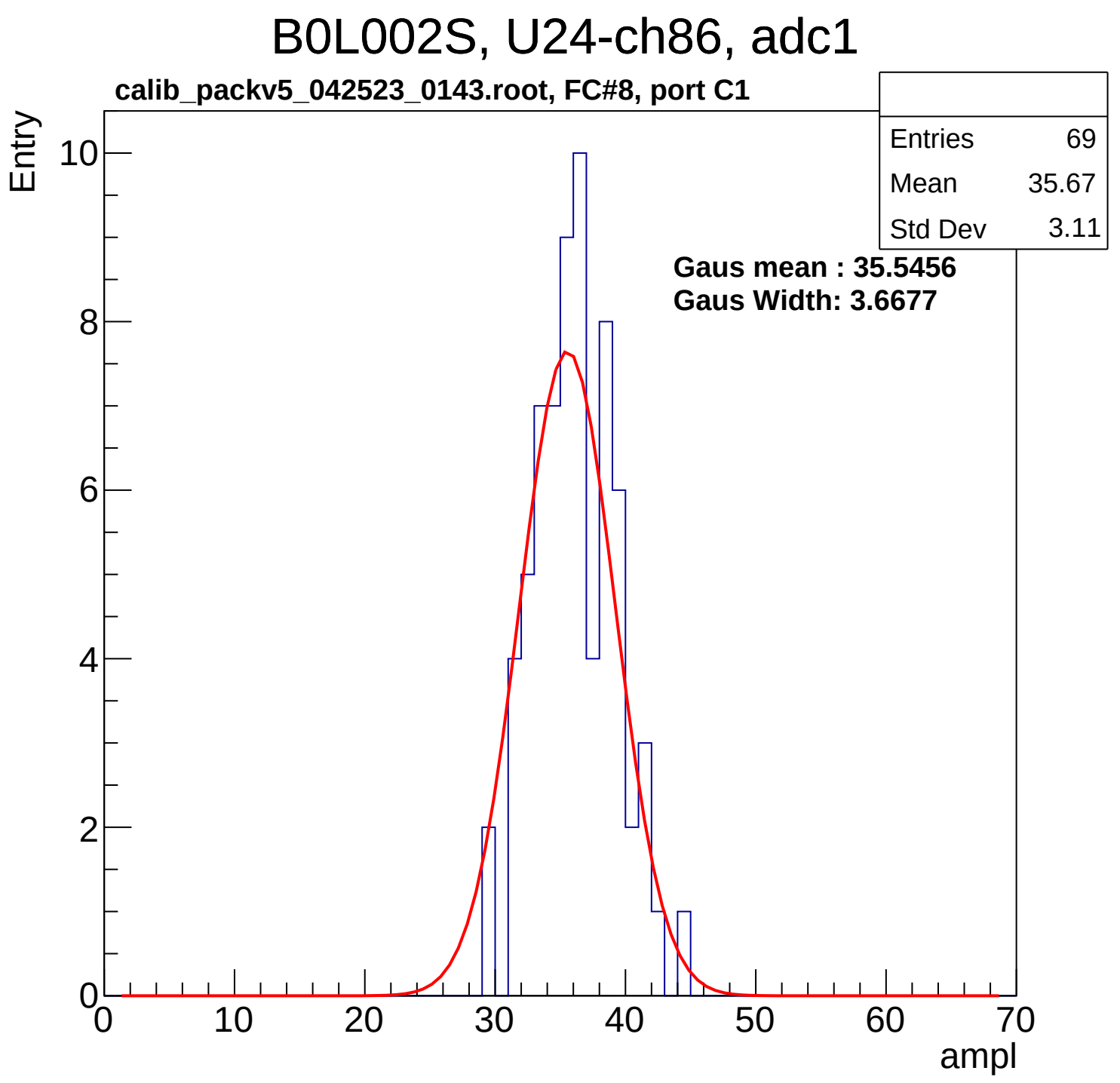

B0L002S, U24-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.29
Std Dev 3.125
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

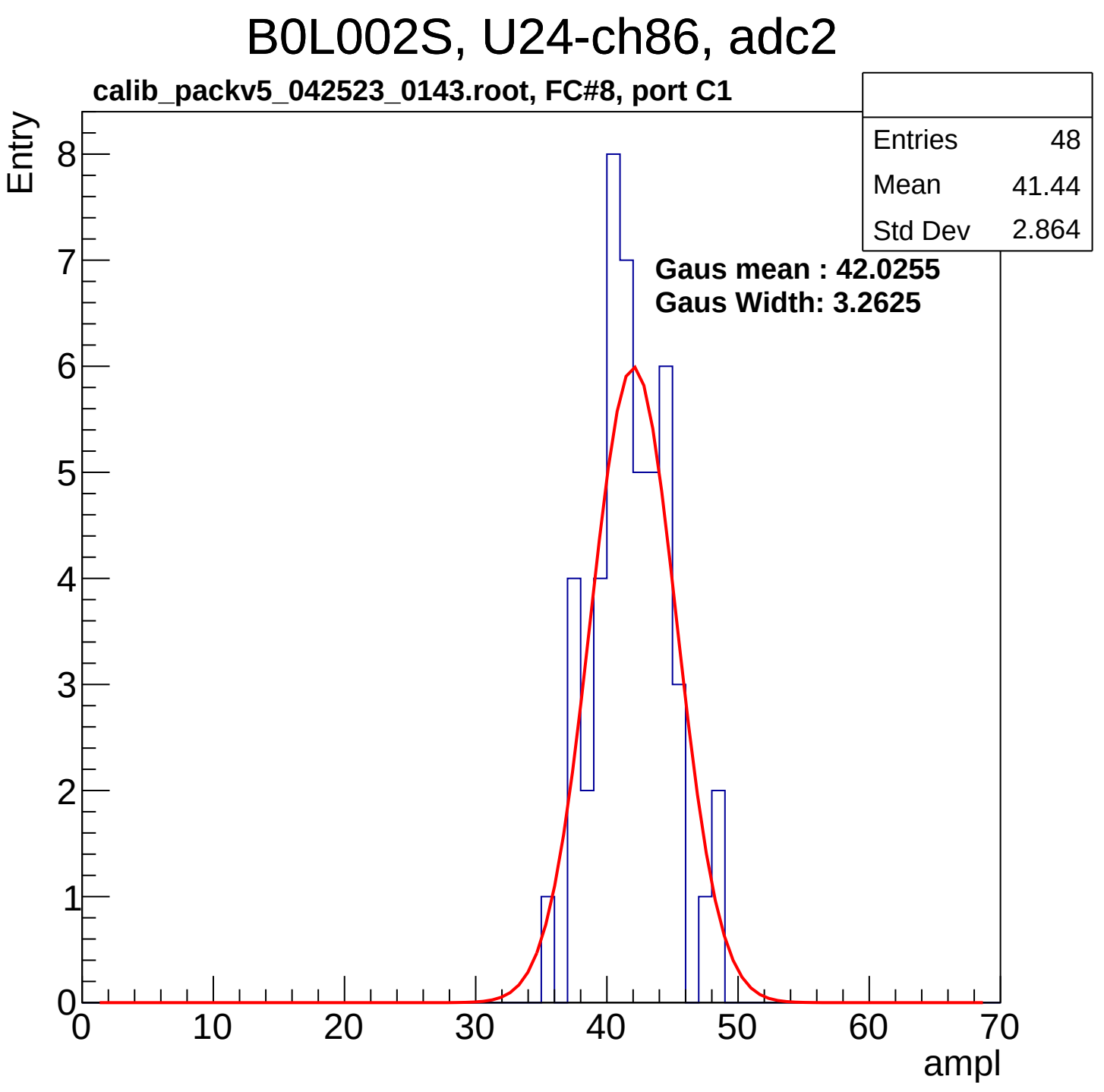

B0L002S, U24-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.21
Std Dev 9.998
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

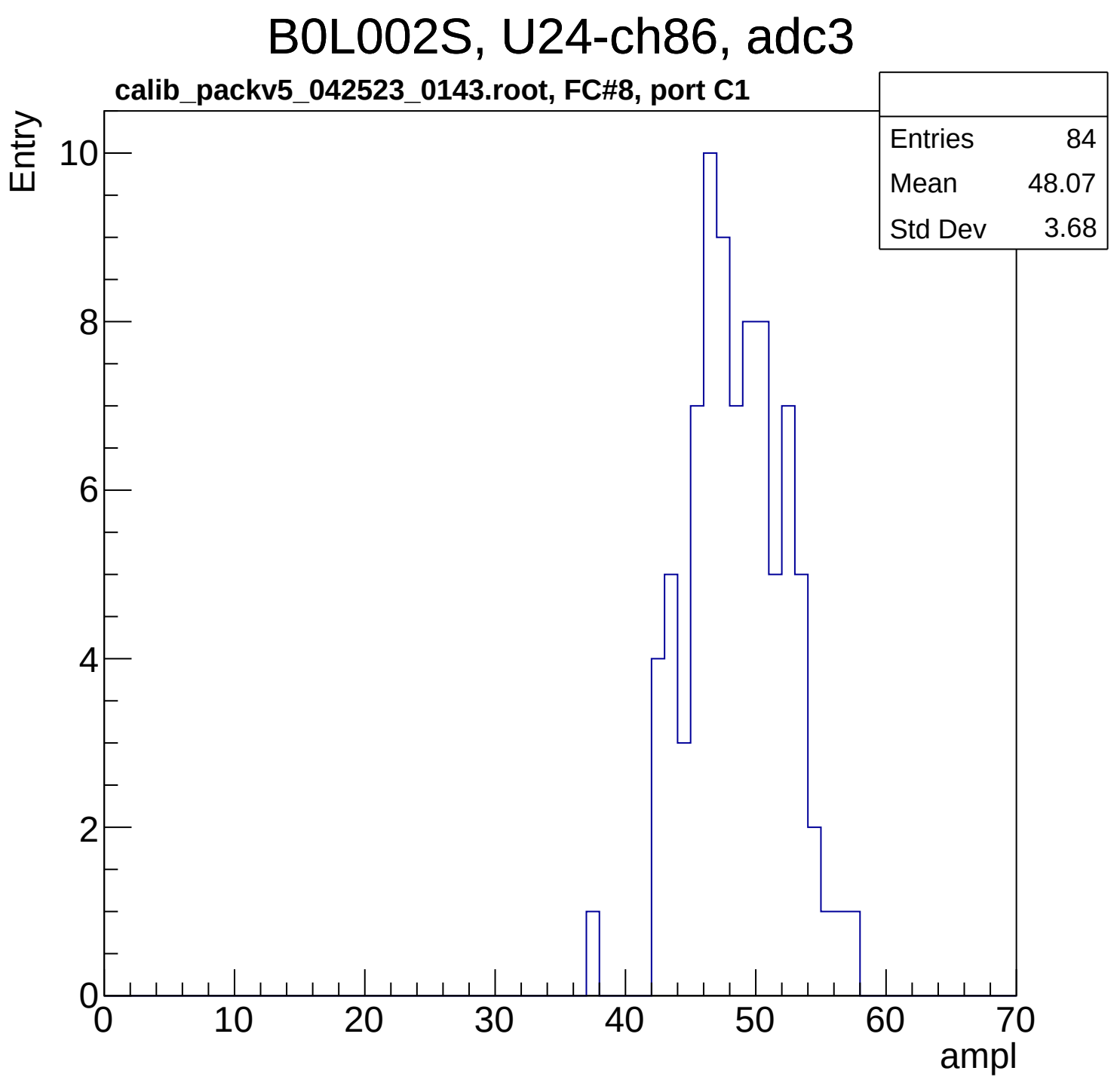

B0L002S, U24-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

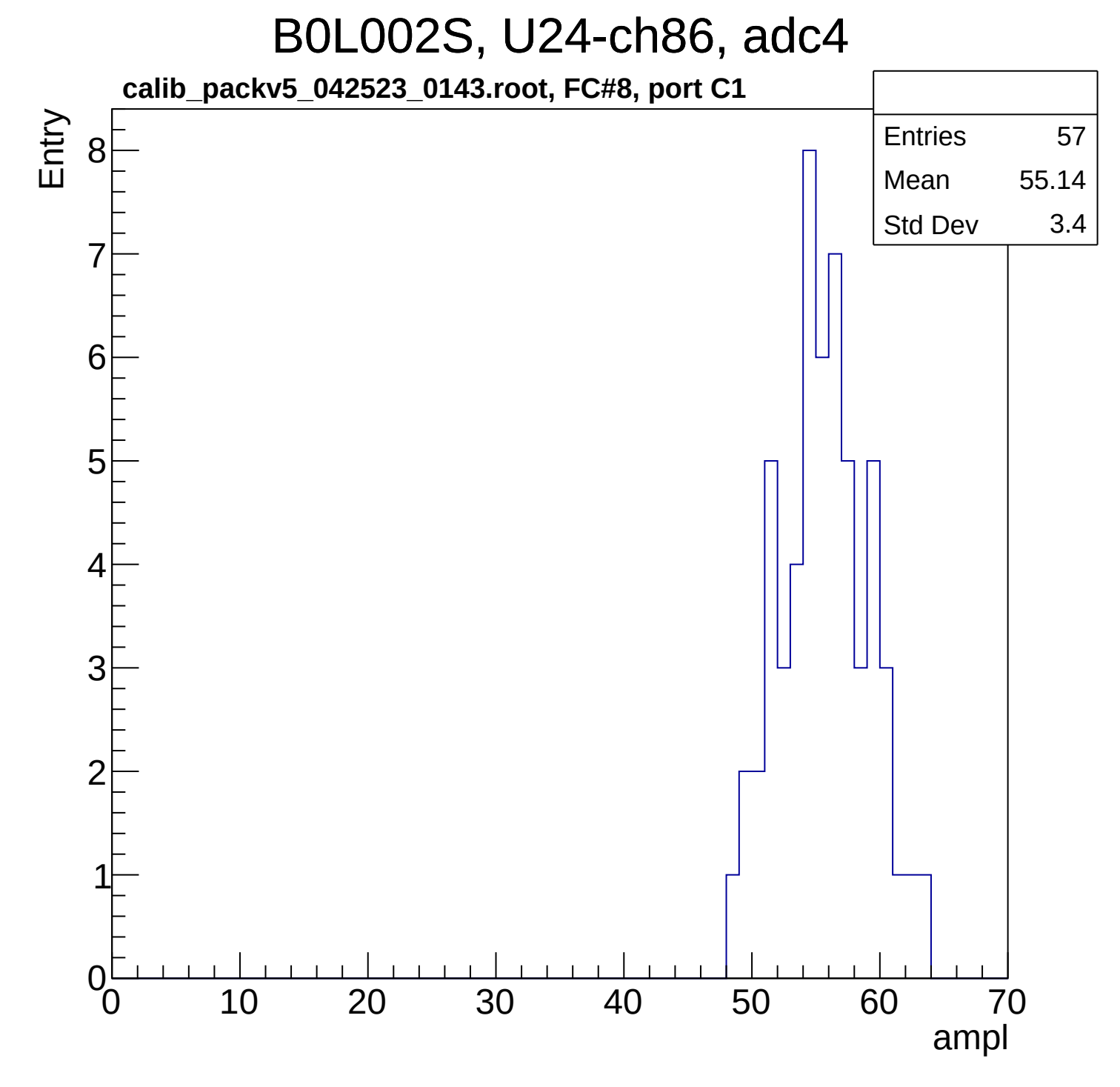

B0L002S, U24-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

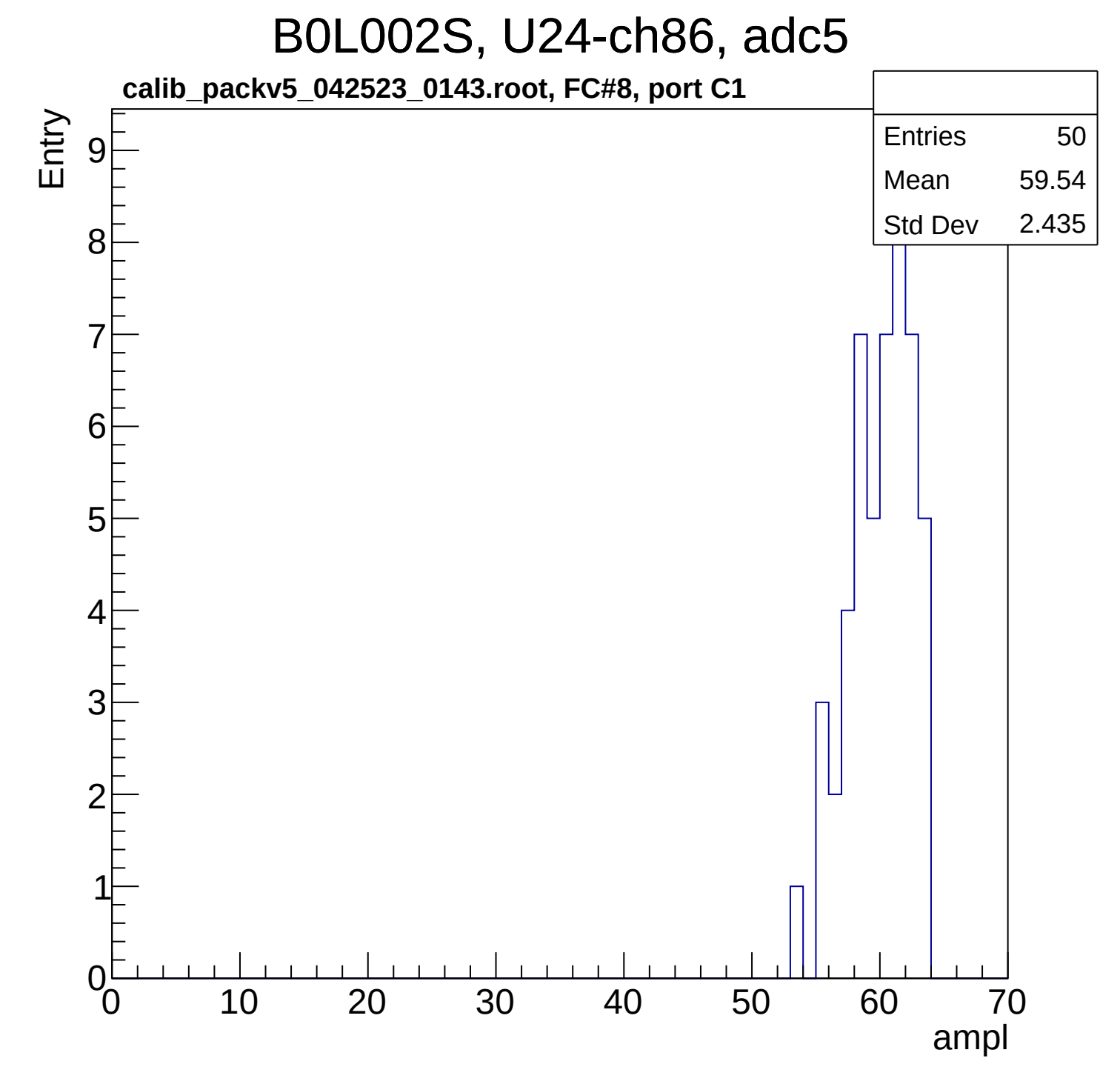

B0L002S, U24-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 28.91
Std Dev 4.714
Gaus mean : 30.1129
Gaus Width: 2.8794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 35.67
Std Dev 3.11
Gaus mean : 35.5456
Gaus Width: 3.6677
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 41.44
Std Dev 2.864
Gaus mean : 42.0255
Gaus Width: 3.2625
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 48.07
Std Dev 3.68
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.14
Std Dev 3.4
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 59.54
Std Dev 2.435
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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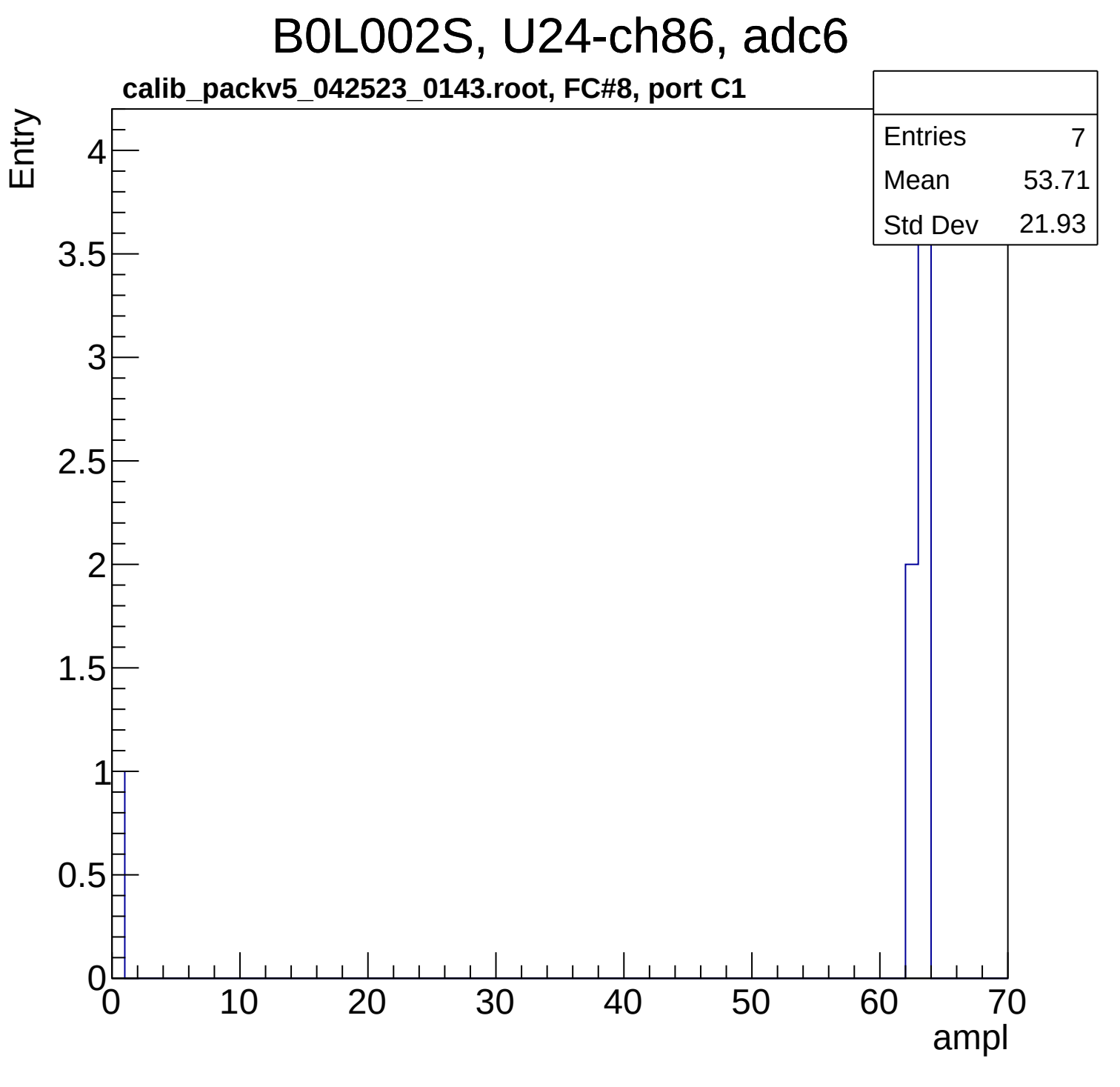
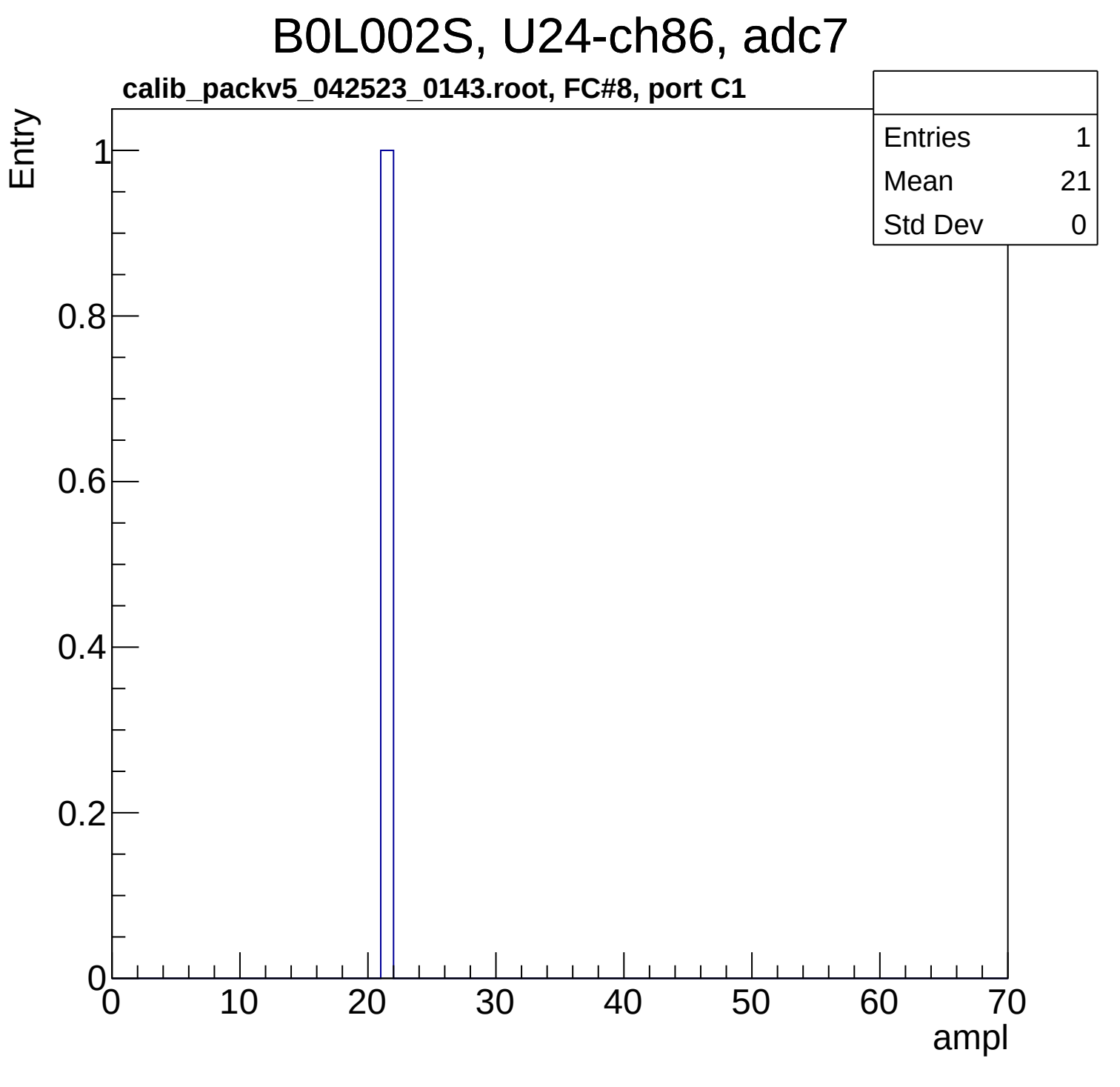
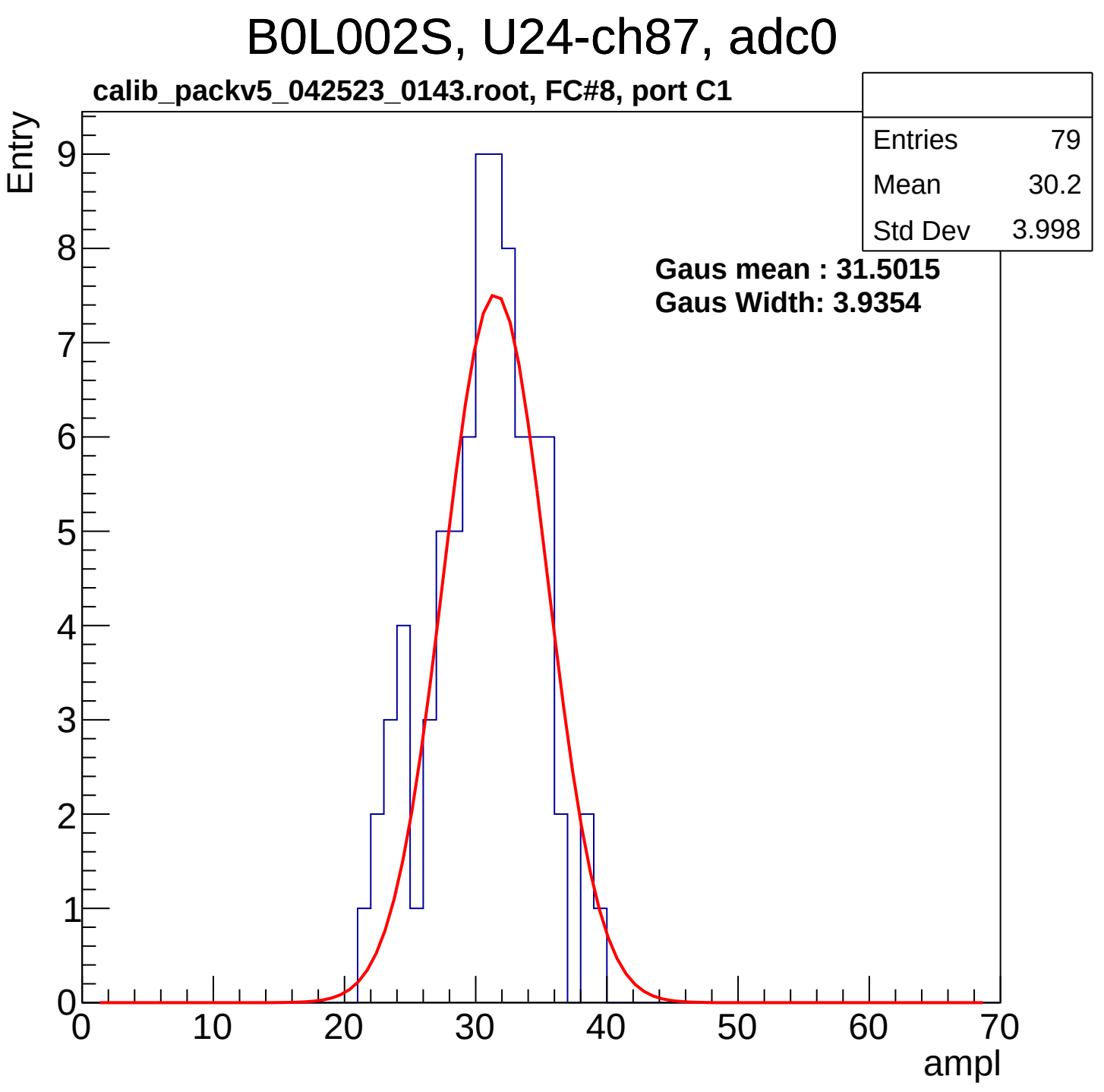
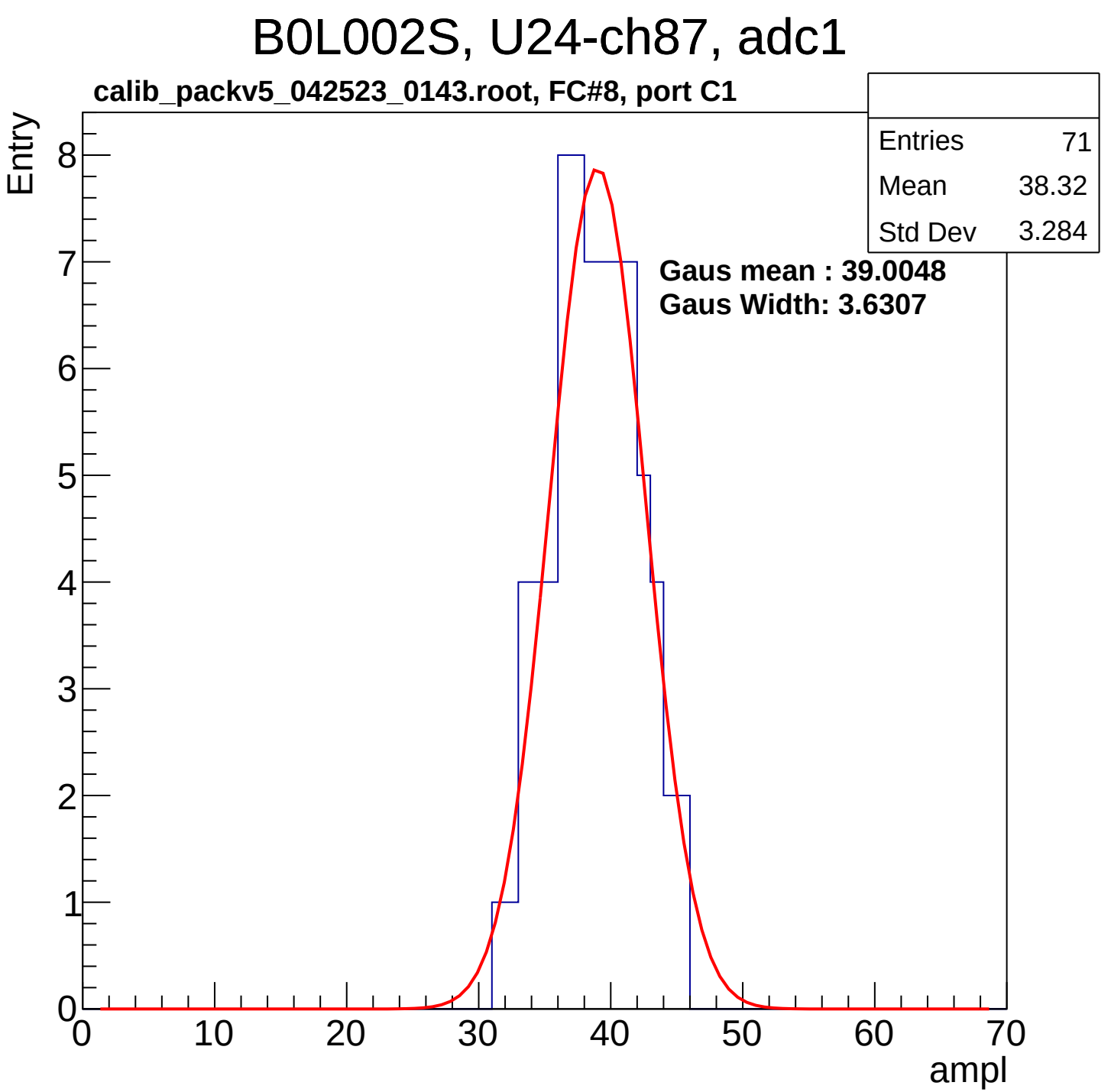
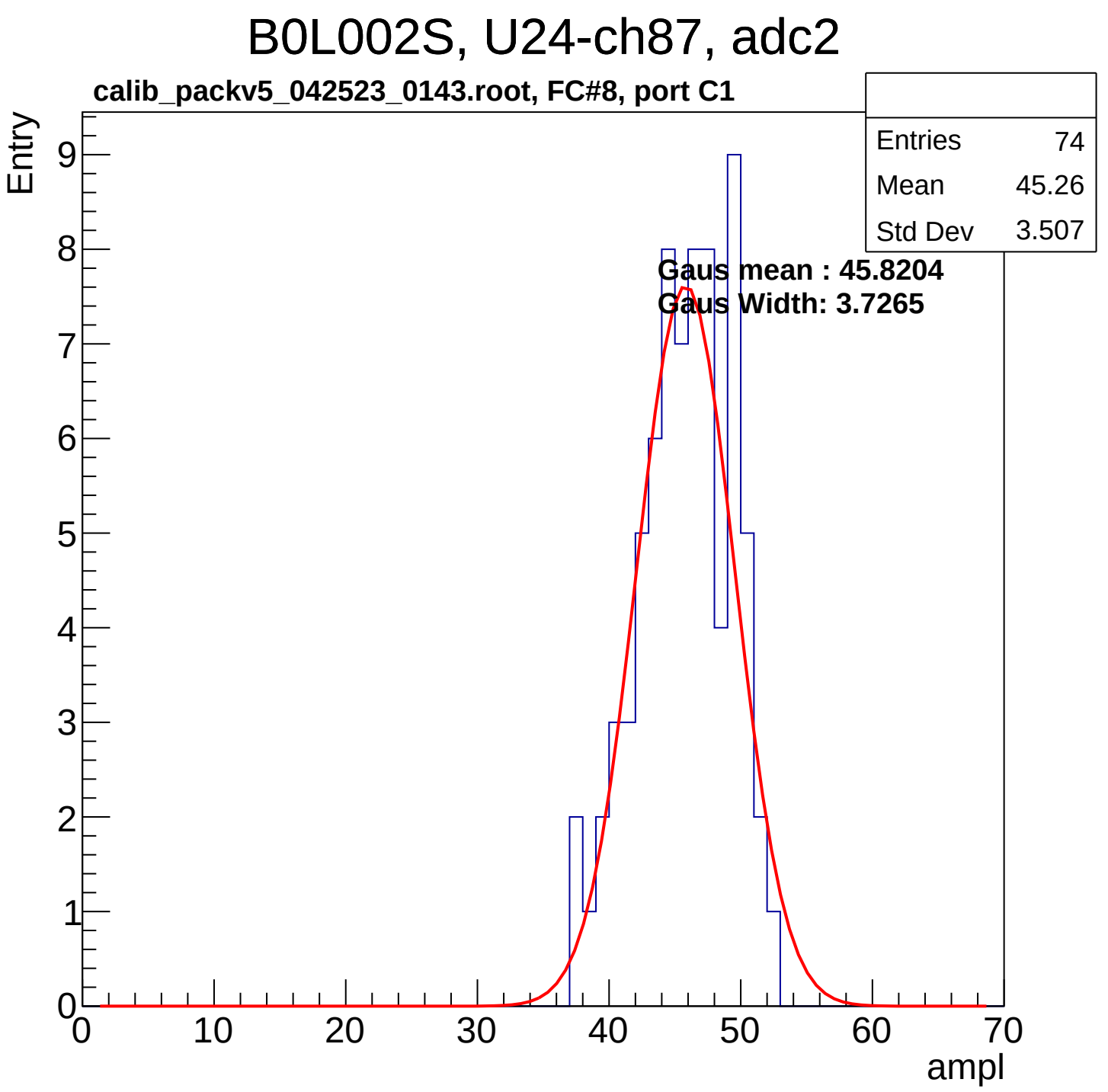
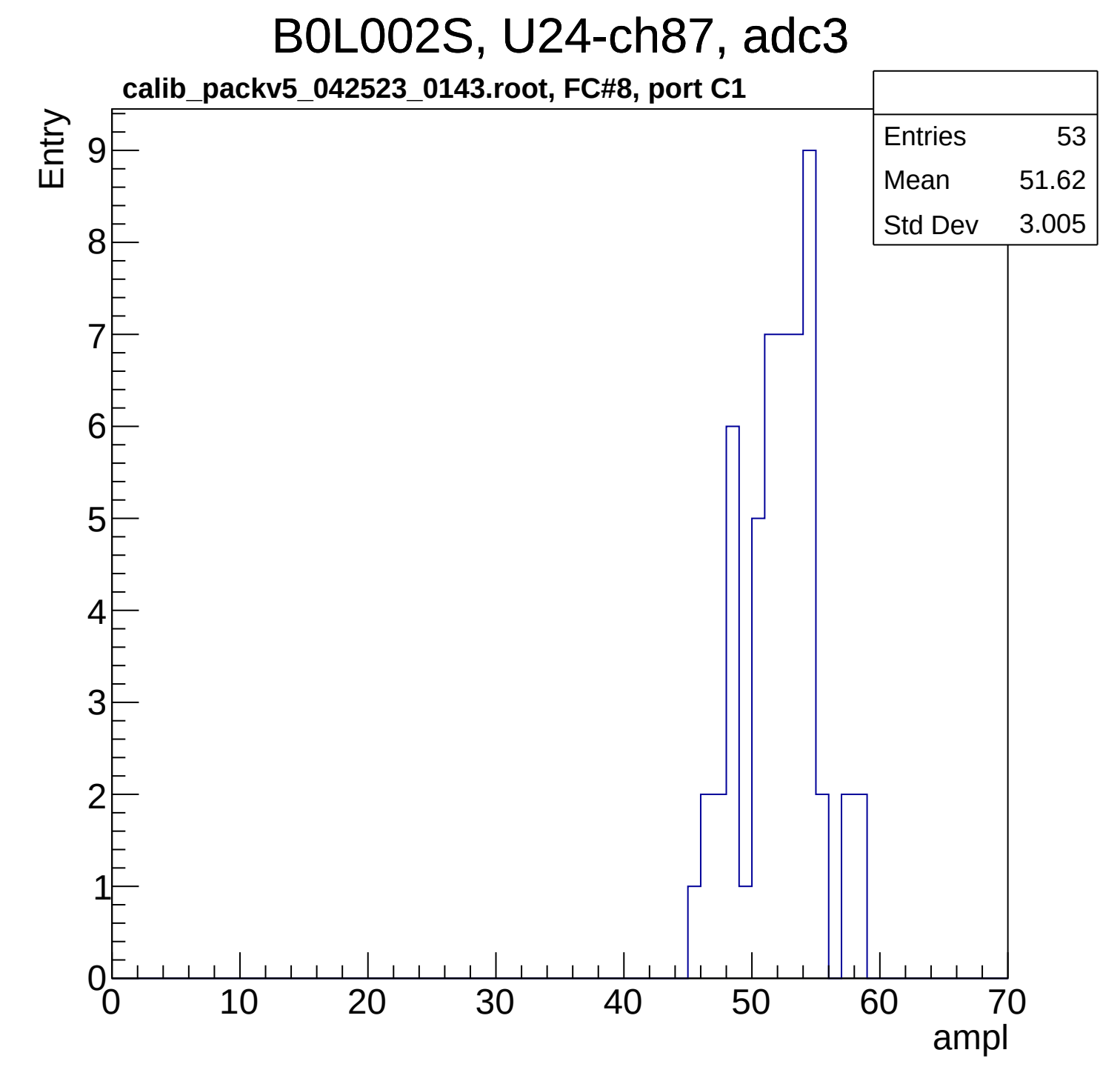

B0L002S, U24-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.71
Std Dev 21.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U24-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.2
Std Dev 3.998
Gaus mean : 31.5015
Gaus Width: 3.9354
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.32
Std Dev 3.284
Gaus mean : 39.0048
Gaus Width: 3.6307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 45.26
Std Dev 3.507
Gaus mean : 45.8204
Gaus Width: 3.7265
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U24-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.62
Std Dev 3.005
Entry
ampl
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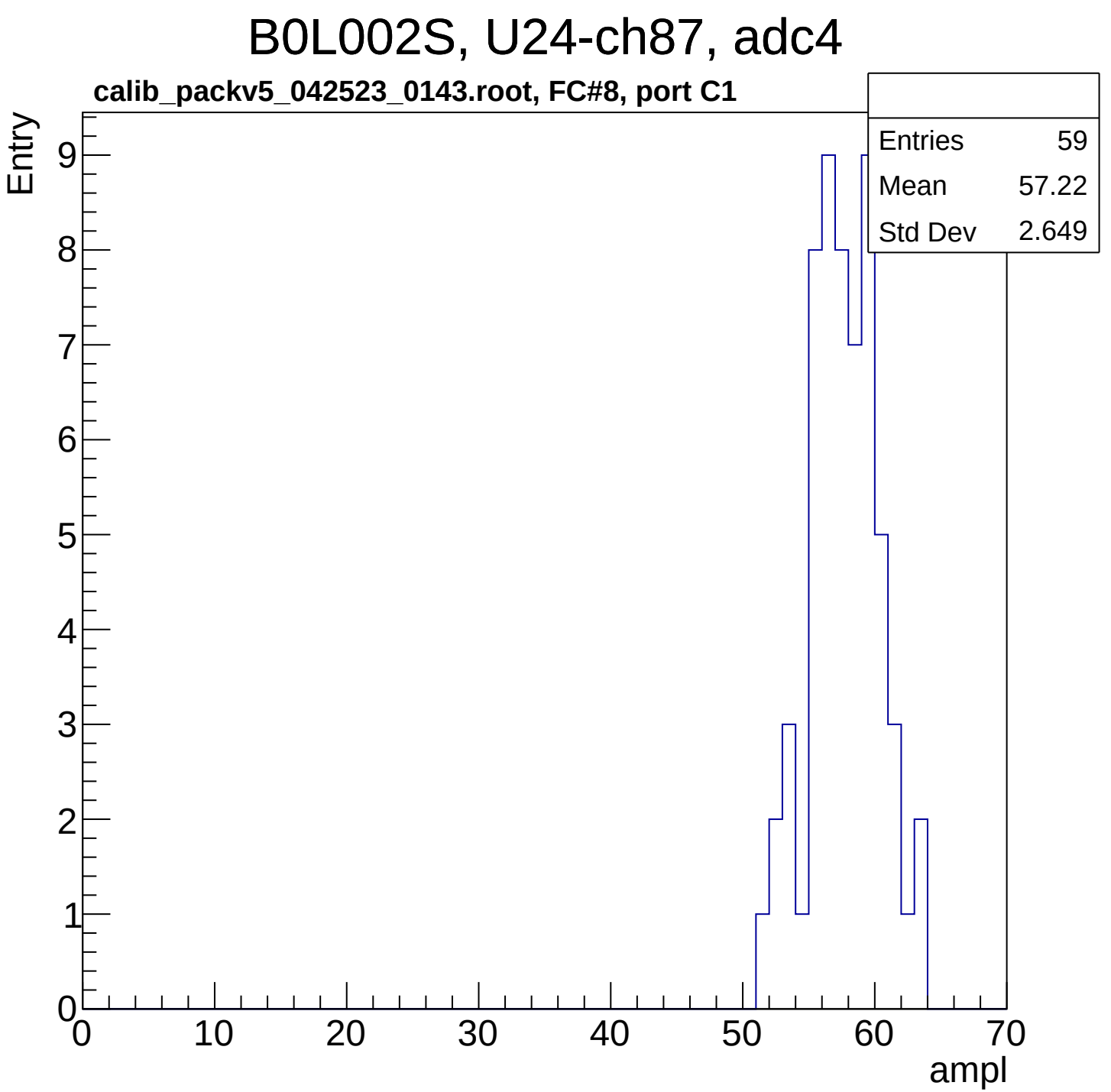
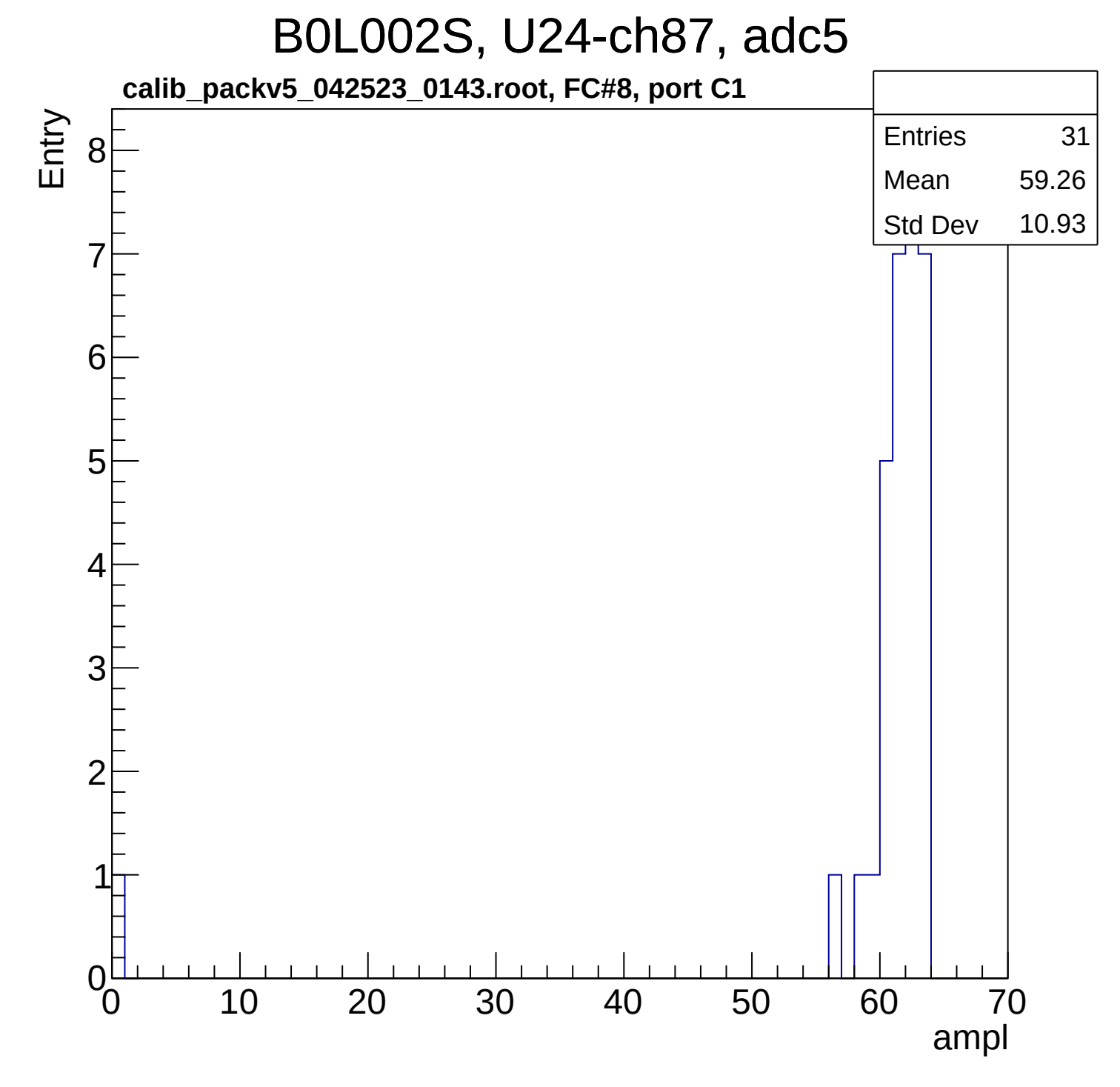
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.22
Std Dev 2.649
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.26
Std Dev 10.93
Entry
ampl
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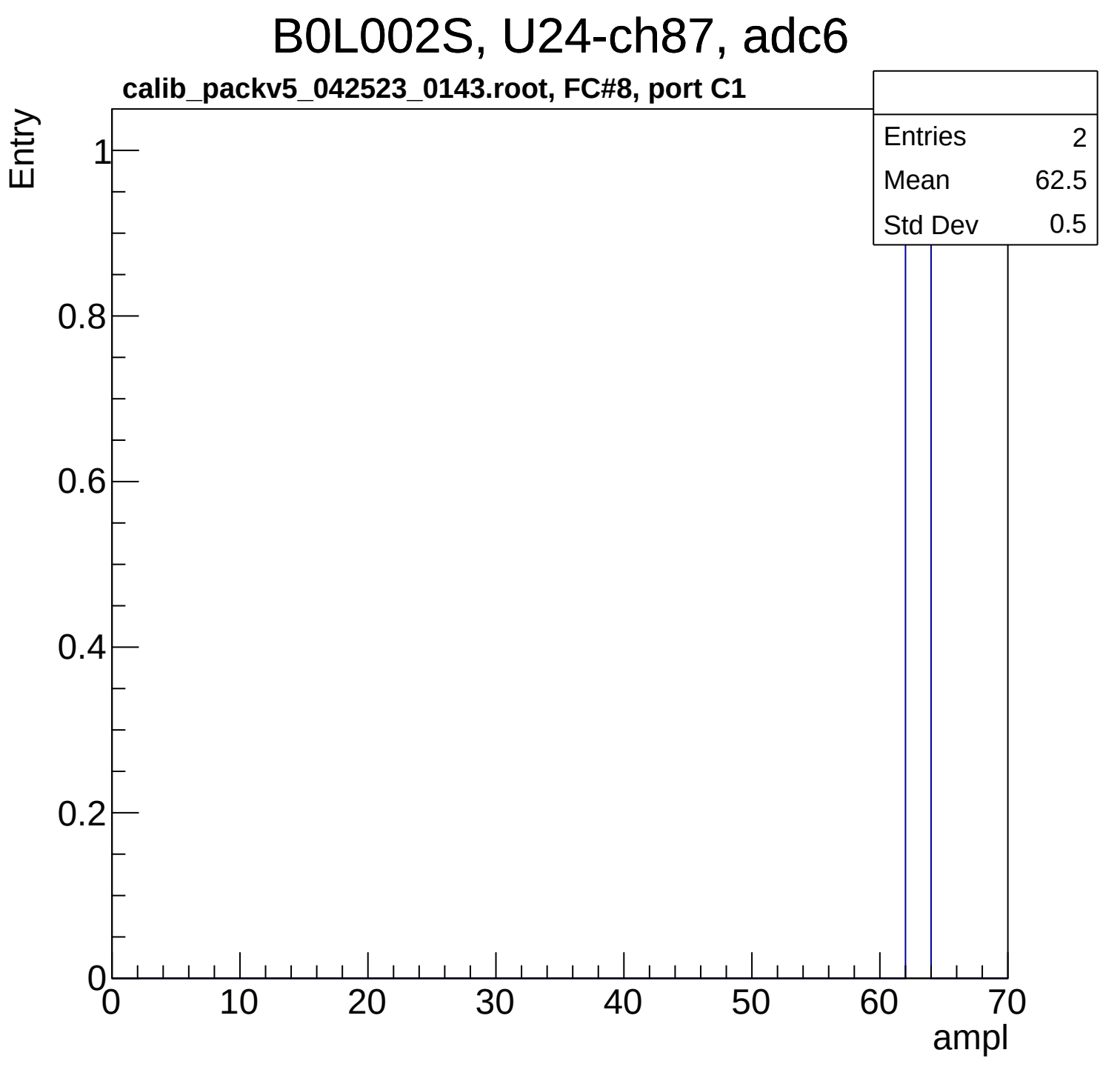
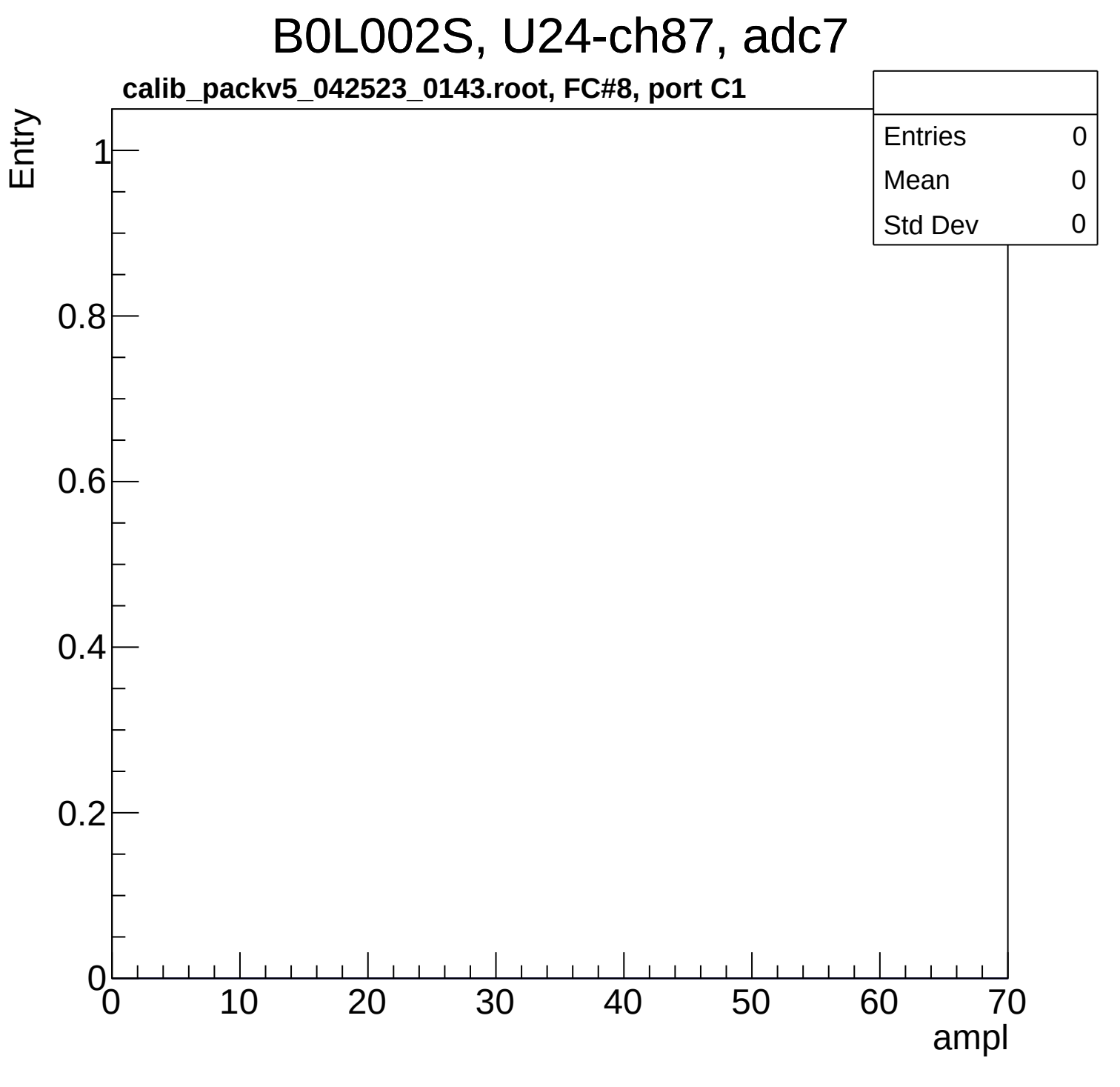
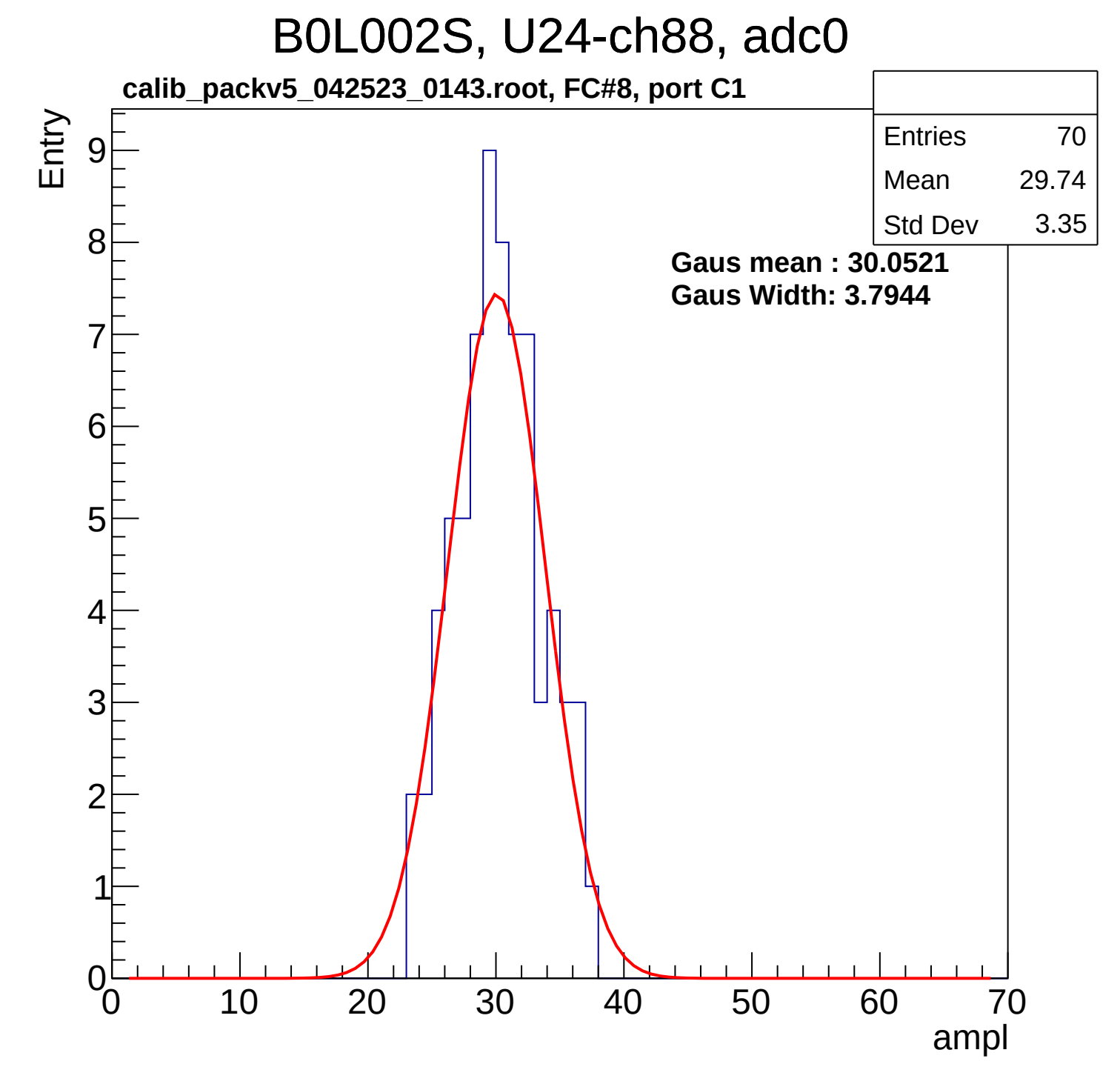
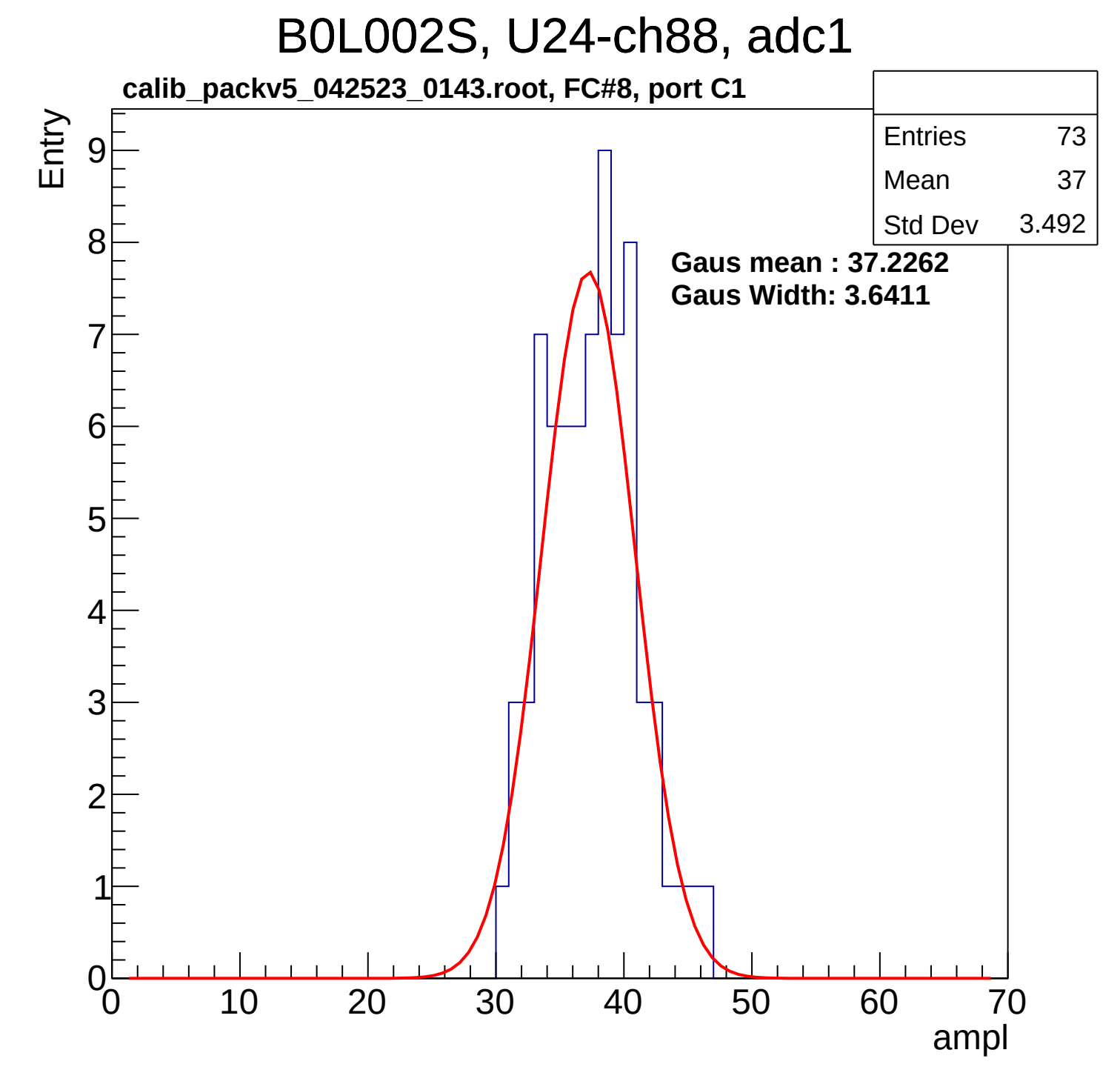
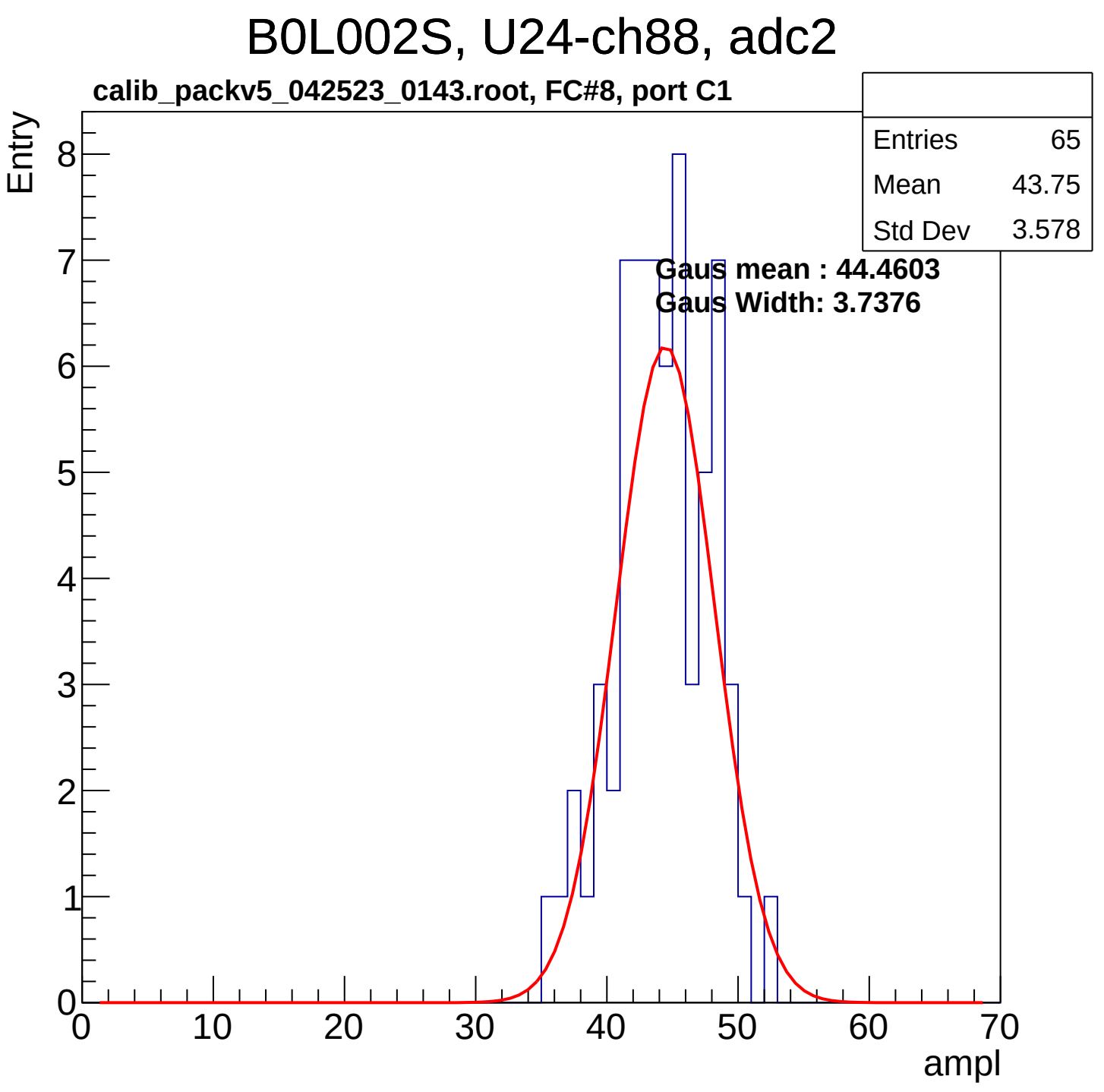
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 29.74
Std Dev 3.35
Gaus mean : 30.0521
Gaus Width: 3.7944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37
Std Dev 3.492
Gaus mean : 37.2262
Gaus Width: 3.6411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.75
Std Dev 3.578
Gaus mean : 44.4603
Gaus Width: 3.7376
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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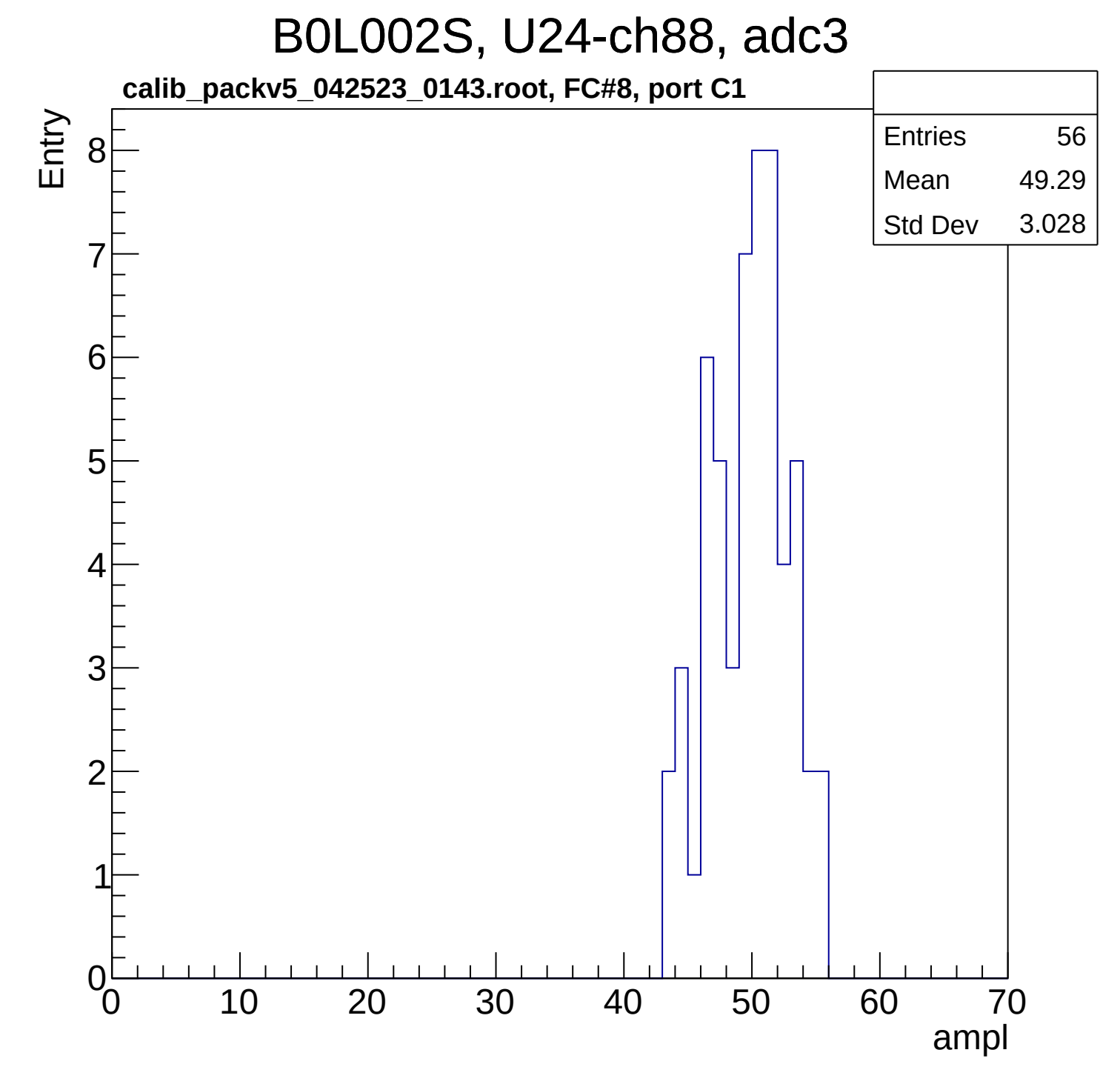
B0L002S, U24-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 49.29
Std Dev 3.028
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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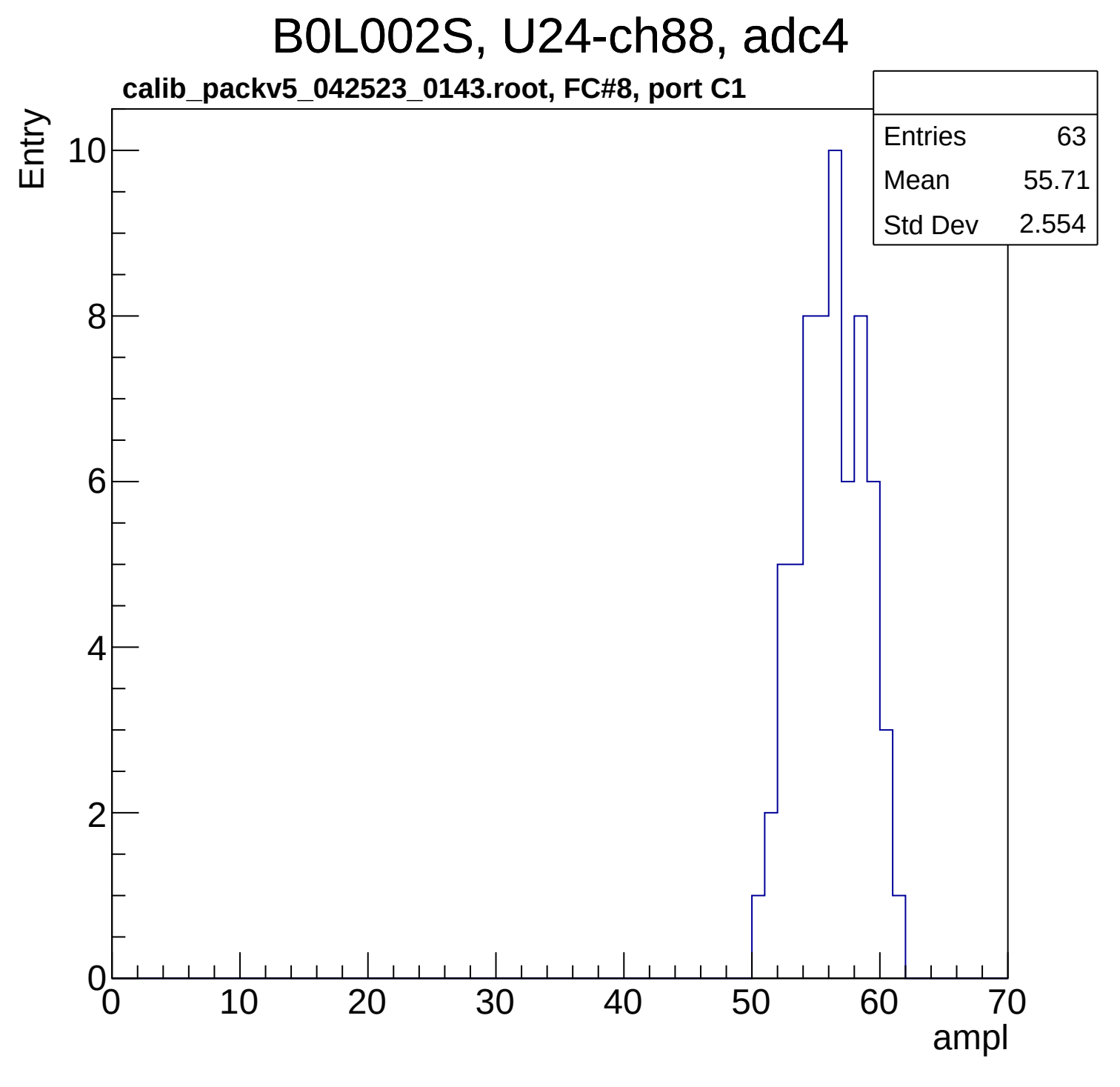
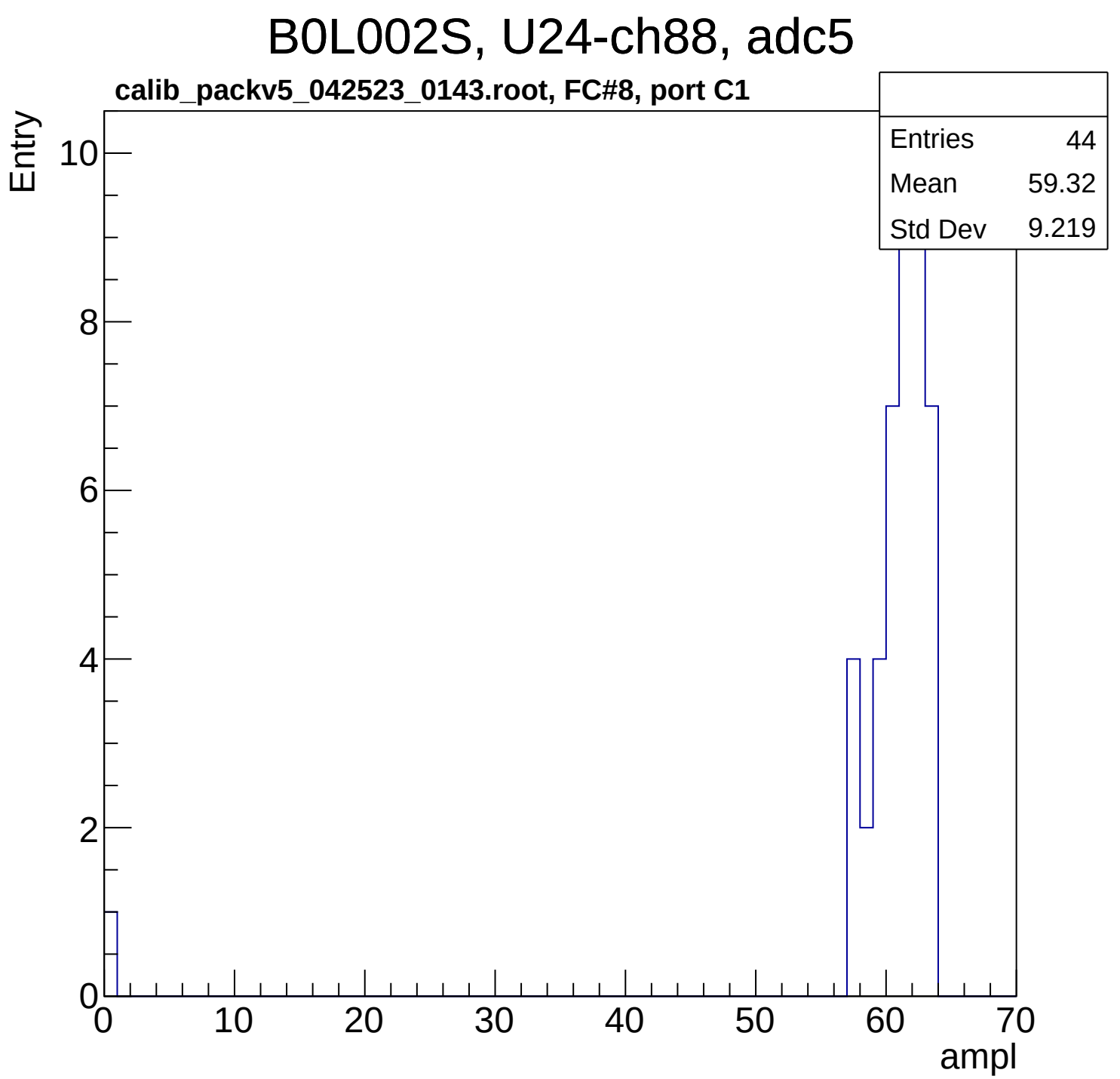
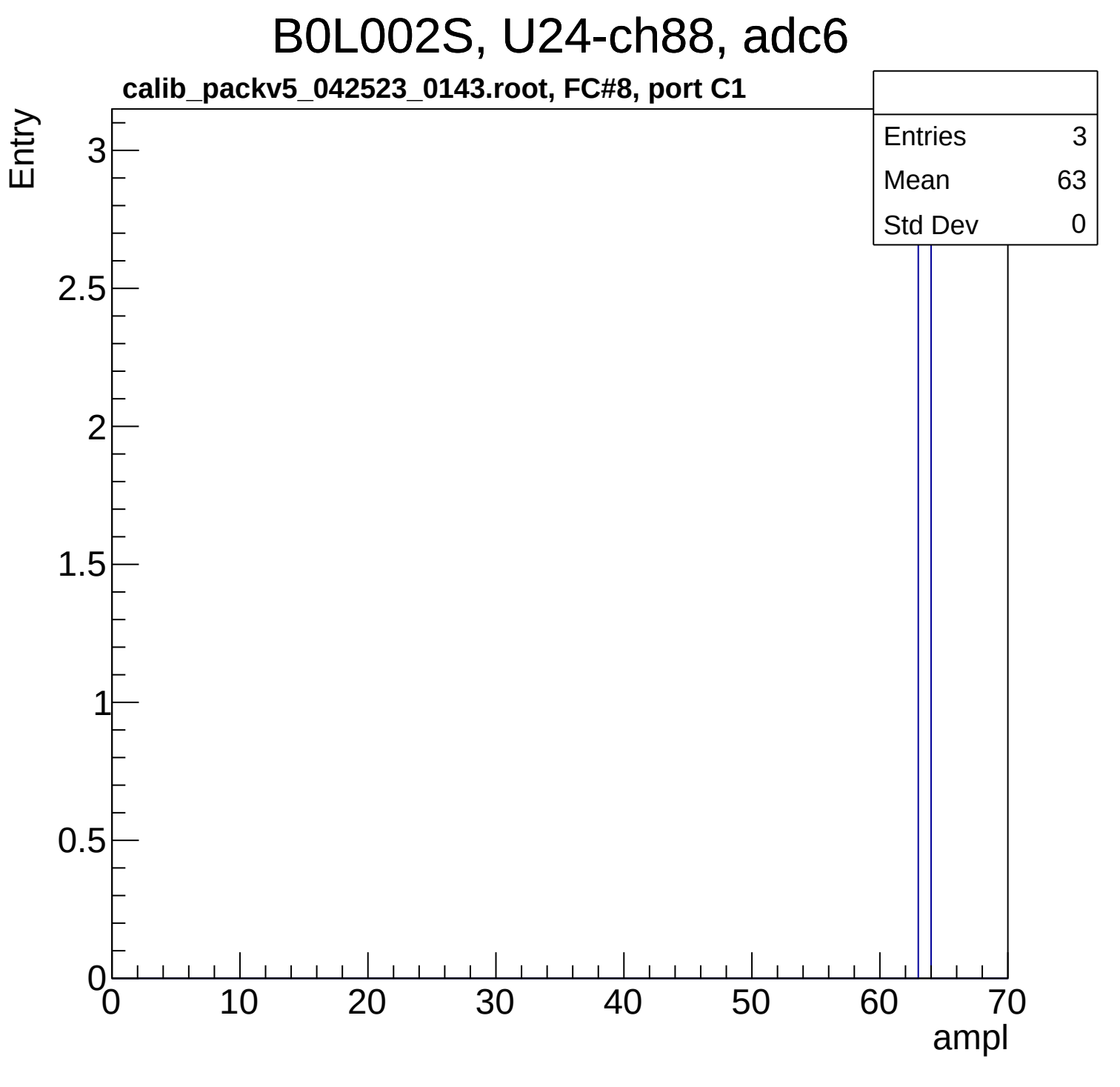

B0L002S, U24-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 55.71
Std Dev 2.554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.32
Std Dev 9.219
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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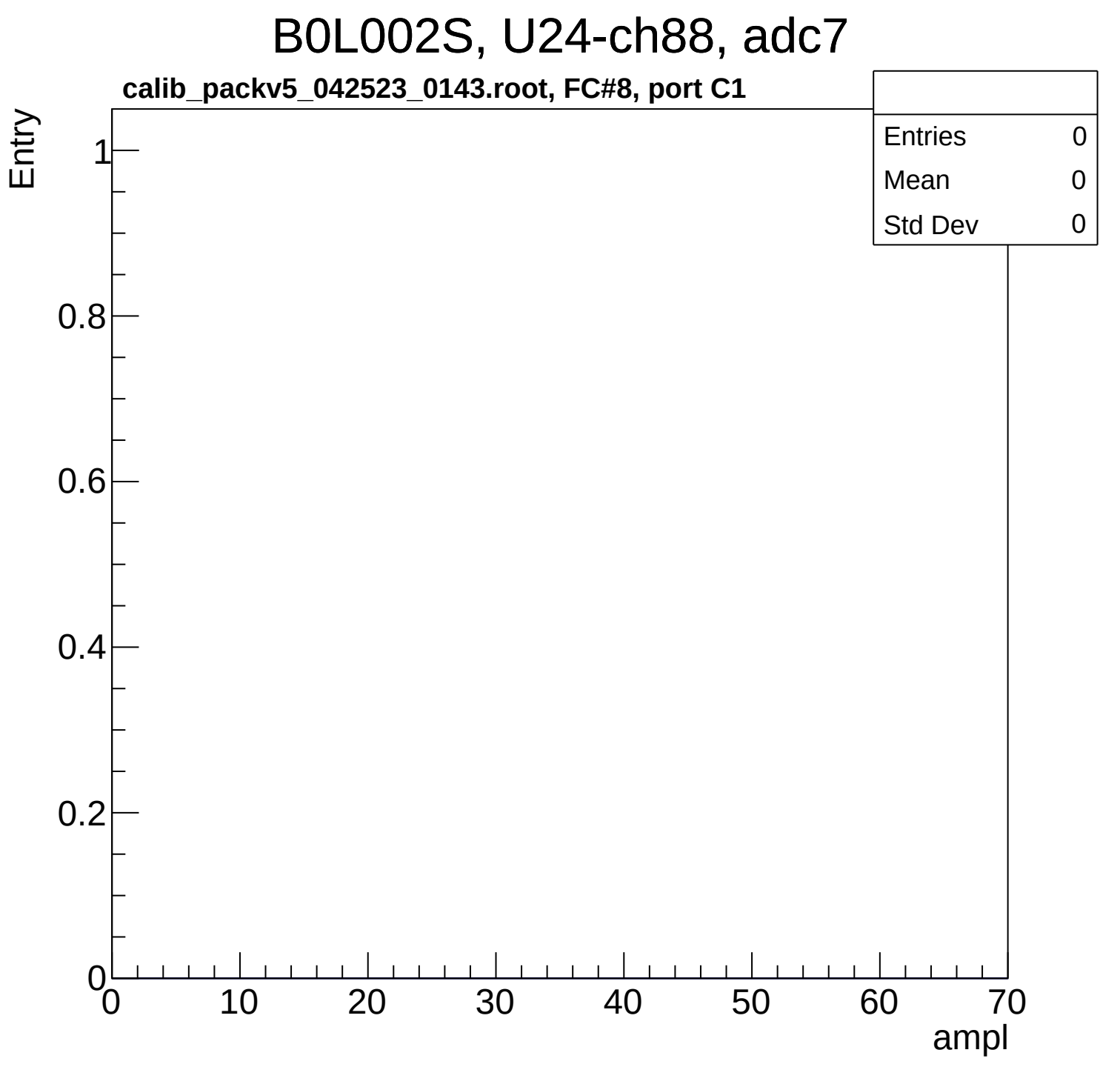
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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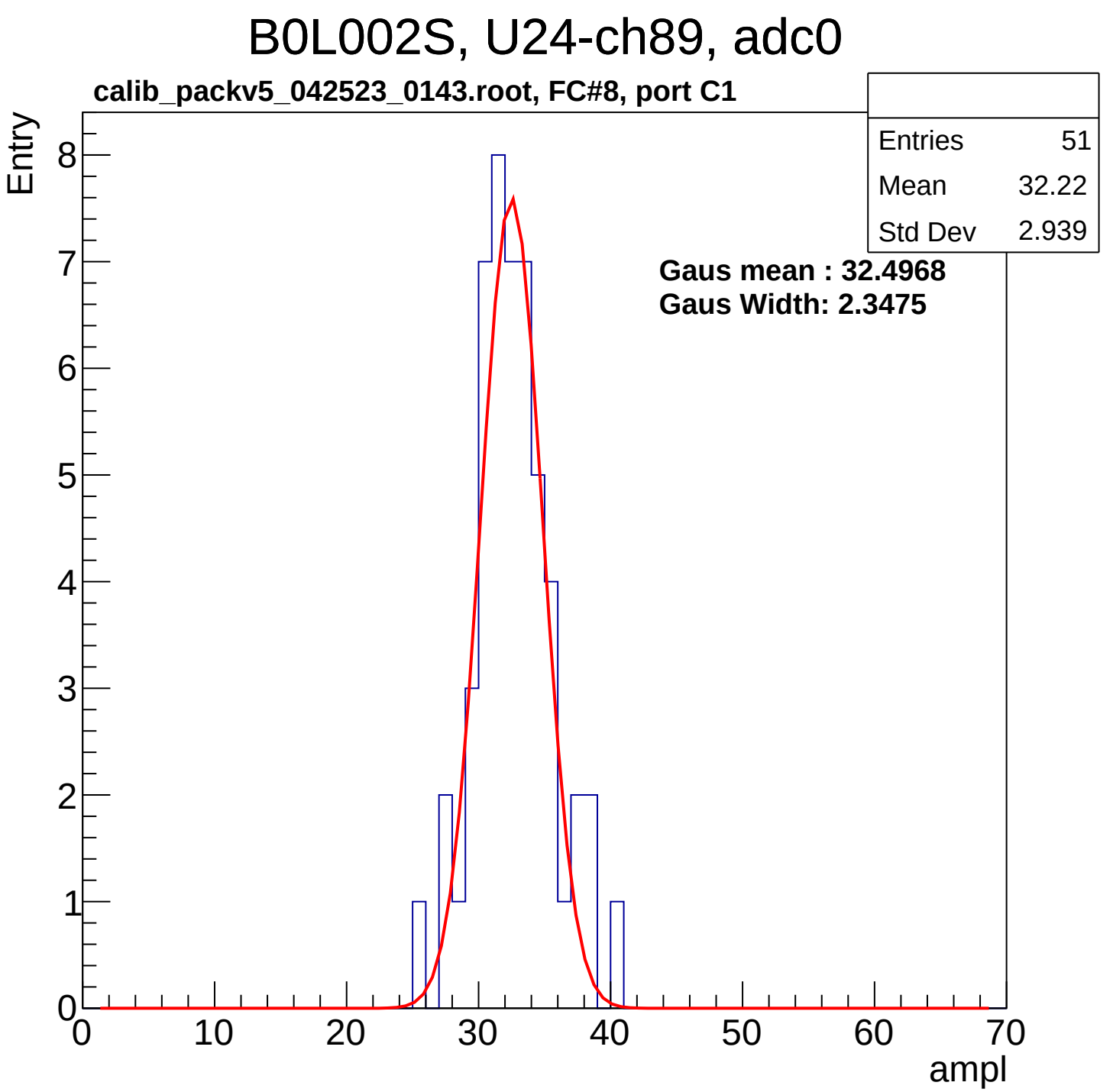
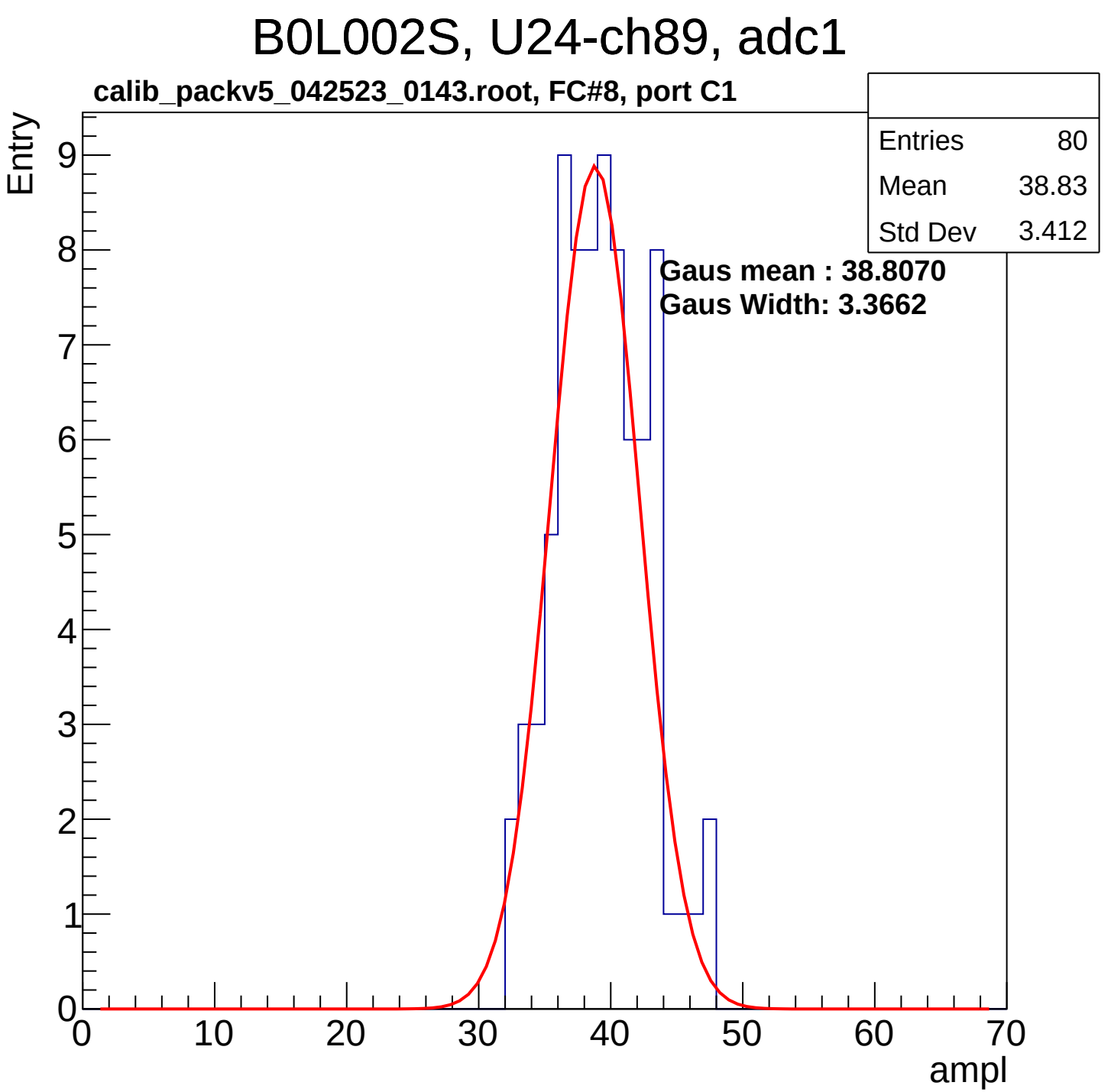
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 32.22
Std Dev 2.939
Gaus mean : 32.4968
Gaus Width: 2.3475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.83
Std Dev 3.412
Gaus mean : 38.8070
Gaus Width: 3.3662
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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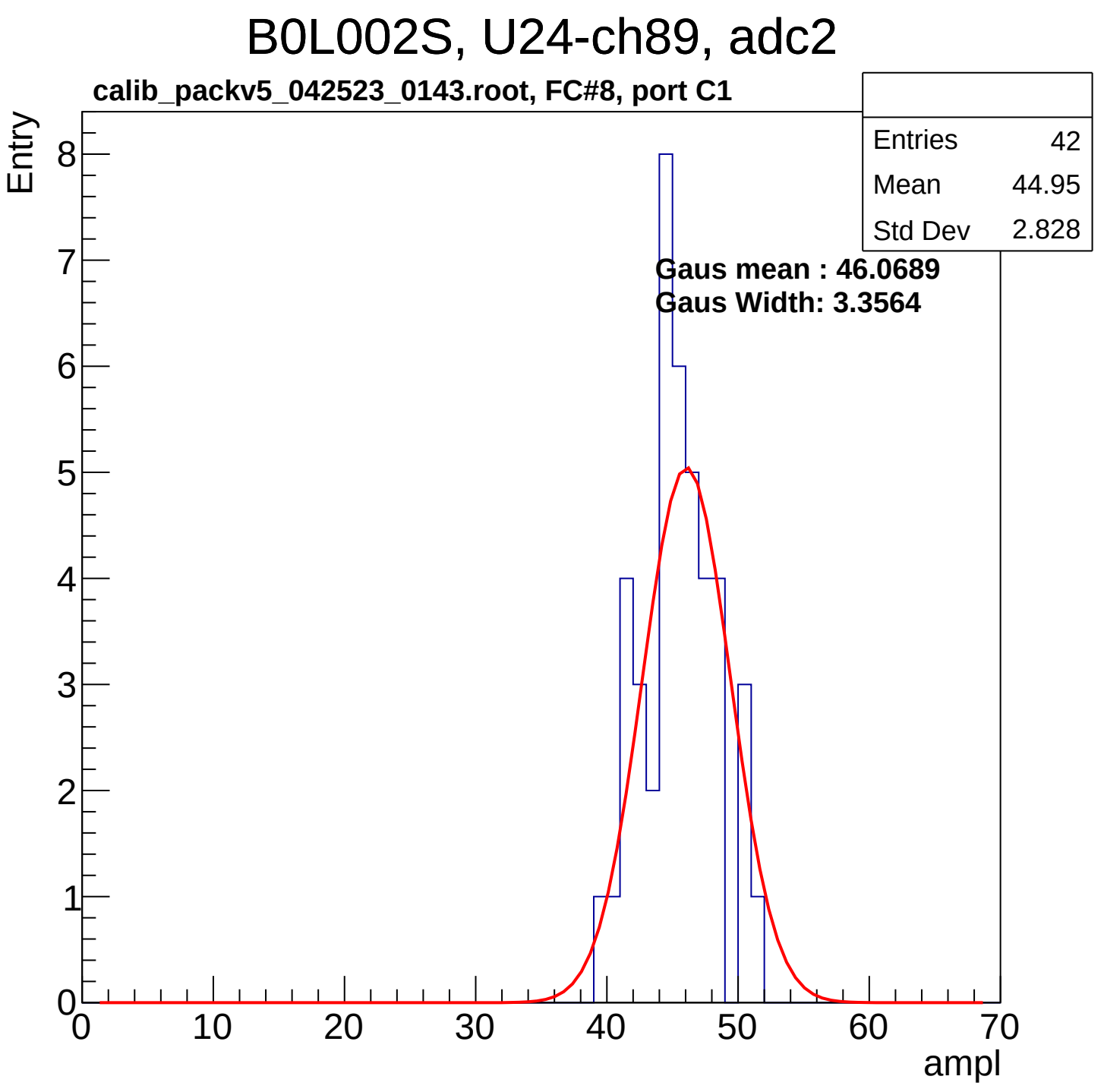
B0L002S, U24-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 44.95
Std Dev 2.828
Gaus mean : 46.0689
Gaus Width: 3.3564
Entry
ampl
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1
2
3
4
5
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7
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0
10
20
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40
50
60
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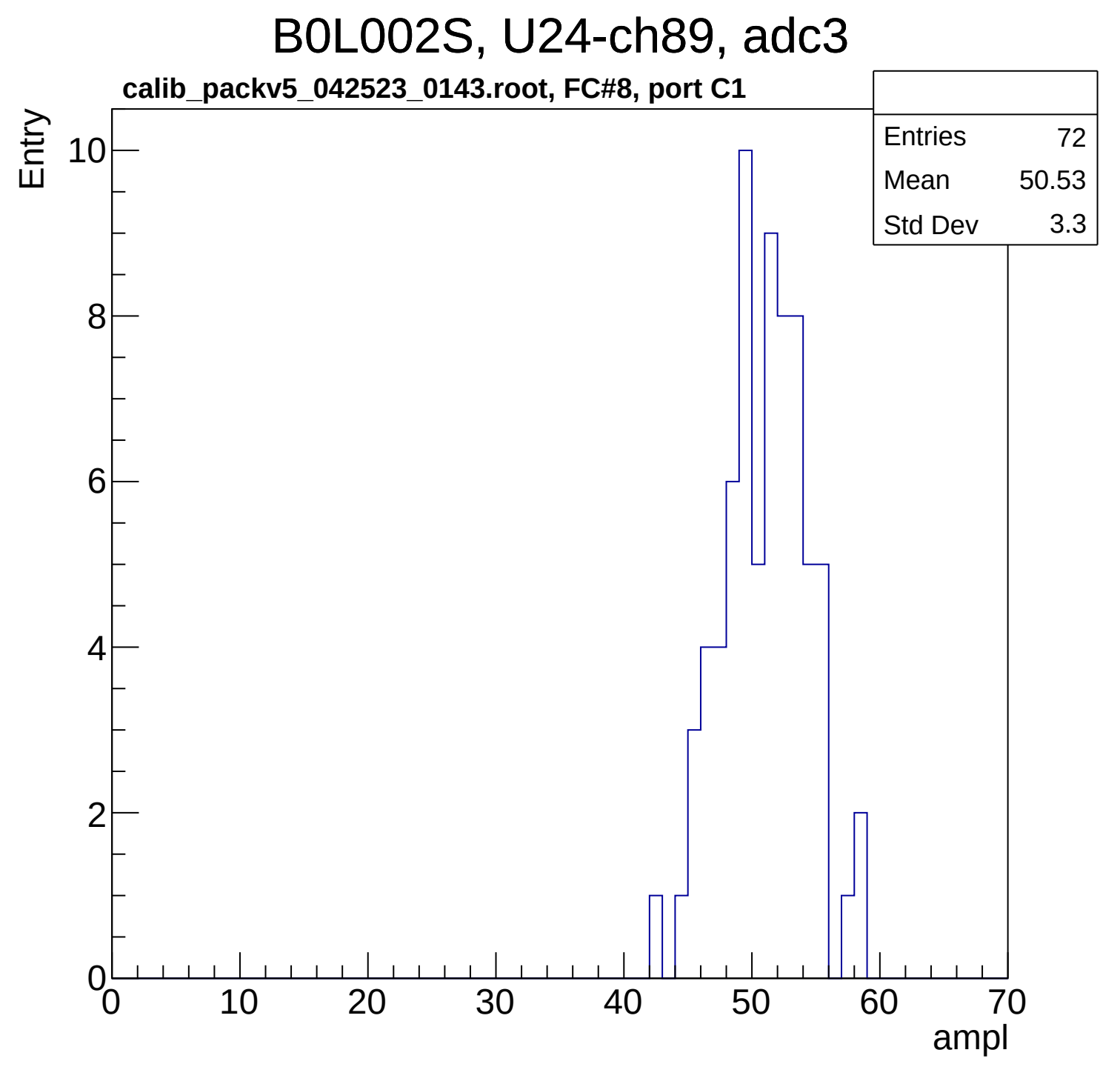
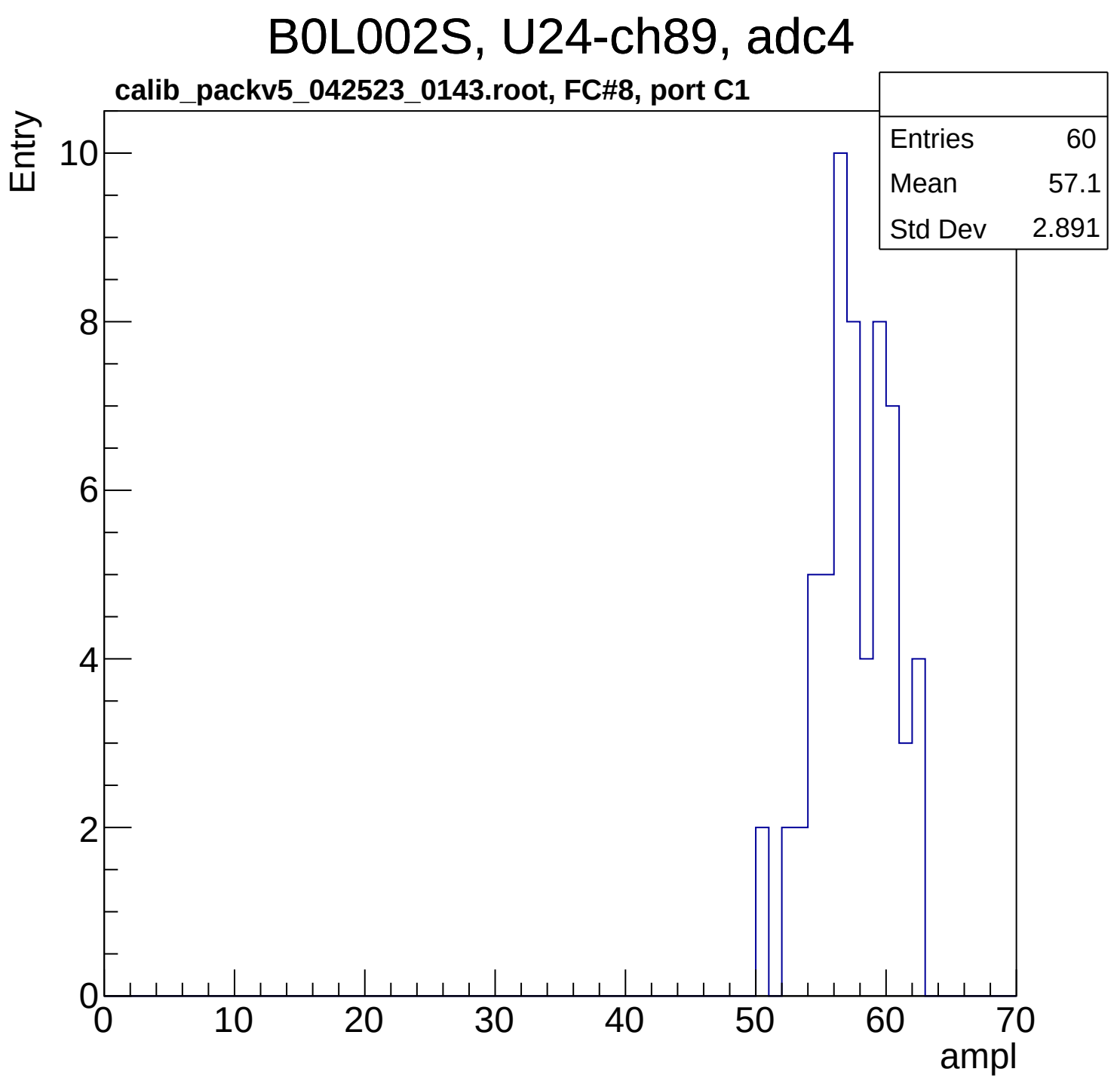
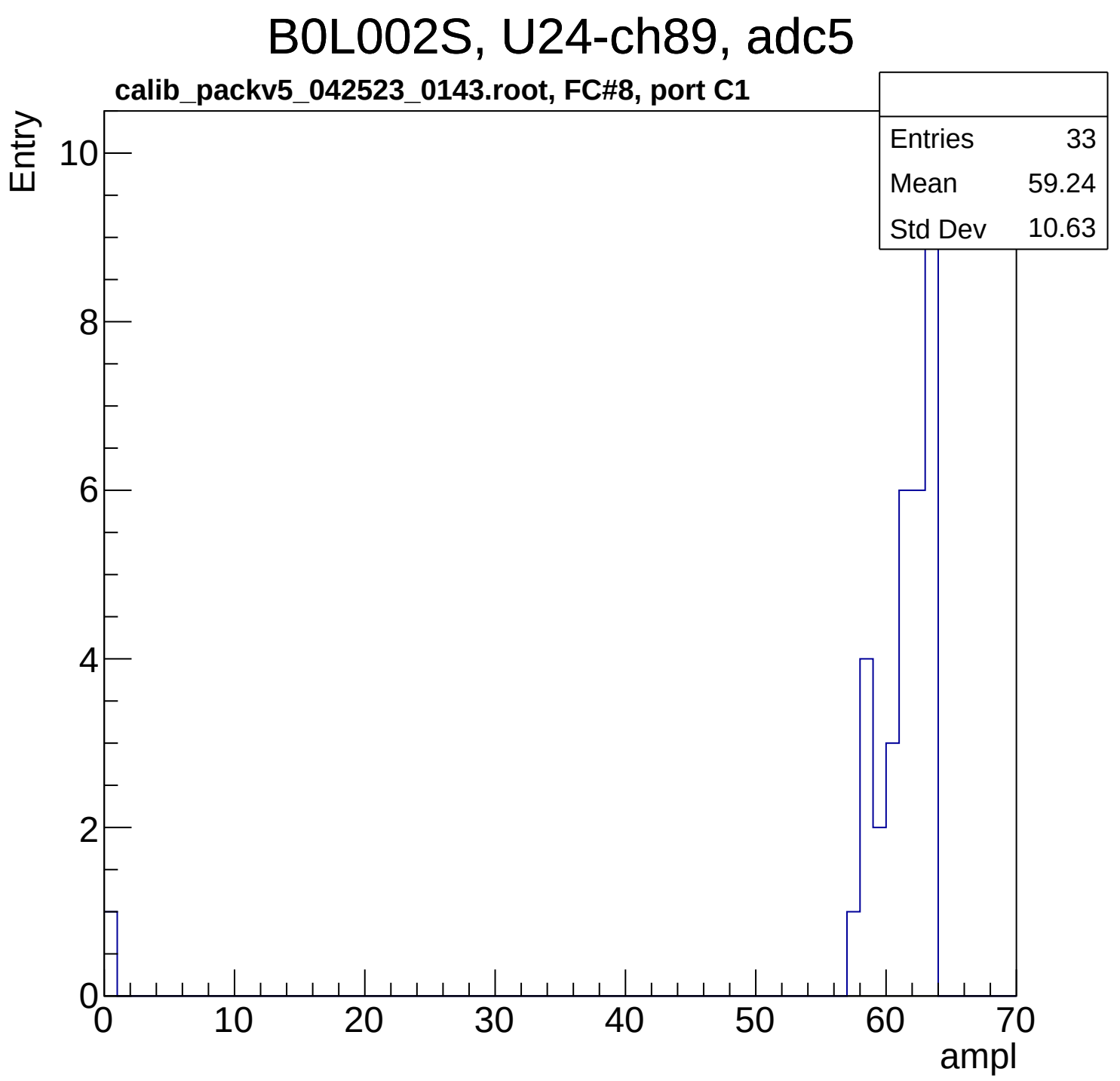
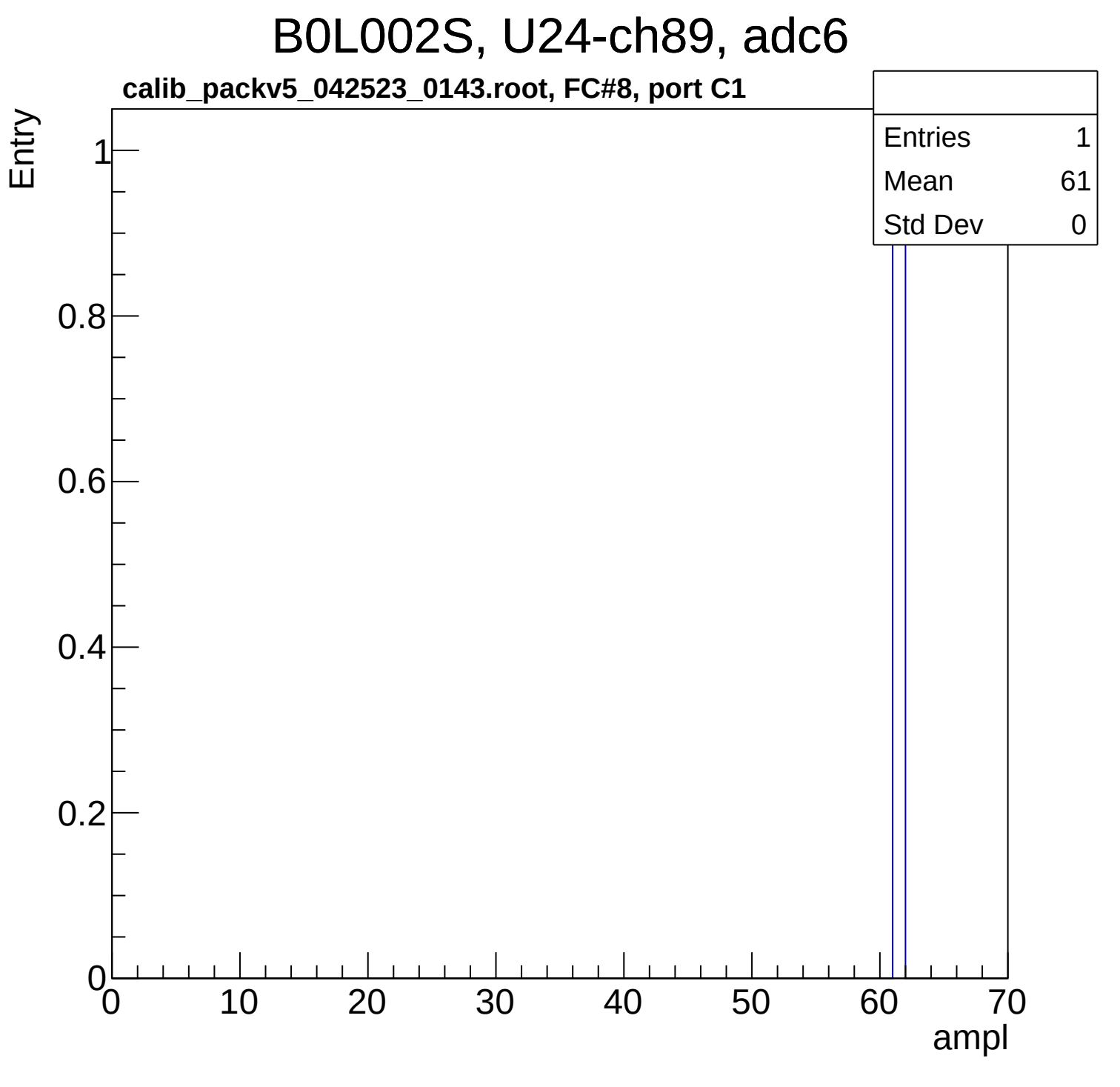
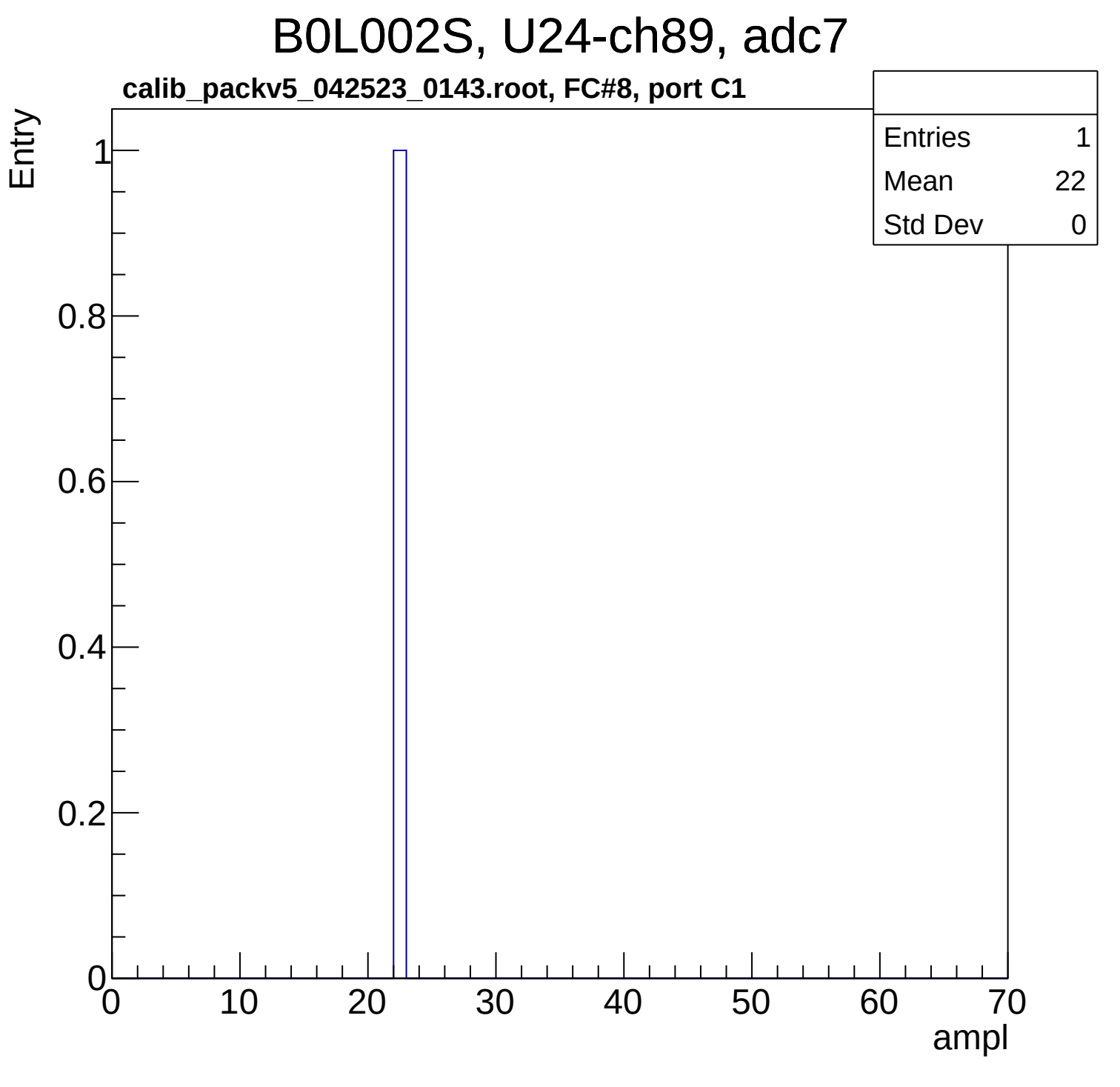
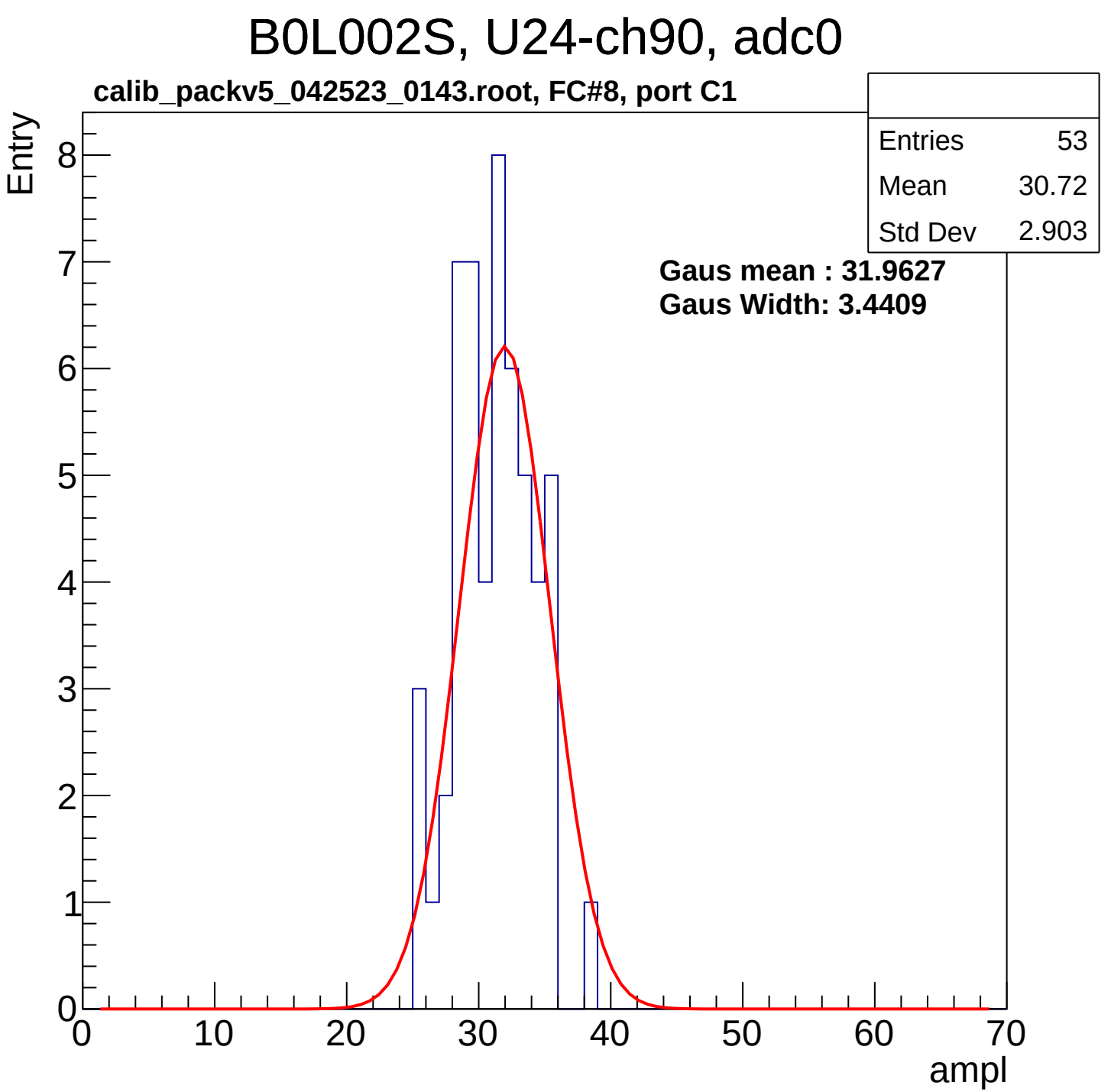

B0L002S, U24-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.53
Std Dev 3.3
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

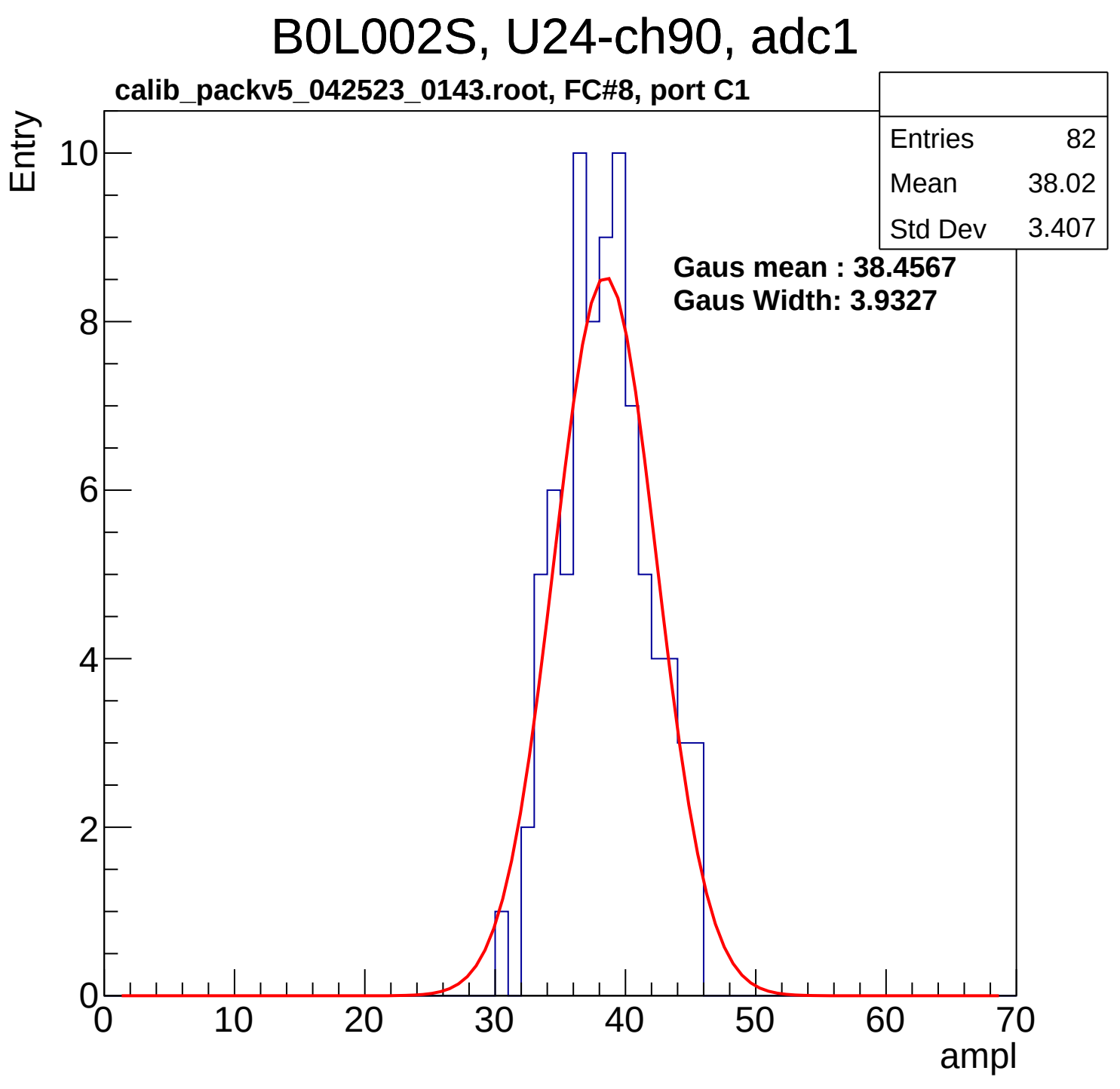

B0L002S, U24-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.1
Std Dev 2.891
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

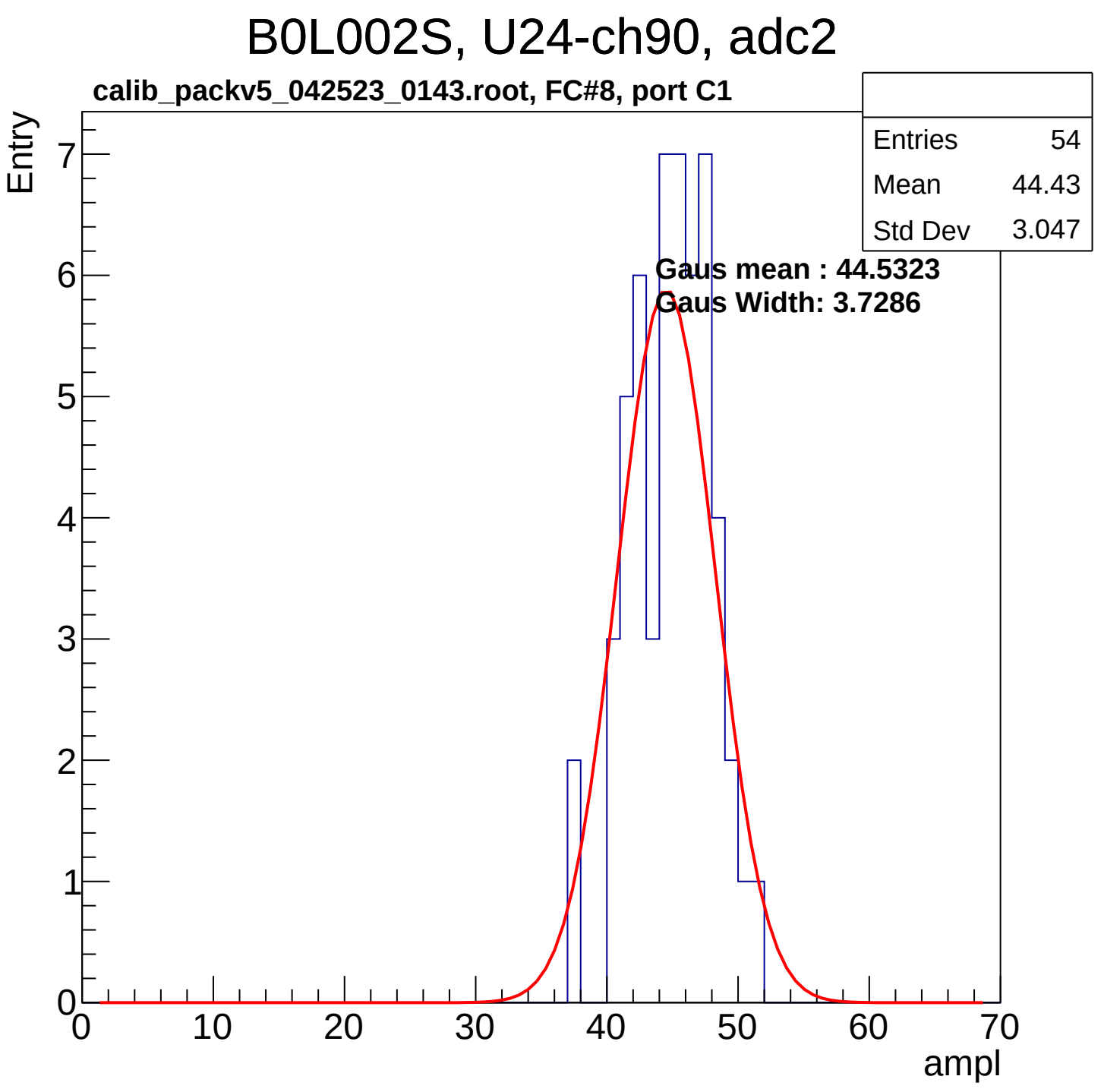

B0L002S, U24-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.24
Std Dev 10.63
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 30.72
Std Dev 2.903
Gaus mean : 31.9627
Gaus Width: 3.4409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 38.02
Std Dev 3.407
Gaus mean : 38.4567
Gaus Width: 3.9327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.43
Std Dev 3.047
Gaus mean : 44.5323
Gaus Width: 3.7286
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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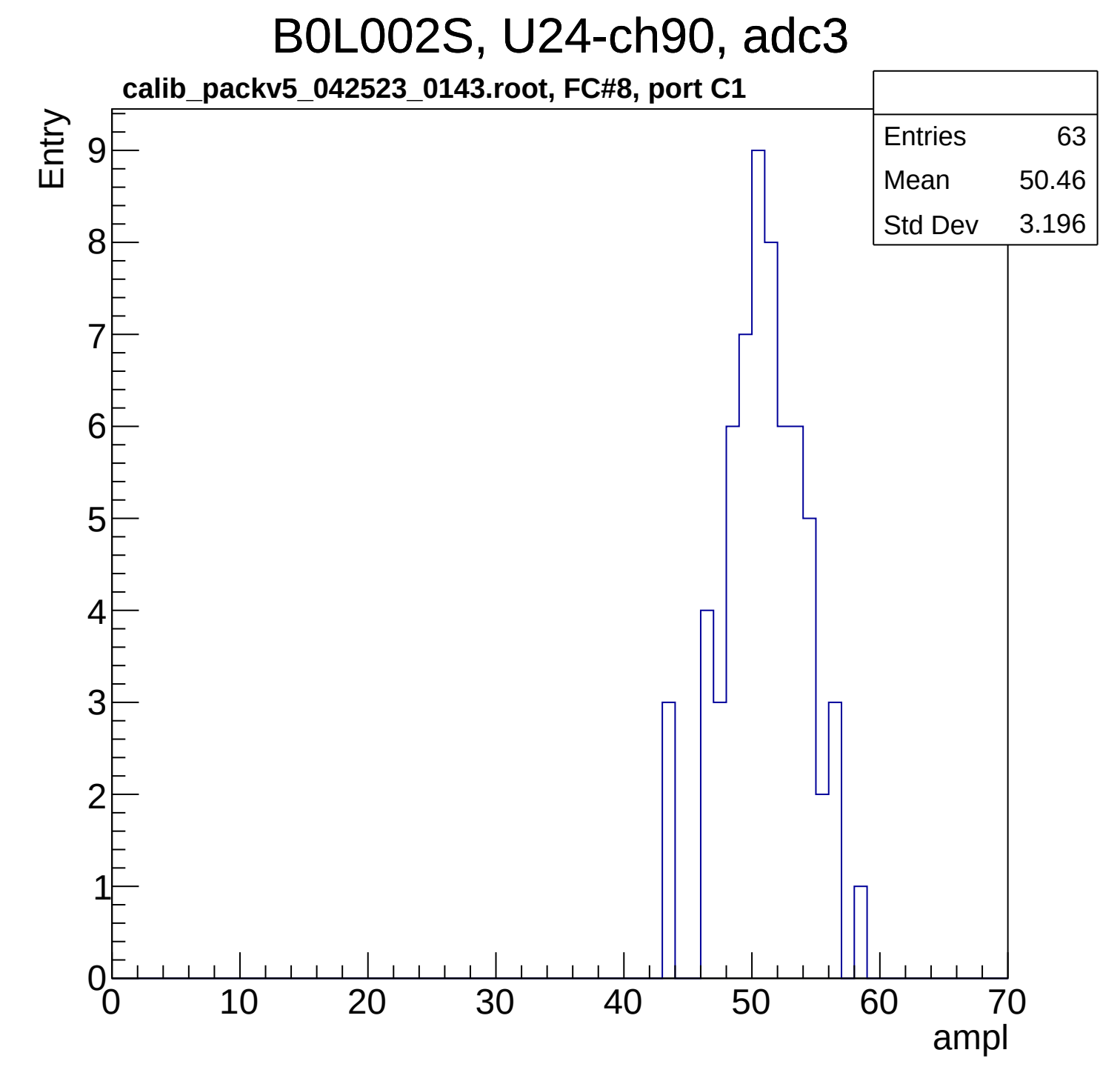
B0L002S, U24-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.46
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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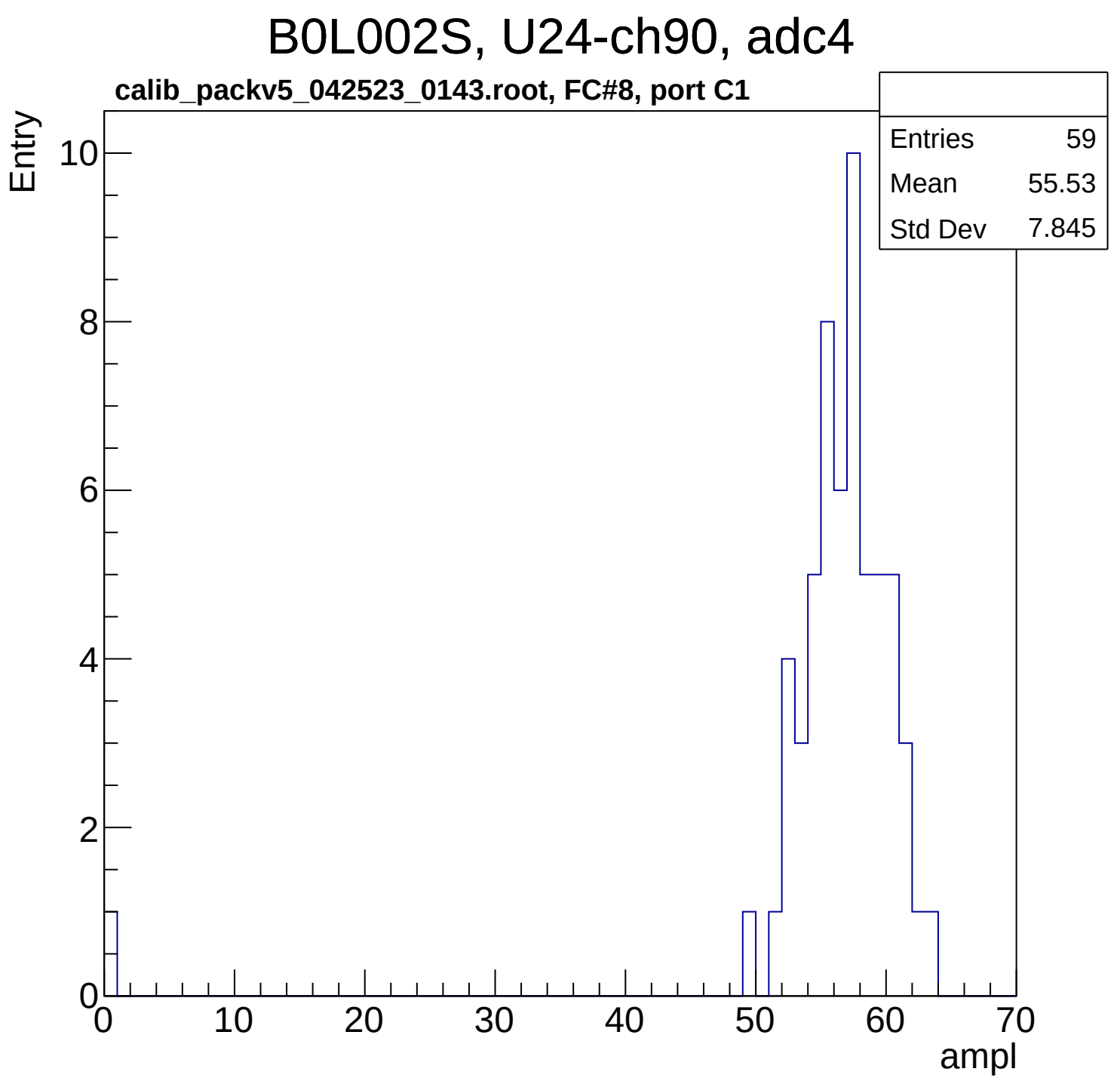
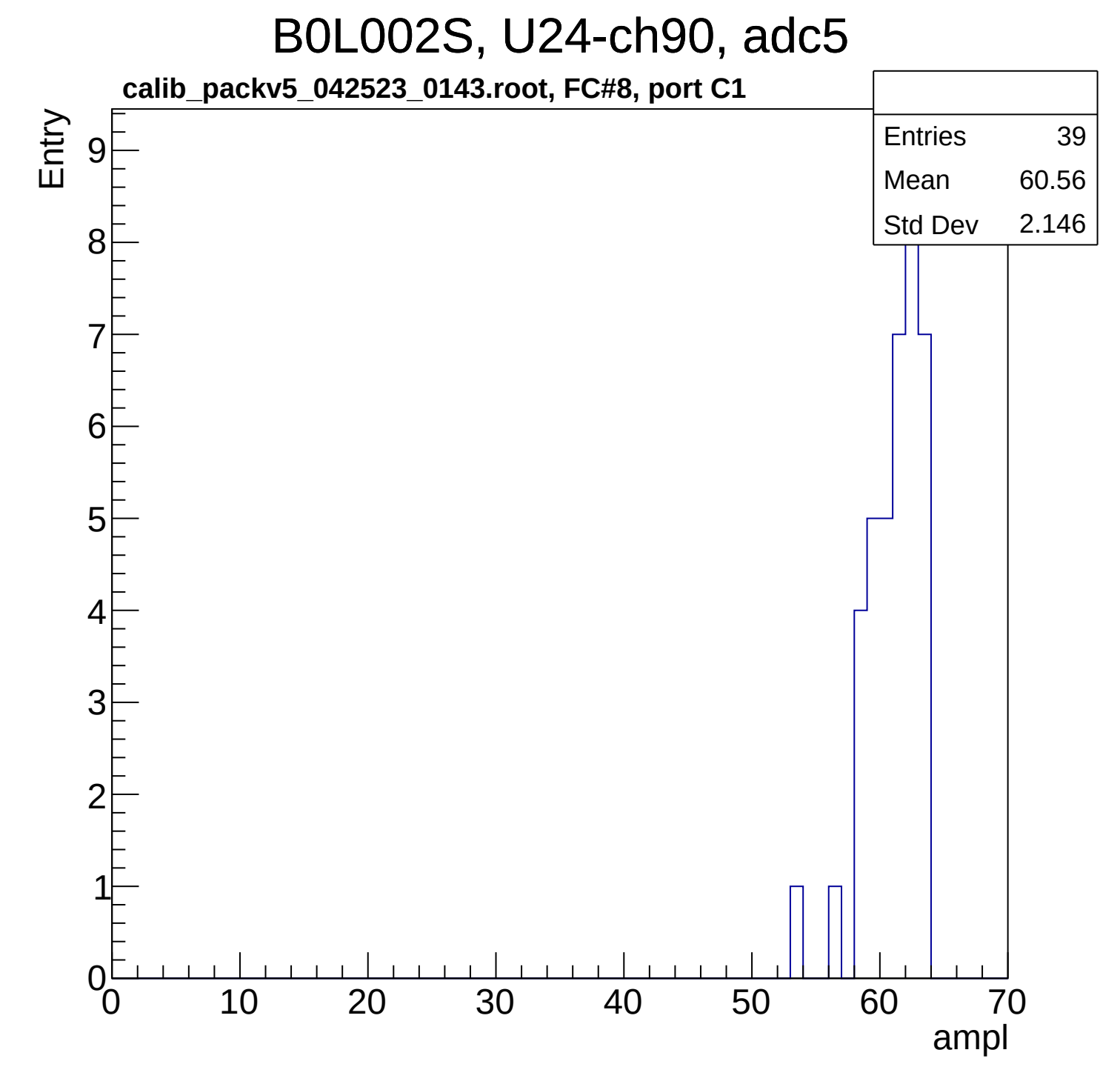
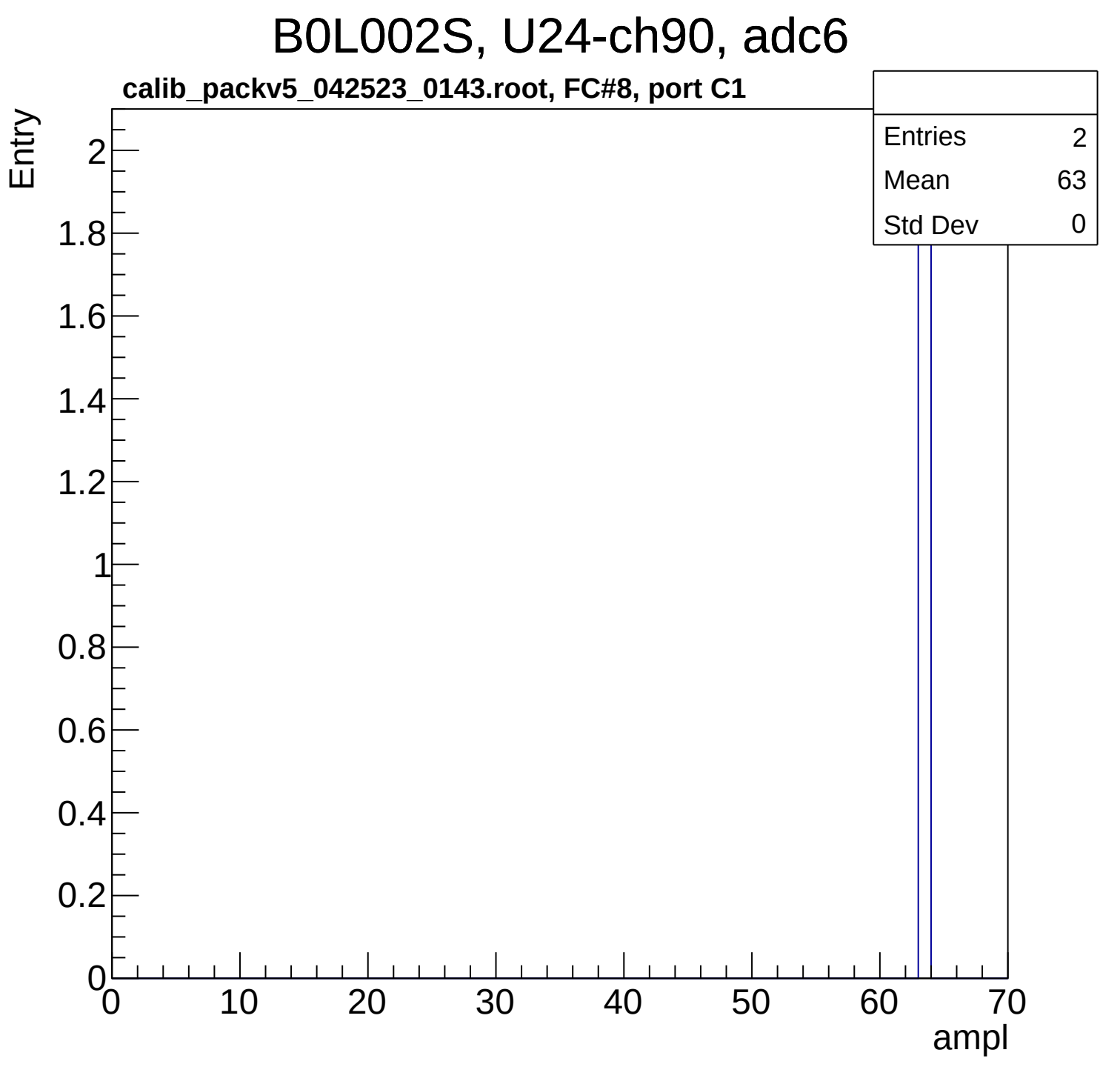
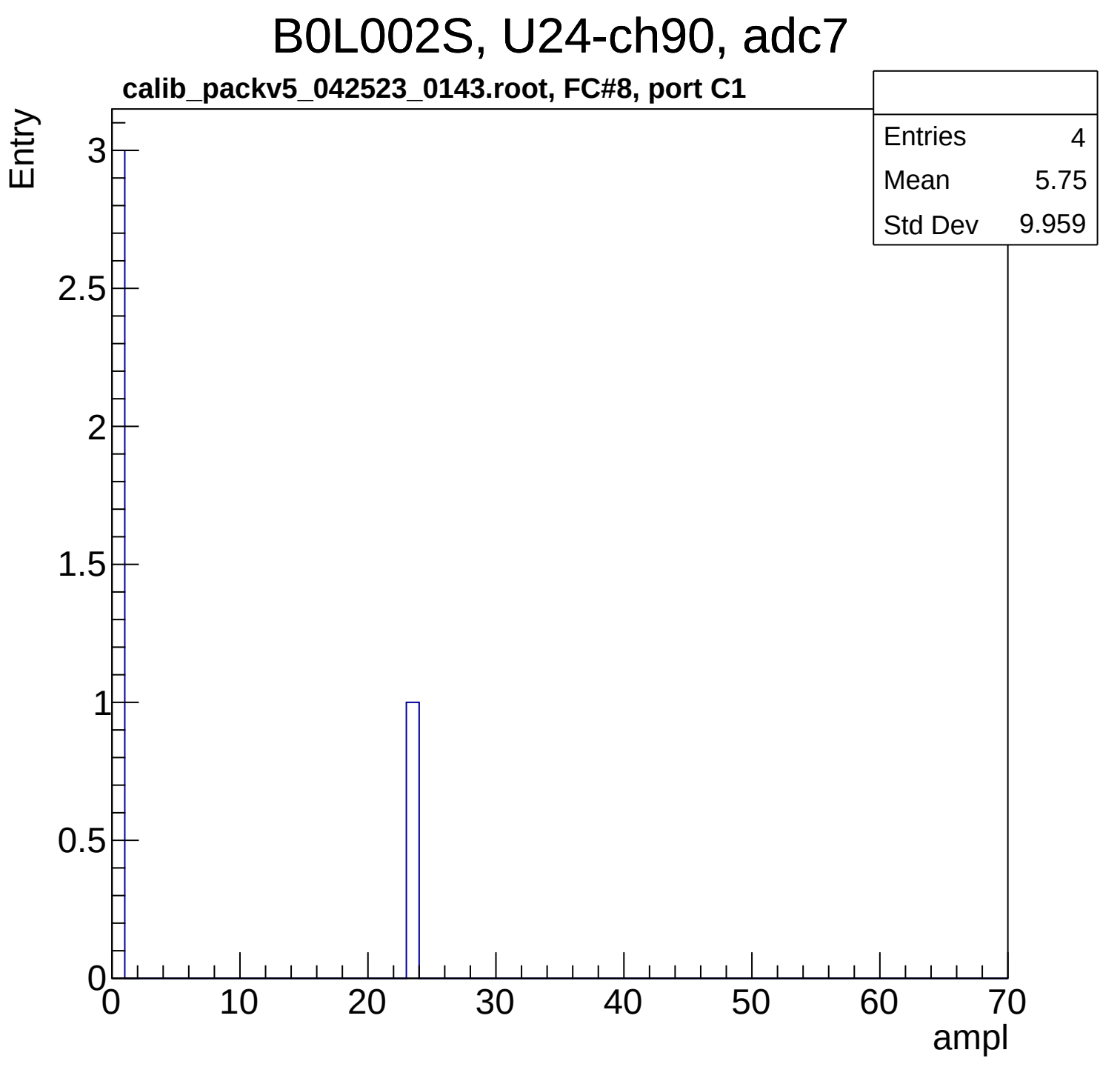
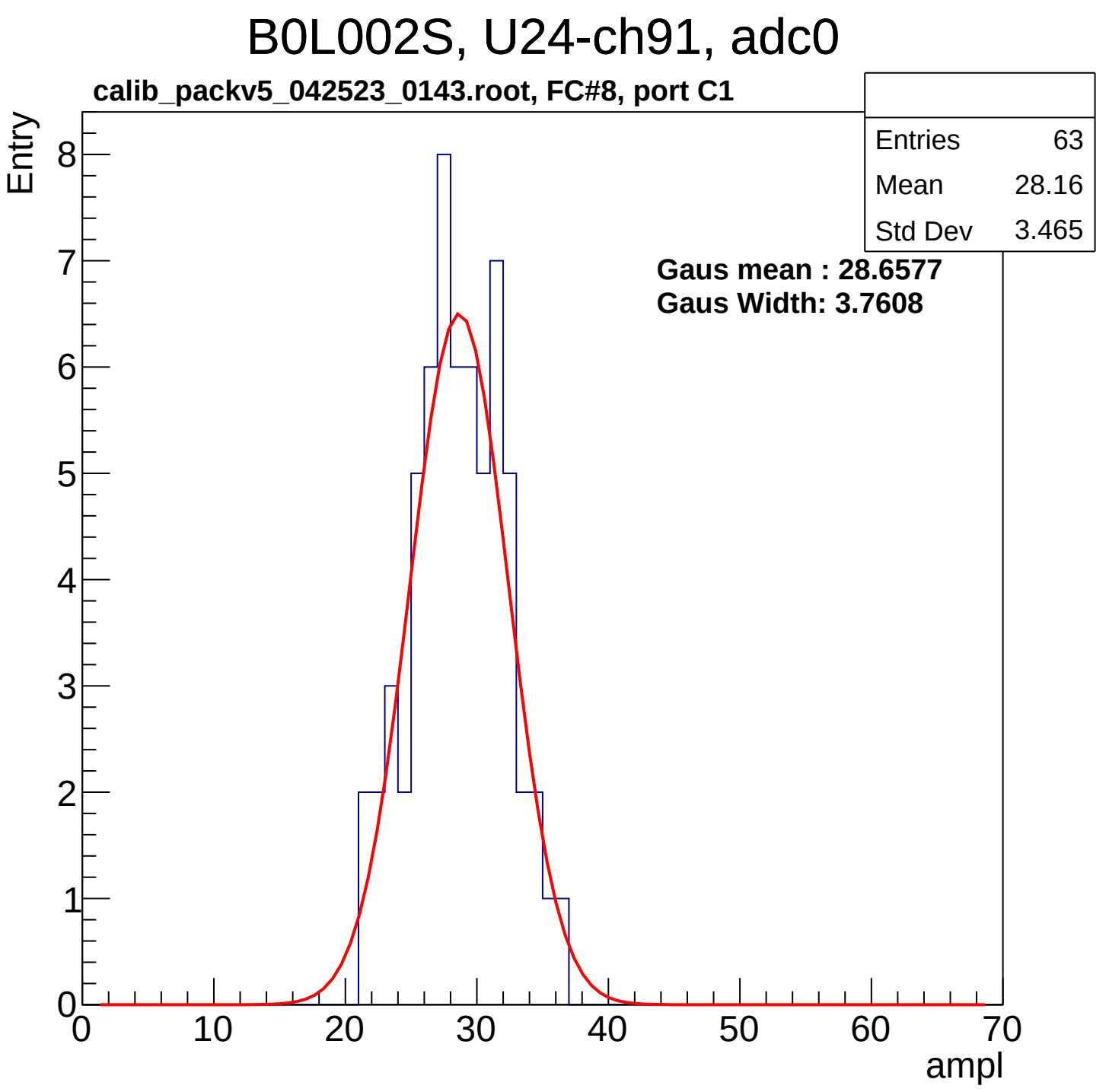

B0L002S, U24-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.53
Std Dev 7.845
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.56
Std Dev 2.146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 28.16
Std Dev 3.465
Gaus mean : 28.6577
Gaus Width: 3.7608
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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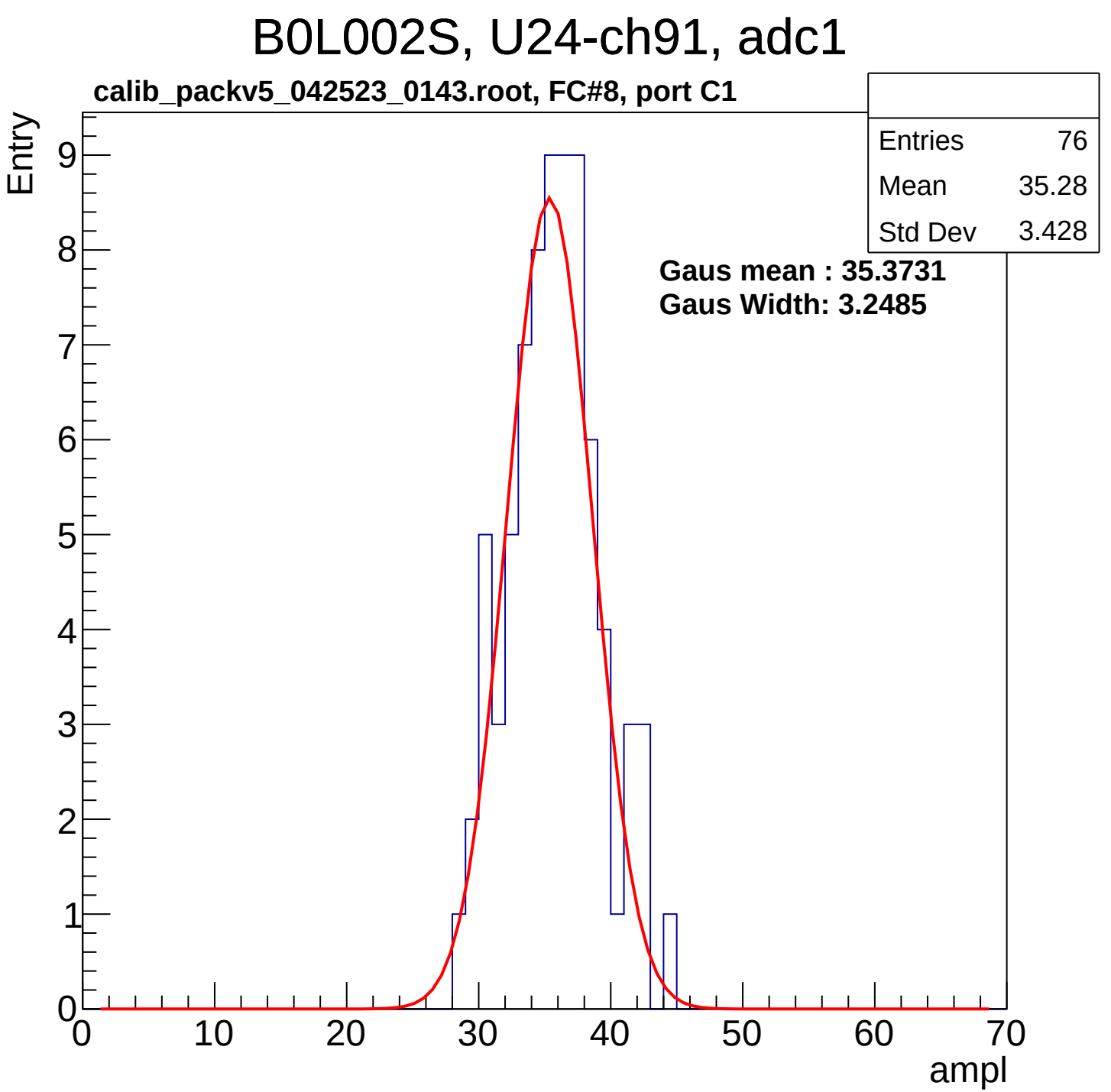
B0L002S, U24-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 35.28
Std Dev 3.428
Gaus mean : 35.3731
Gaus Width: 3.2485
Entry
ampl
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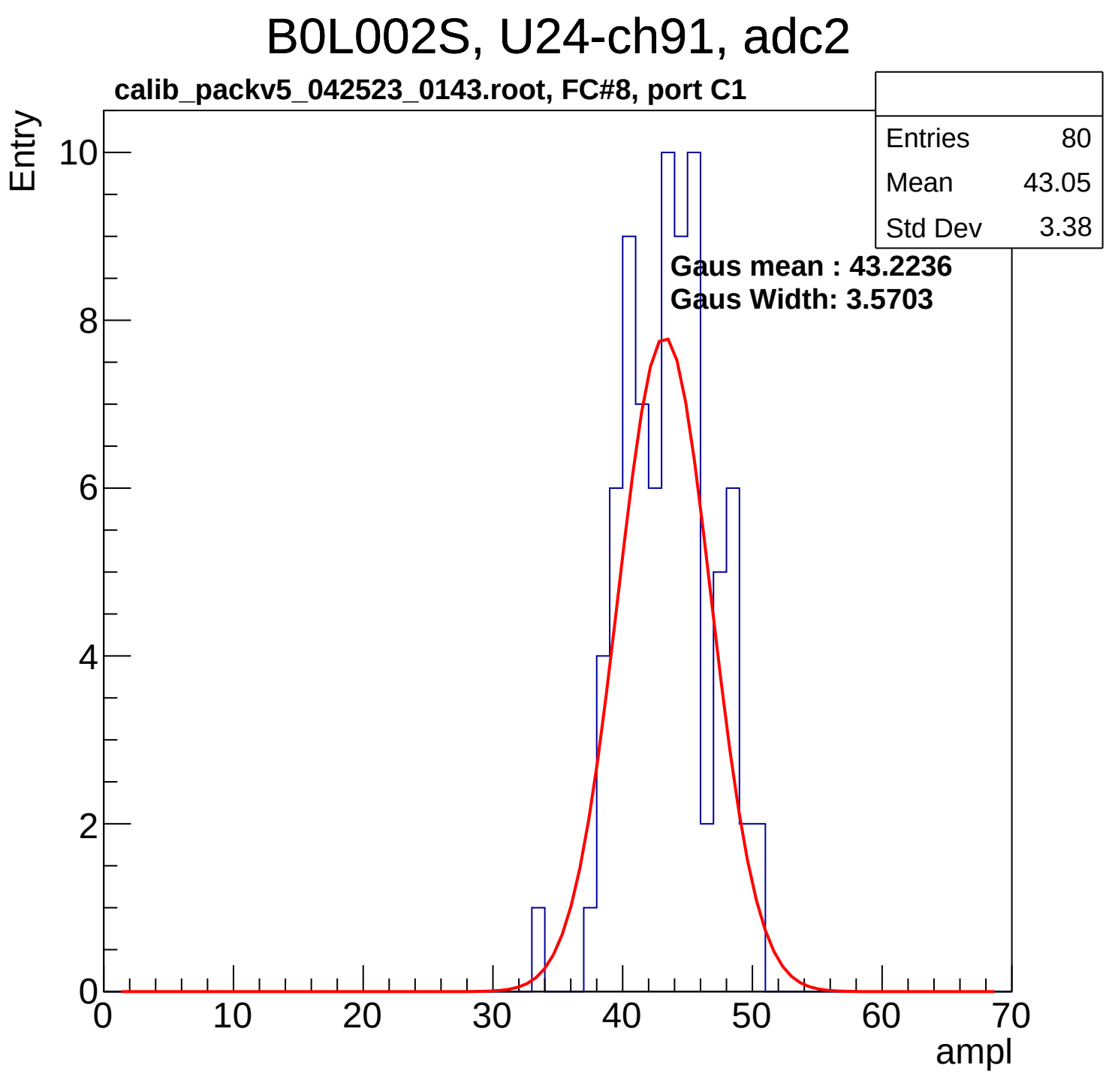
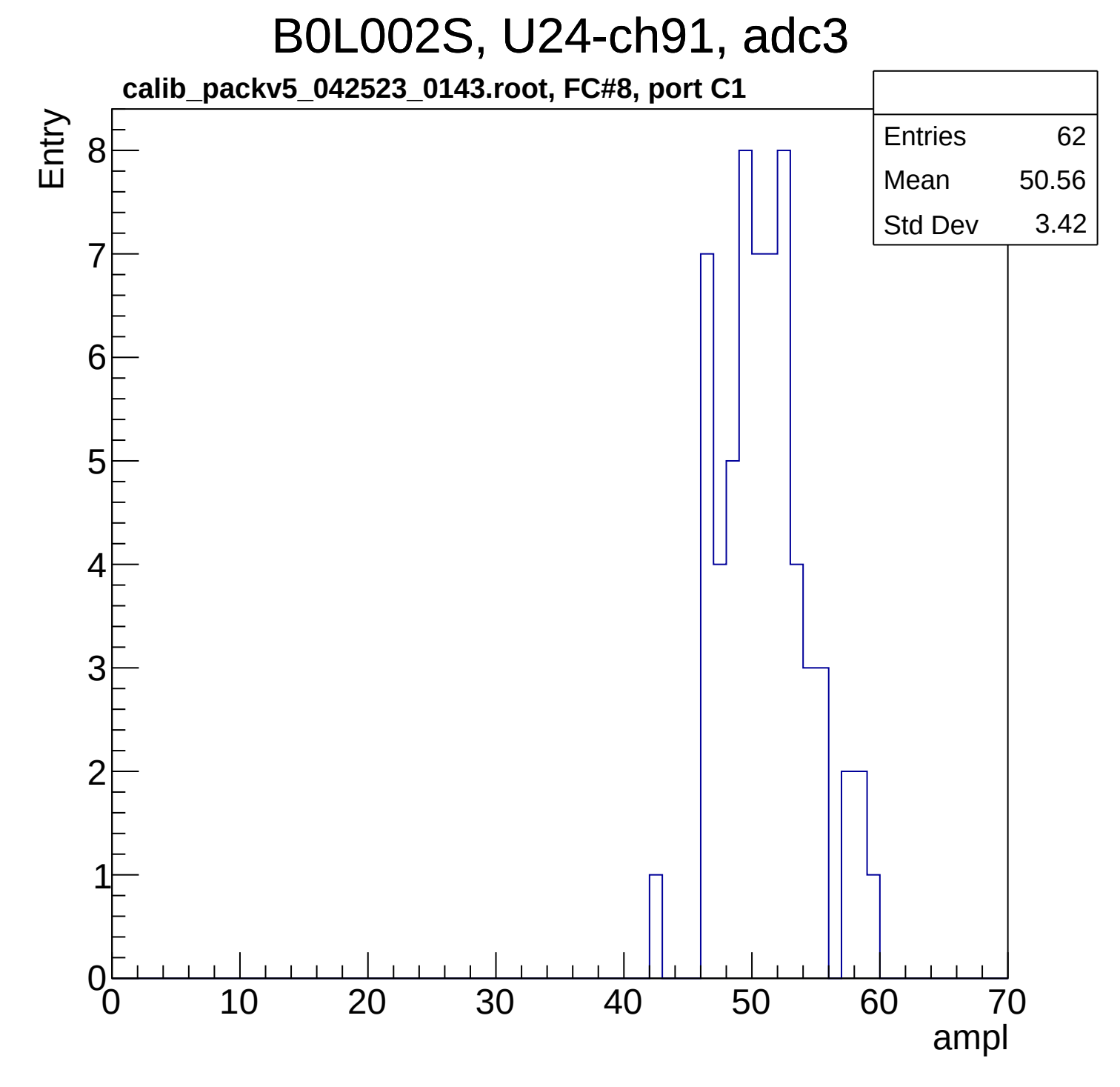
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.05
Std Dev 3.38
Gaus mean : 43.2236
Gaus Width: 3.5703
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.56
Std Dev 3.42
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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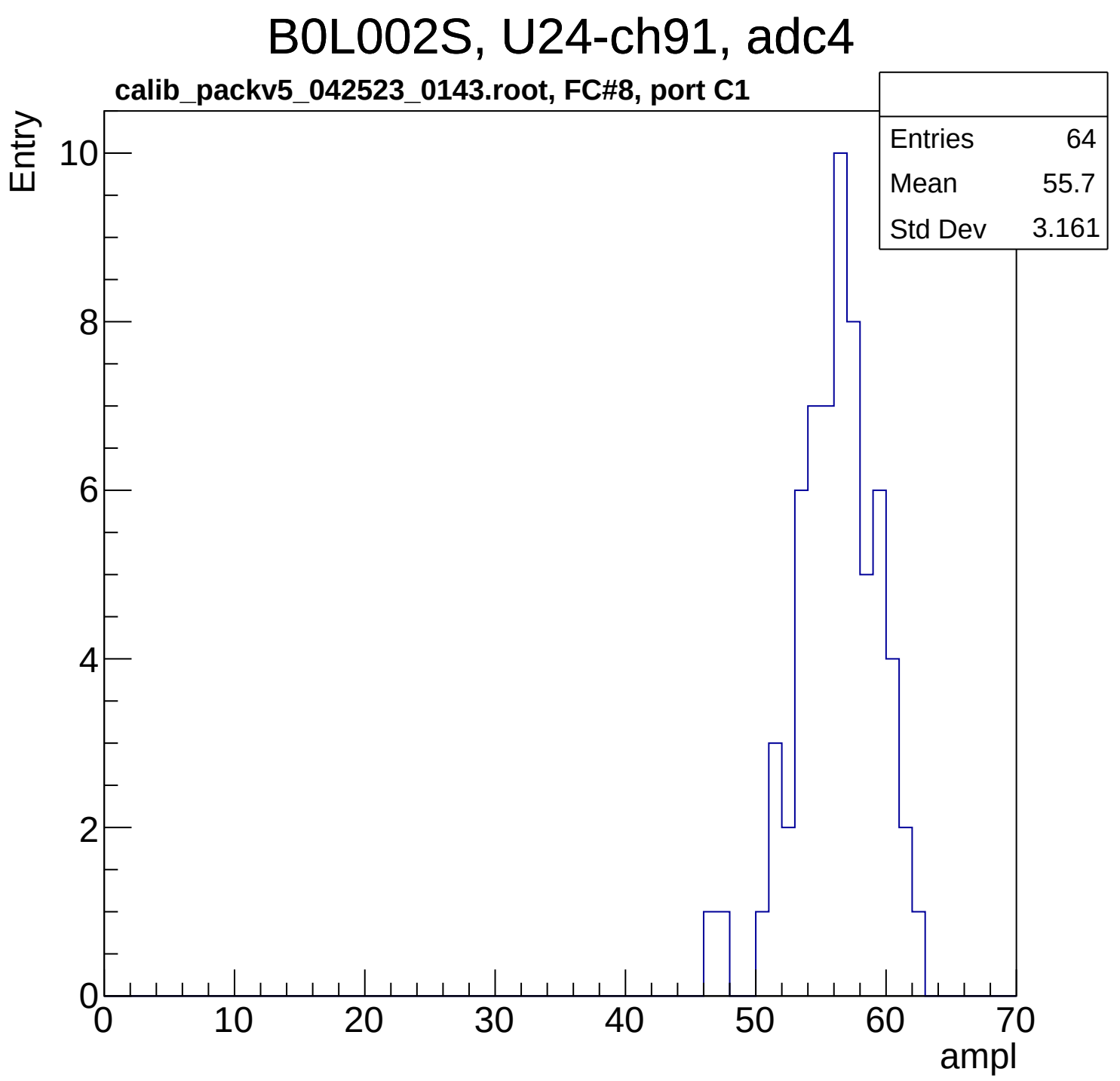
B0L002S, U24-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.7
Std Dev 3.161
Entry
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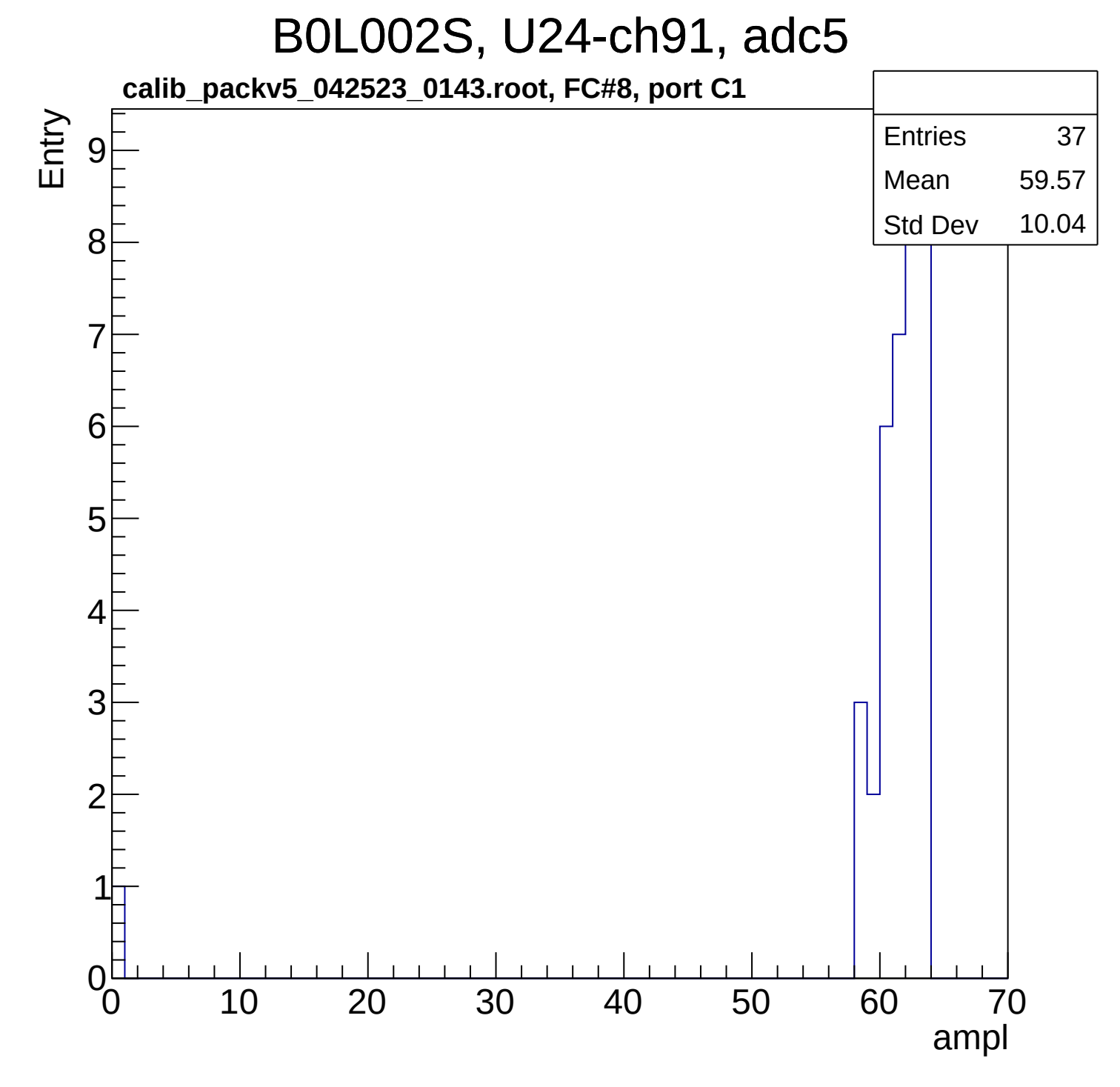
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.57
Std Dev 10.04
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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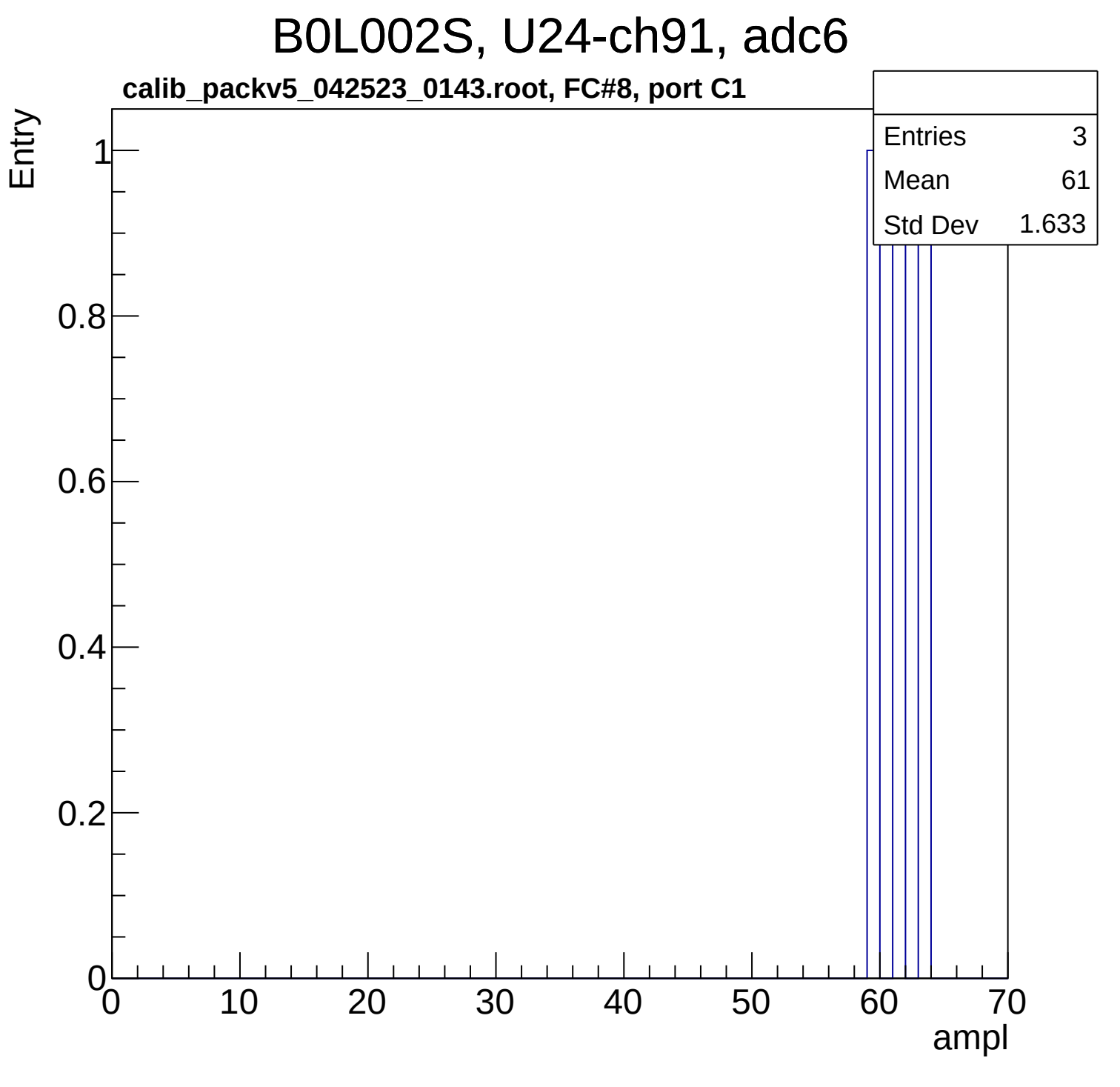
B0L002S, U24-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61
Std Dev 1.633
Entry
ampl
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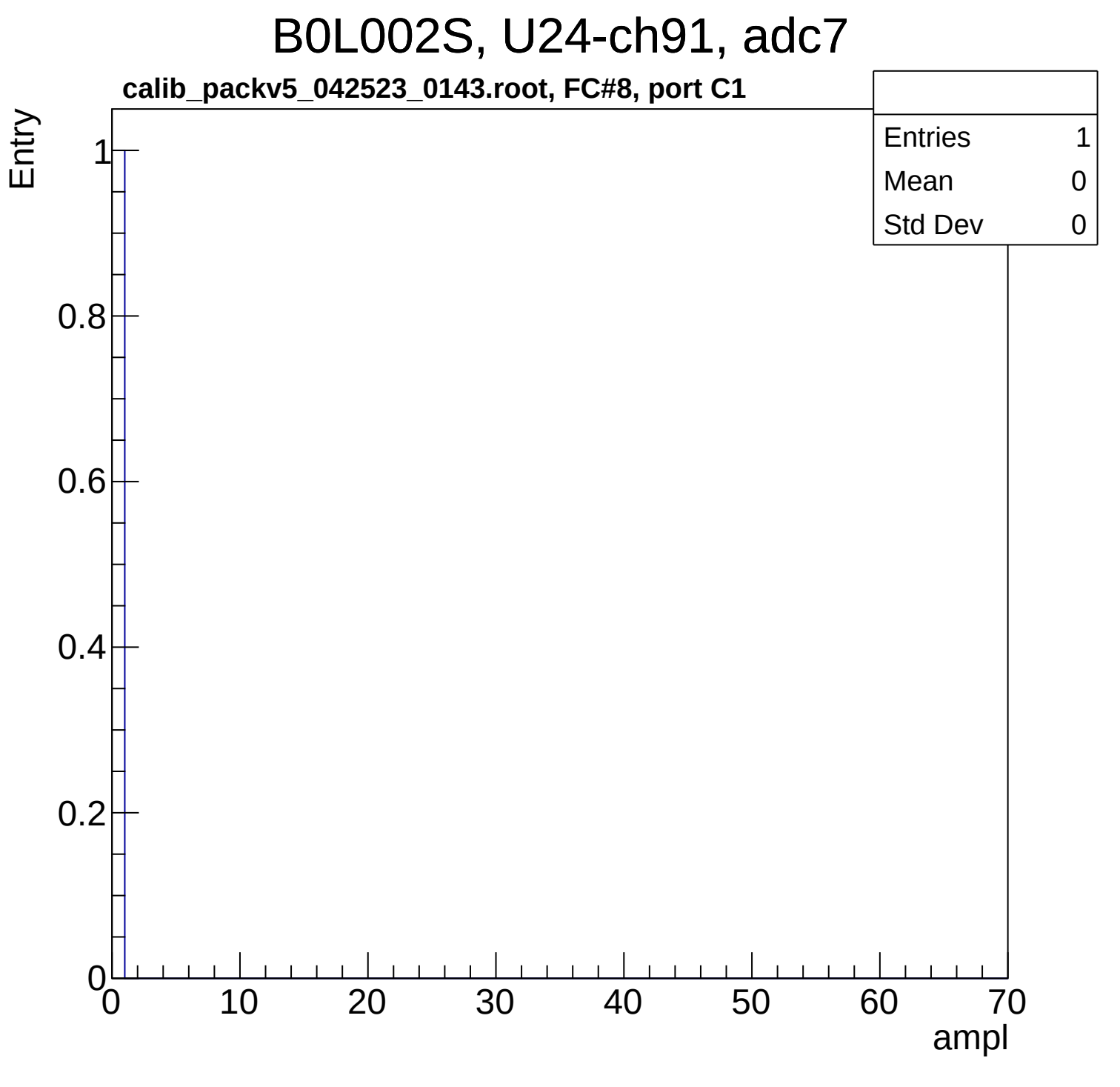
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L002S, U24-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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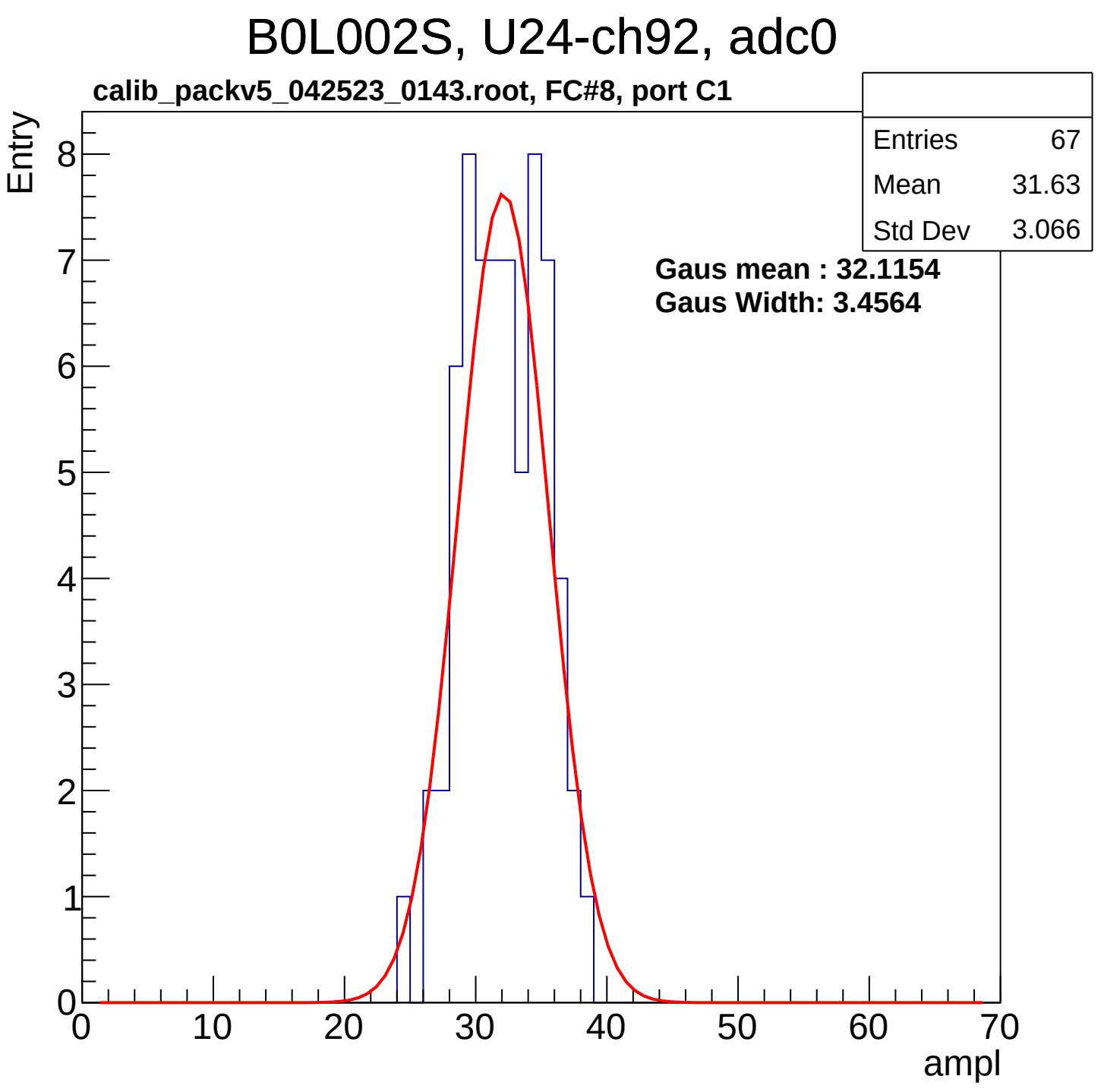
50
60
70
ampl


B0L002S, U24-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.63
Std Dev 3.066
Gaus mean : 32.1154
Gaus Width: 3.4564
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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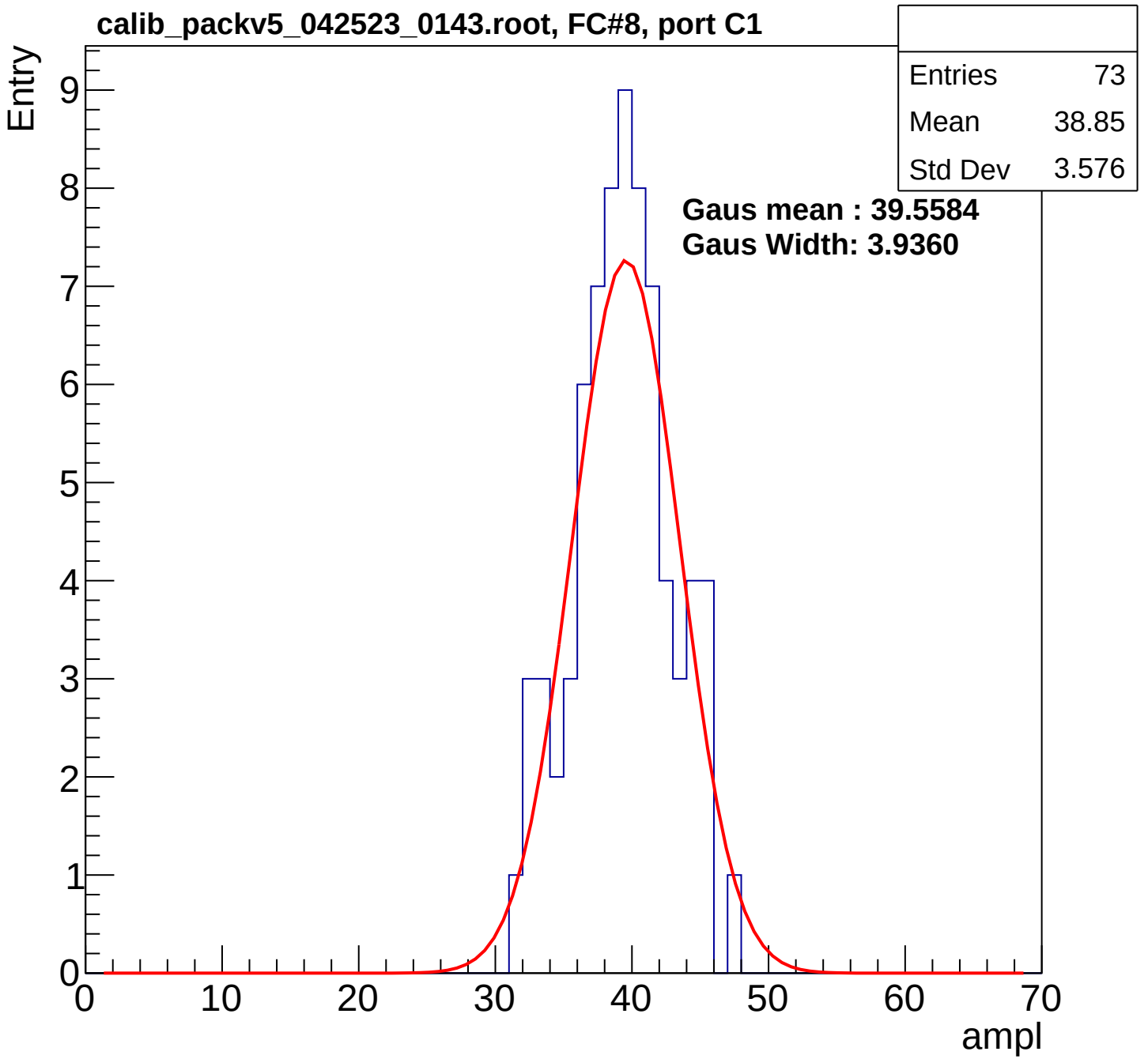
40
50
60
70


B0L002S, U24-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.85
Std Dev 3.576
Gaus mean : 39.5584
Gaus Width: 3.9360
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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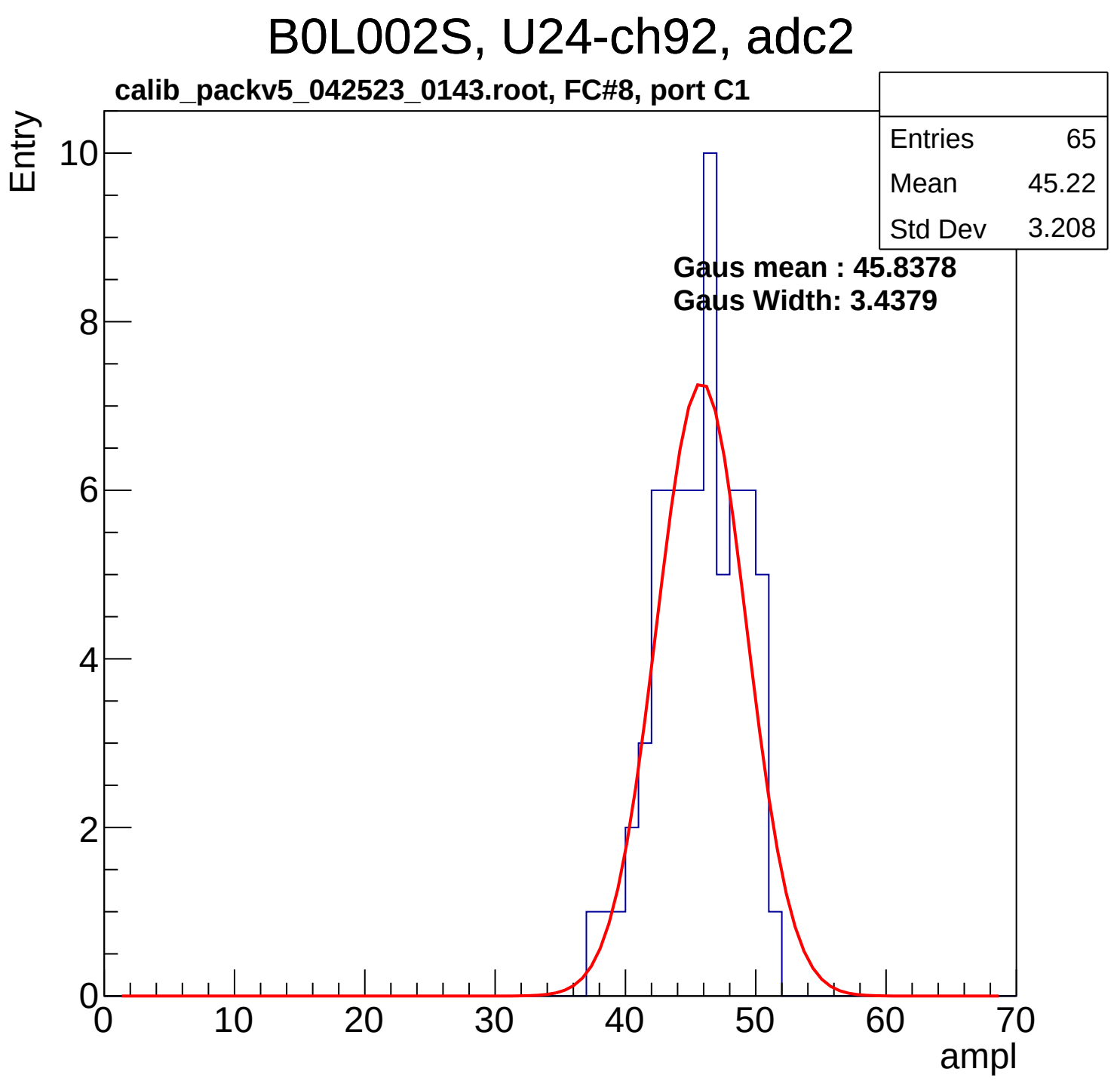
B0L002S, U24-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.22
Std Dev 3.208
Gaus mean : 45.8378
Gaus Width: 3.4379
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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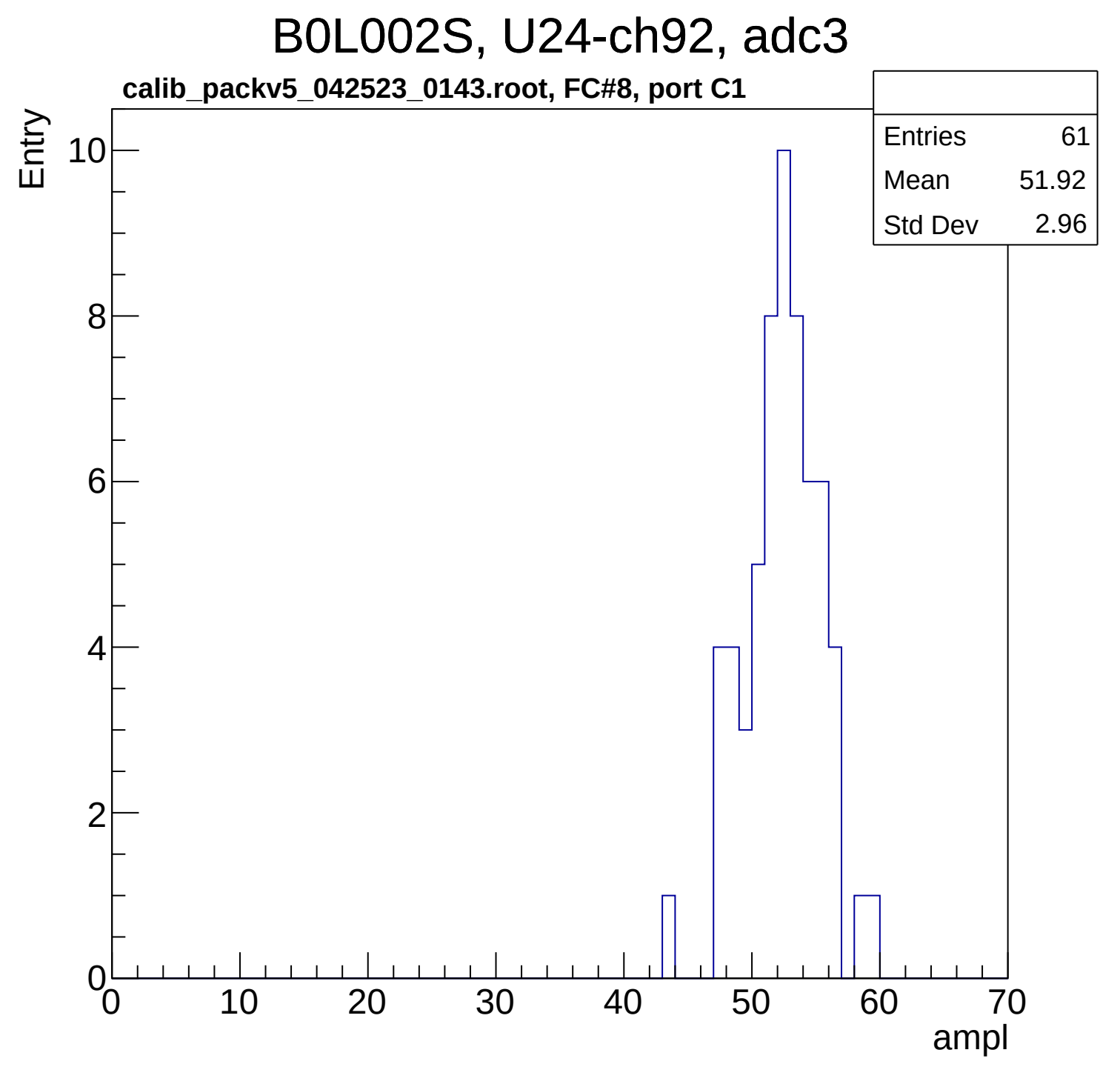
B0L002S, U24-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.92
Std Dev 2.96
Entry
ampl
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6
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10
0
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50
60
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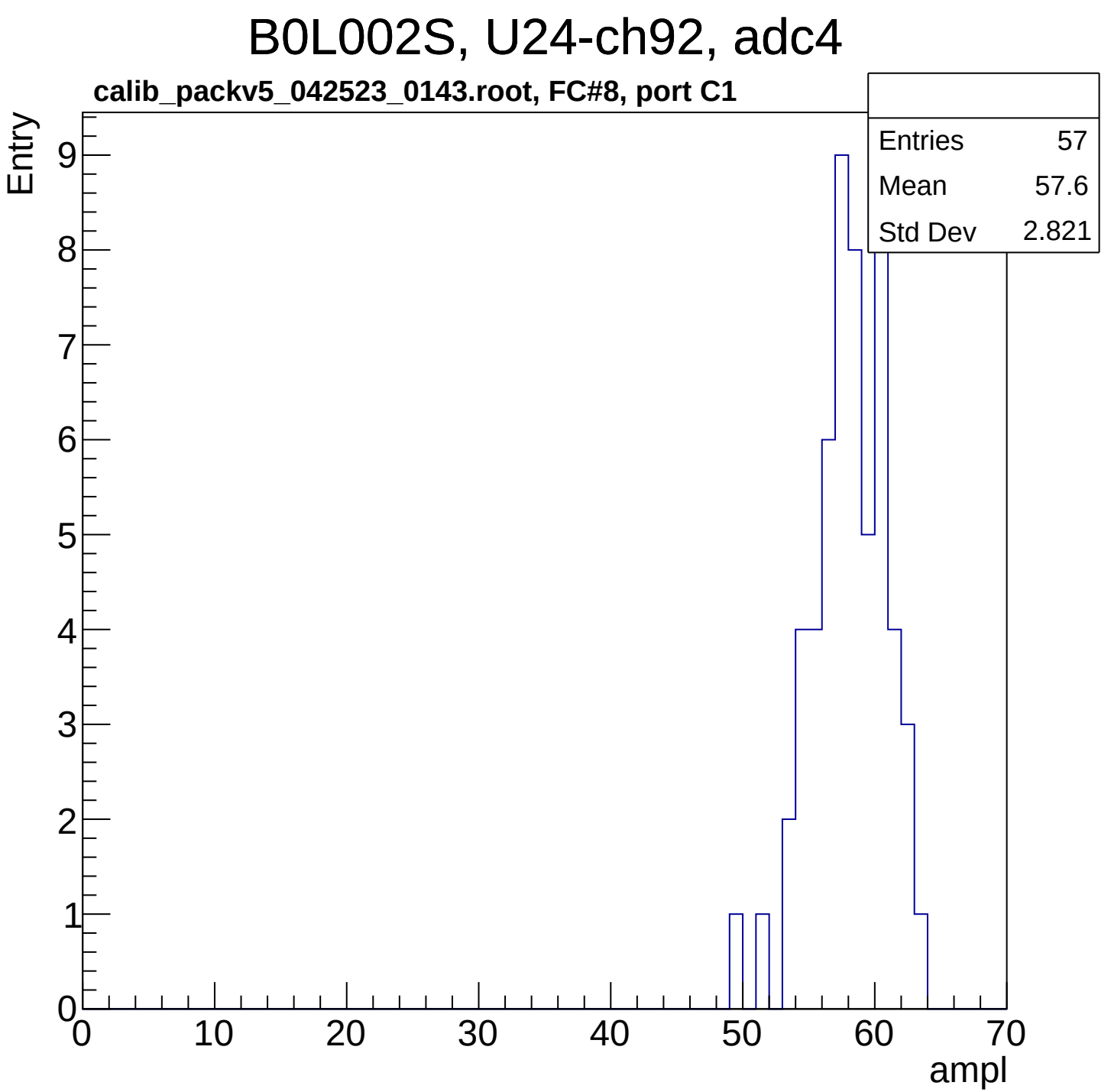
B0L002S, U24-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.6
Std Dev 2.821
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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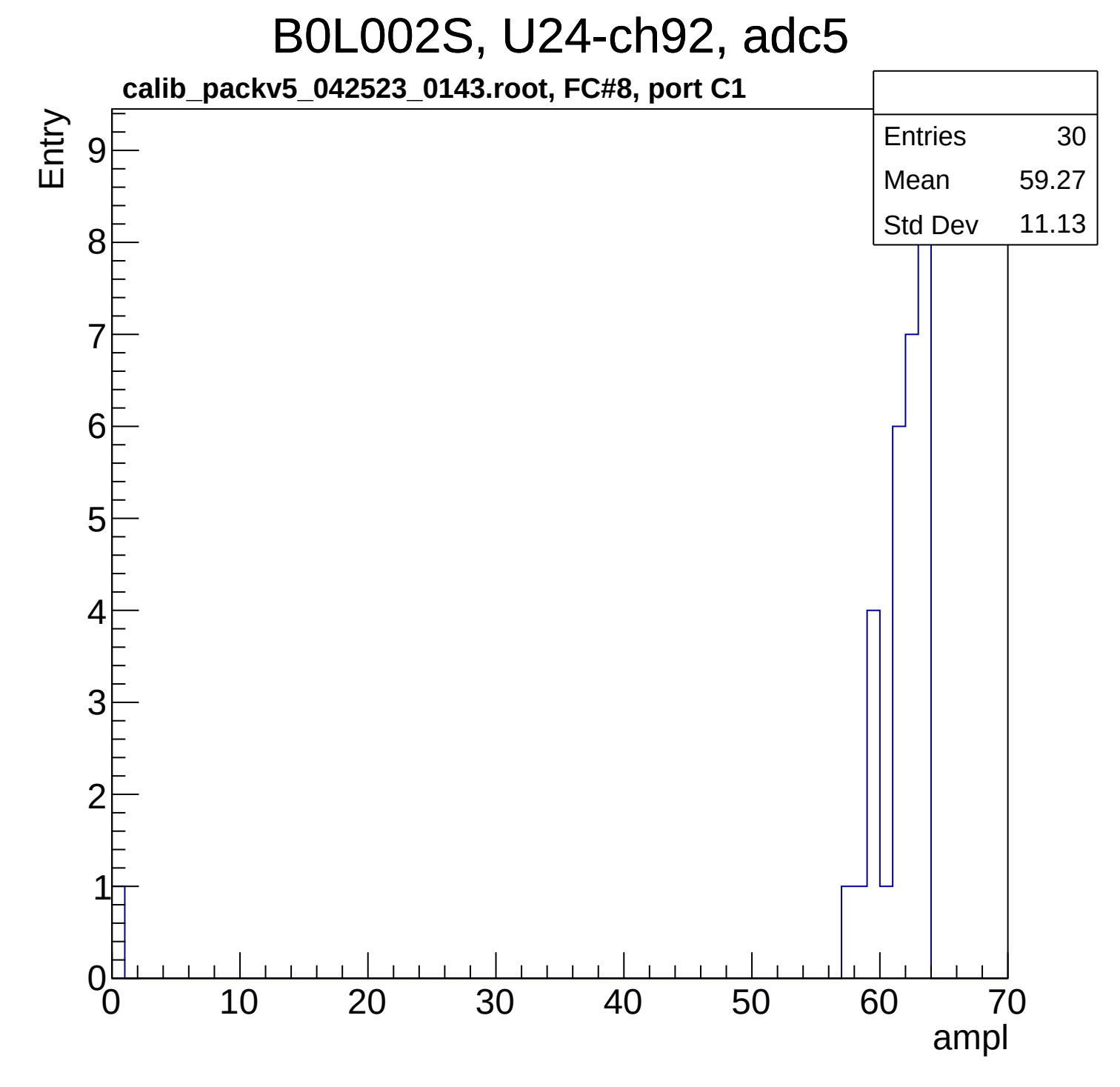
B0L002S, U24-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.27
Std Dev 11.13
Entry
ampl
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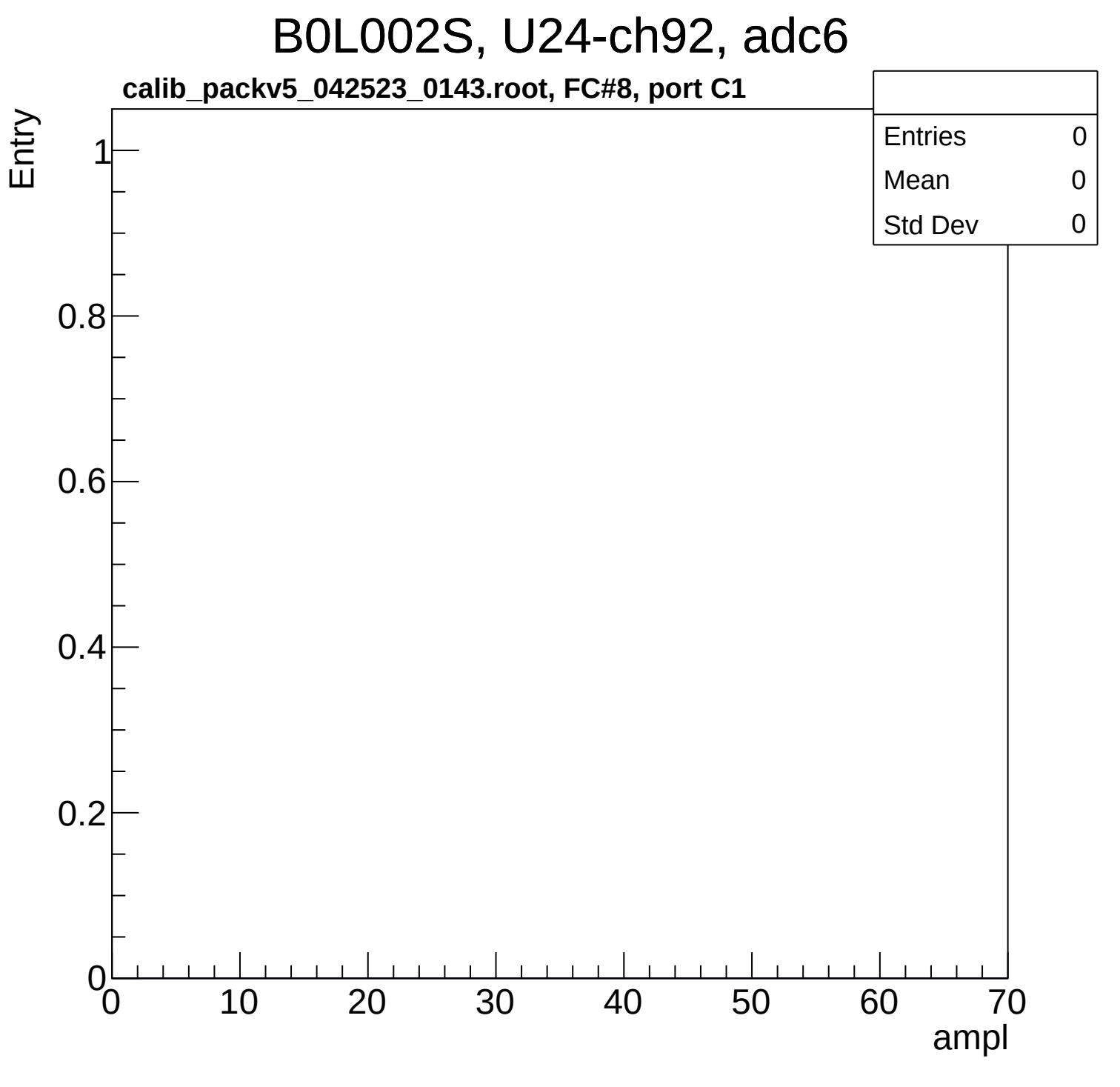
B0L002S, U24-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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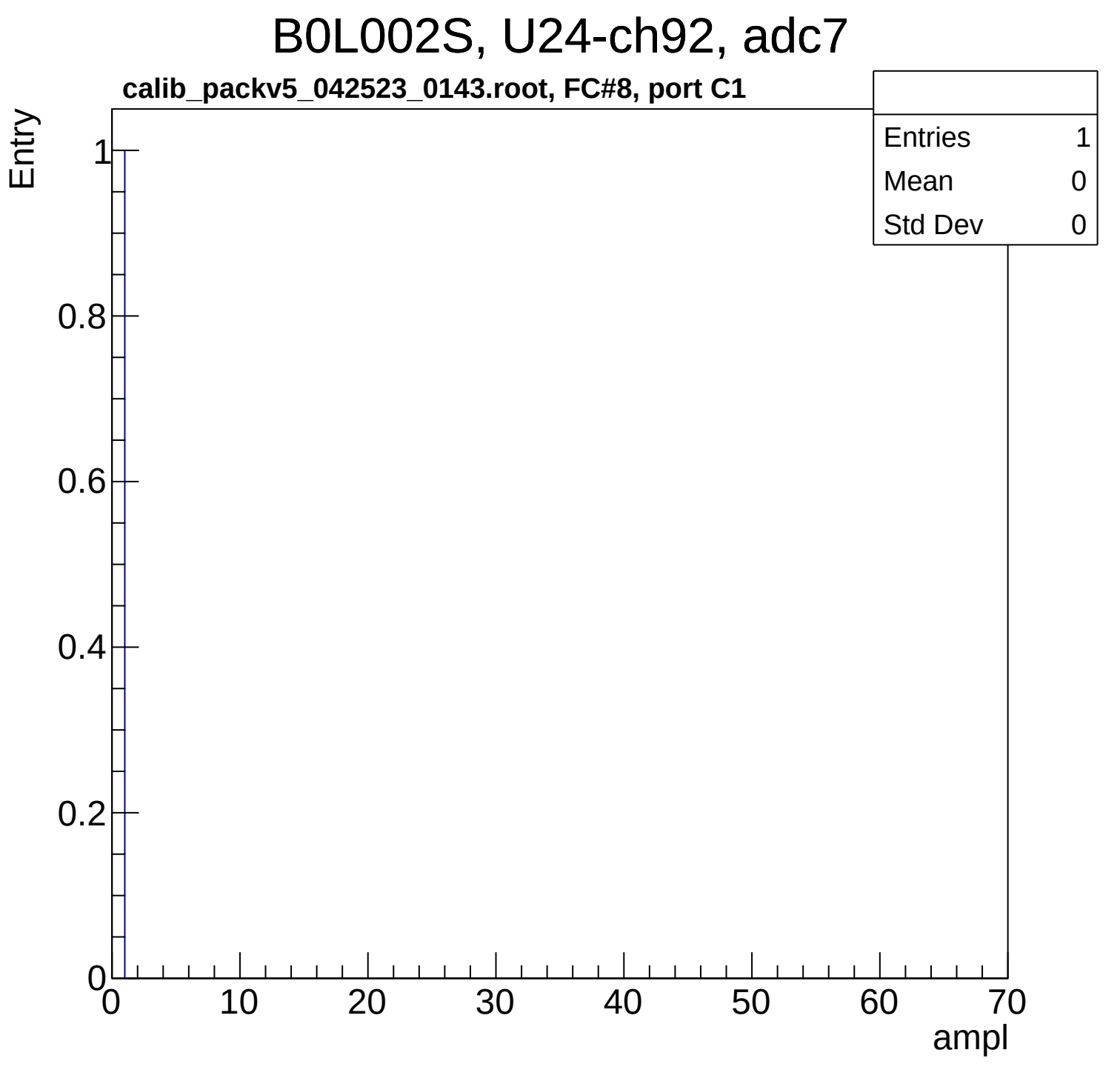
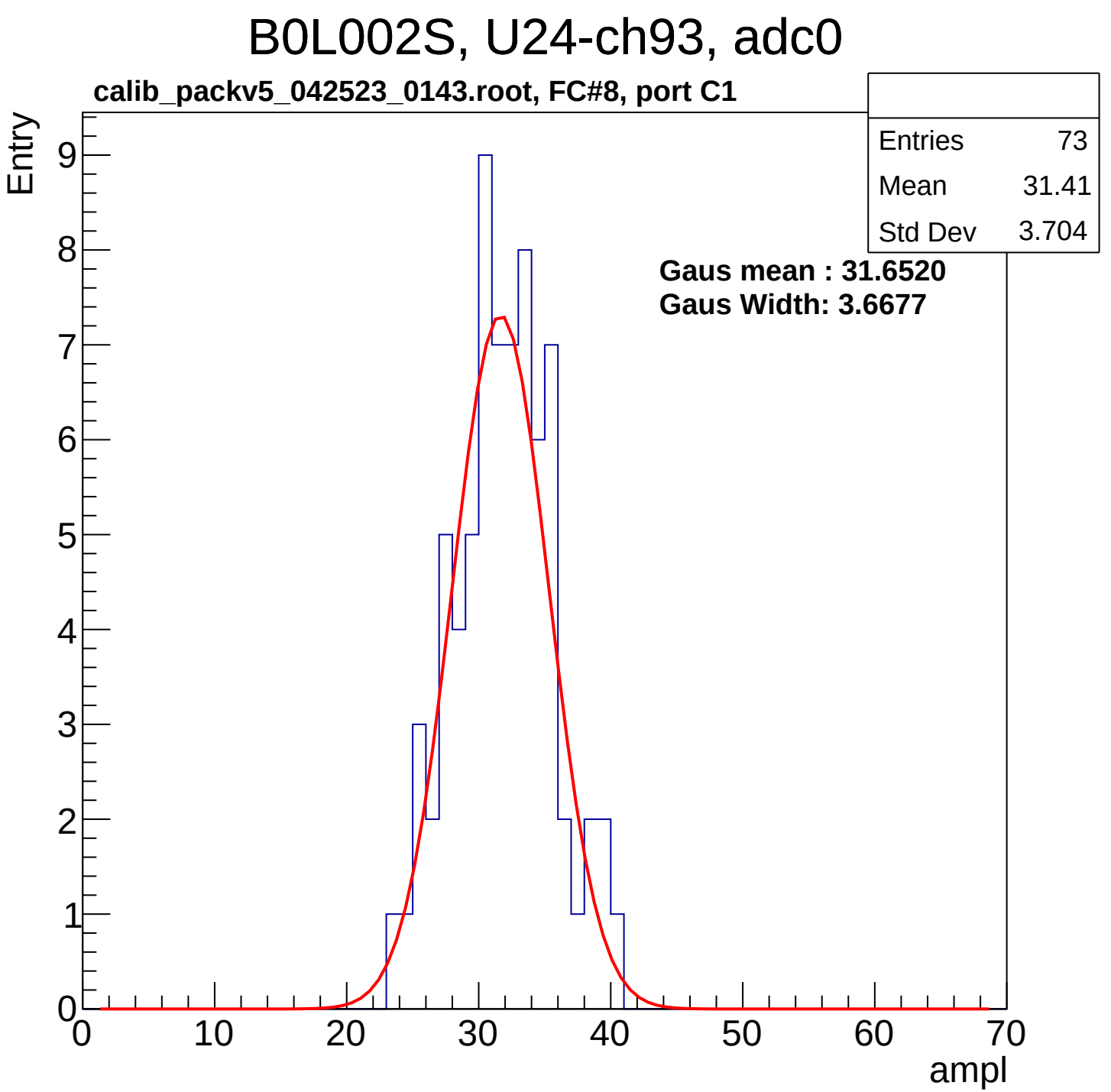
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 31.41
Std Dev 3.704
Gaus mean : 31.6520
Gaus Width: 3.6677
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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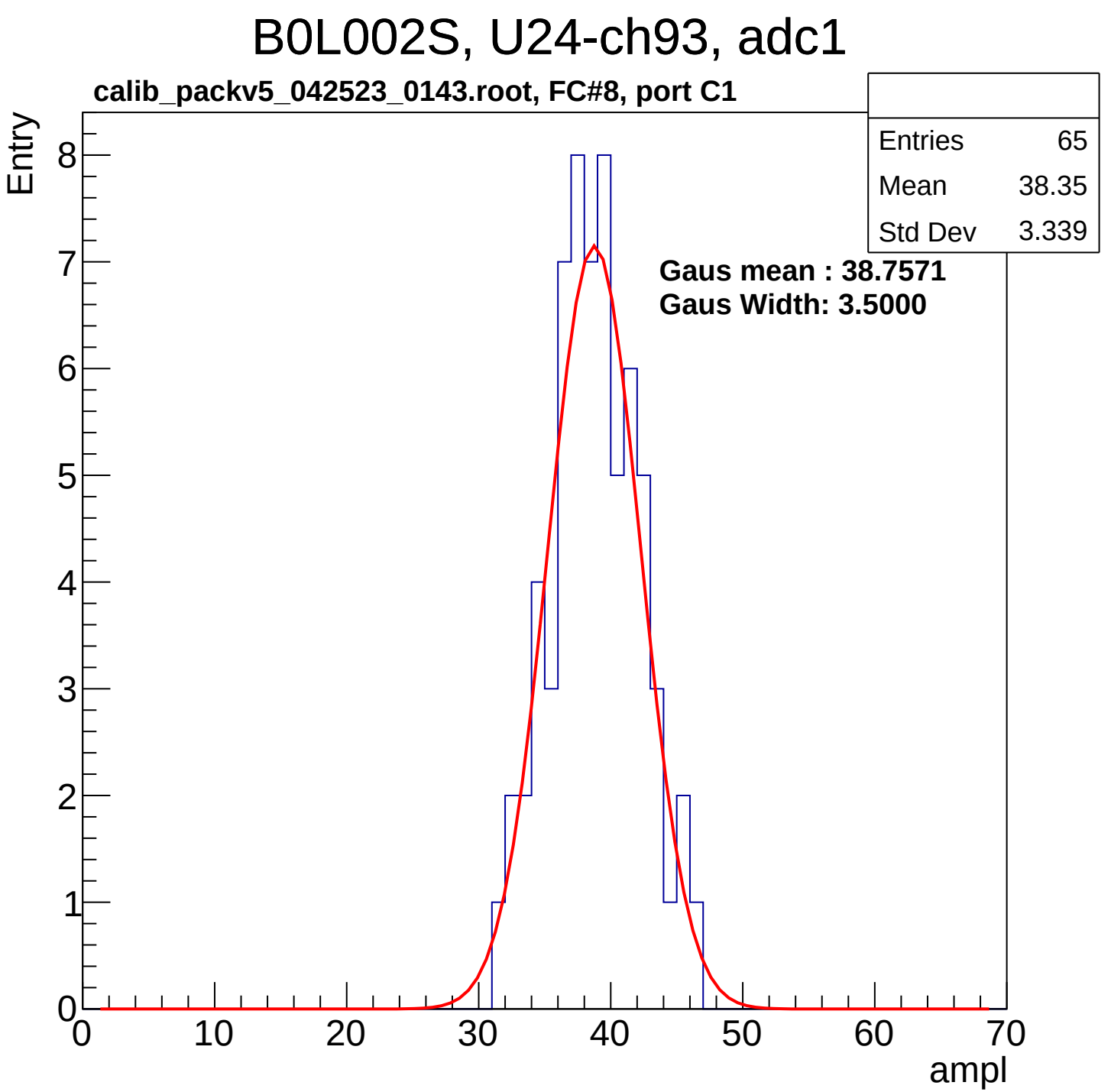
B0L002S, U24-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.35
Std Dev 3.339
Gaus mean : 38.7571
Gaus Width: 3.5000
Entry
ampl
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1
2
3
4
5
6
7
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0
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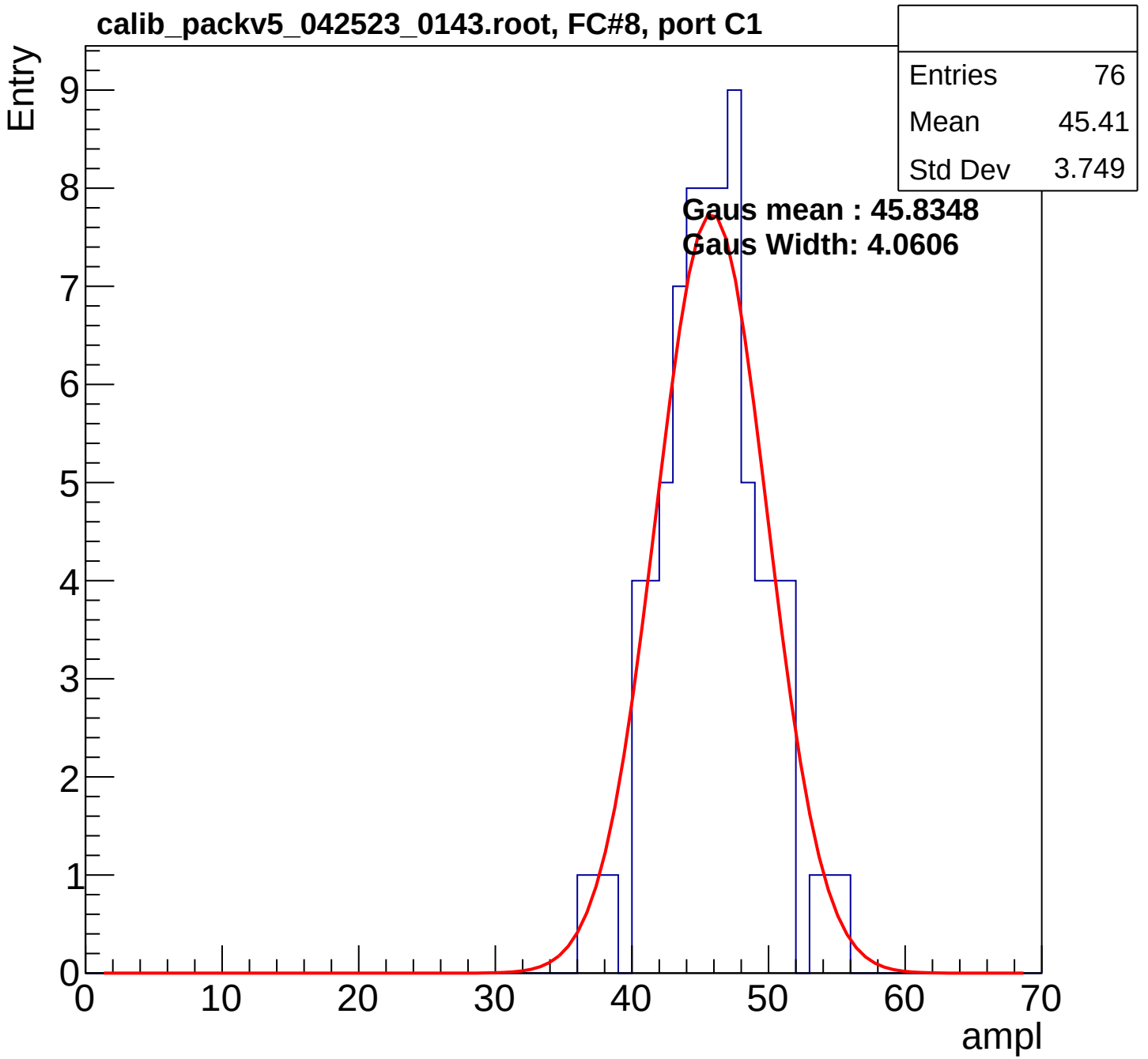
B0L002S, U24-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 45.41
Std Dev 3.749
Gaus mean : 45.8348
Gaus Width: 4.0606
Entry
ampl
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2
3
4
5
6
7
8
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0
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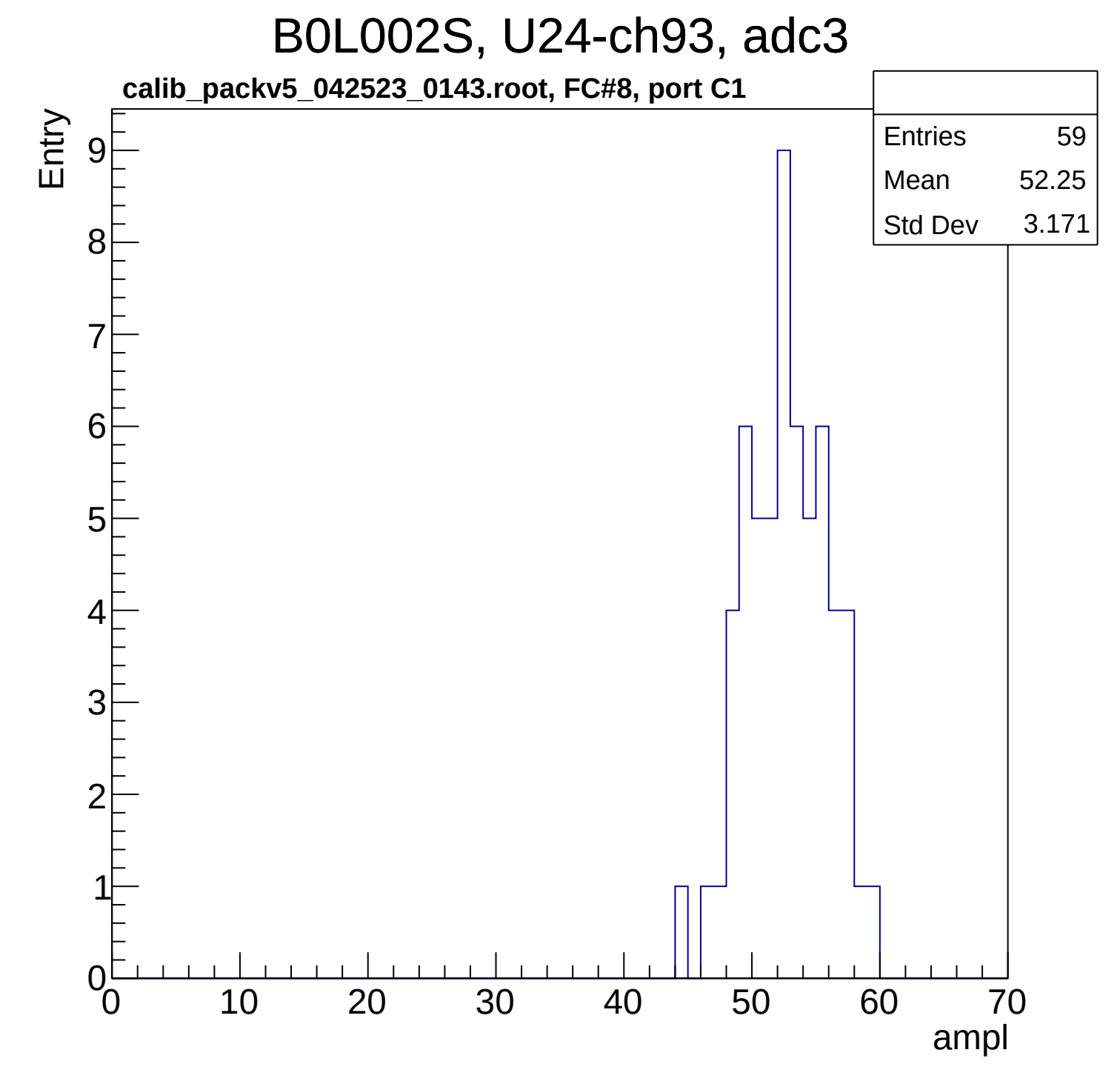
B0L002S, U24-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 52.25
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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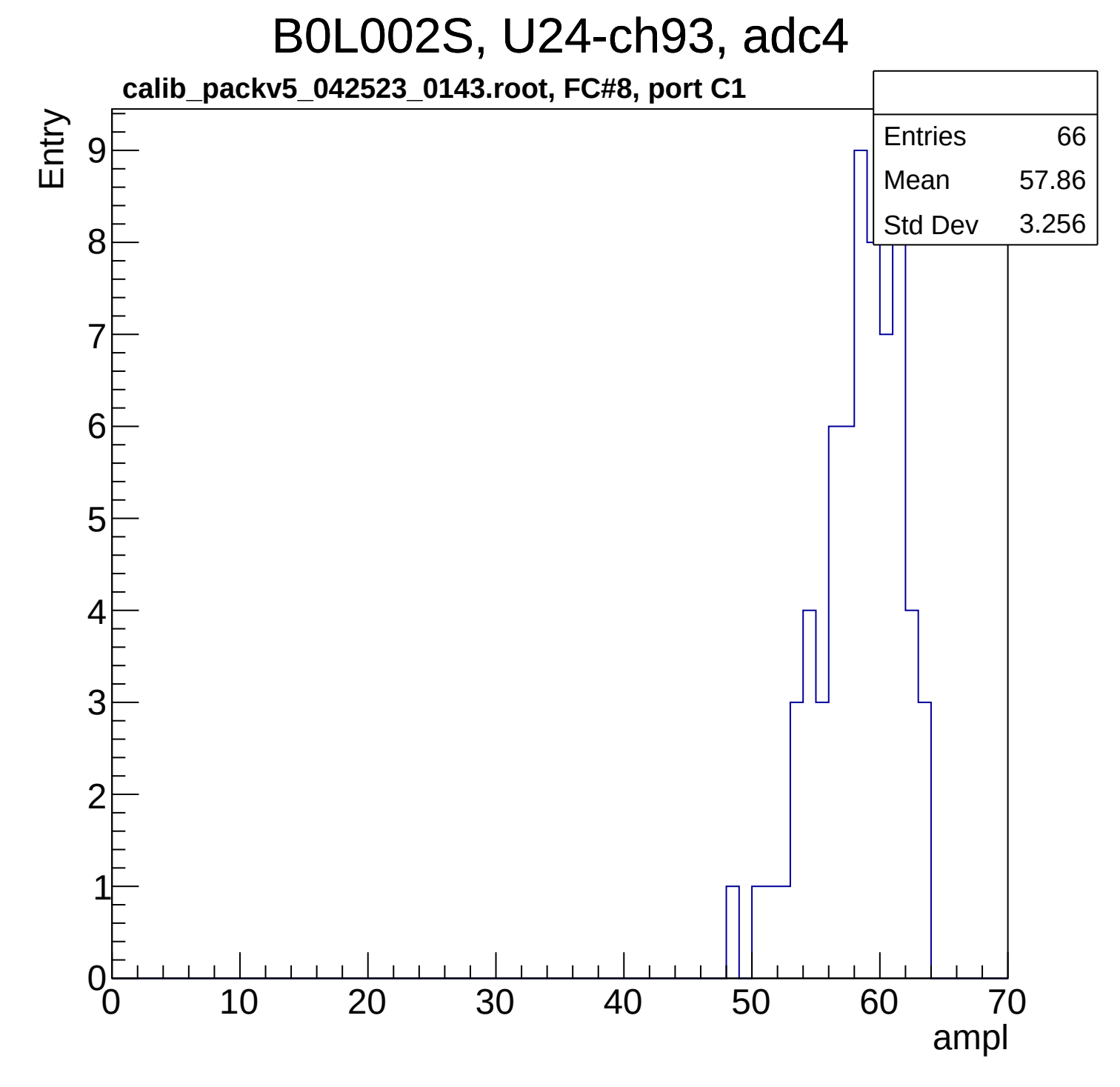
B0L002S, U24-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.86
Std Dev 3.256
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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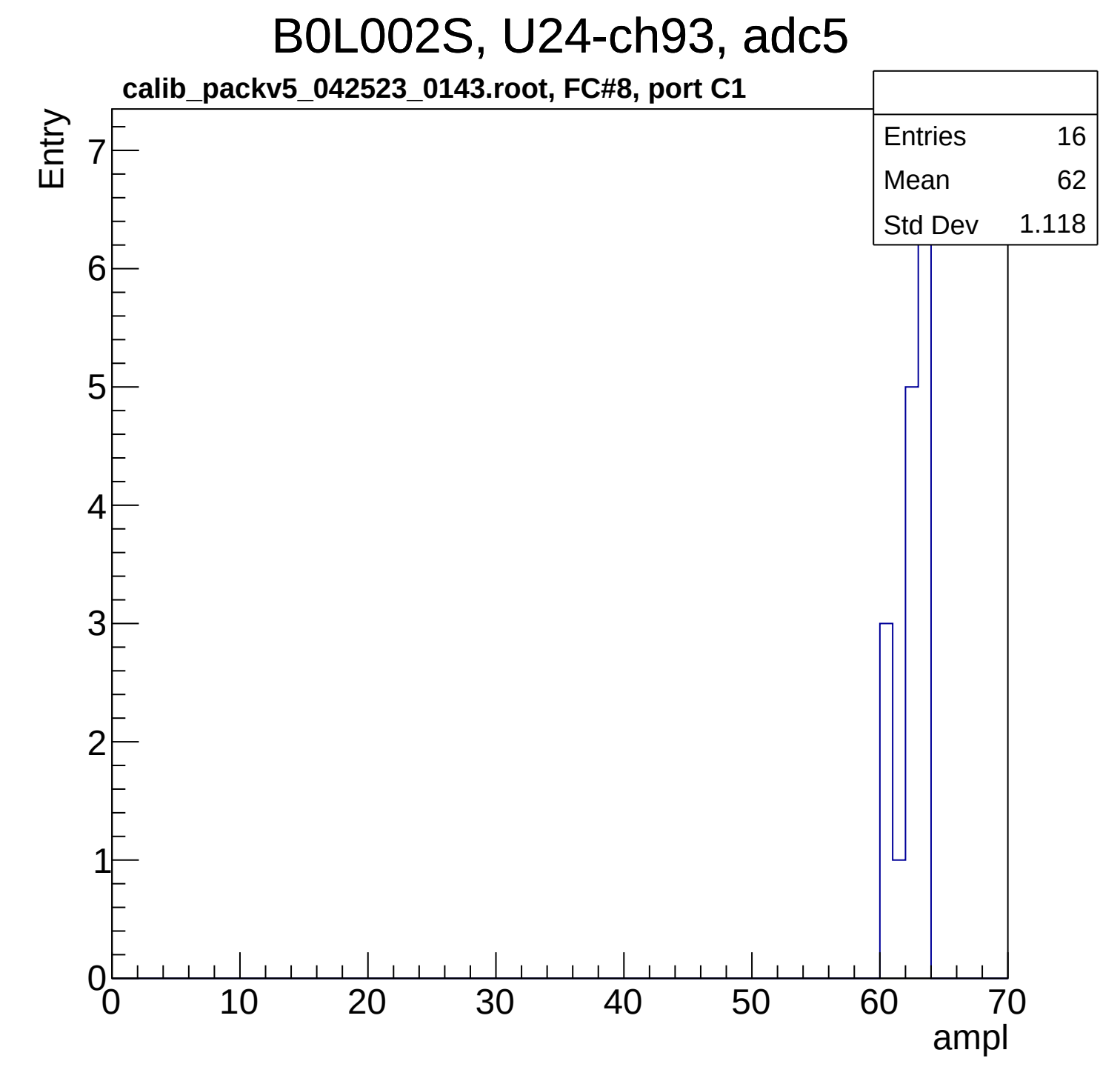
B0L002S, U24-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 62
Std Dev 1.118
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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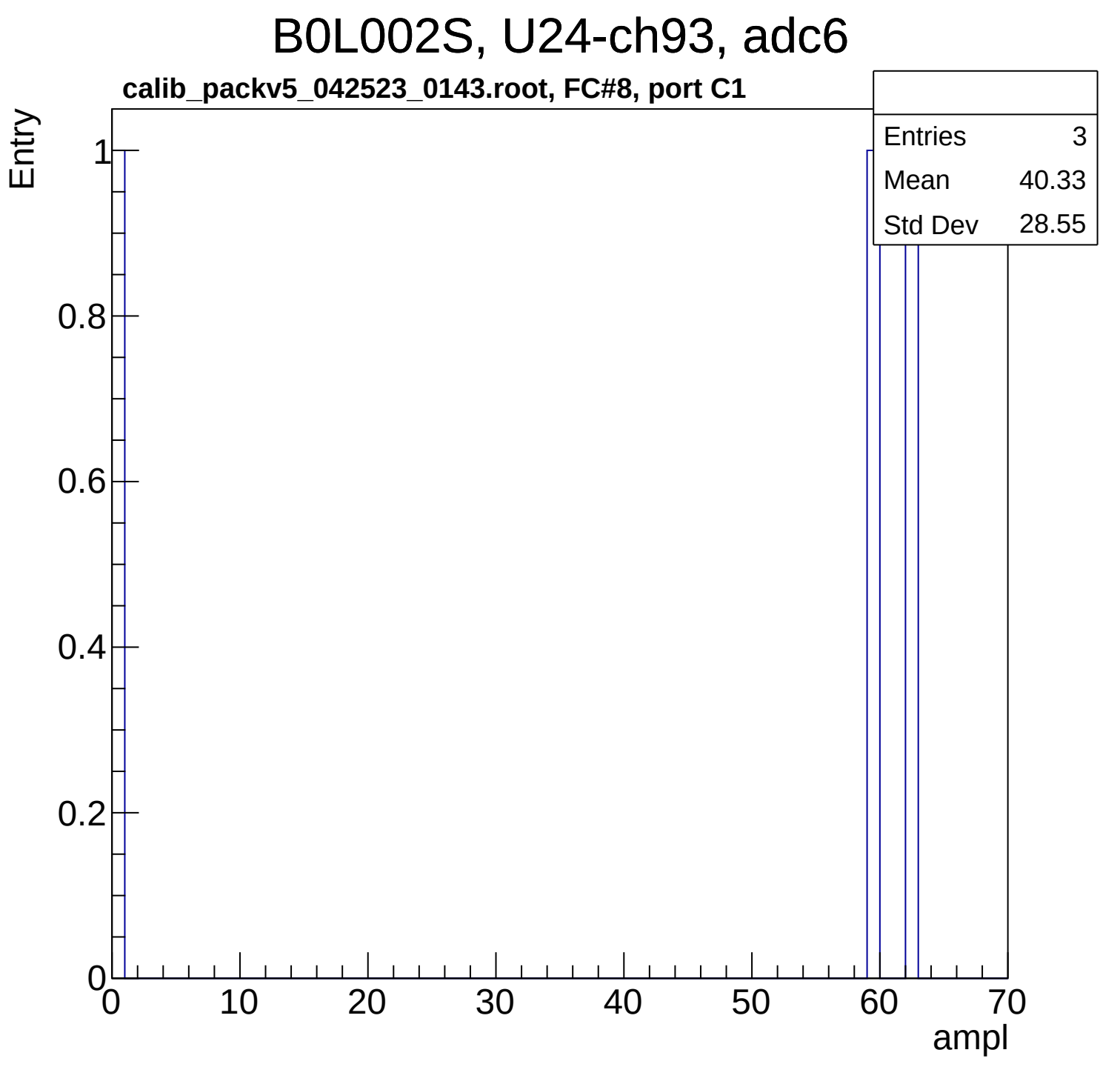
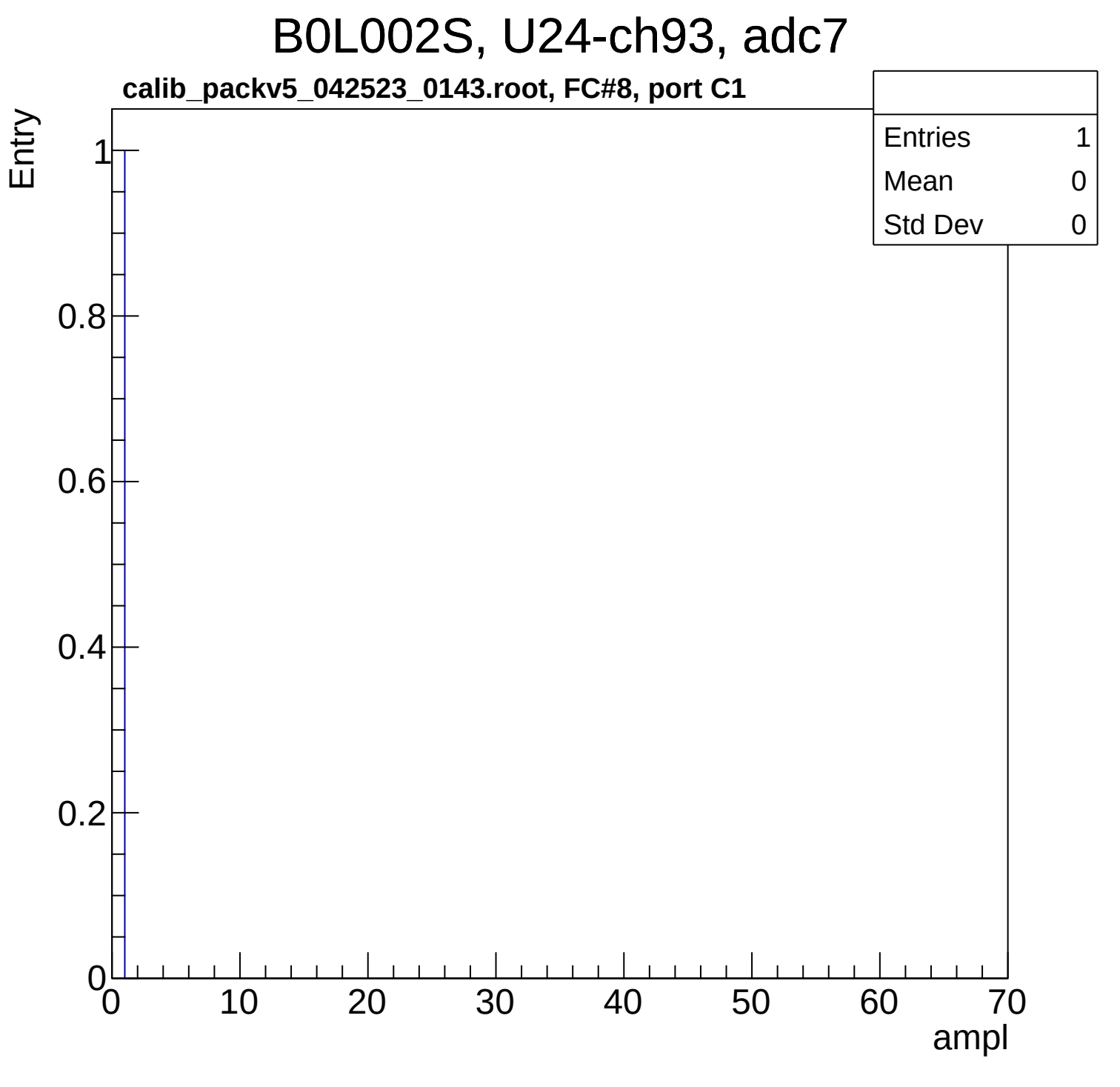

B0L002S, U24-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 40.33
Std Dev 28.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L002S, U24-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
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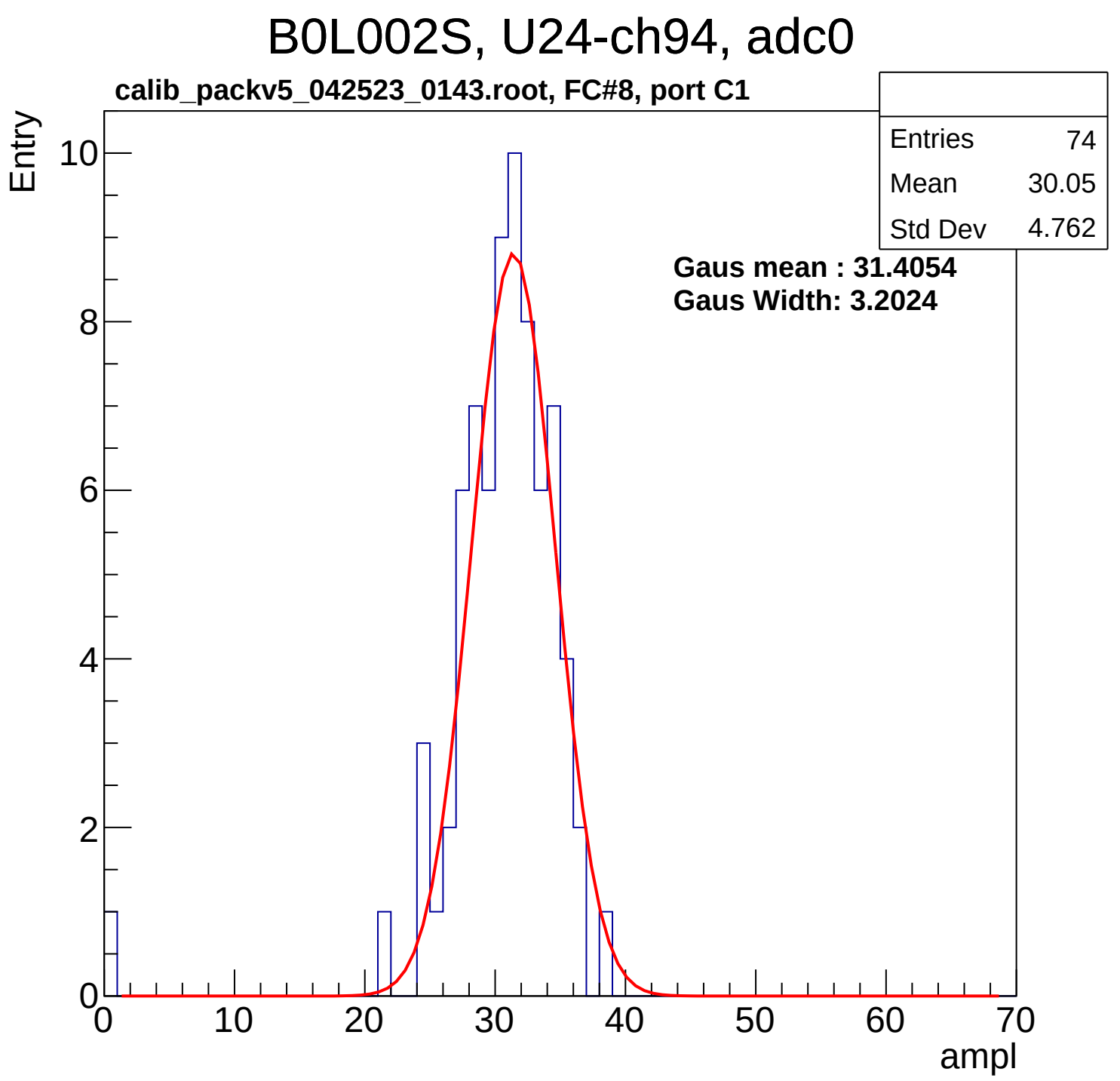
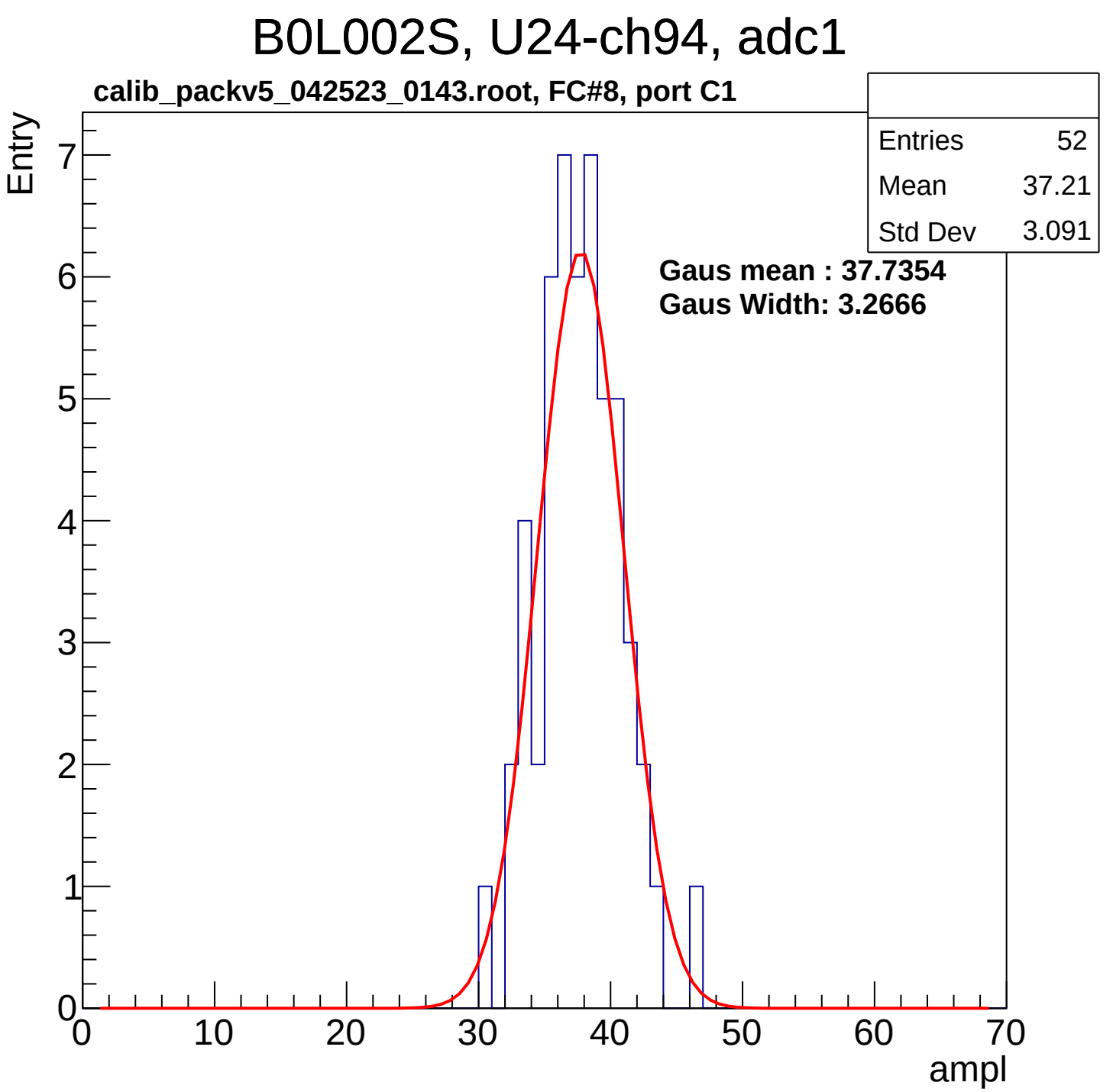
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 30.05
Std Dev 4.762
Gaus mean : 31.4054
Gaus Width: 3.2024
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 37.21
Std Dev 3.091
Gaus mean : 37.7354
Gaus Width: 3.2666
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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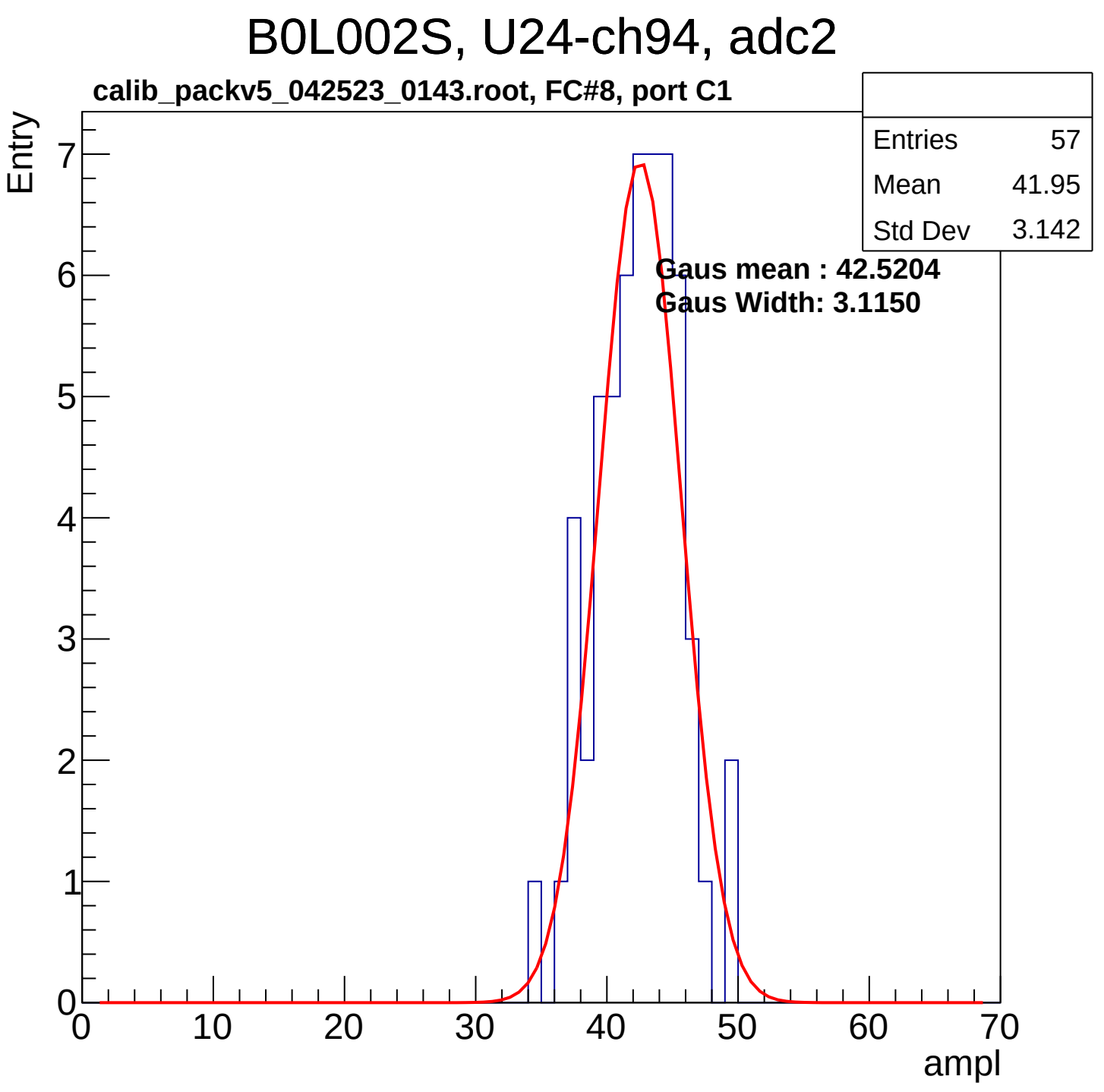
B0L002S, U24-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 41.95
Std Dev 3.142
Gaus mean : 42.5204
Gaus Width: 3.1150
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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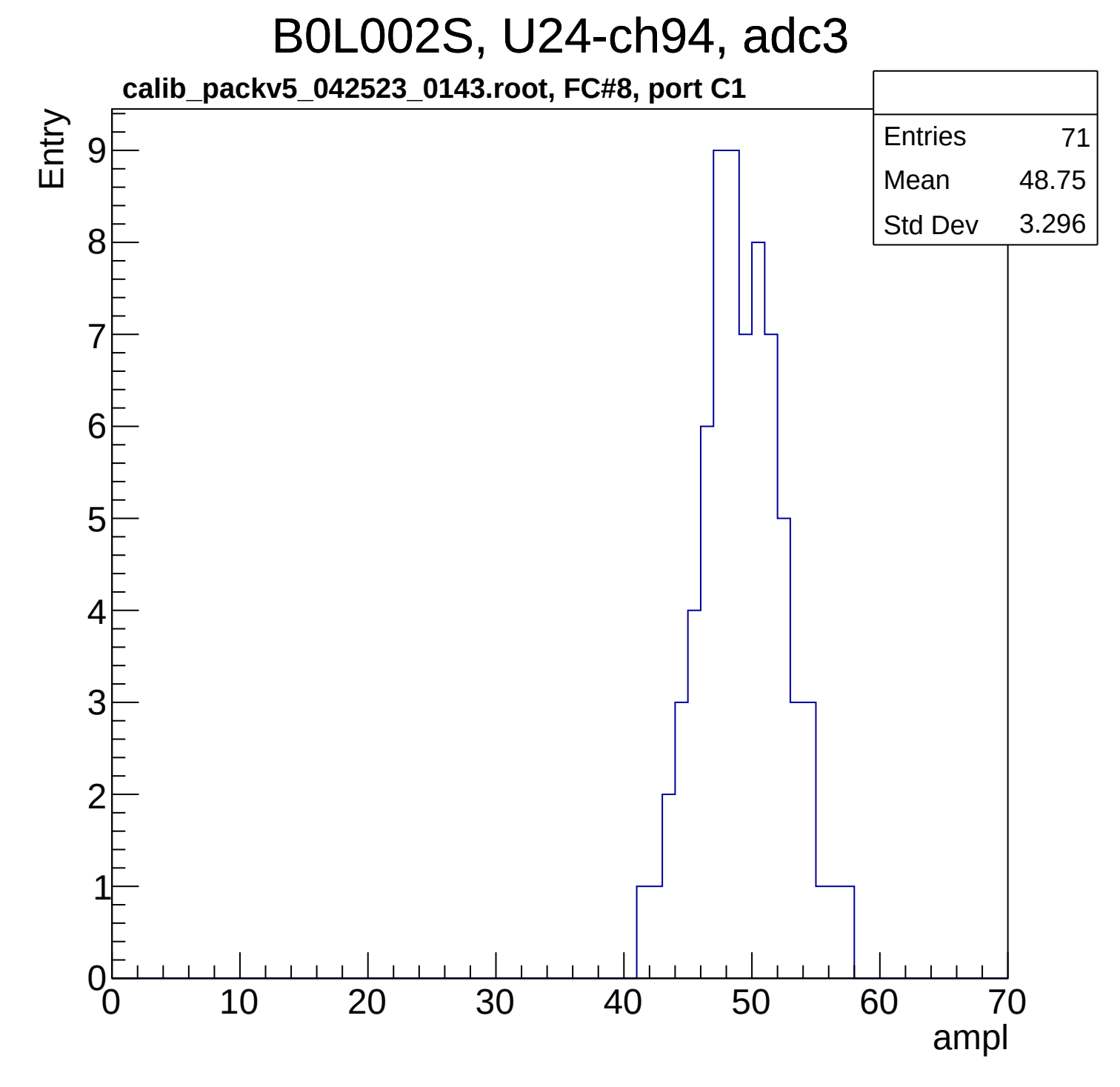
B0L002S, U24-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 48.75
Std Dev 3.296
Entry
ampl
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2
3
4
5
6
7
8
9
0
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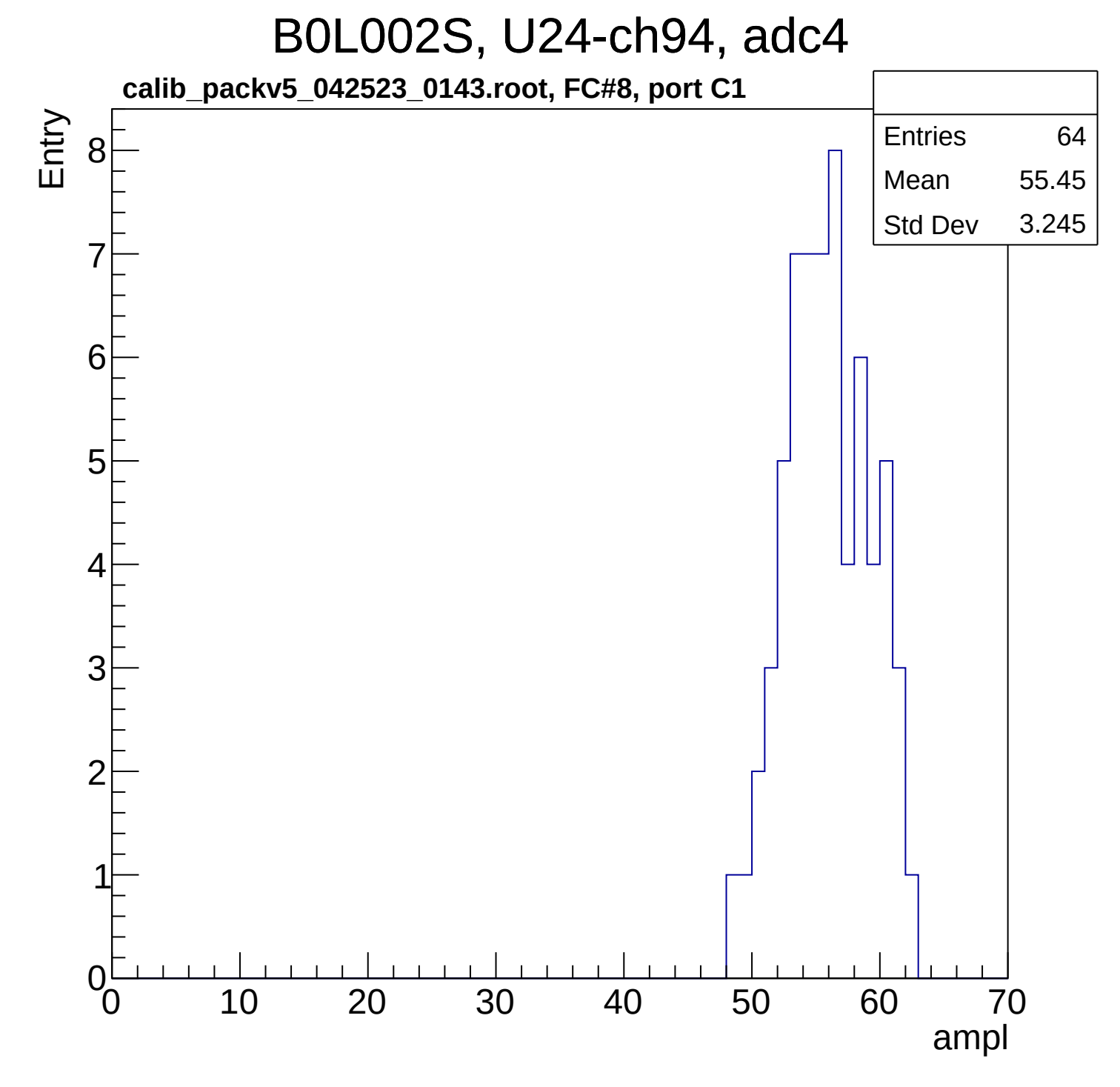
B0L002S, U24-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 55.45
Std Dev 3.245
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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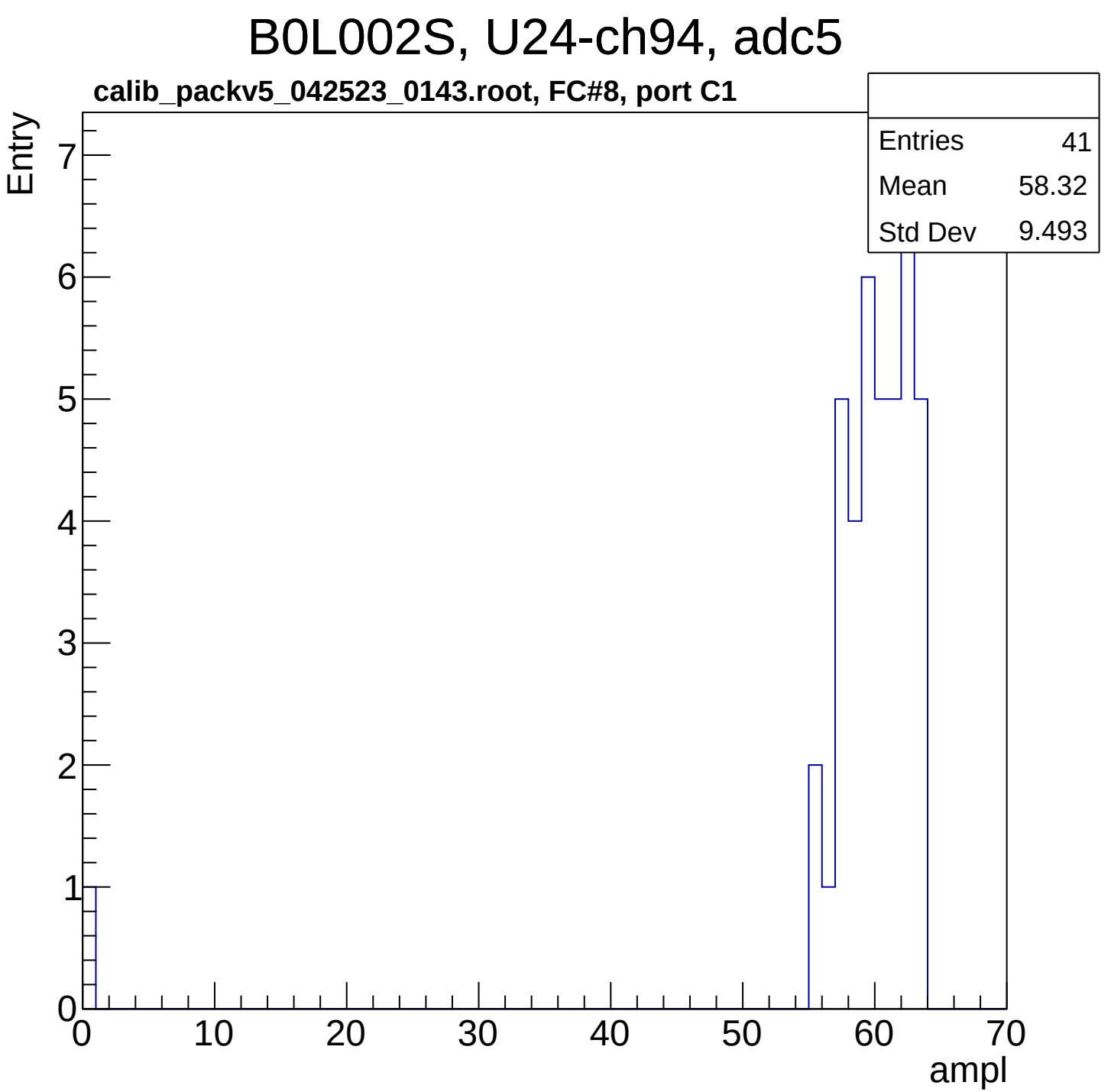
B0L002S, U24-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.32
Std Dev 9.493
Entry
ampl
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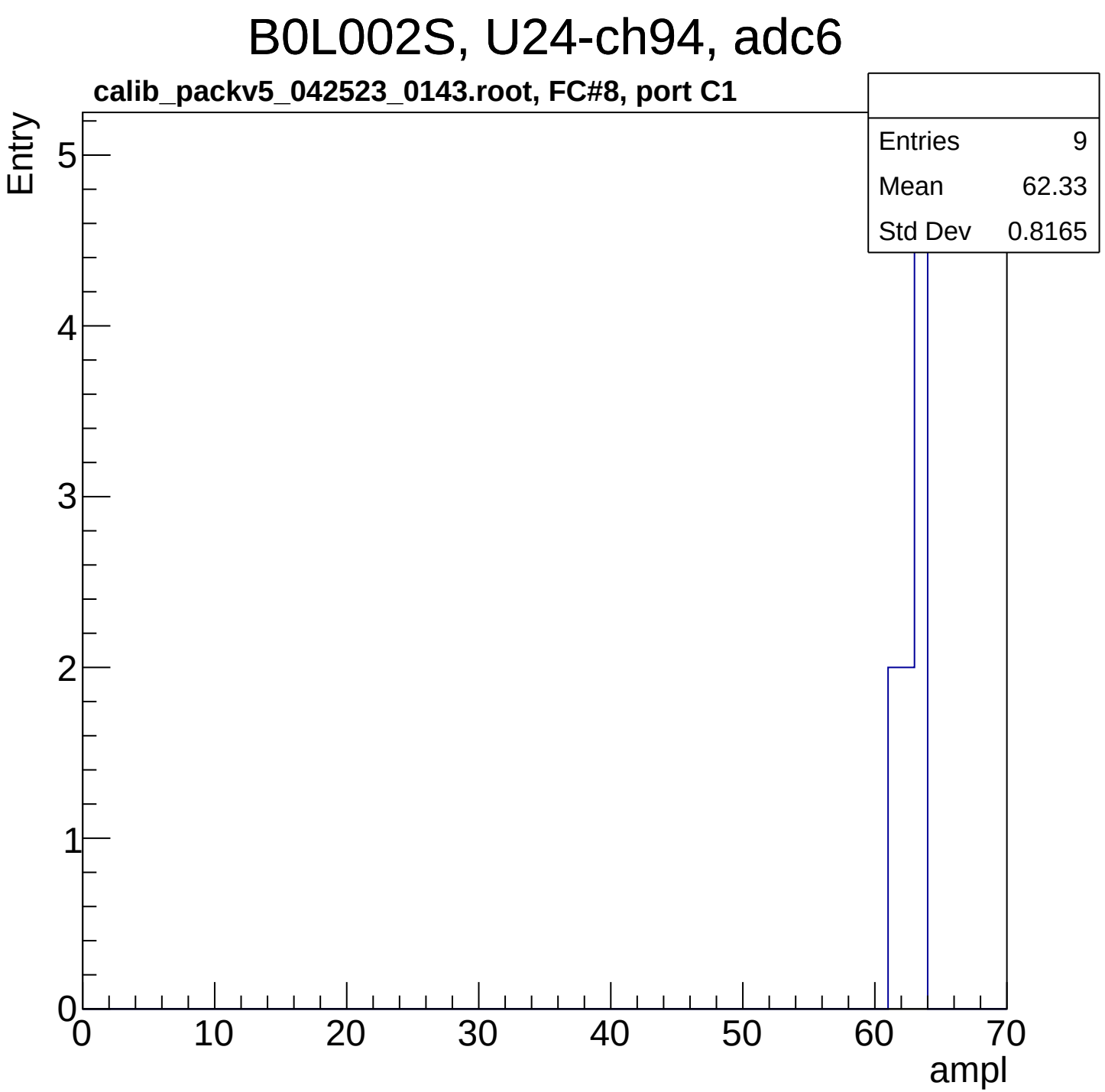
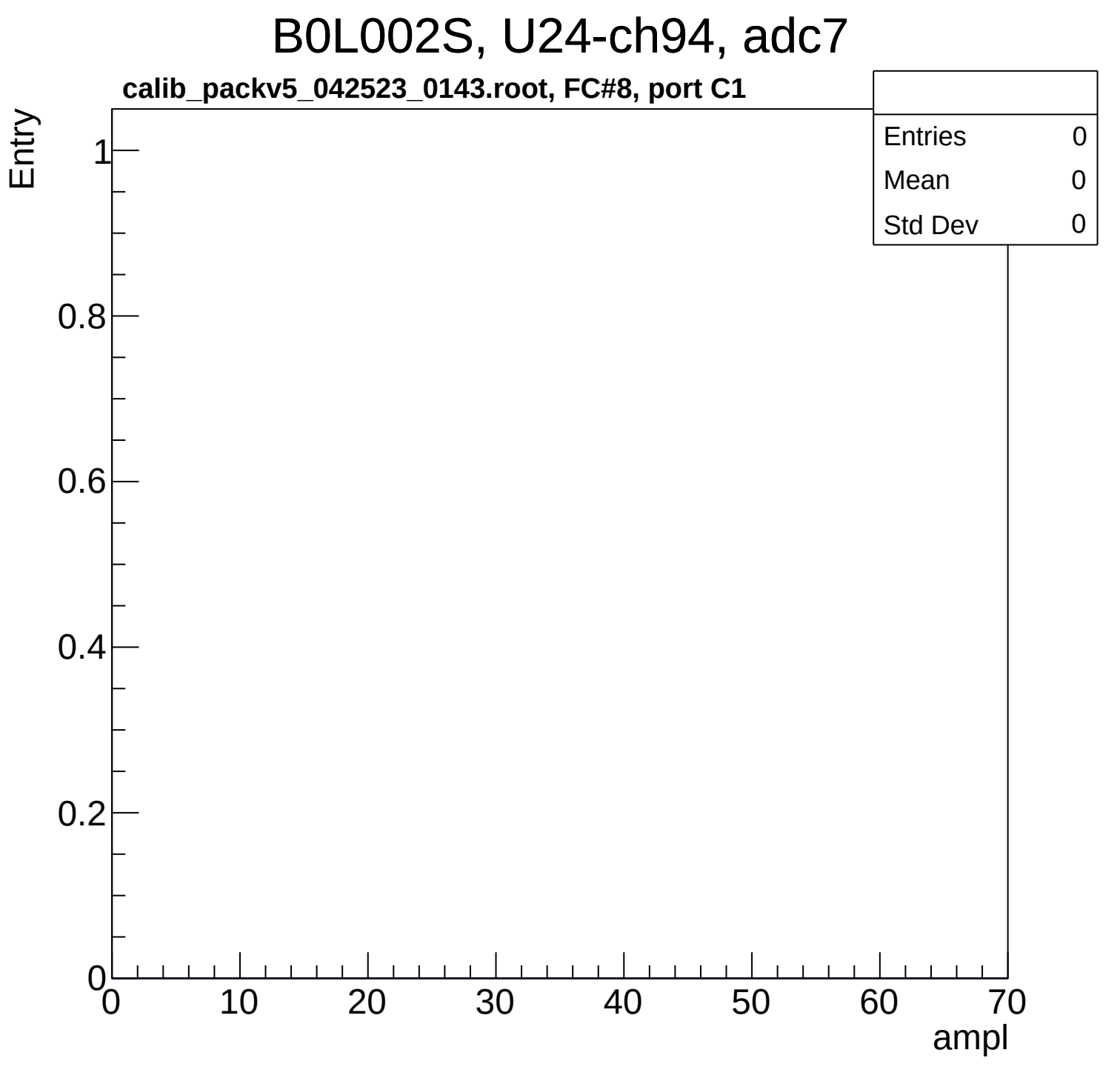
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 62.33
Std Dev 0.8165
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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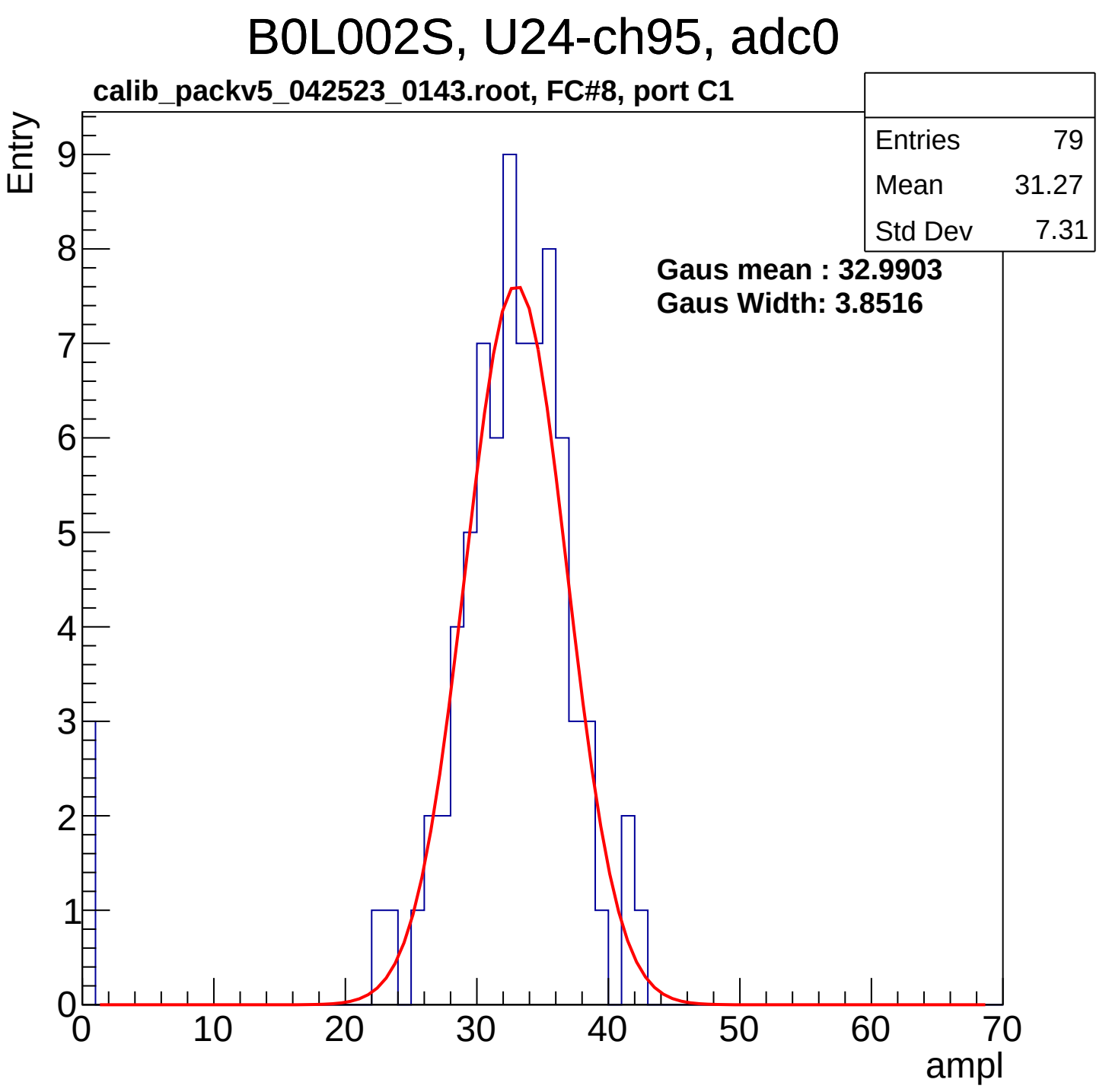
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 31.27
Std Dev 7.31
Gaus mean : 32.9903
Gaus Width: 3.8516
Entry
ampl
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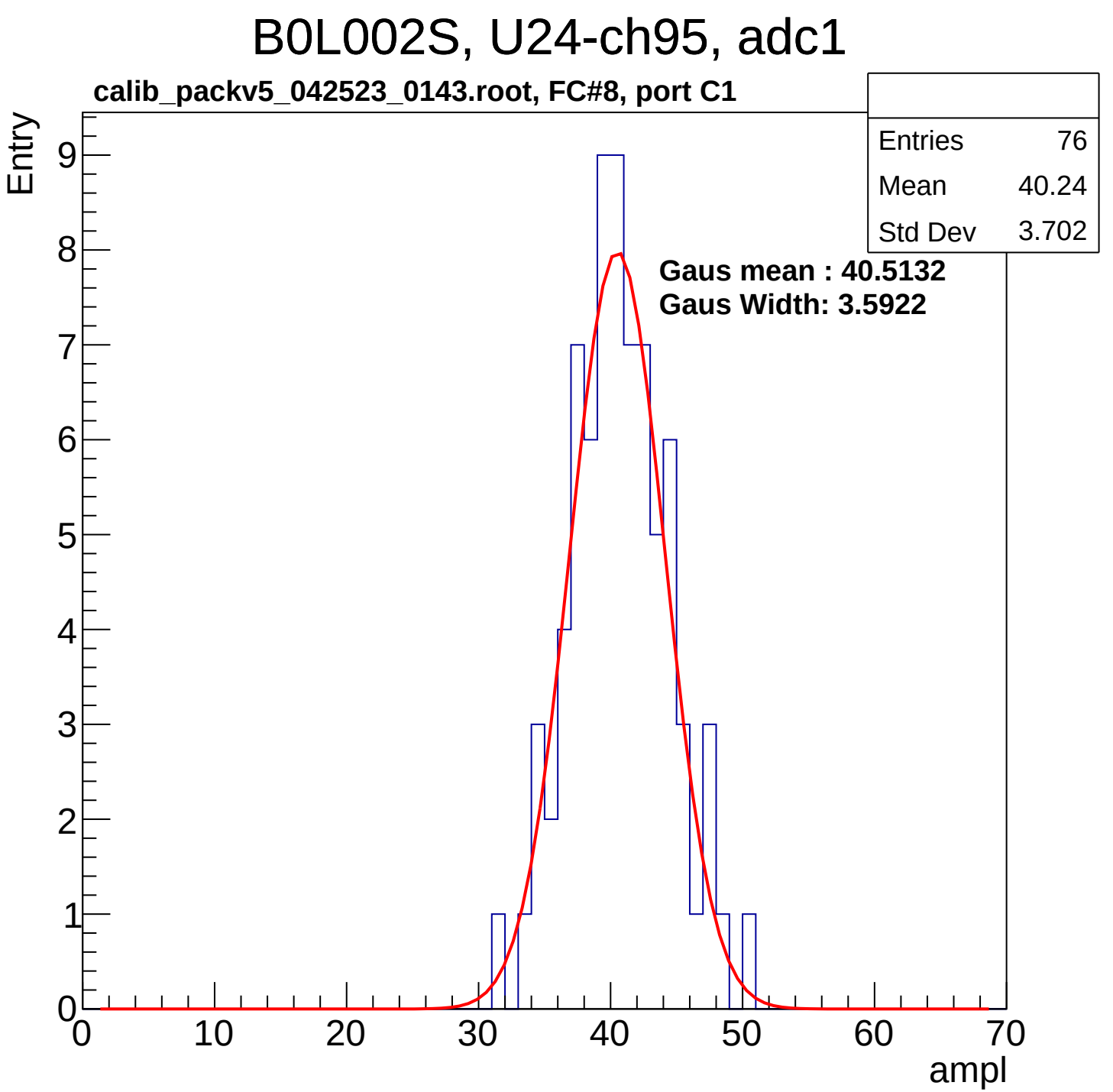
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 40.24
Std Dev 3.702
Gaus mean : 40.5132
Gaus Width: 3.5922
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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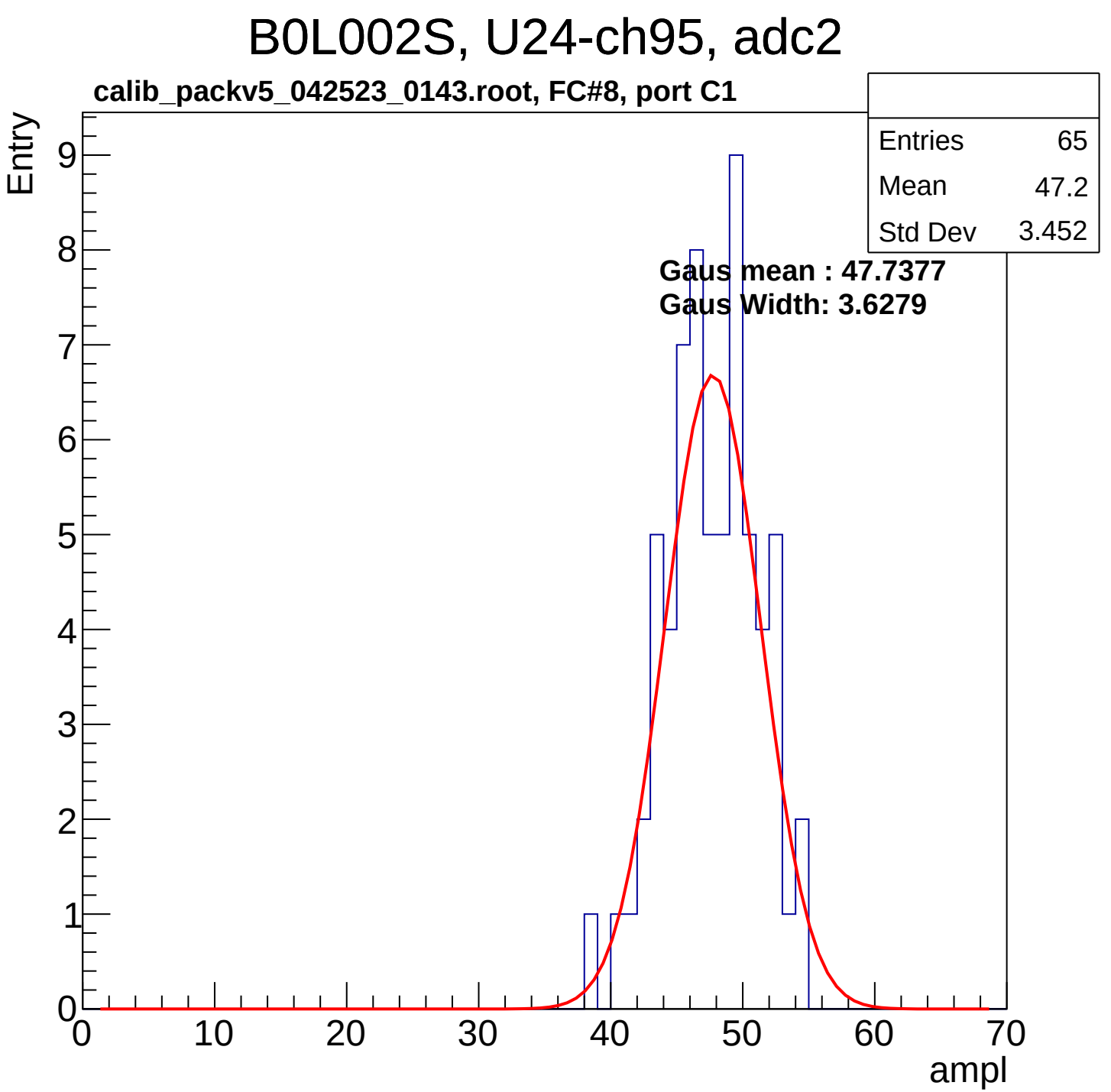
B0L002S, U24-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 47.2
Std Dev 3.452
Gaus mean : 47.7377
Gaus Width: 3.6279
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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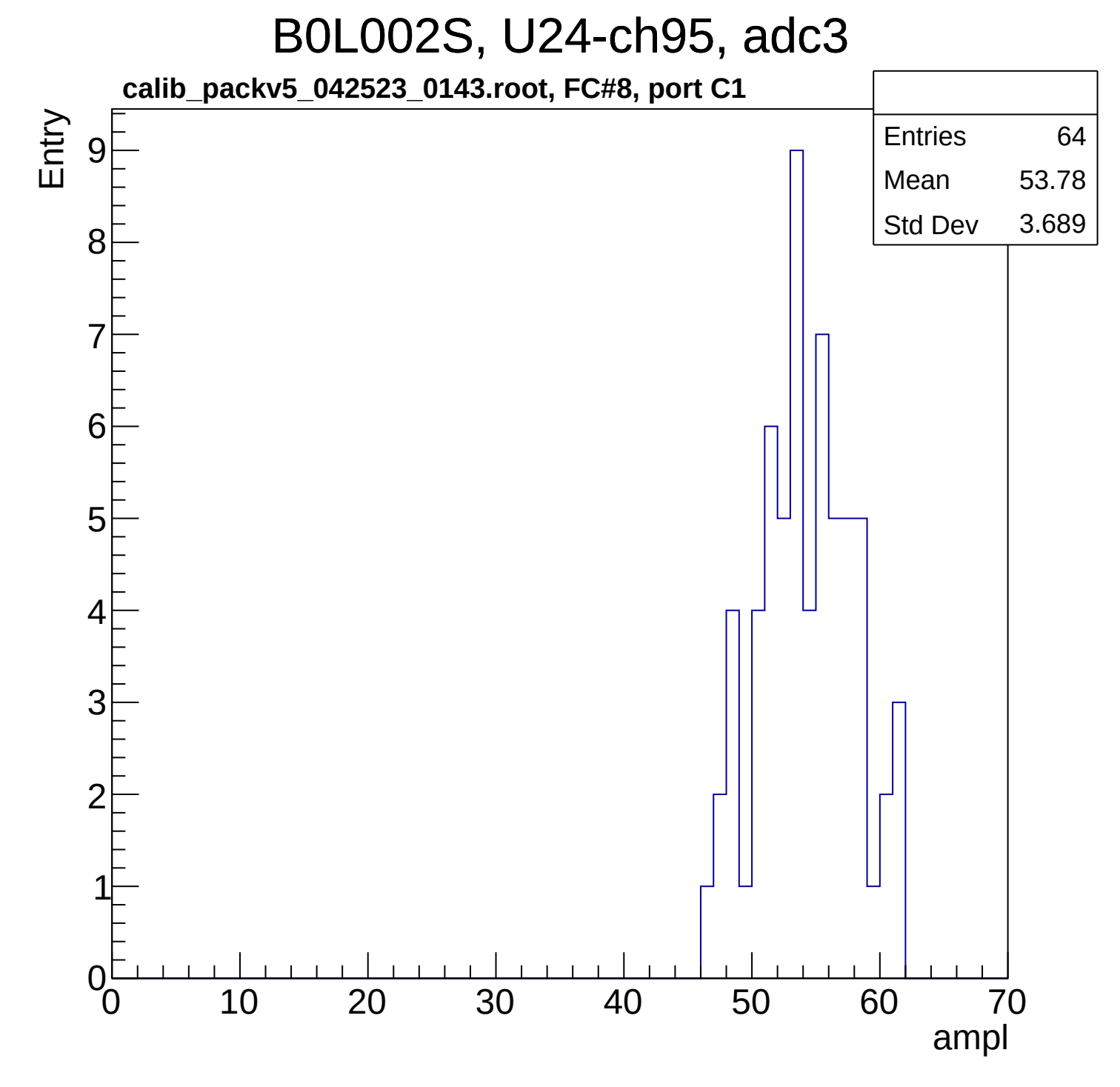
B0L002S, U24-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 53.78
Std Dev 3.689
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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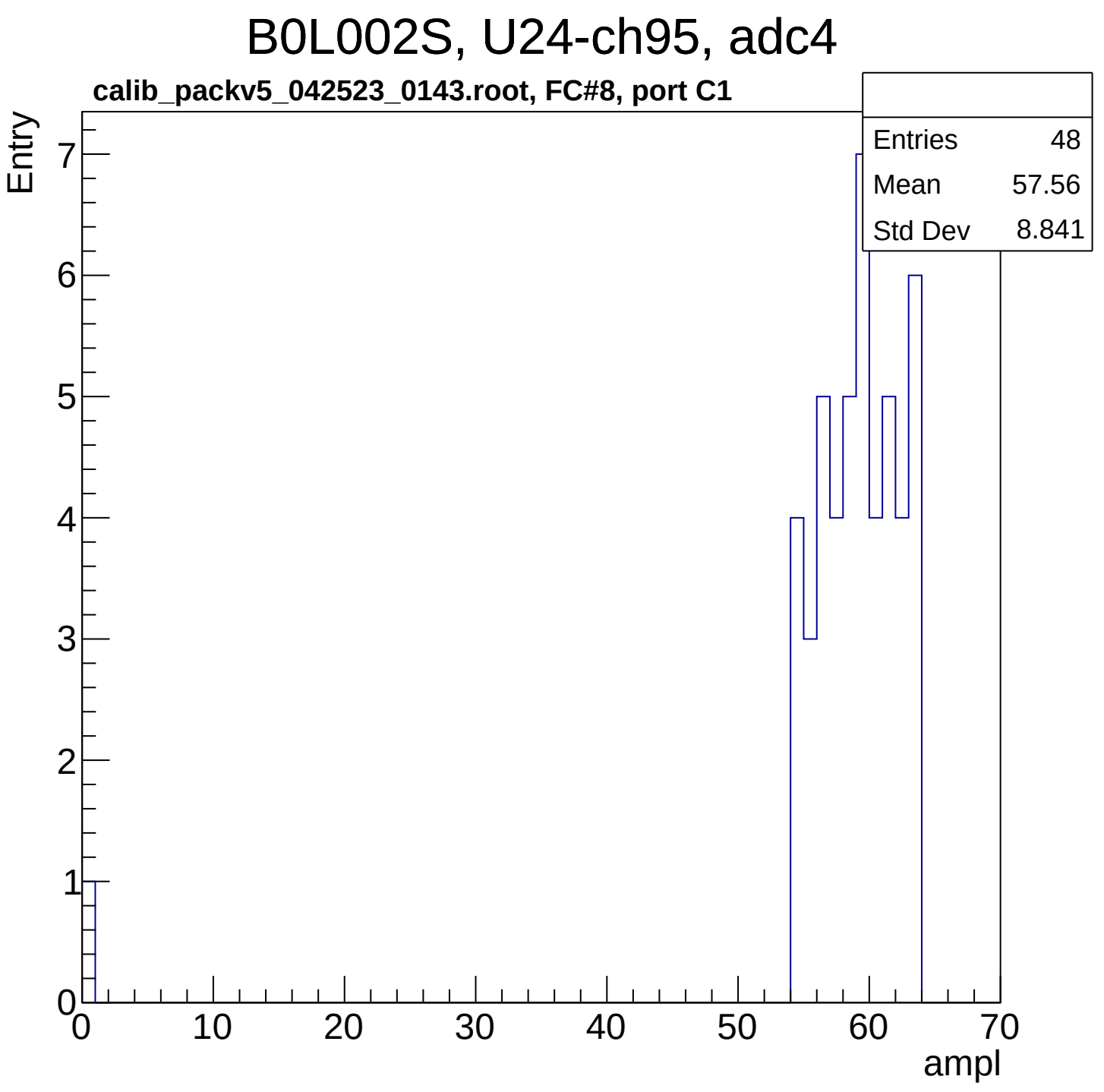
B0L002S, U24-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.56
Std Dev 8.841
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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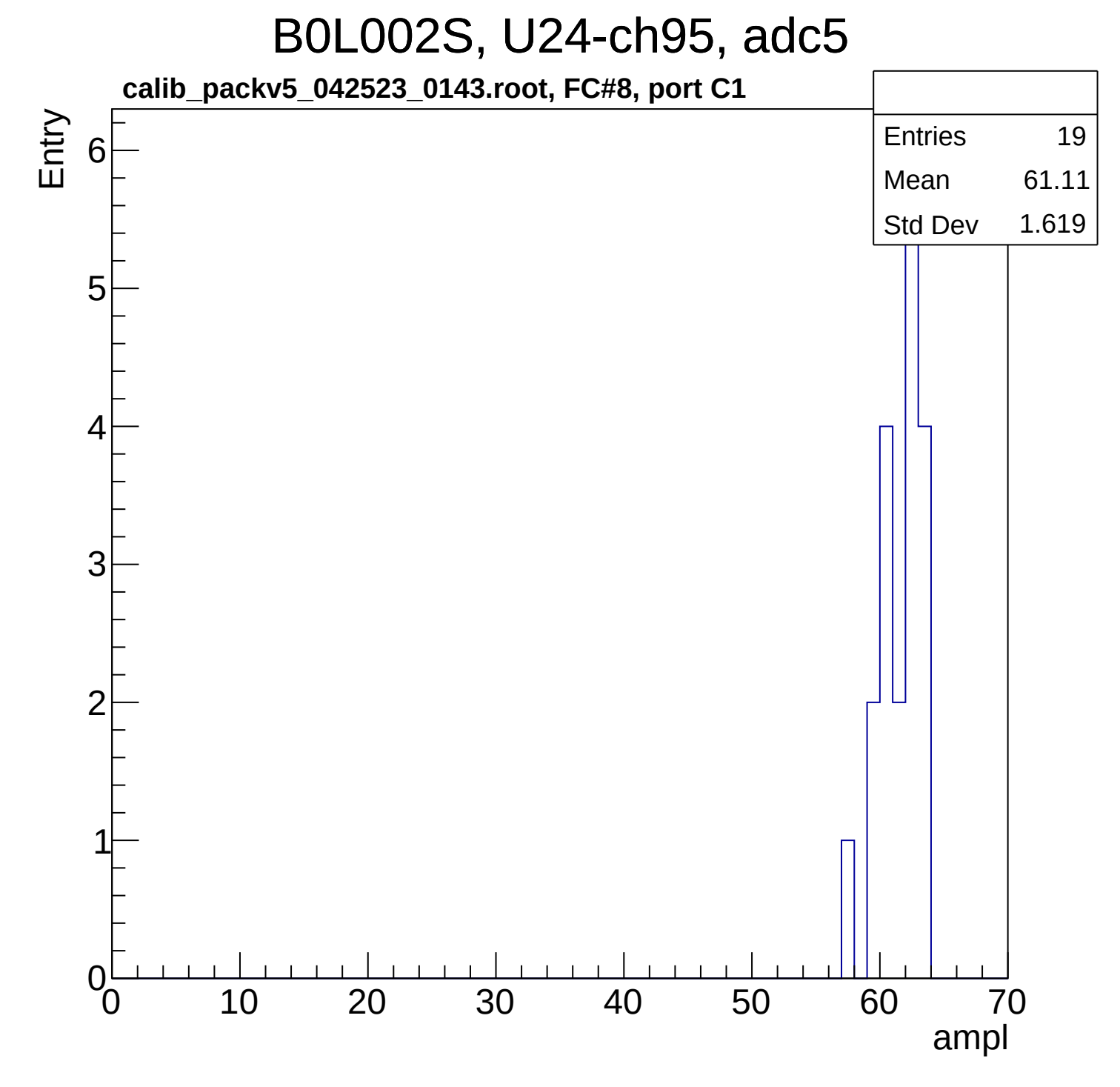
B0L002S, U24-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.11
Std Dev 1.619
Entry
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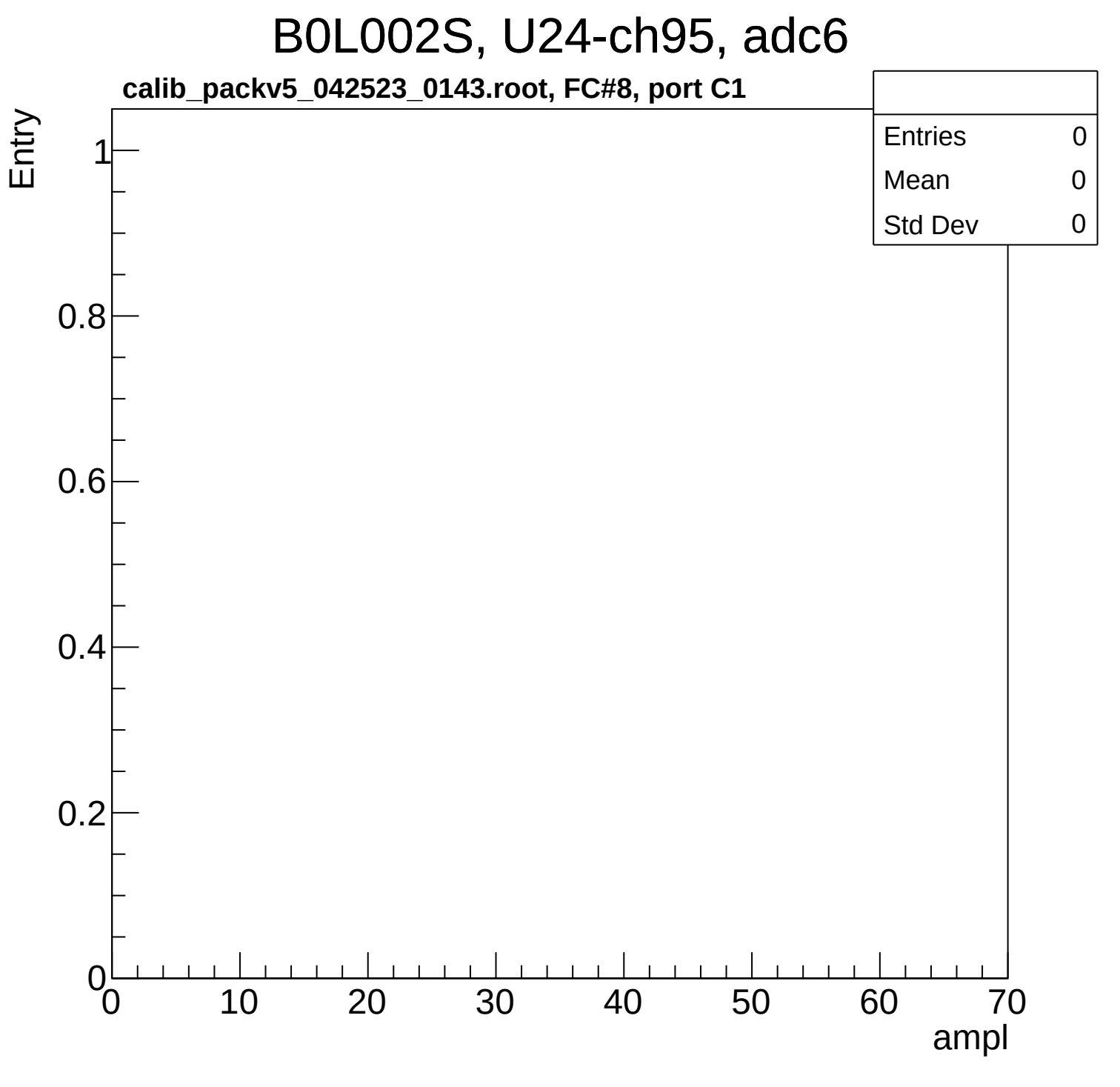
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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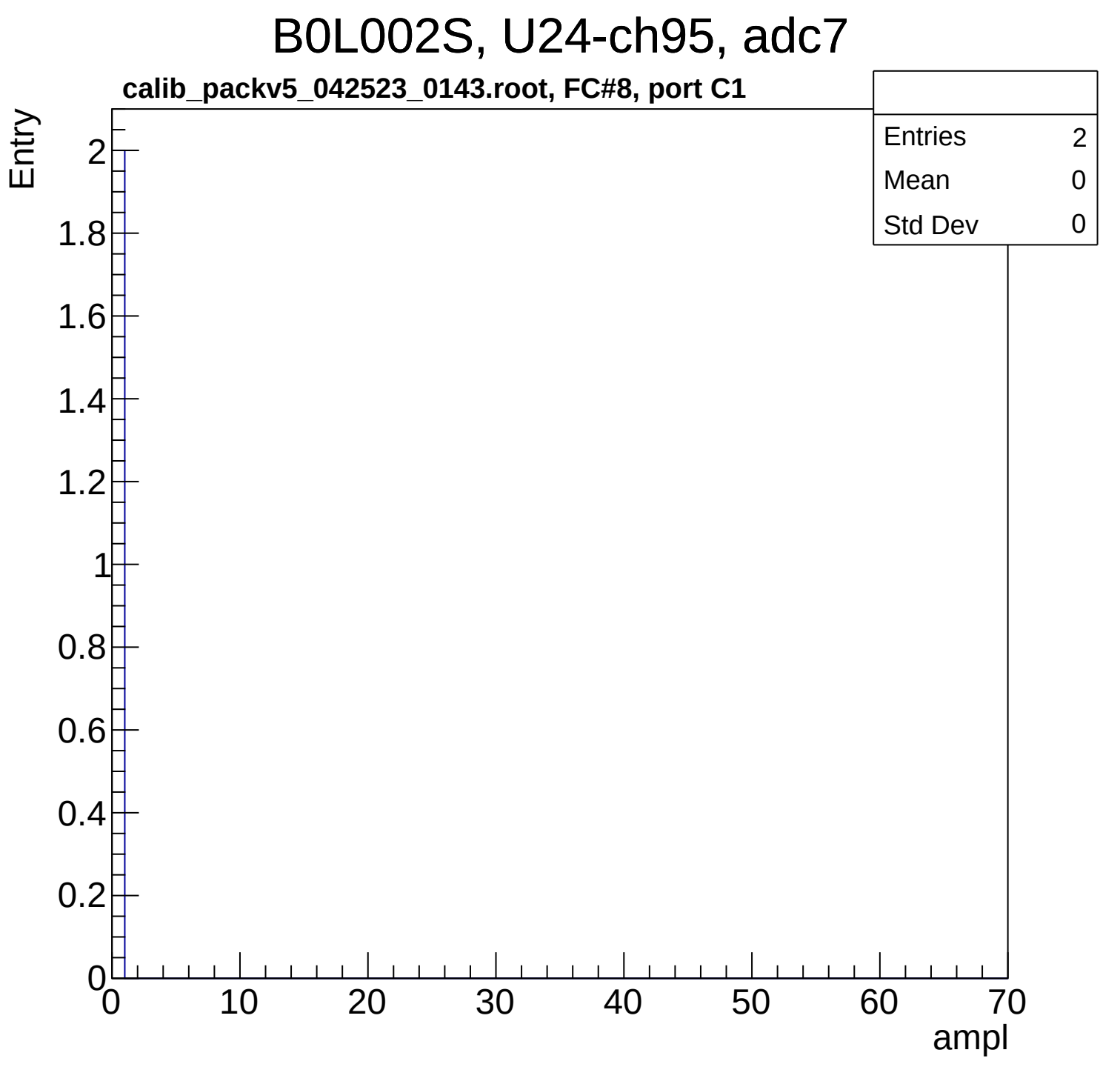
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
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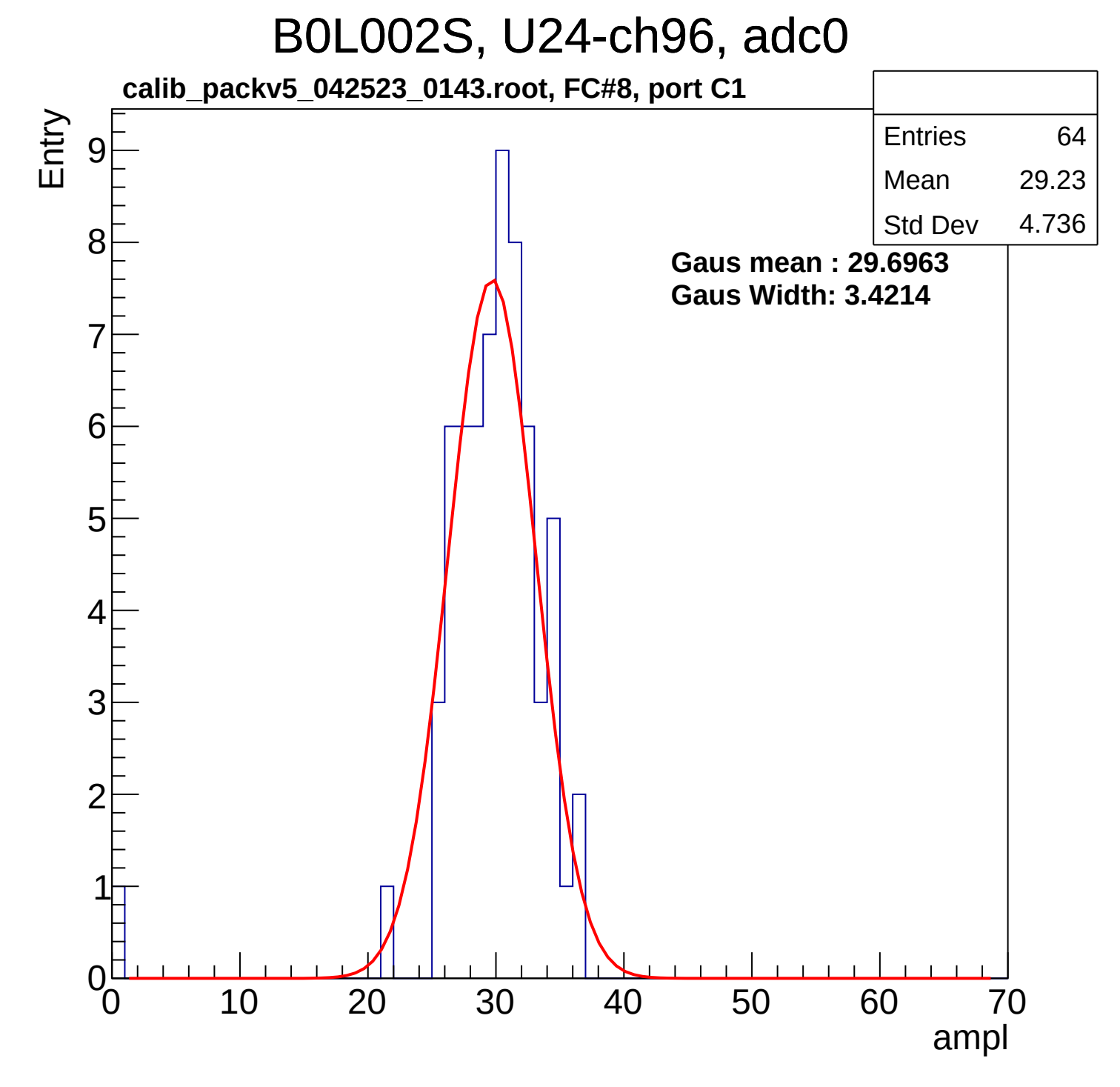
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.23
Std Dev 4.736
Gaus mean : 29.6963
Gaus Width: 3.4214
Entry
ampl
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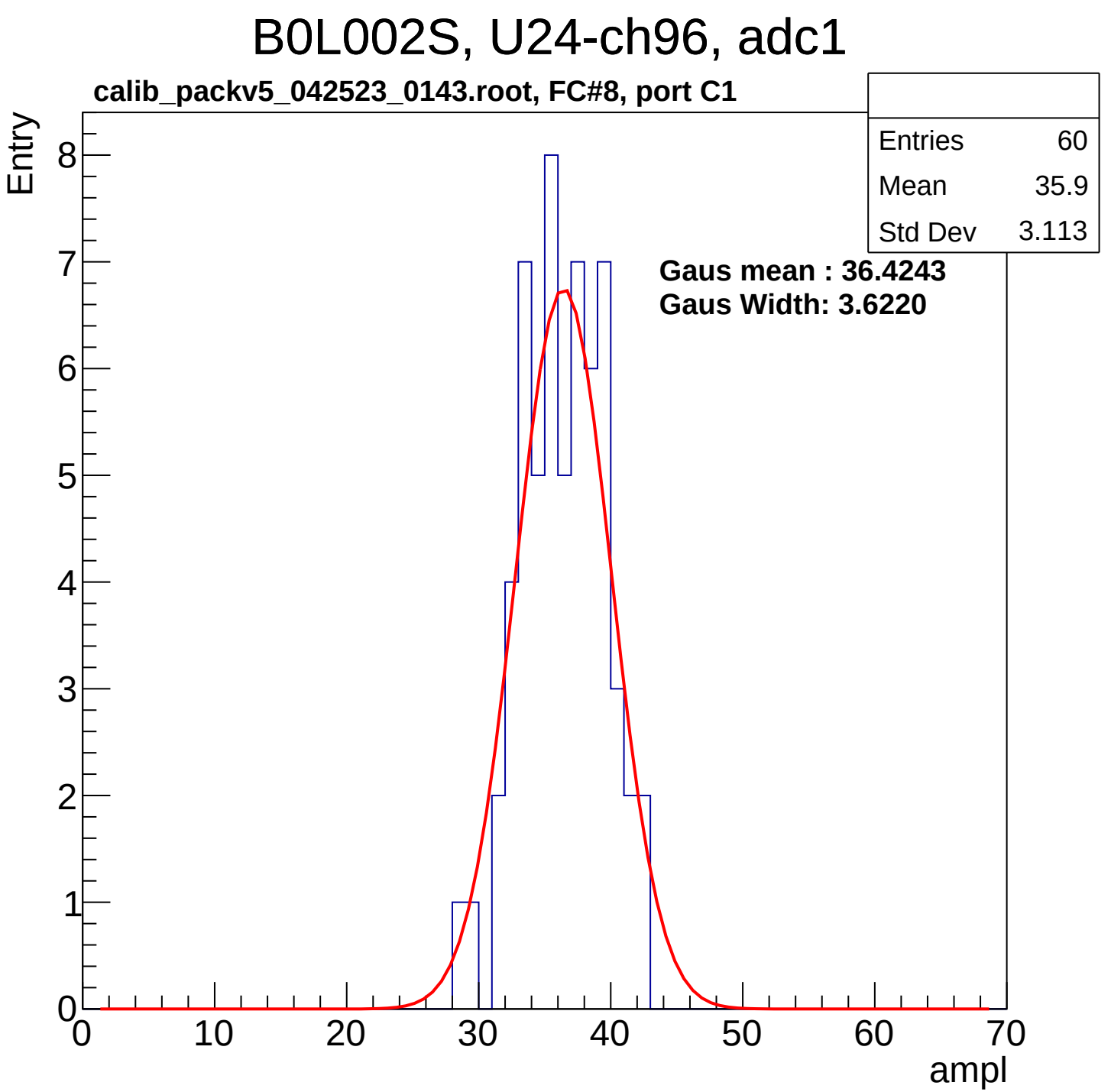
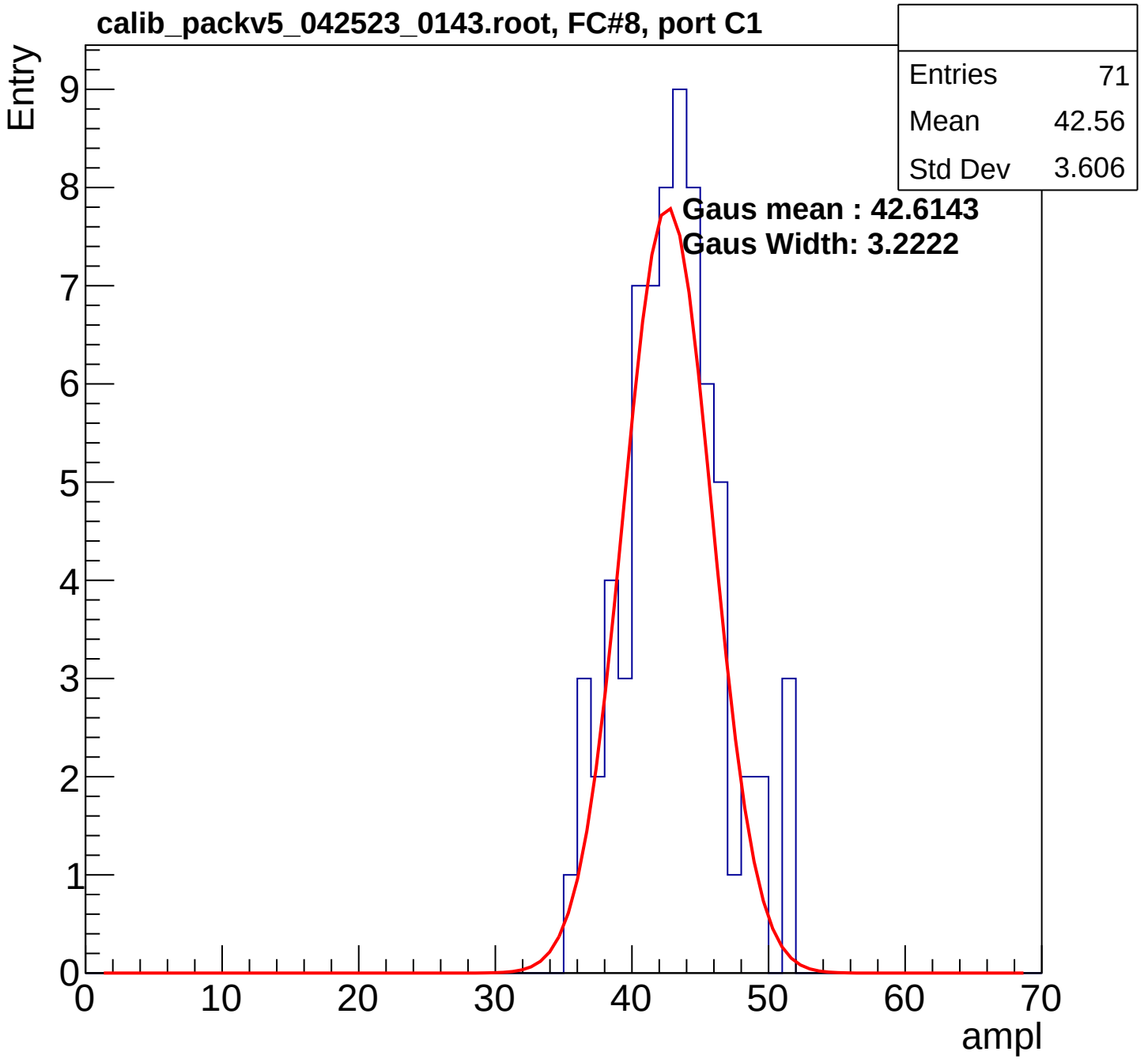
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 35.9
Std Dev 3.113
Gaus mean : 36.4243
Gaus Width: 3.6220
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 42.56
Std Dev 3.606
Gaus mean : 42.6143
Gaus Width: 3.2222
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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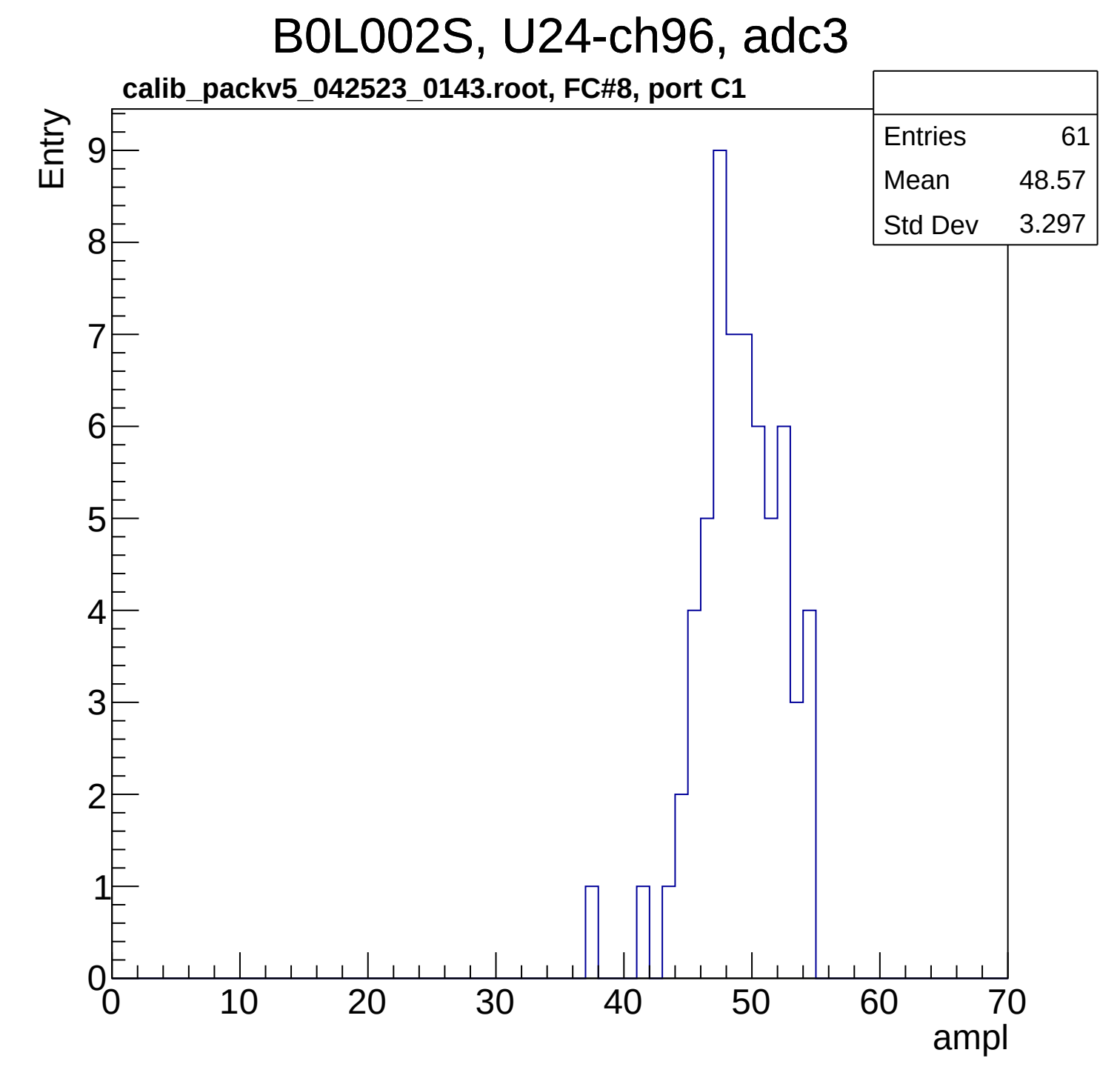
B0L002S, U24-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 48.57
Std Dev 3.297
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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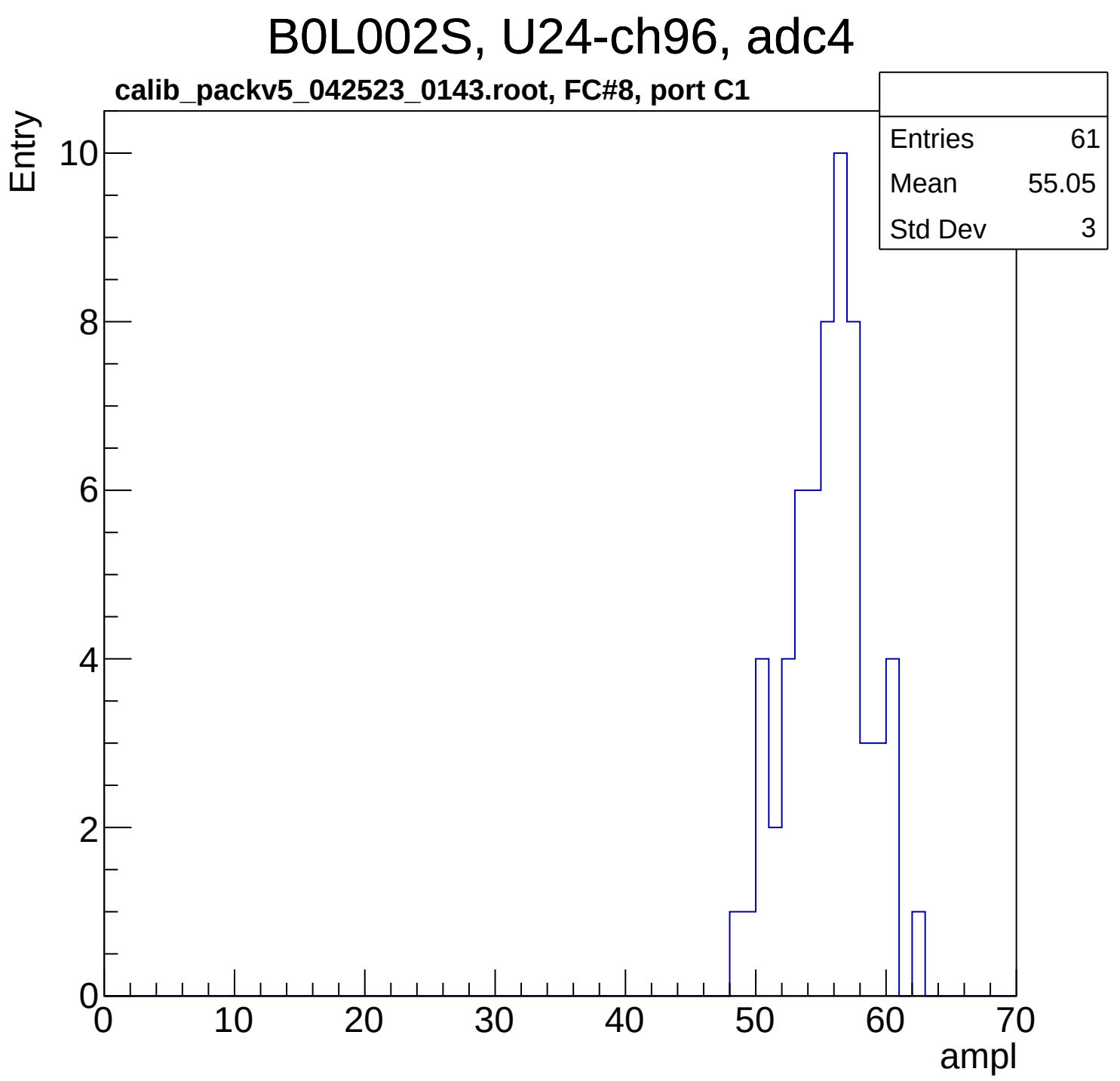
B0L002S, U24-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.05
Std Dev 3
Entry
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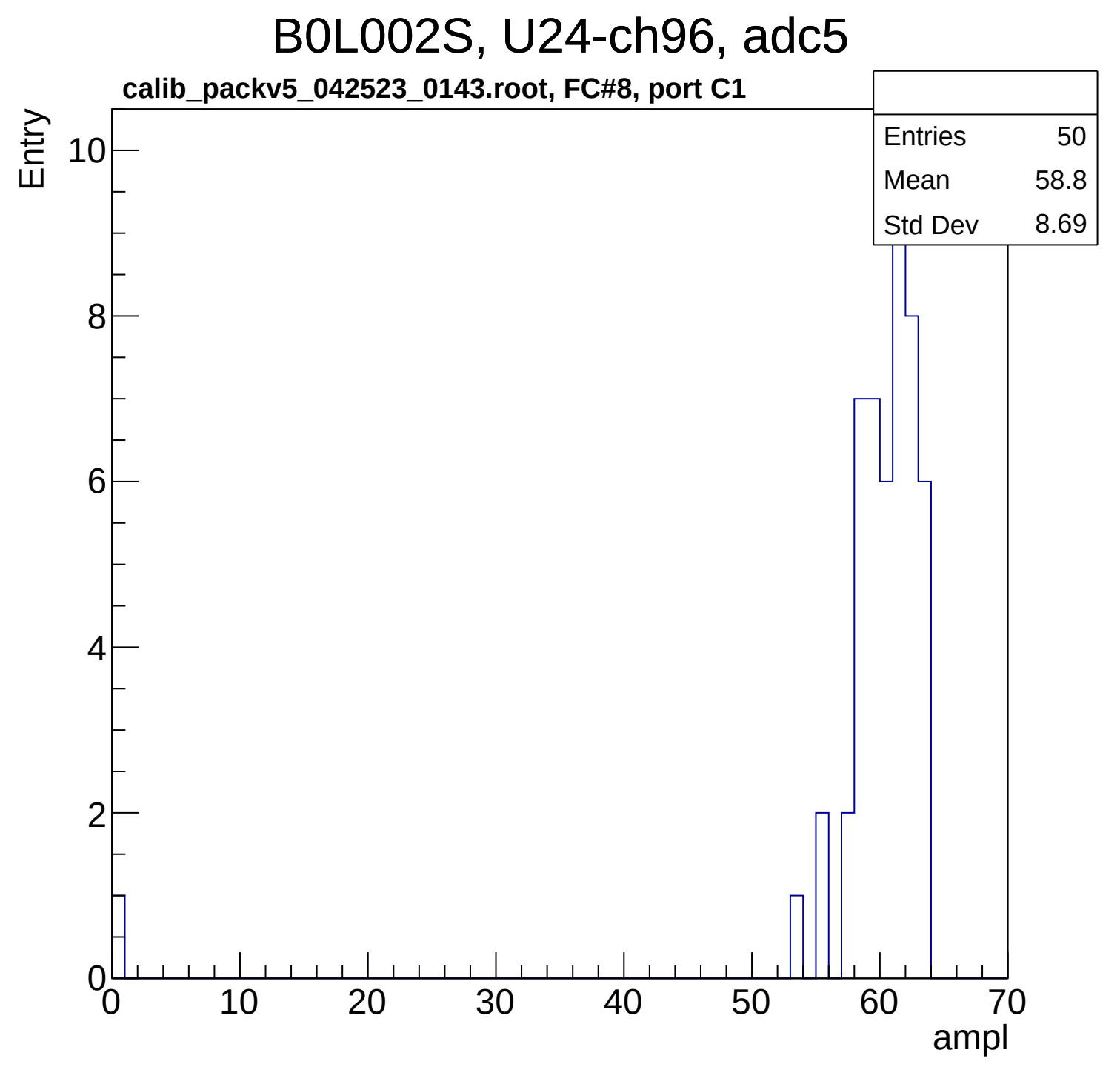
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.8
Std Dev 8.69
Entry
0
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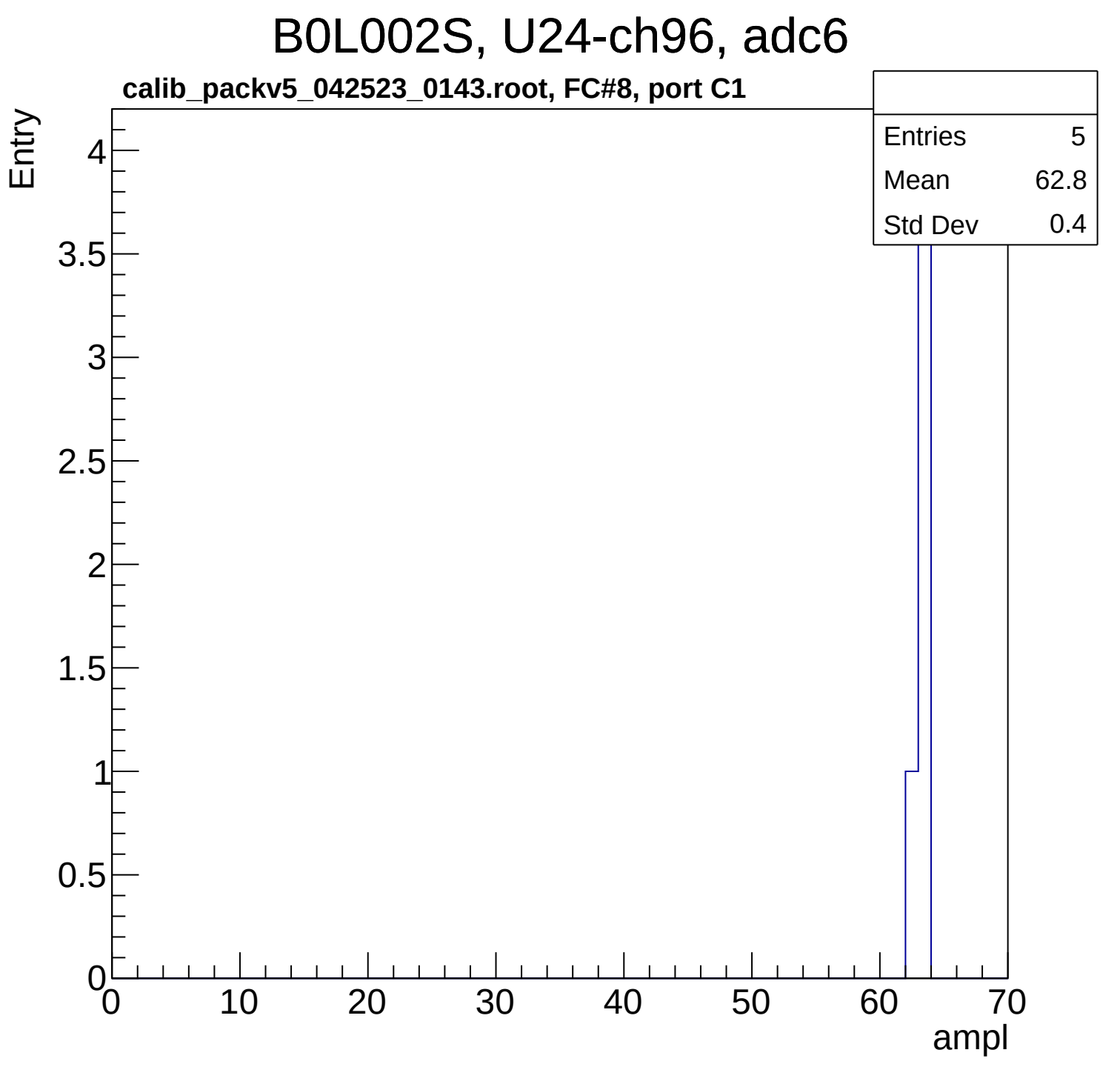
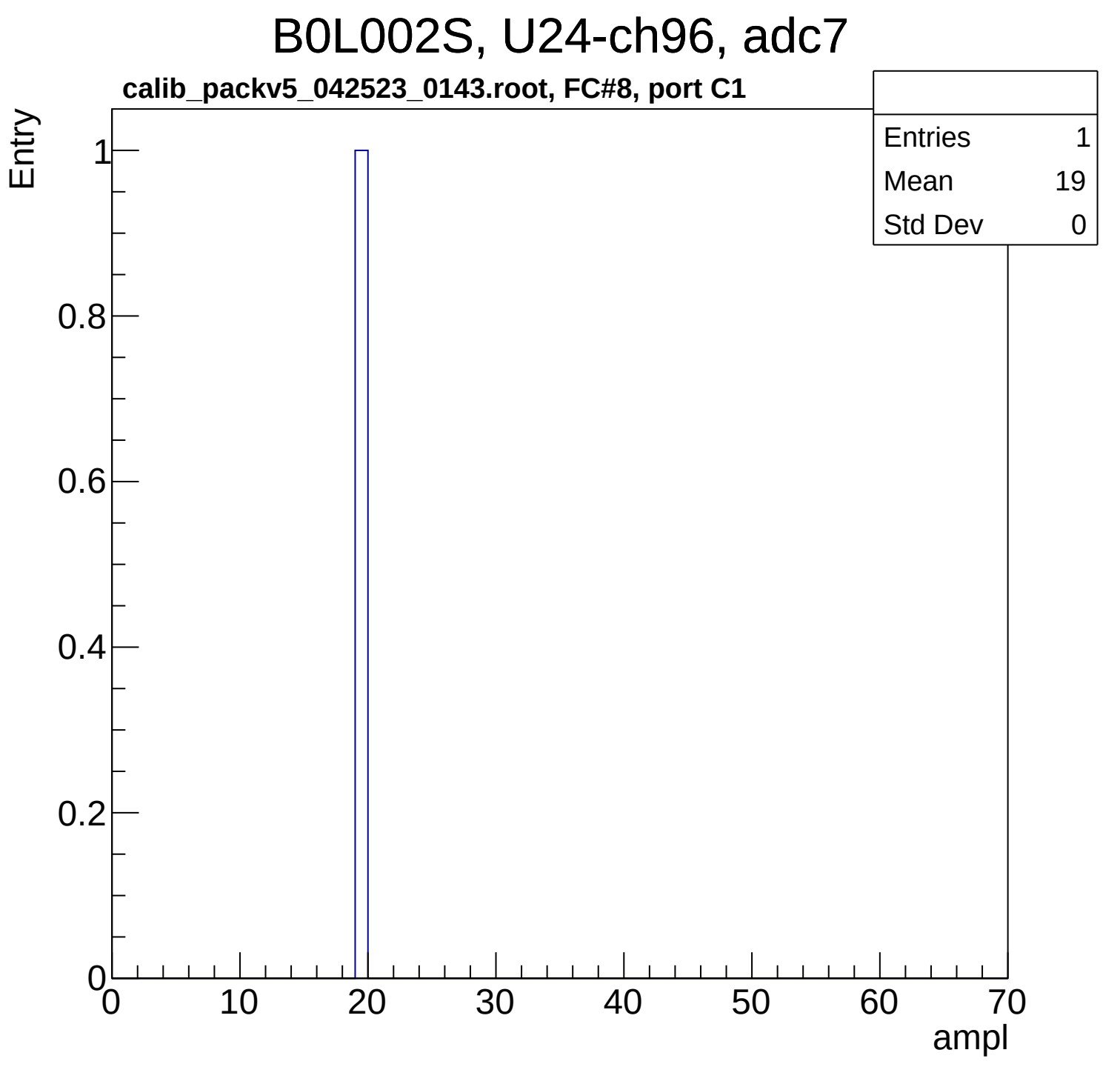
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U24-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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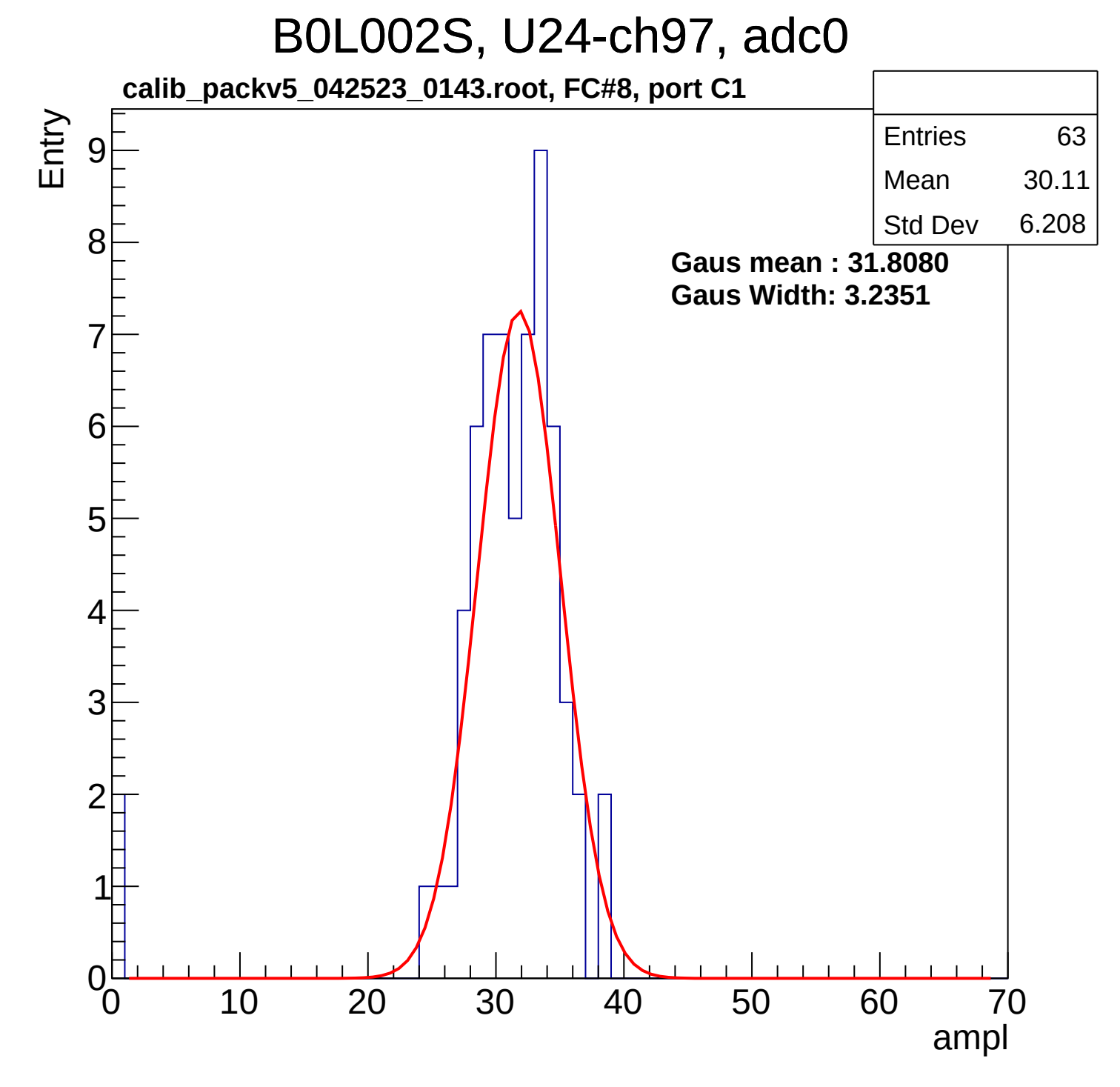
50
60
70
ampl


B0L002S, U24-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.11
Std Dev 6.208
Gaus mean : 31.8080
Gaus Width: 3.2351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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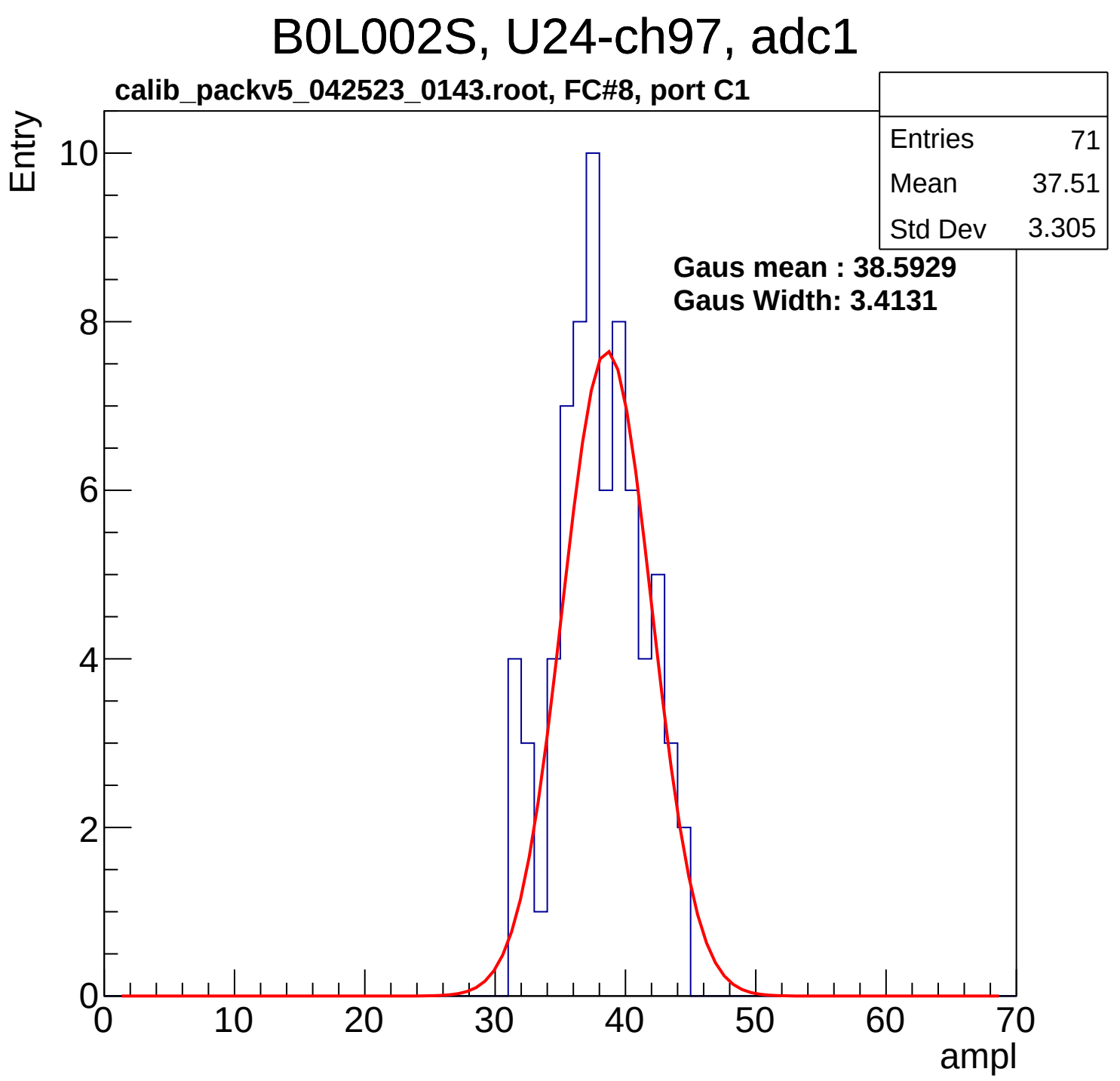
B0L002S, U24-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.51
Std Dev 3.305
Gaus mean : 38.5929
Gaus Width: 3.4131
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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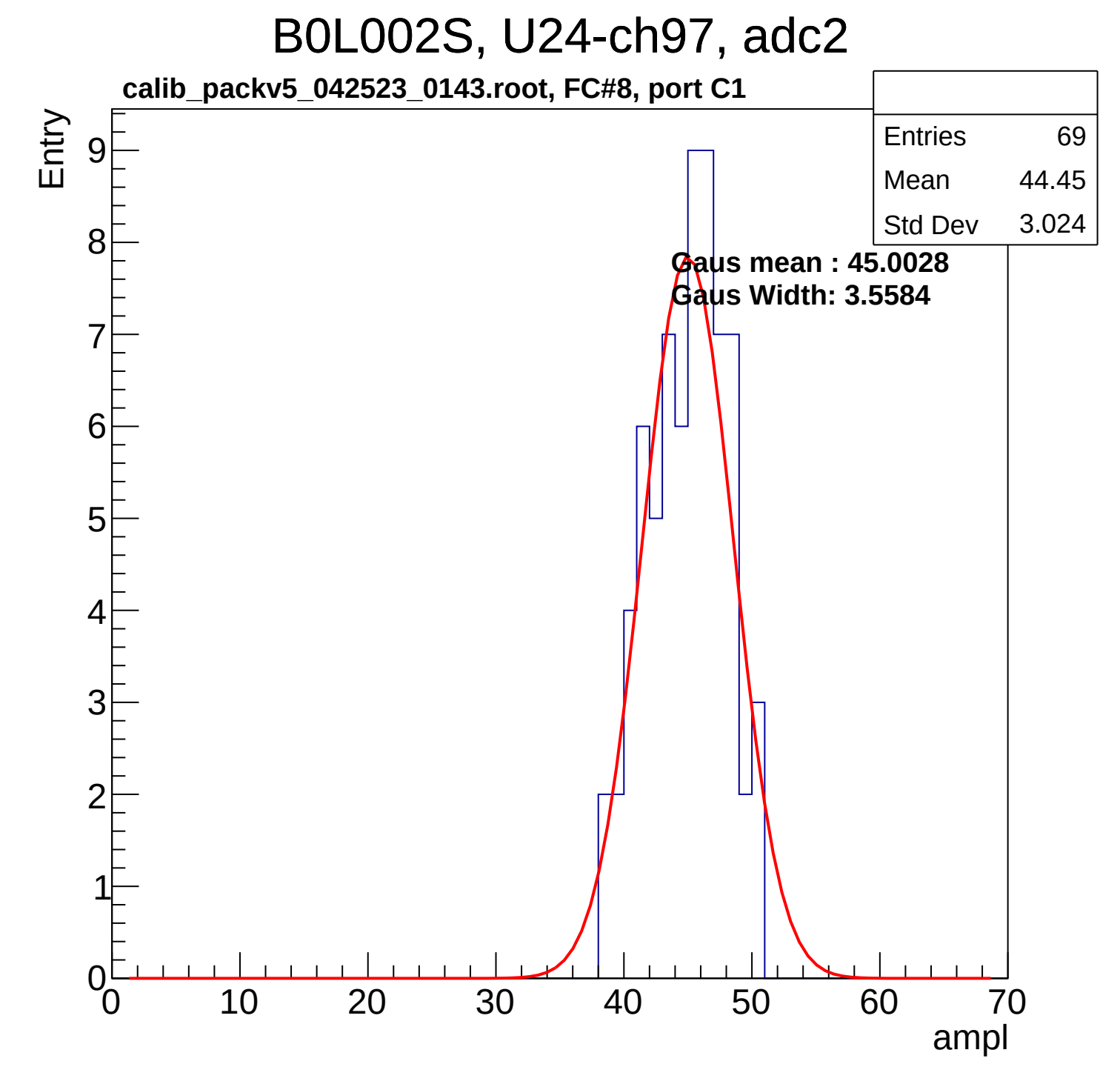
B0L002S, U24-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.45
Std Dev 3.024
Gaus mean : 45.0028
Gaus Width: 3.5584
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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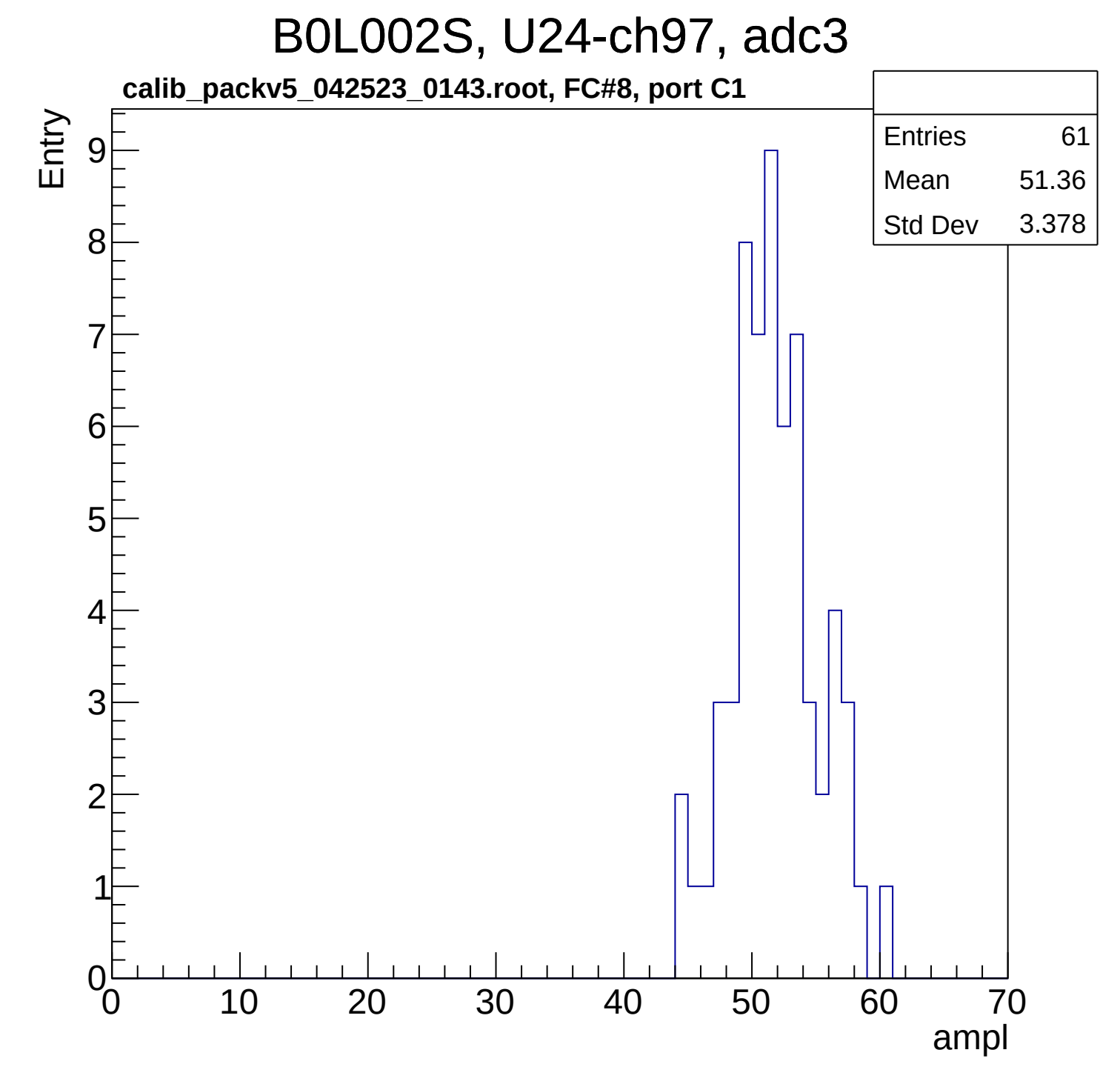
B0L002S, U24-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.36
Std Dev 3.378
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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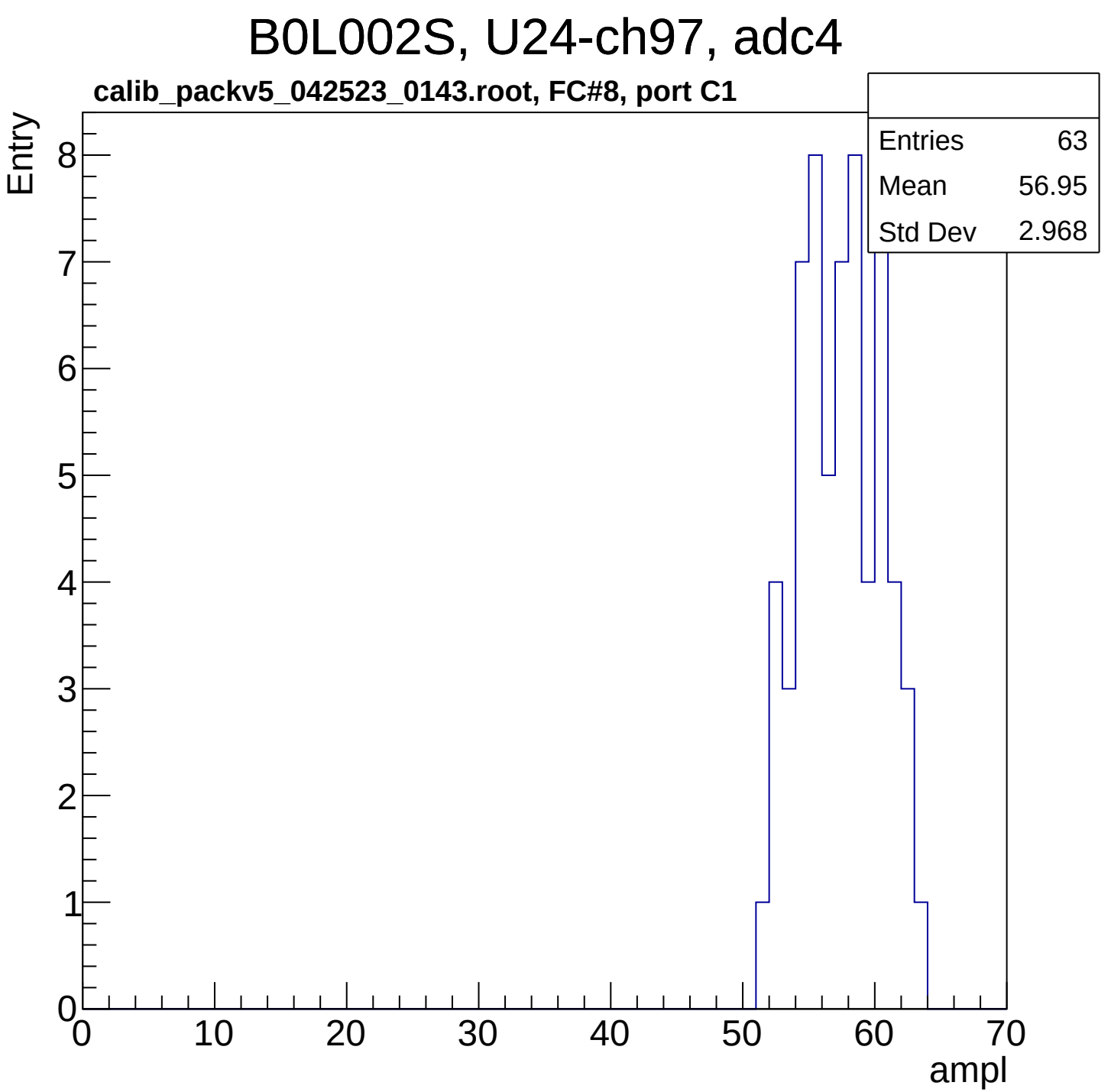
B0L002S, U24-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.95
Std Dev 2.968
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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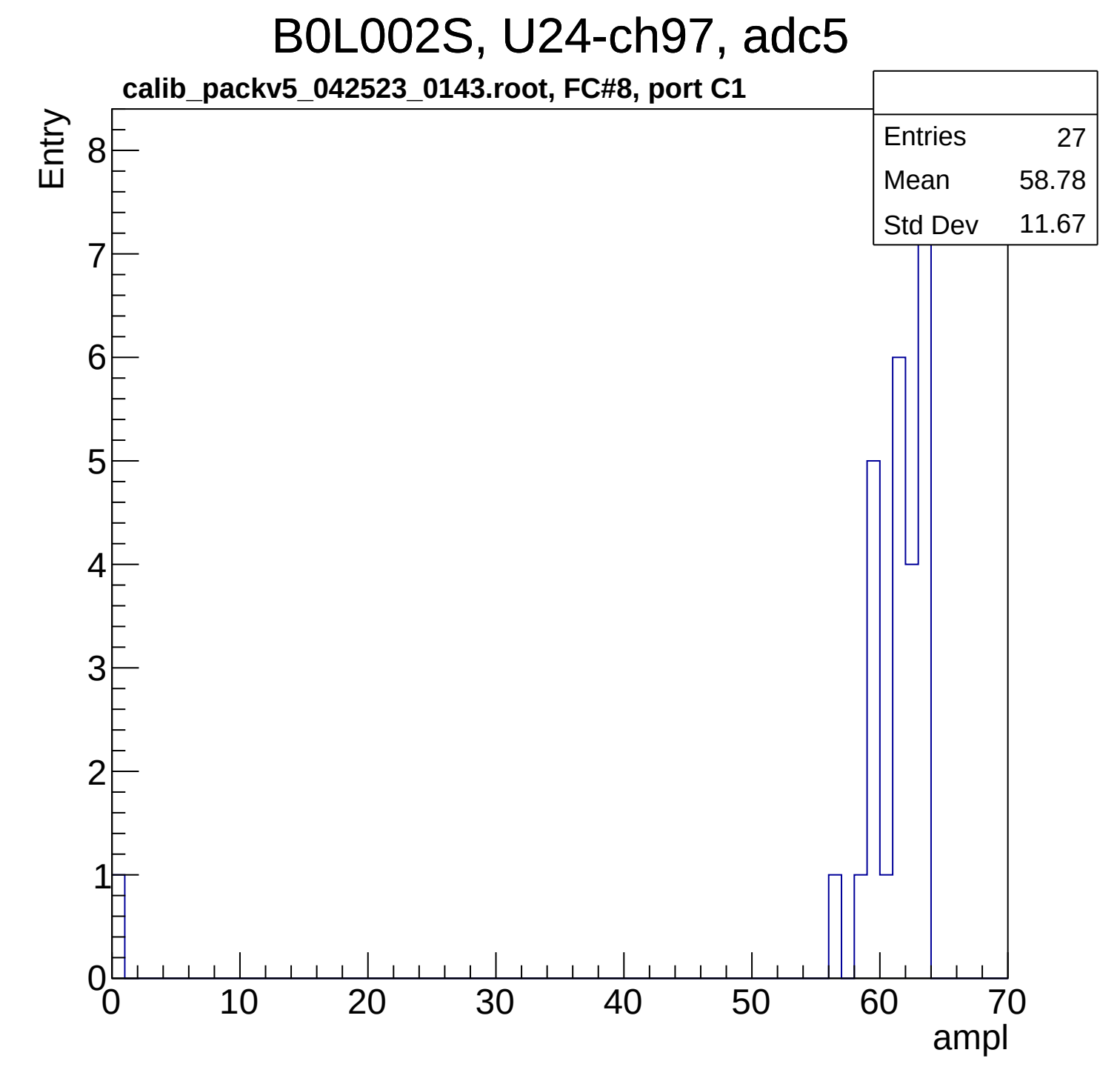
B0L002S, U24-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.78
Std Dev 11.67
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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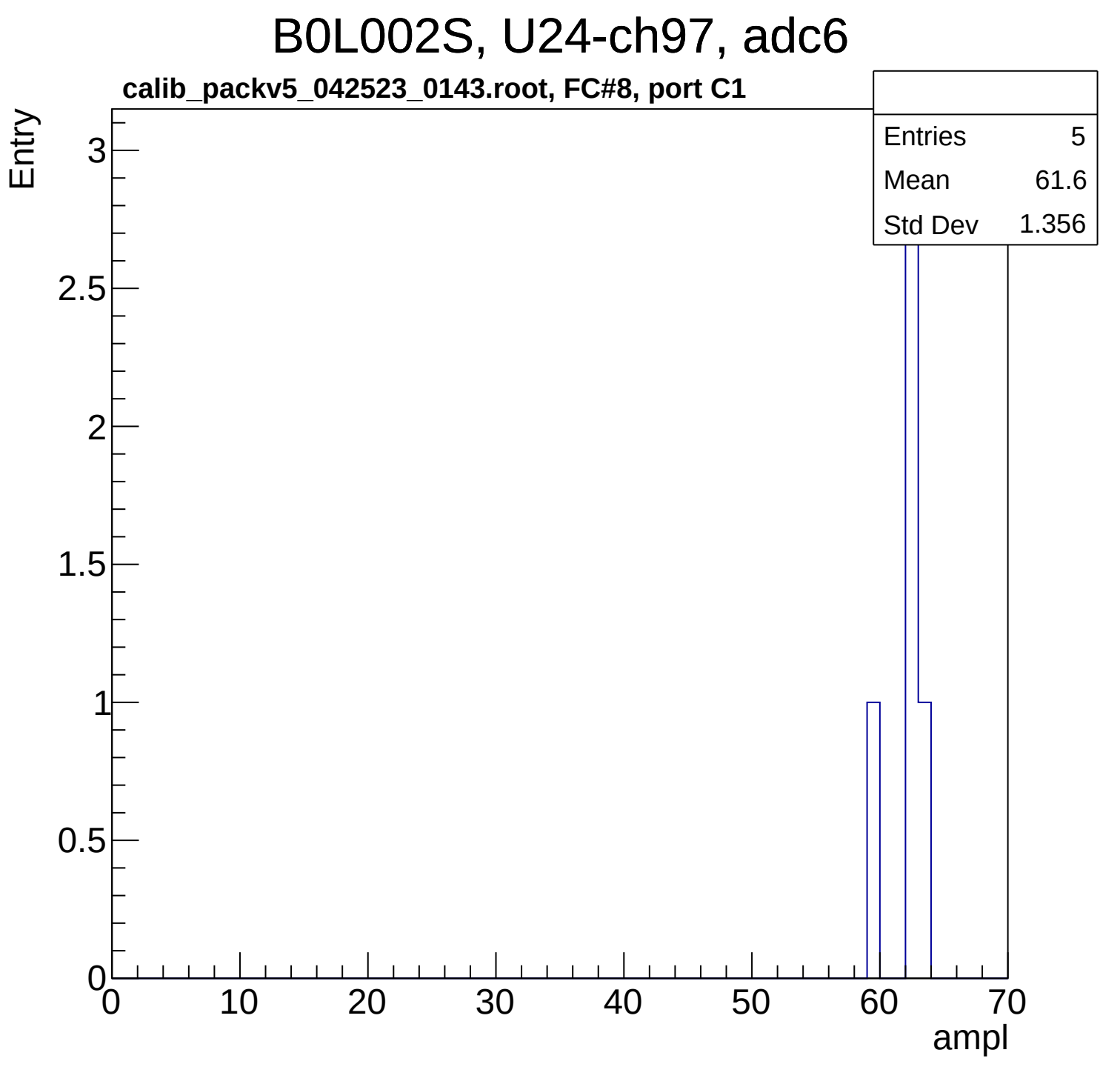
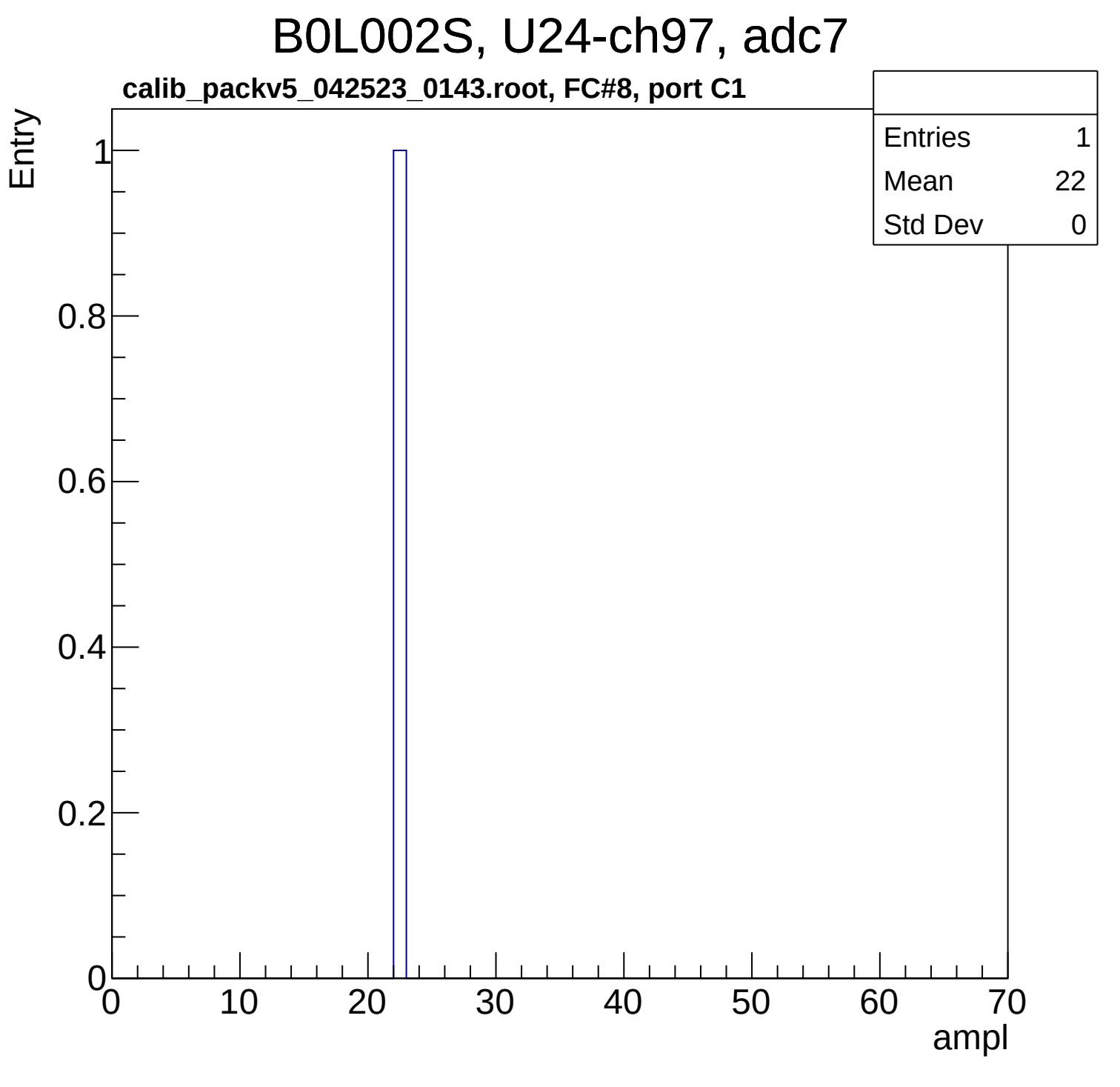

B0L002S, U24-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.6
Std Dev 1.356
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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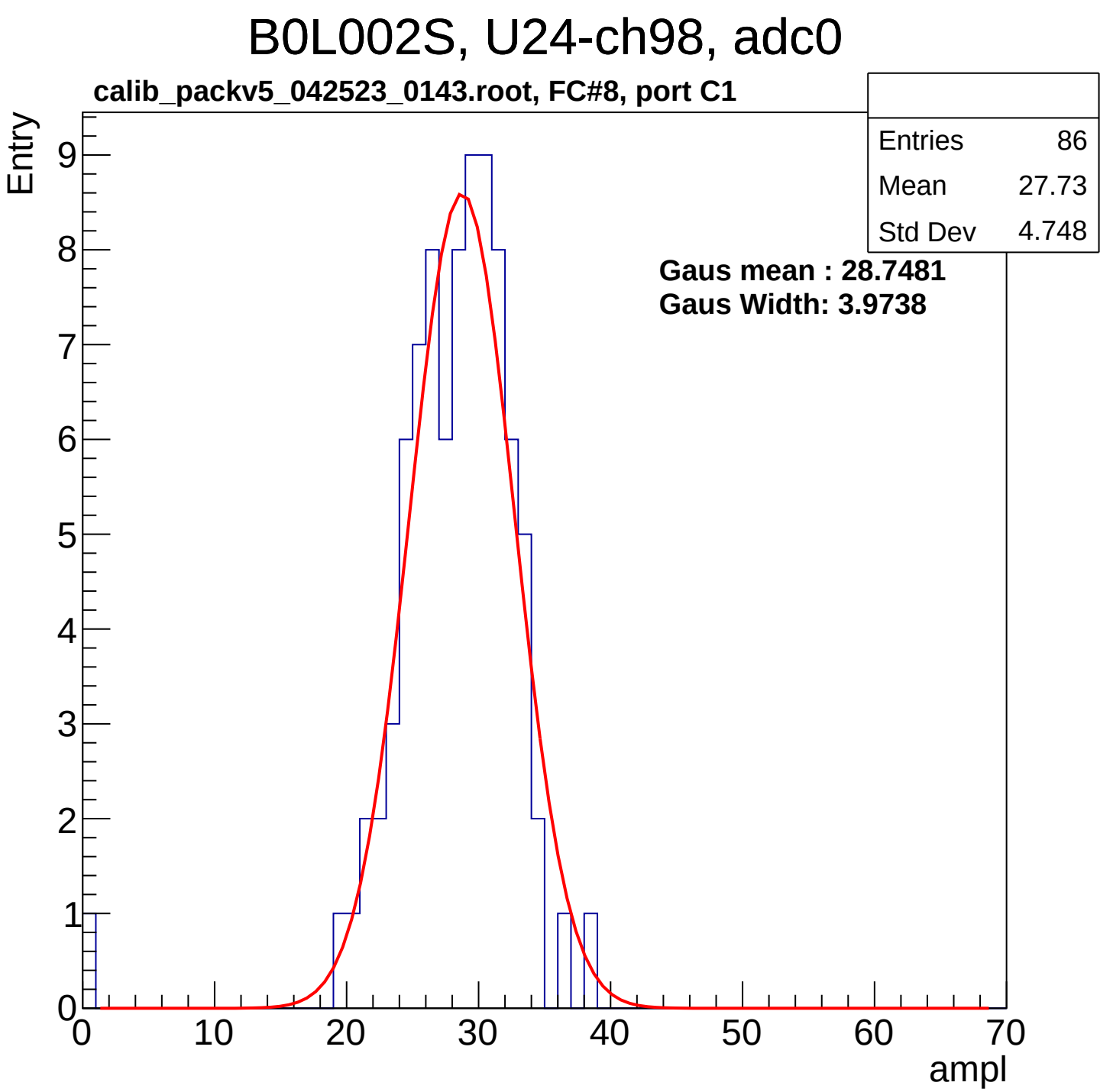
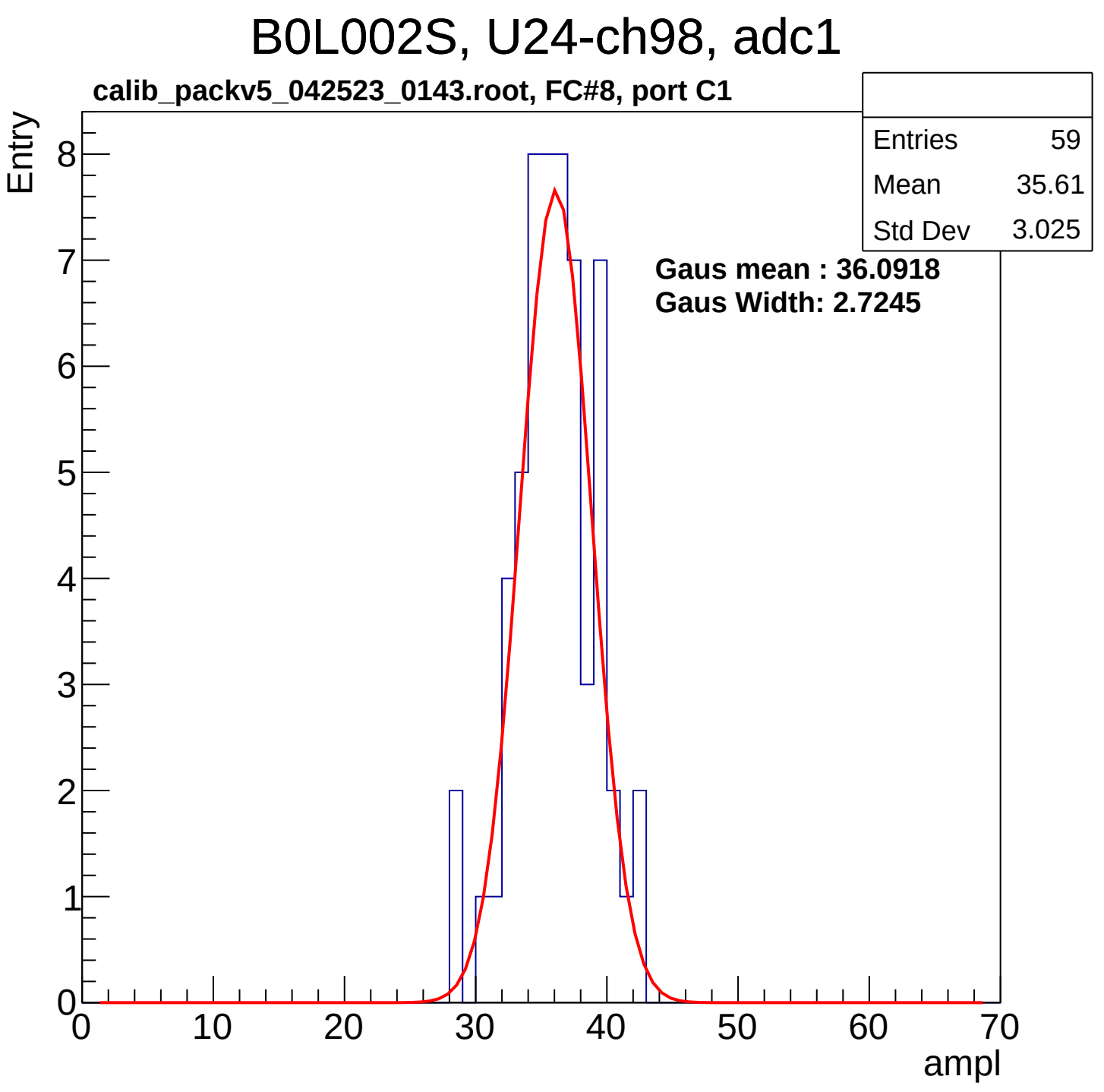
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 27.73
Std Dev 4.748
Gaus mean : 28.7481
Gaus Width: 3.9738
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 35.61
Std Dev 3.025
Gaus mean : 36.0918
Gaus Width: 2.7245
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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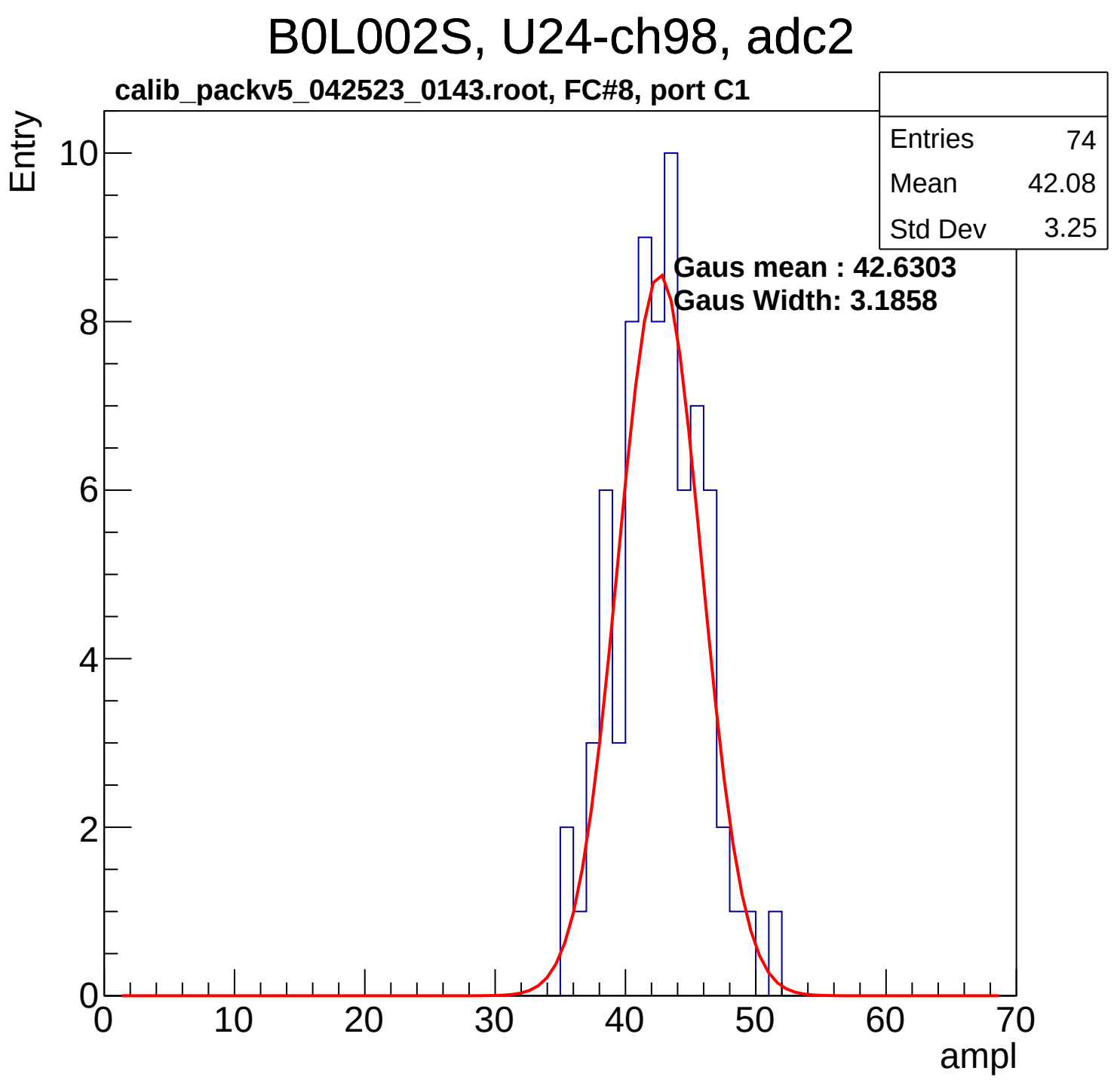
B0L002S, U24-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 42.08
Std Dev 3.25
Gaus mean : 42.6303
Gaus Width: 3.1858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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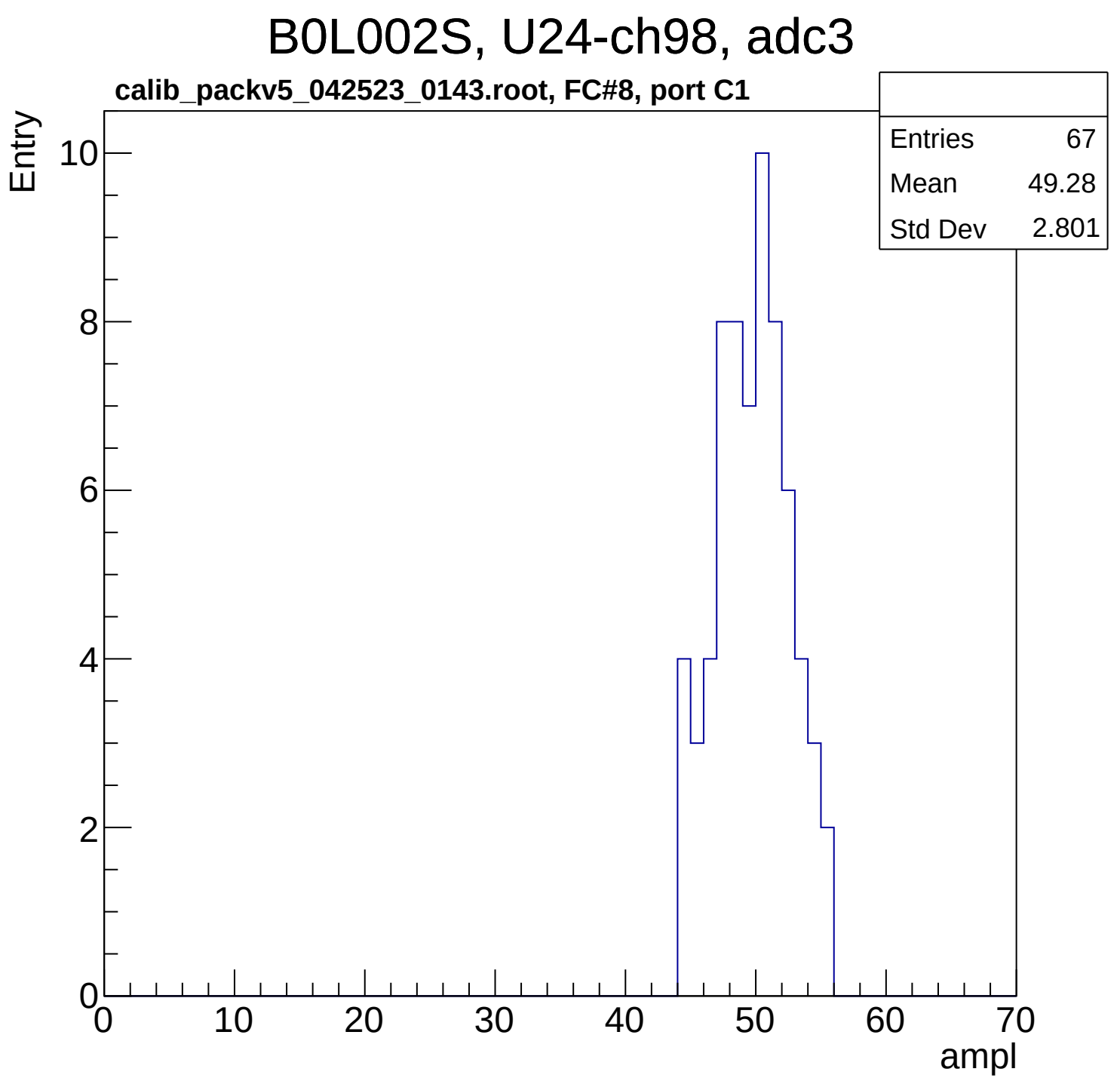
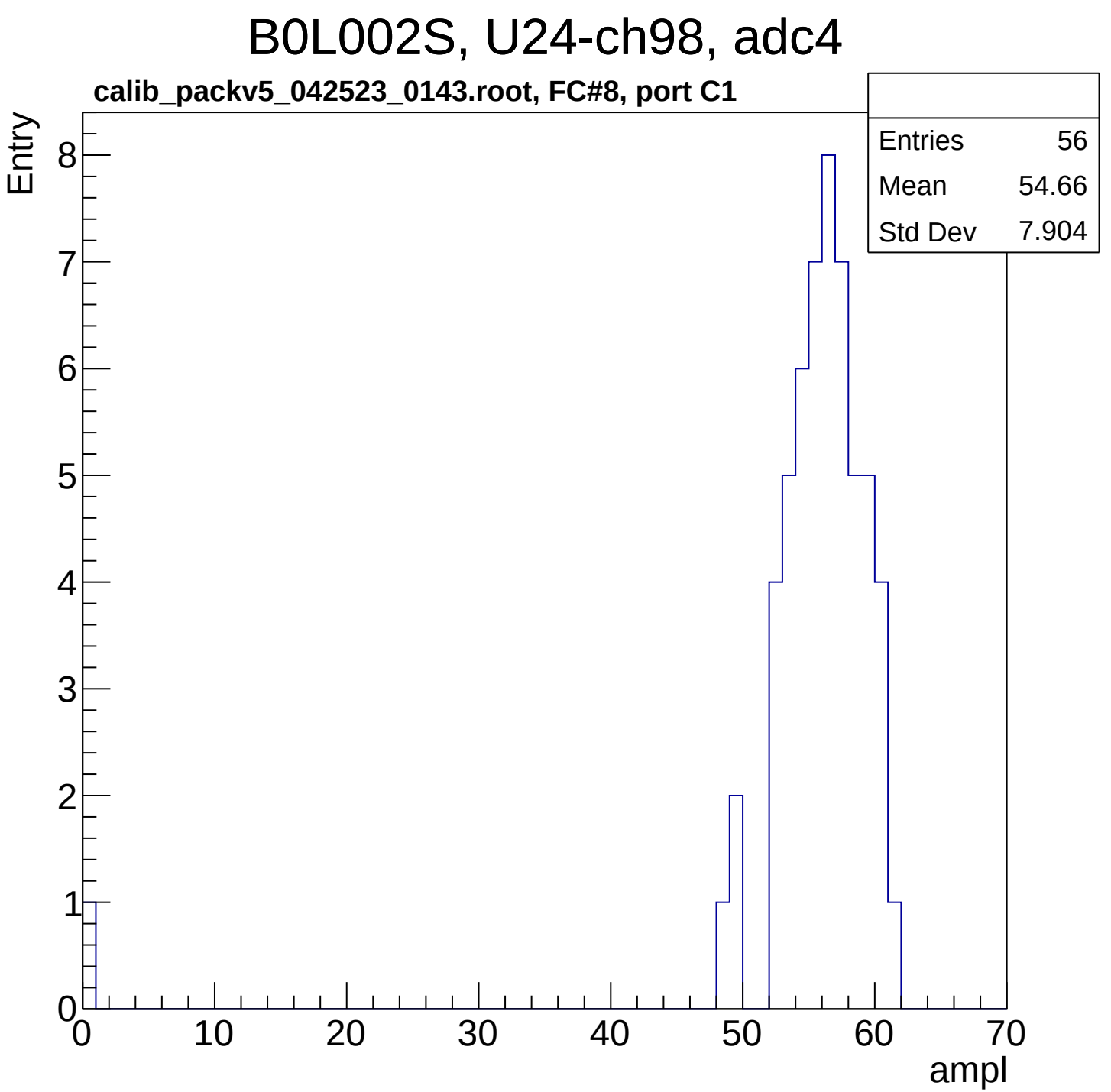
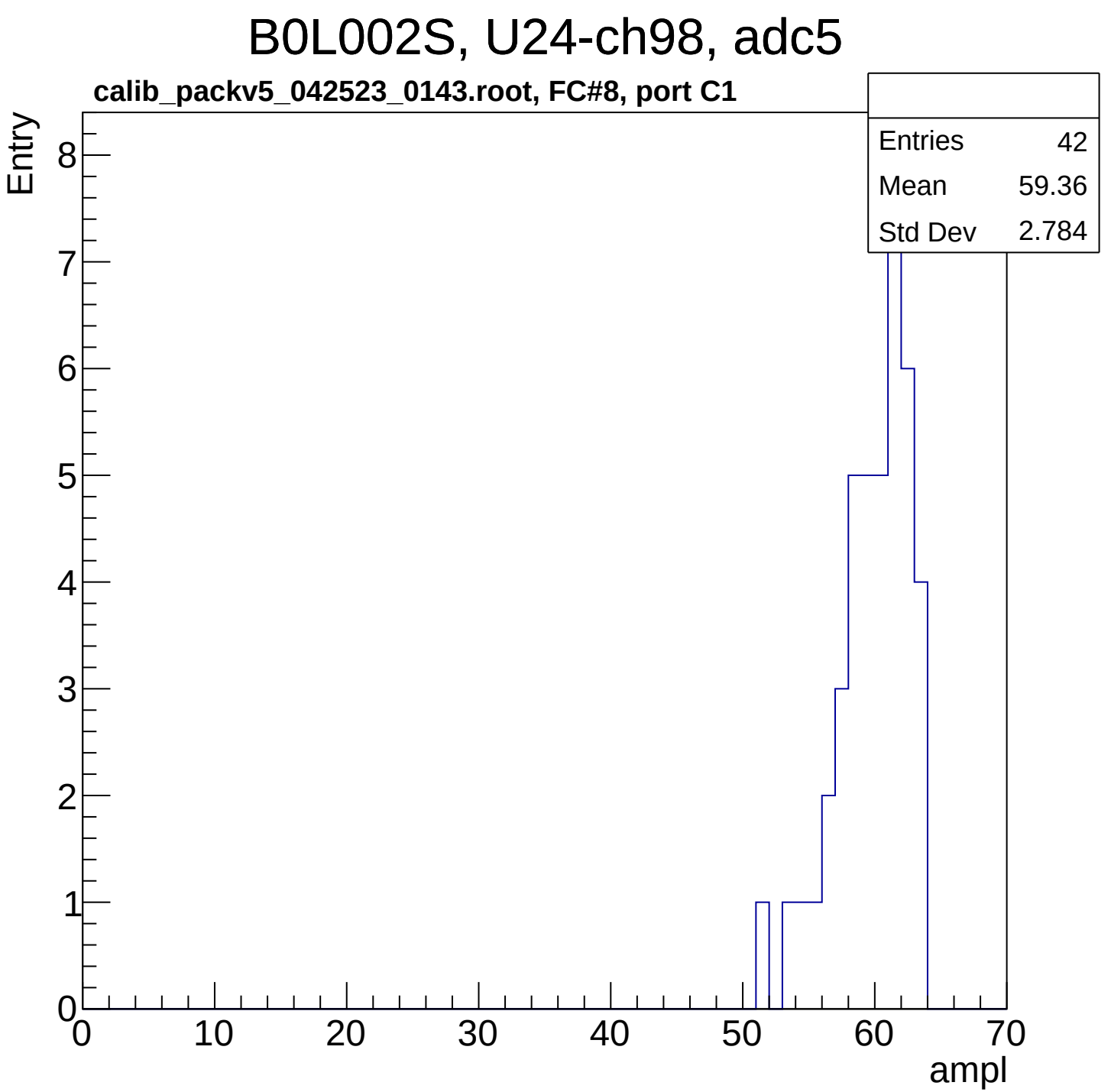
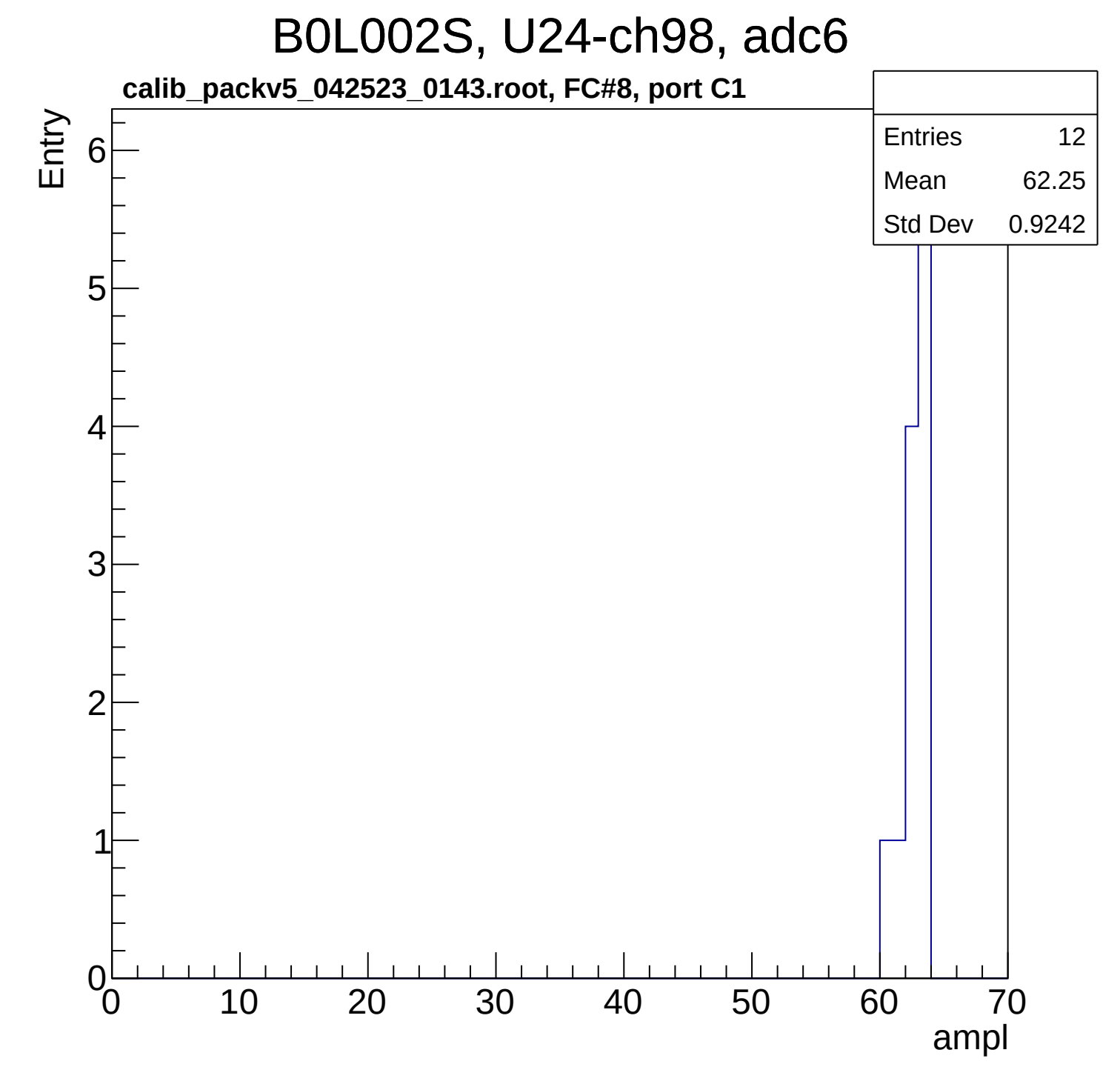
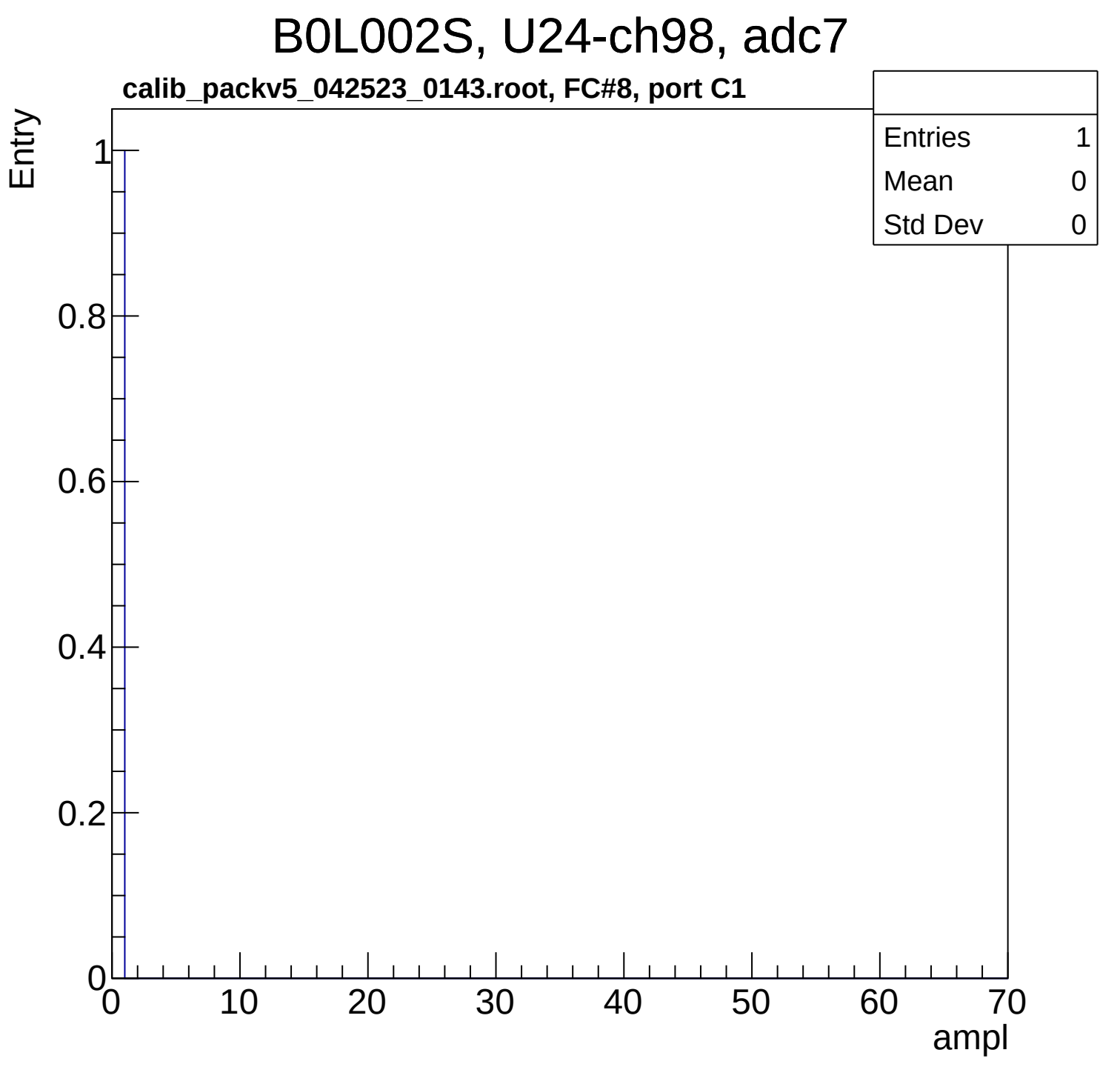
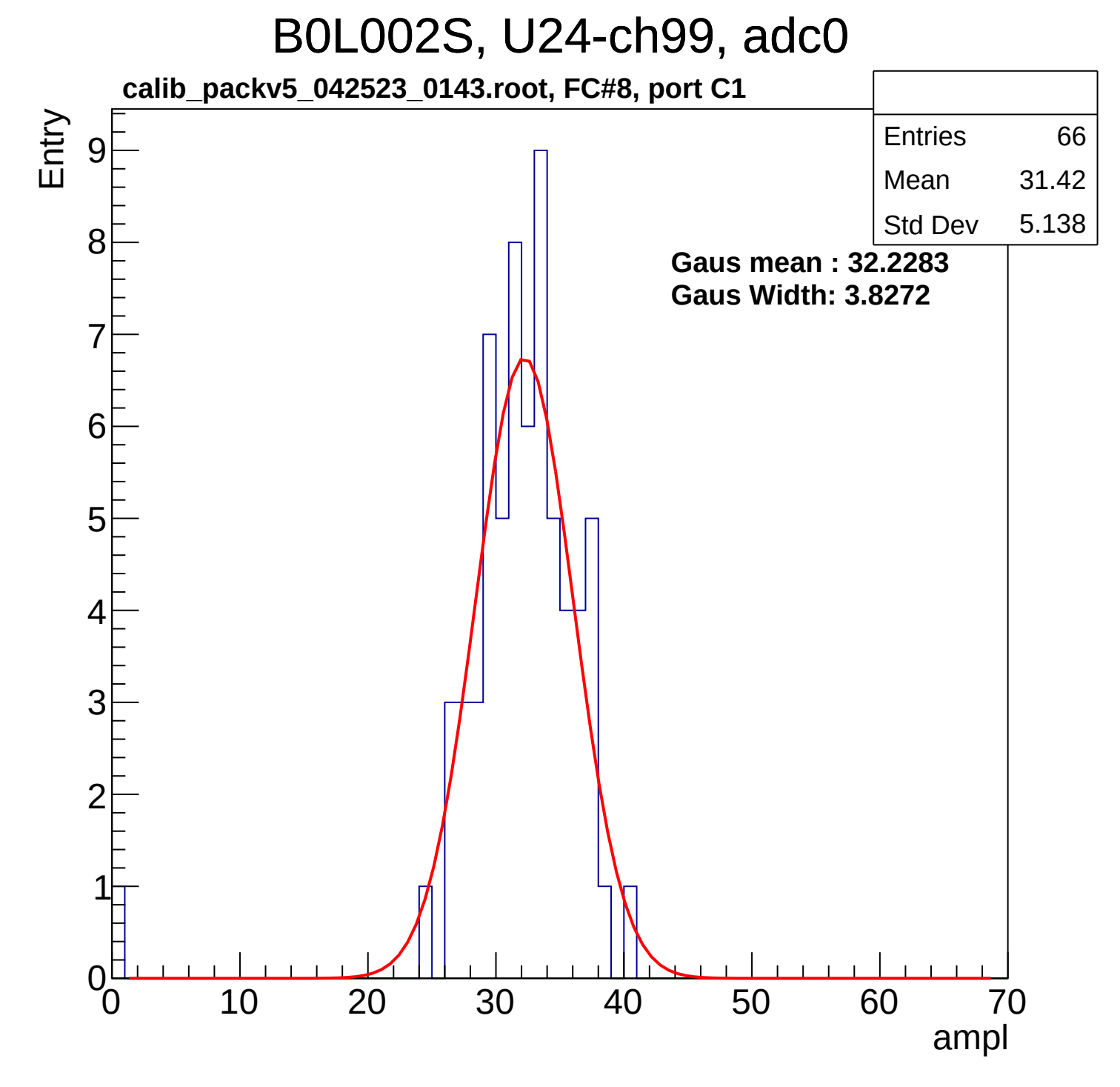

B0L002S, U24-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 49.28
Std Dev 2.801
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 54.66
Std Dev 7.904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.36
Std Dev 2.784
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 62.25
Std Dev 0.9242
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.42
Std Dev 5.138
Gaus mean : 32.2283
Gaus Width: 3.8272
Entry
ampl
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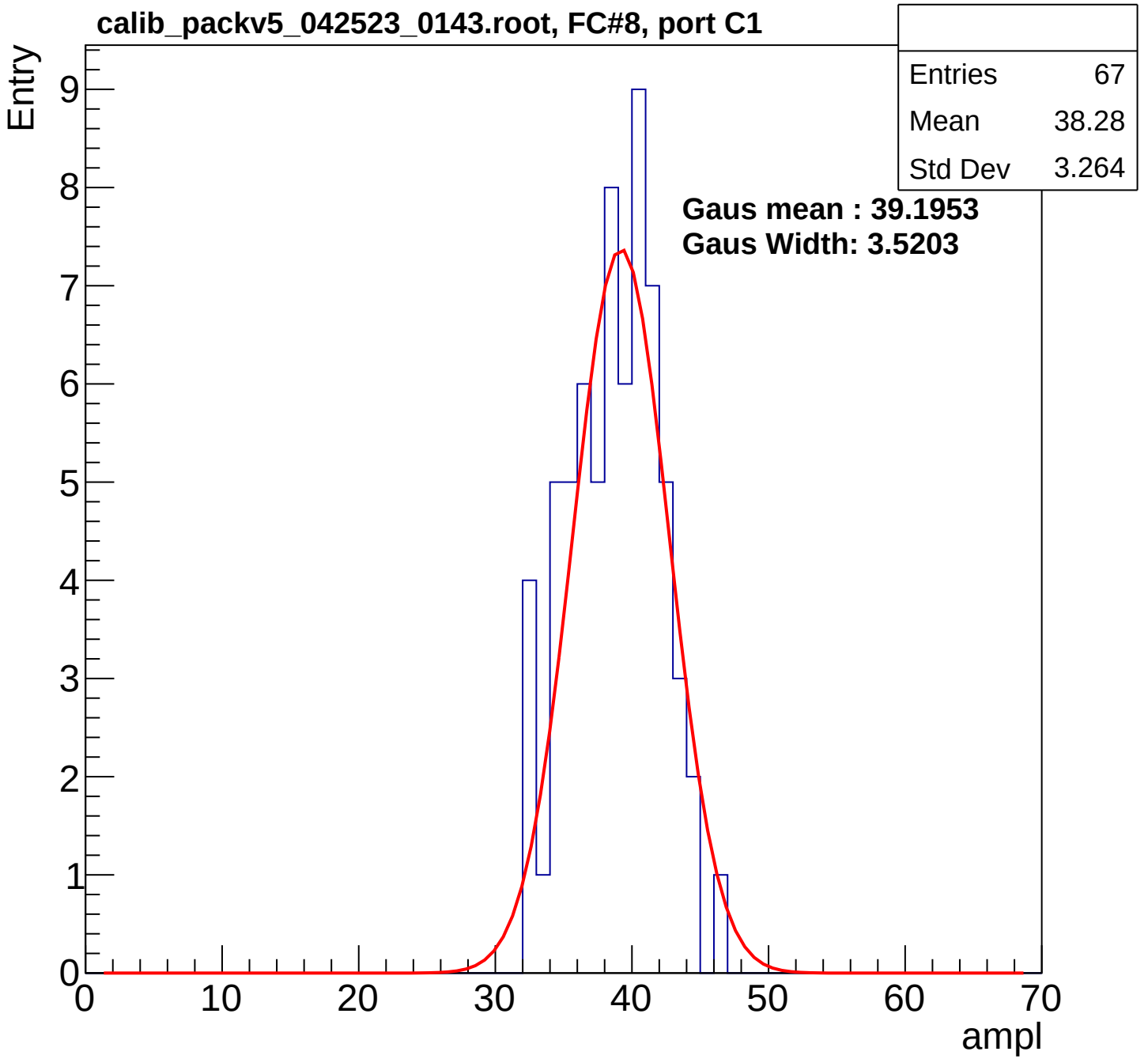
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.28
Std Dev 3.264
Gaus mean : 39.1953
Gaus Width: 3.5203
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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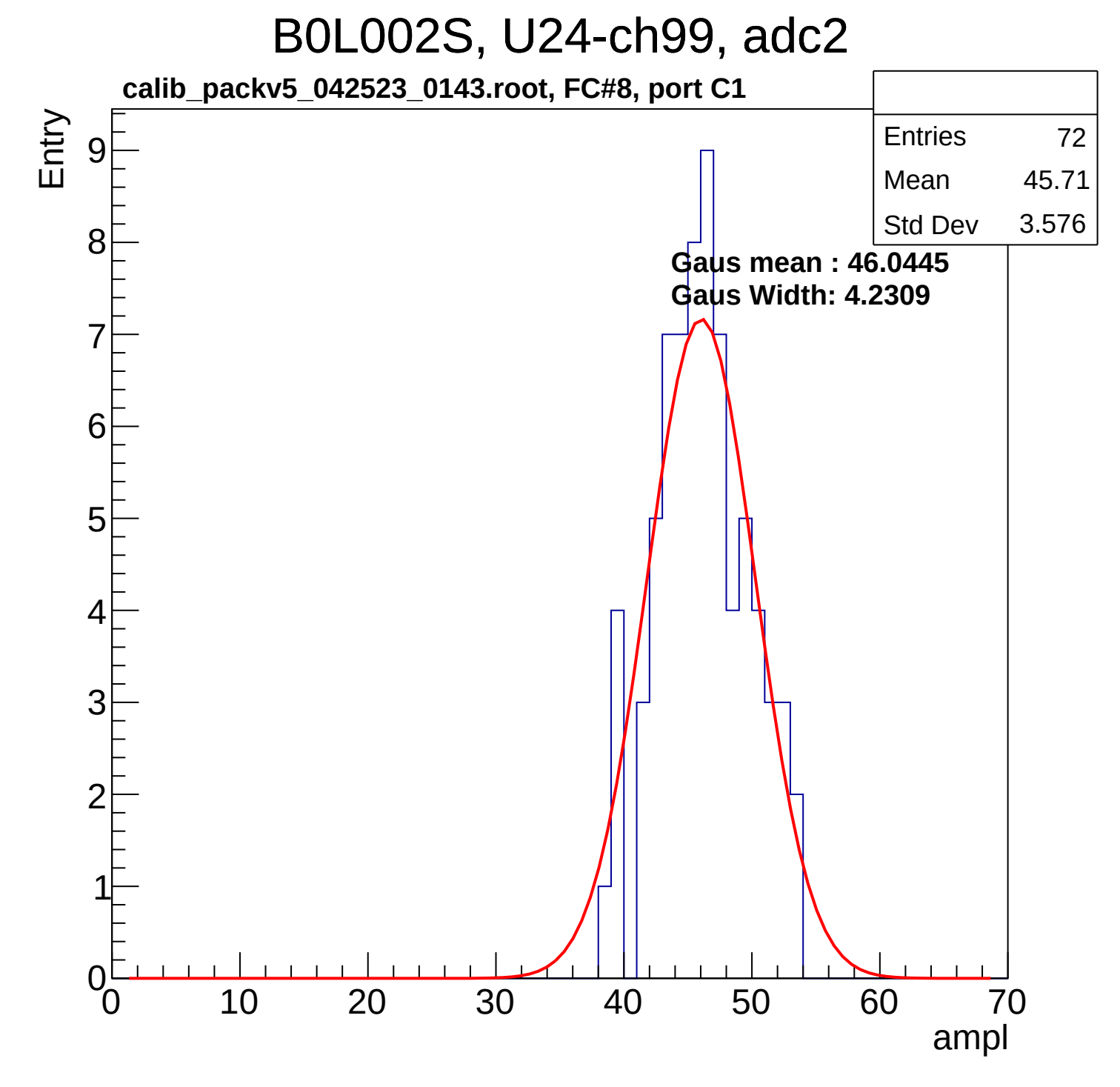
B0L002S, U24-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 45.71
Std Dev 3.576
Gaus mean : 46.0445
Gaus Width: 4.2309
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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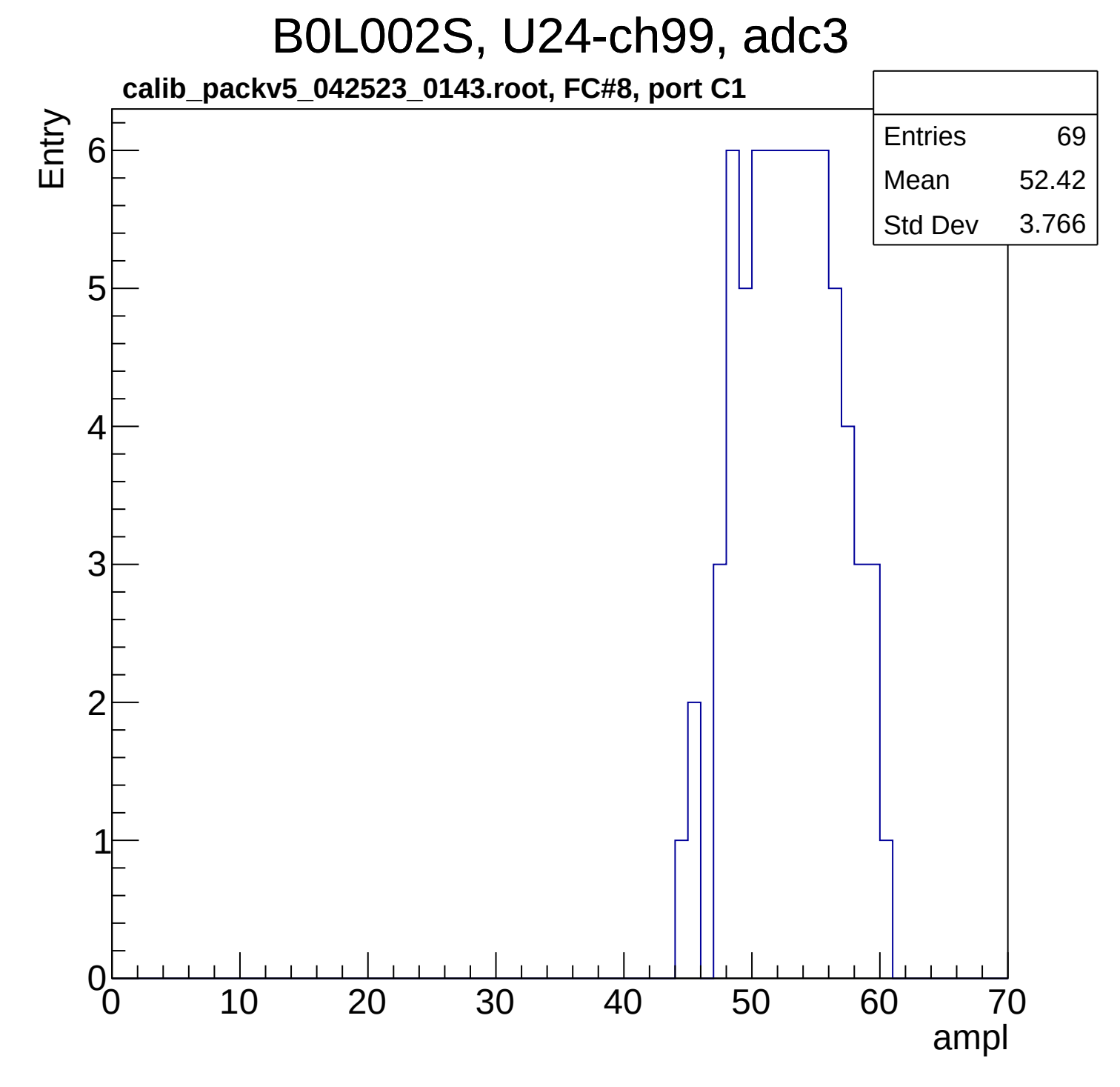
B0L002S, U24-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.42
Std Dev 3.766
Entry
ampl
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0
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60
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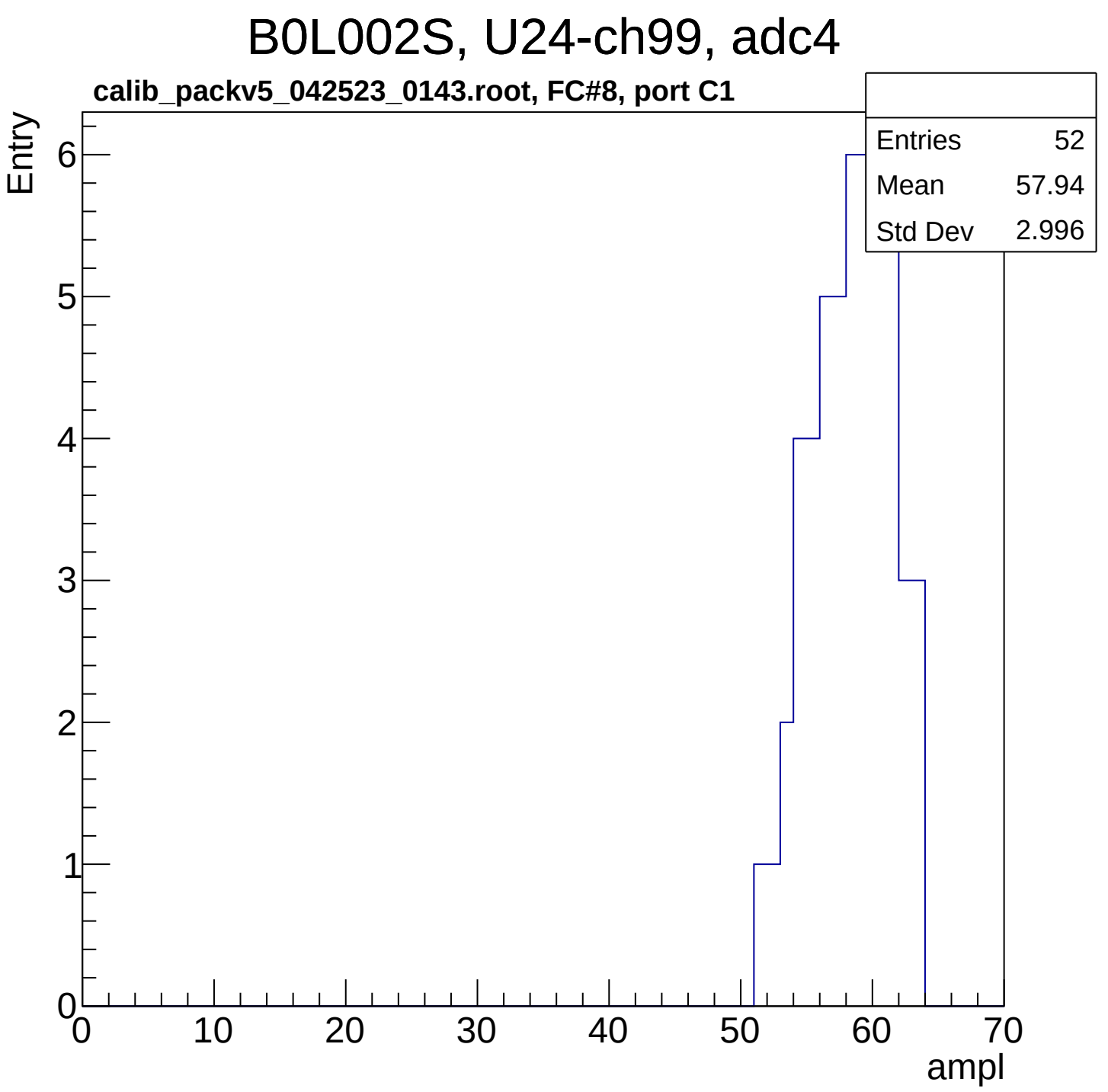
B0L002S, U24-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.94
Std Dev 2.996
Entry
ampl
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1
2
3
4
5
6
0
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20
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40
50
60
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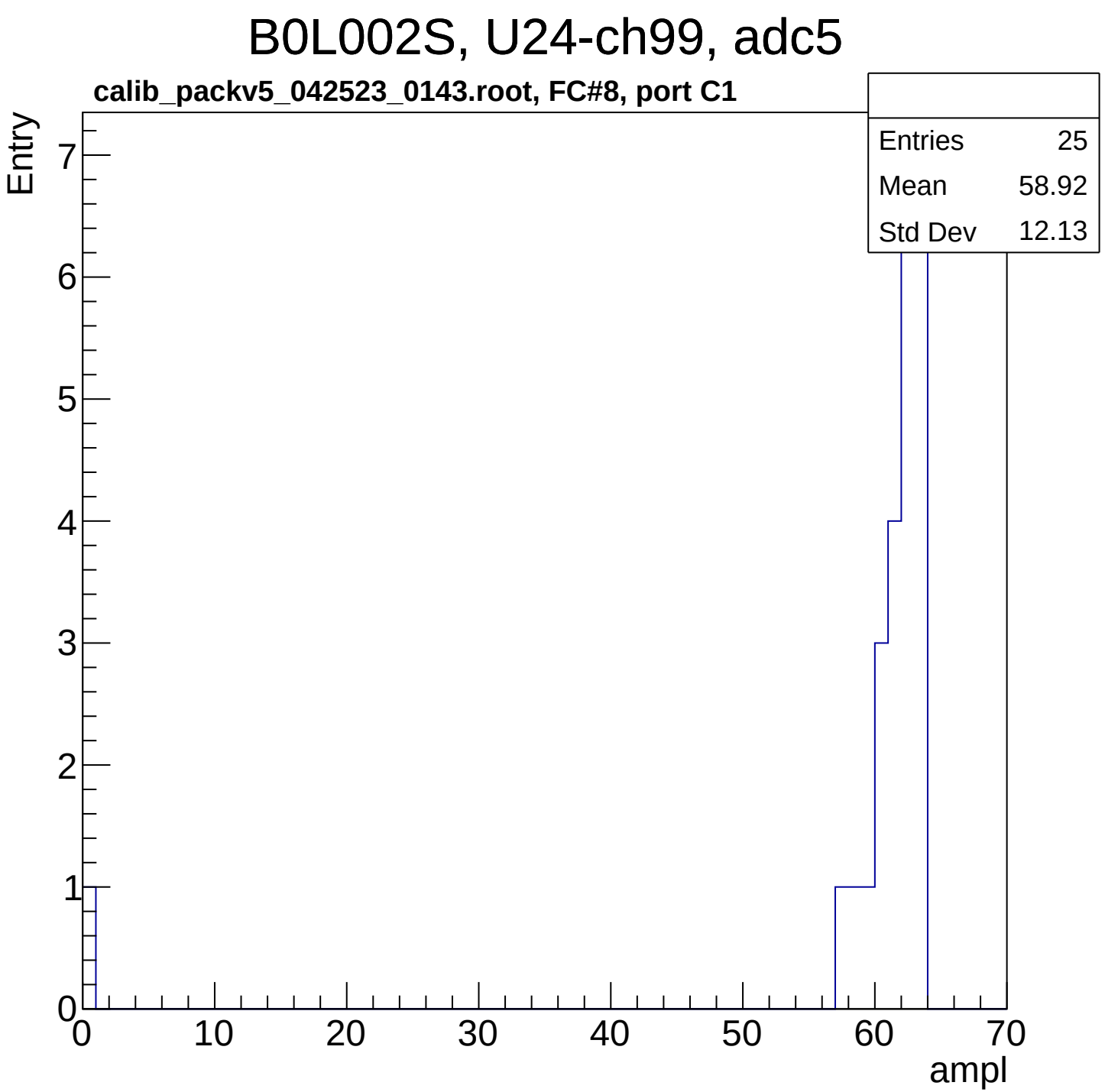
B0L002S, U24-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.92
Std Dev 12.13
Entry
ampl
0
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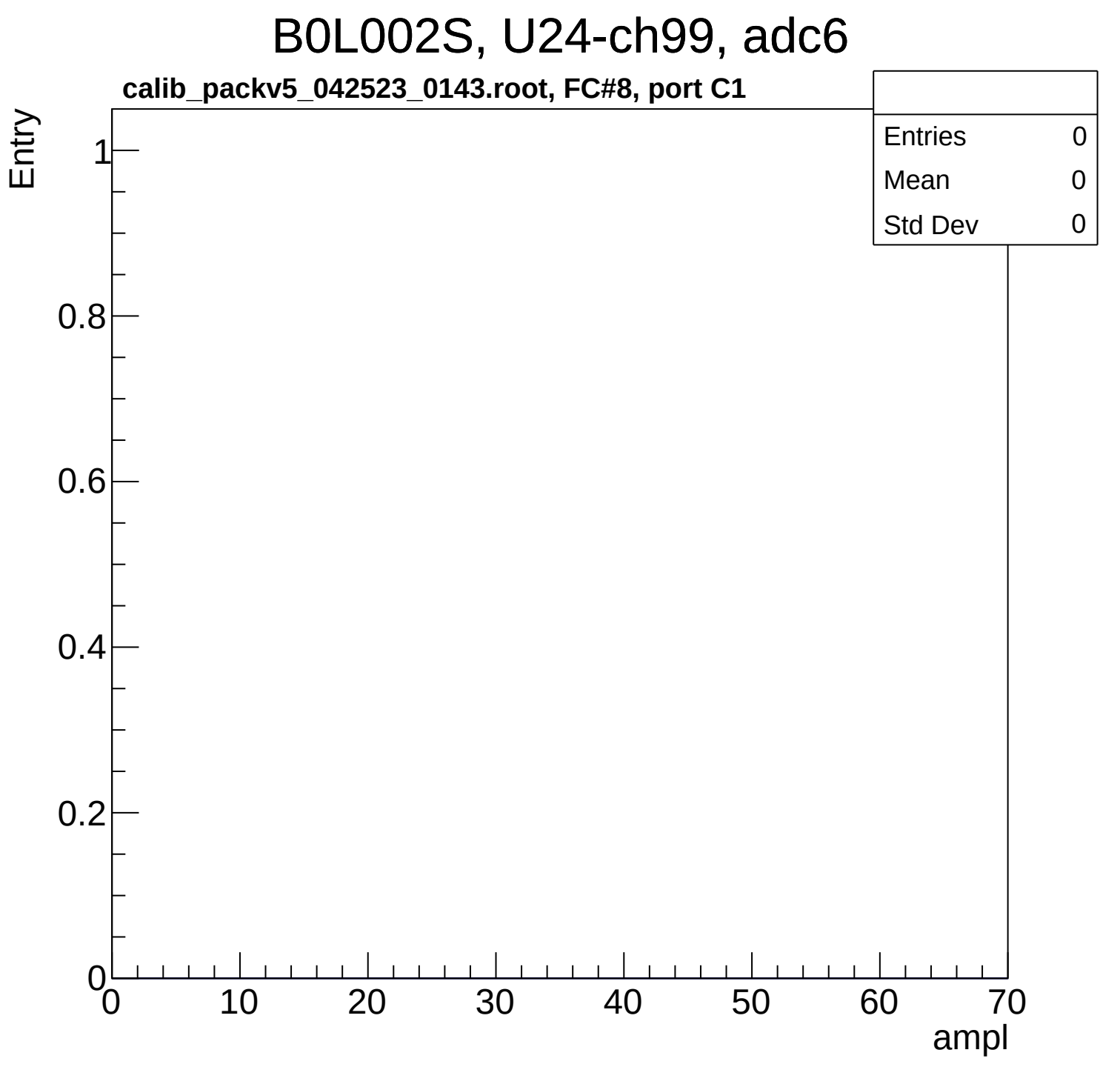
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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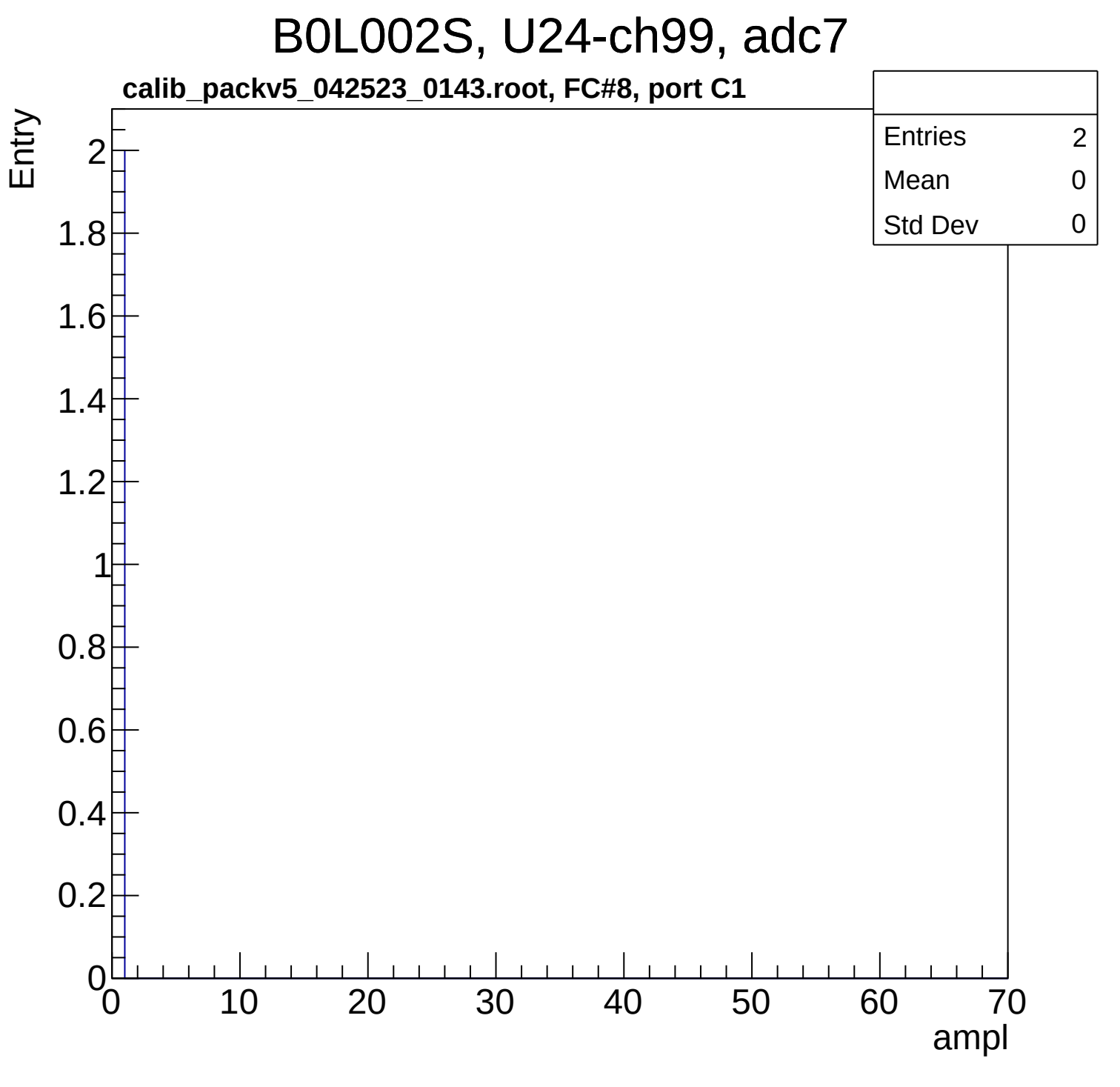
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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10
20
30
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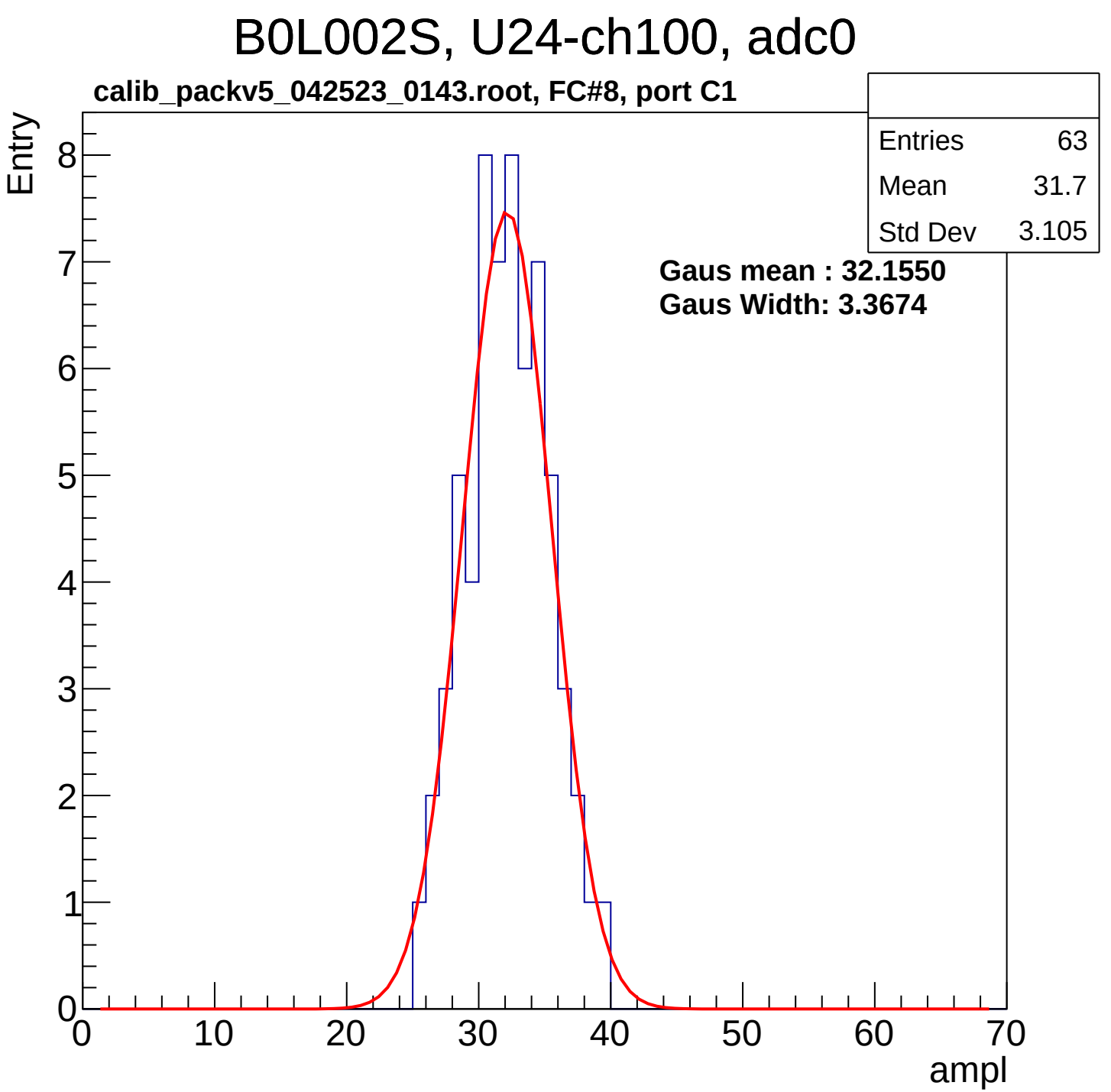
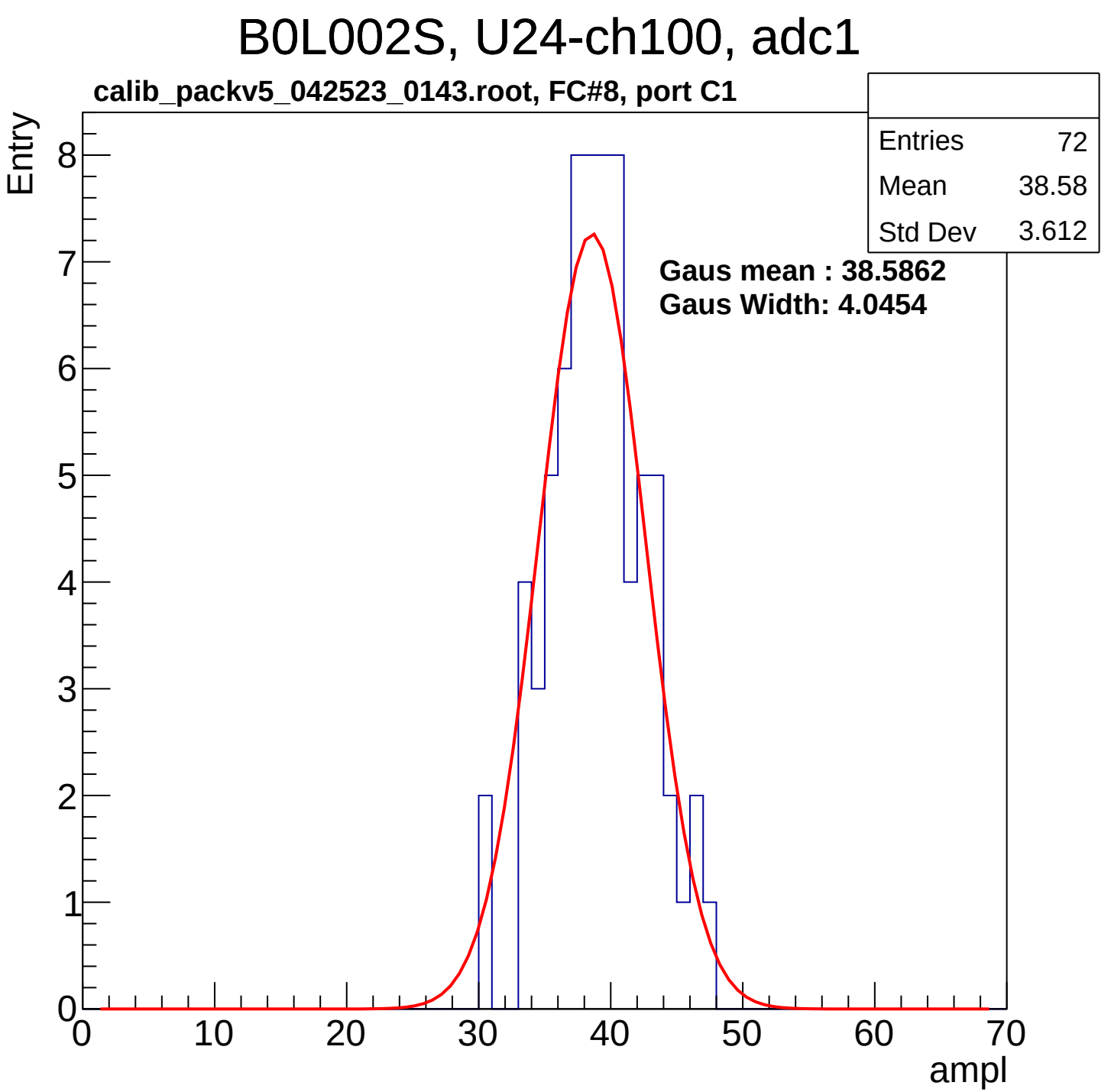
50
60
70
ampl


B0L002S, U24-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.7
Std Dev 3.105
Gaus mean : 32.1550
Gaus Width: 3.3674
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.58
Std Dev 3.612
Gaus mean : 38.5862
Gaus Width: 4.0454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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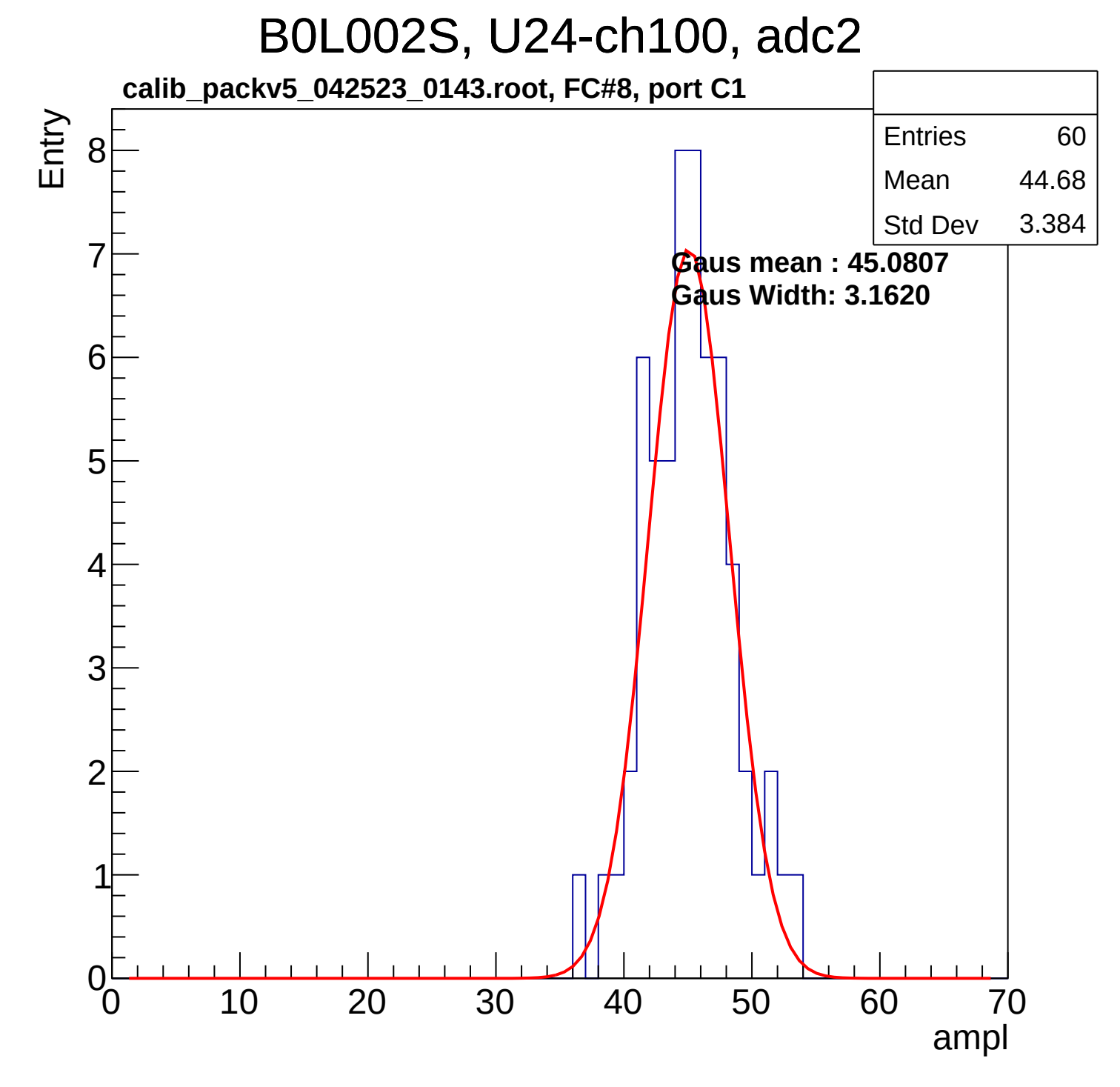
B0L002S, U24-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.68
Std Dev 3.384
Gaus mean : 45.0807
Gaus Width: 3.1620
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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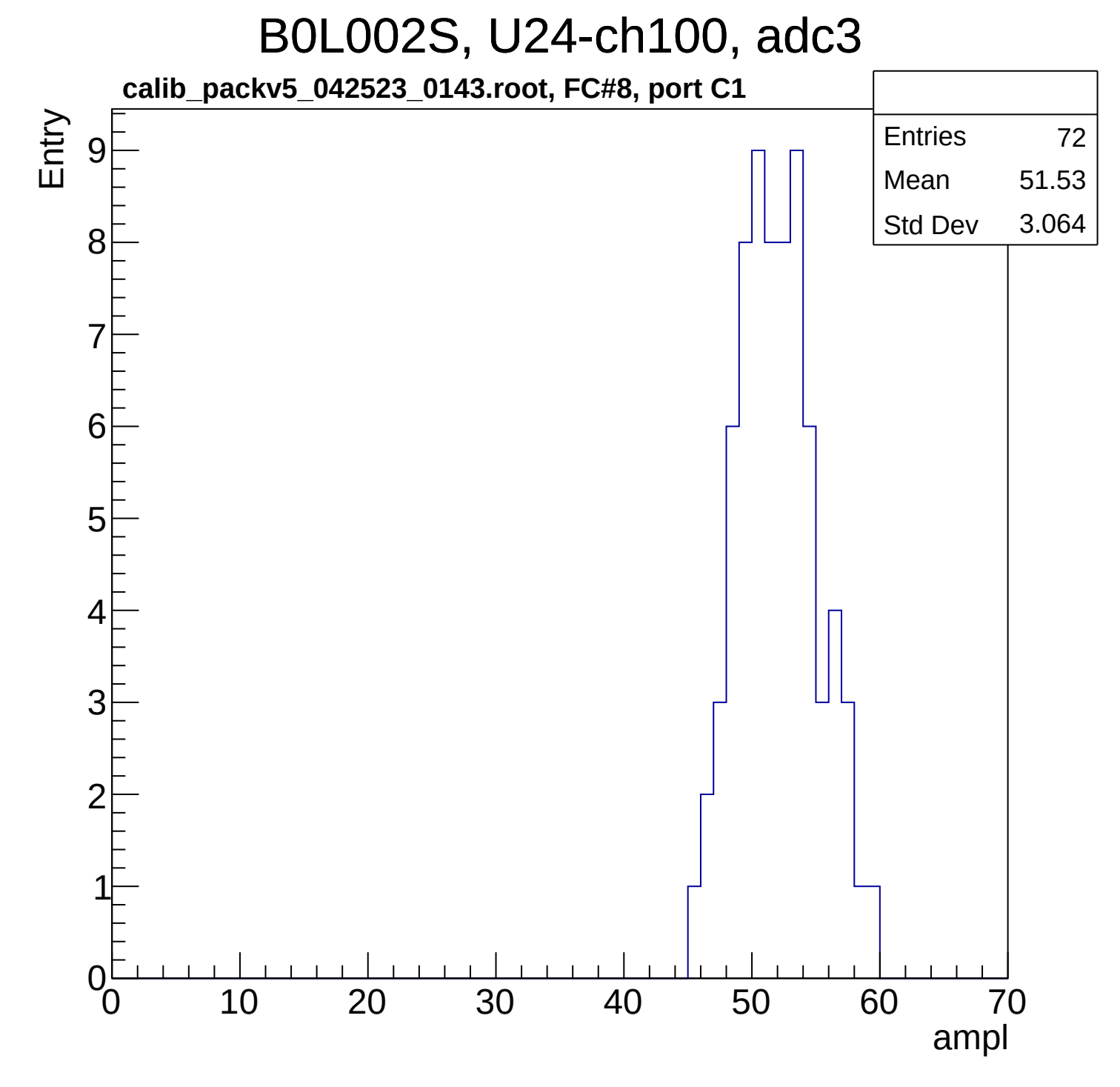
B0L002S, U24-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.53
Std Dev 3.064
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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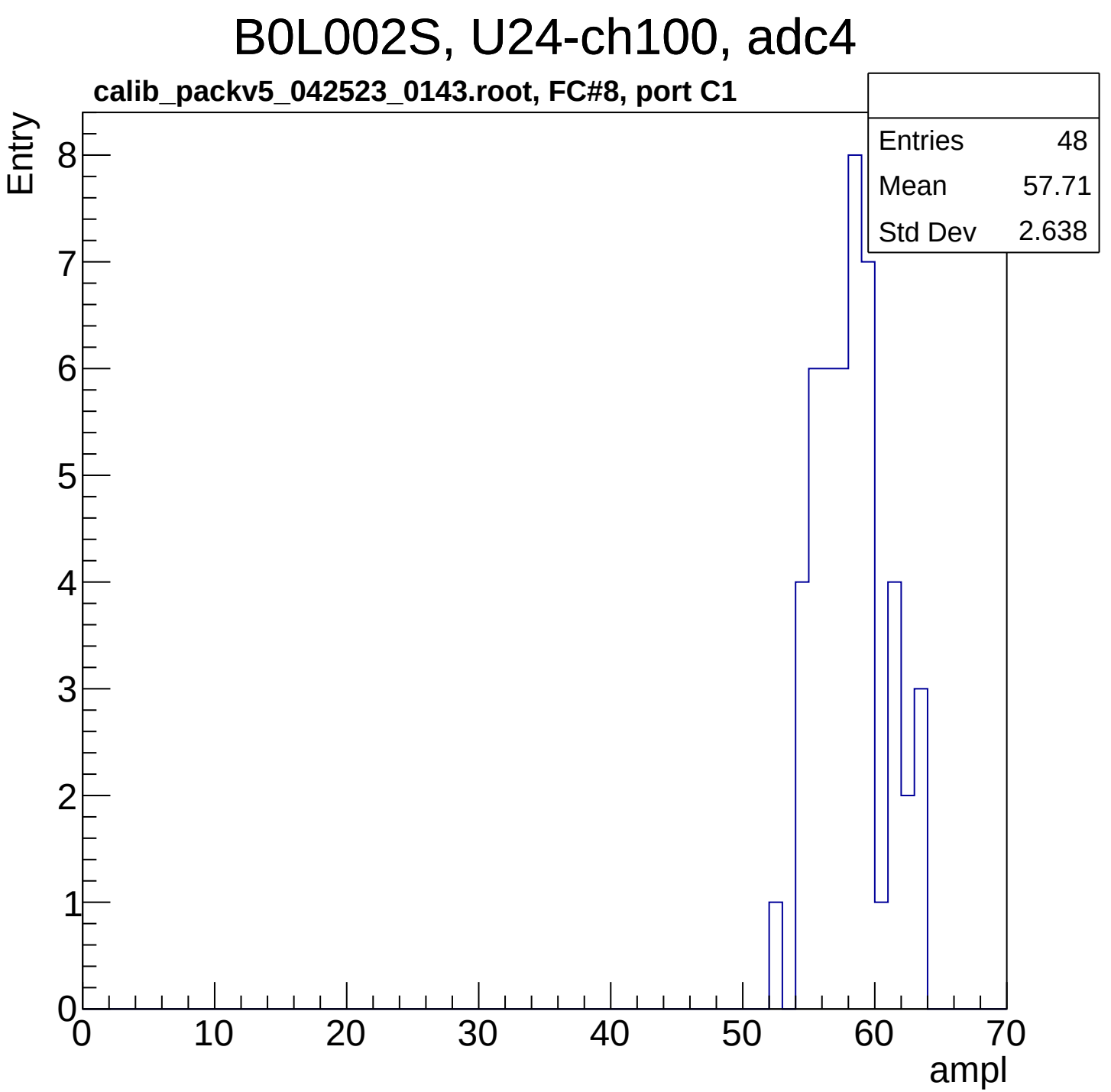
B0L002S, U24-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.71
Std Dev 2.638
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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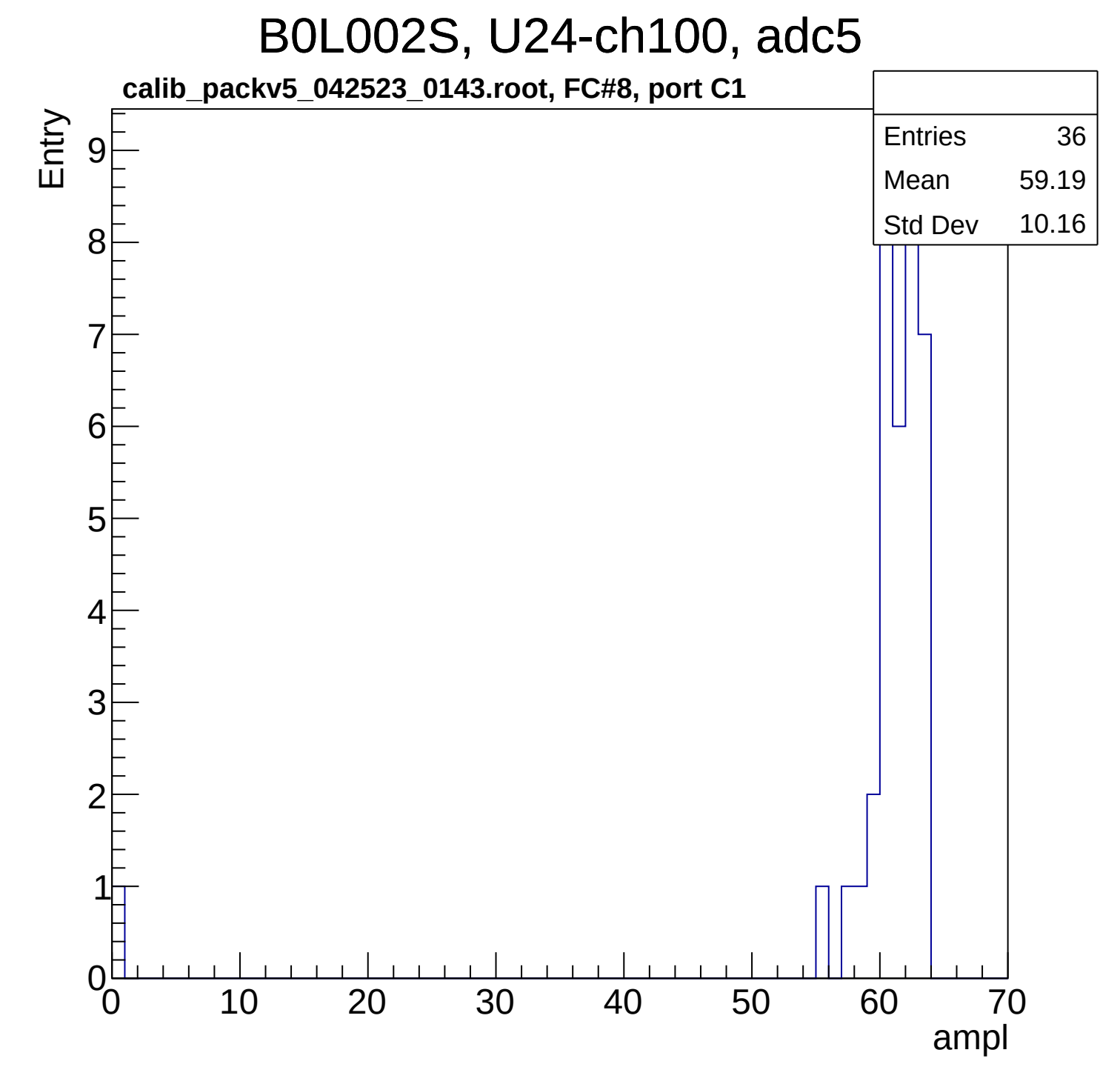
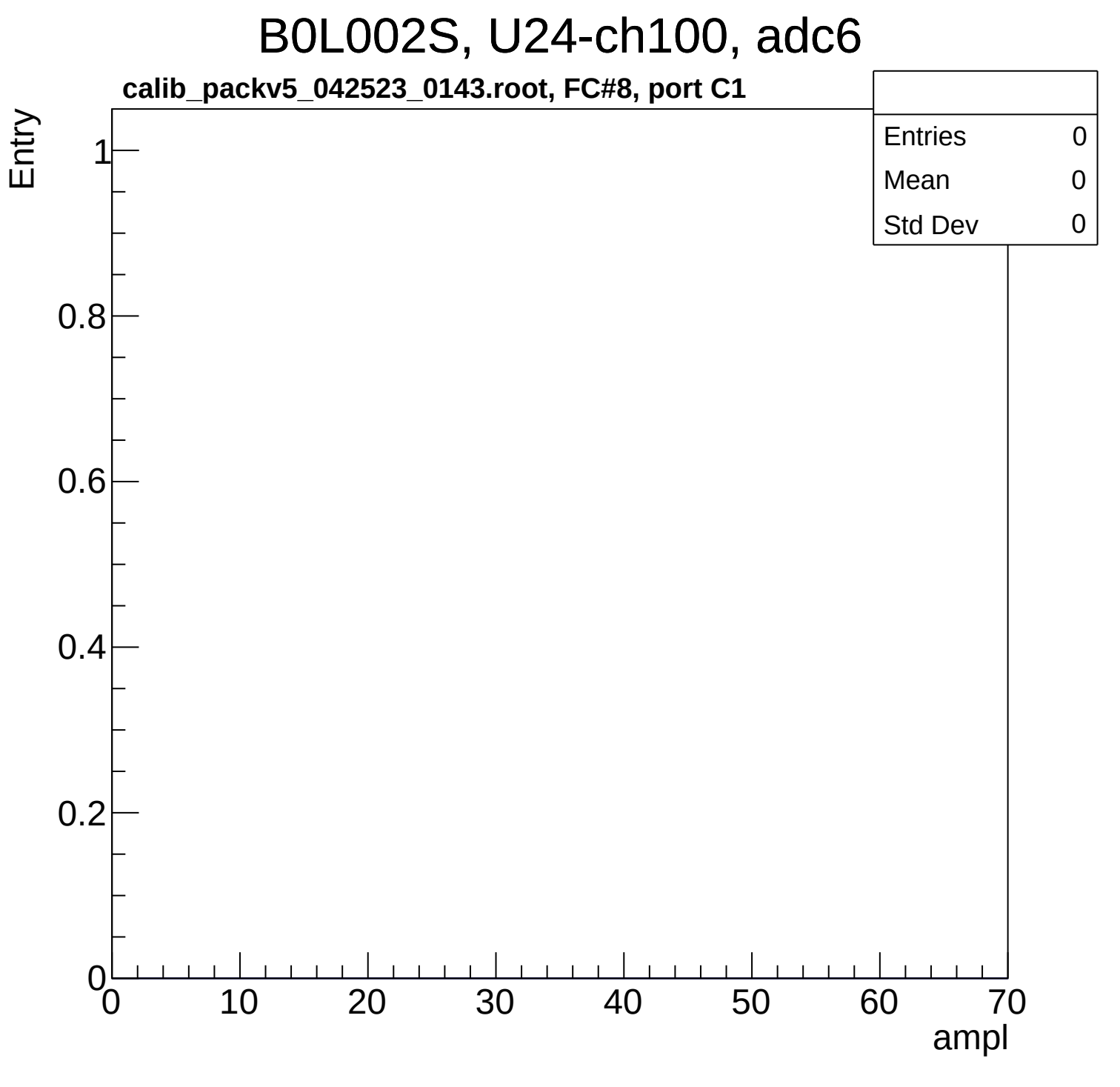
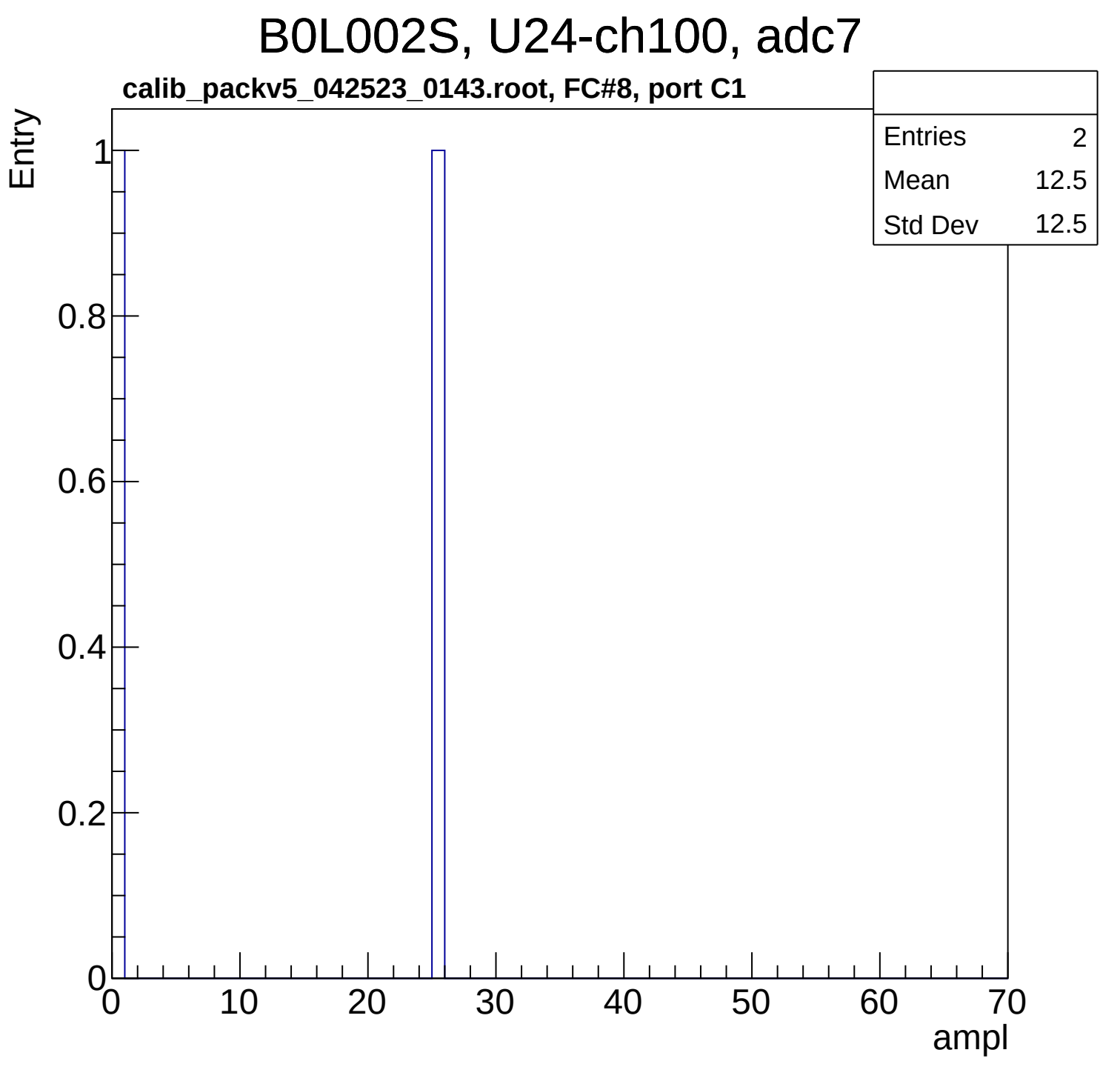
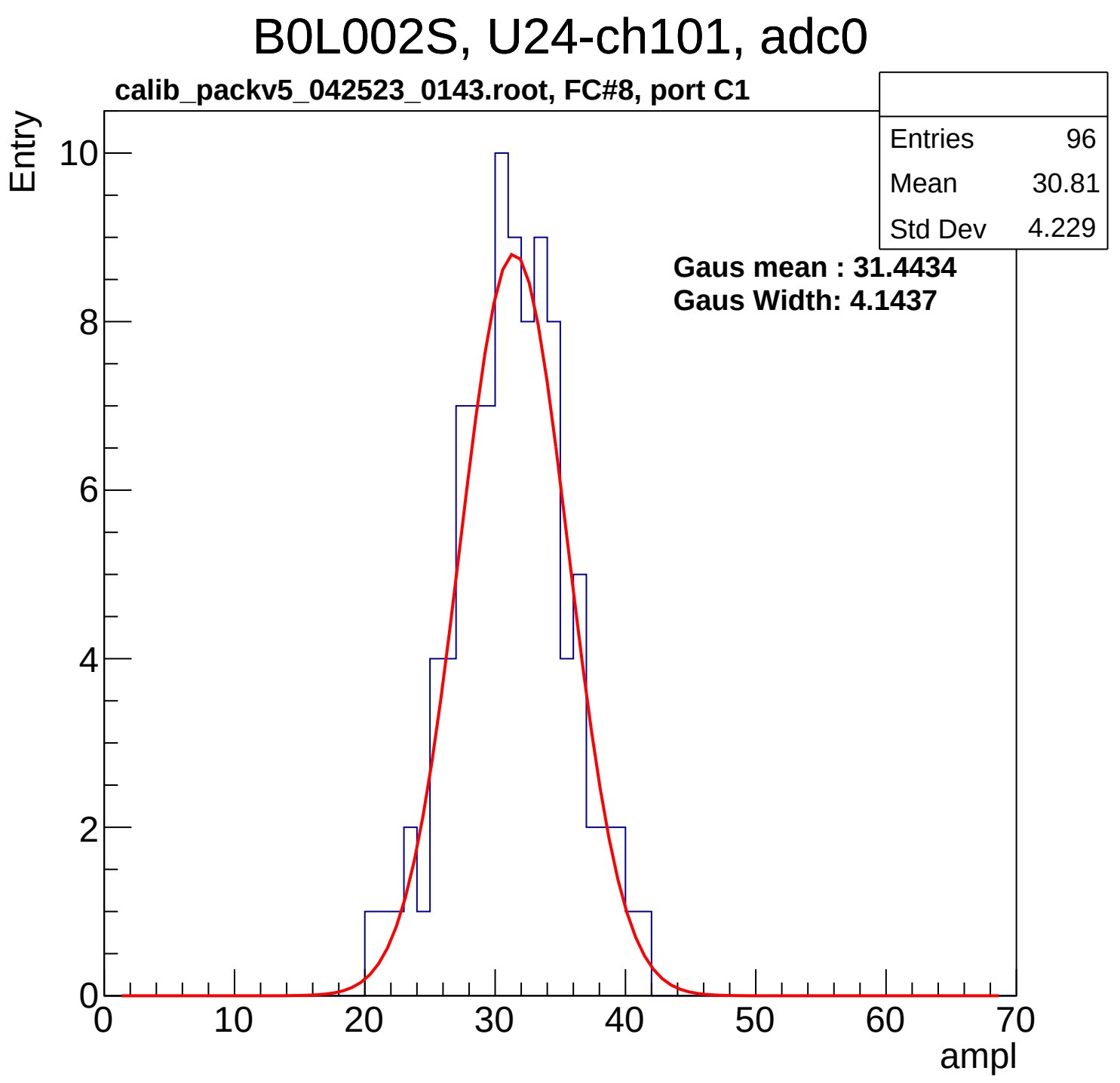

B0L002S, U24-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.19
Std Dev 10.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 96
Mean 30.81
Std Dev 4.229
Gaus mean : 31.4434
Gaus Width: 4.1437
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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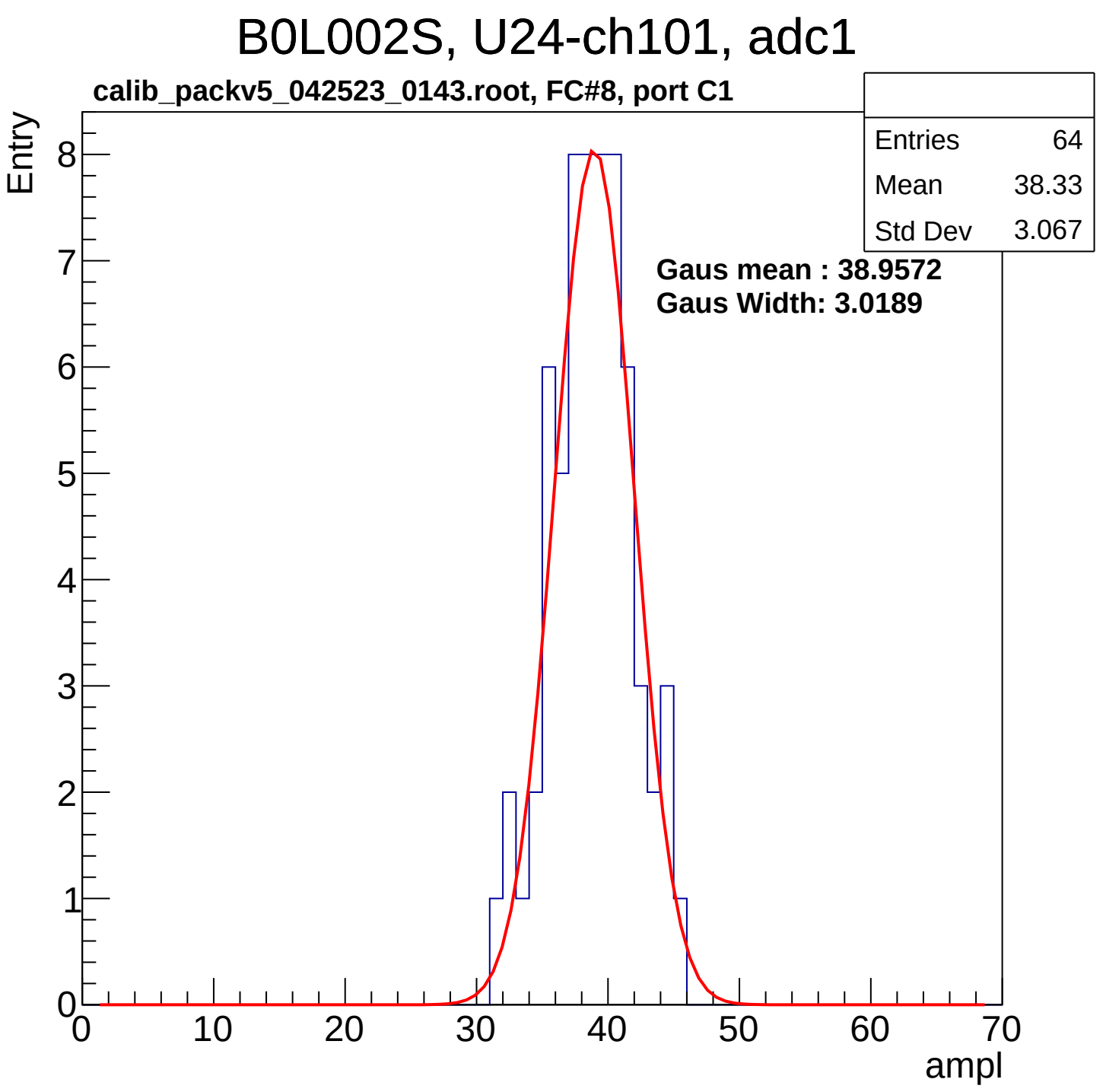
B0L002S, U24-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.33
Std Dev 3.067
Gaus mean : 38.9572
Gaus Width: 3.0189
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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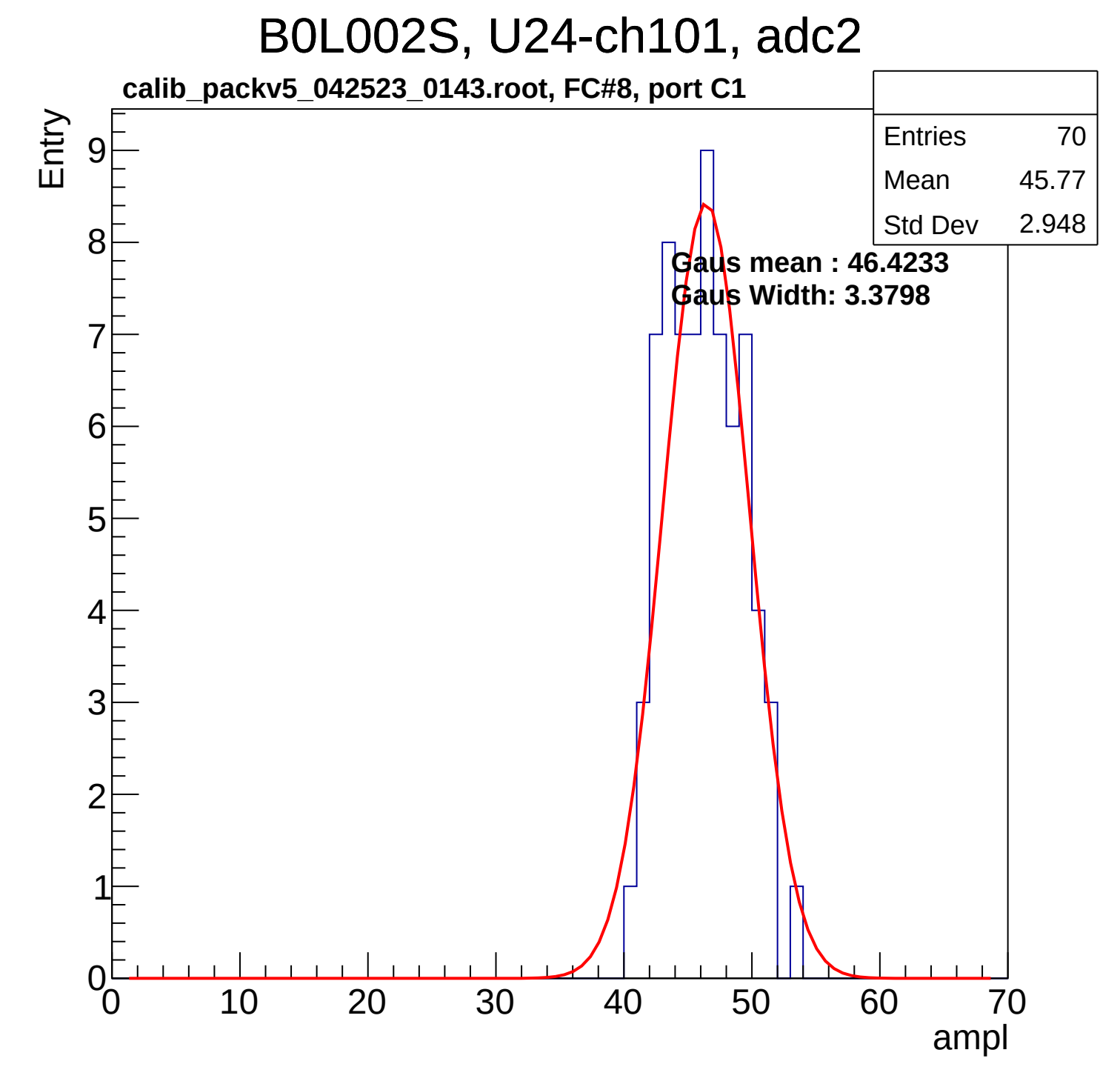
B0L002S, U24-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 45.77
Std Dev 2.948
Gaus mean : 46.4233
Gaus Width: 3.3798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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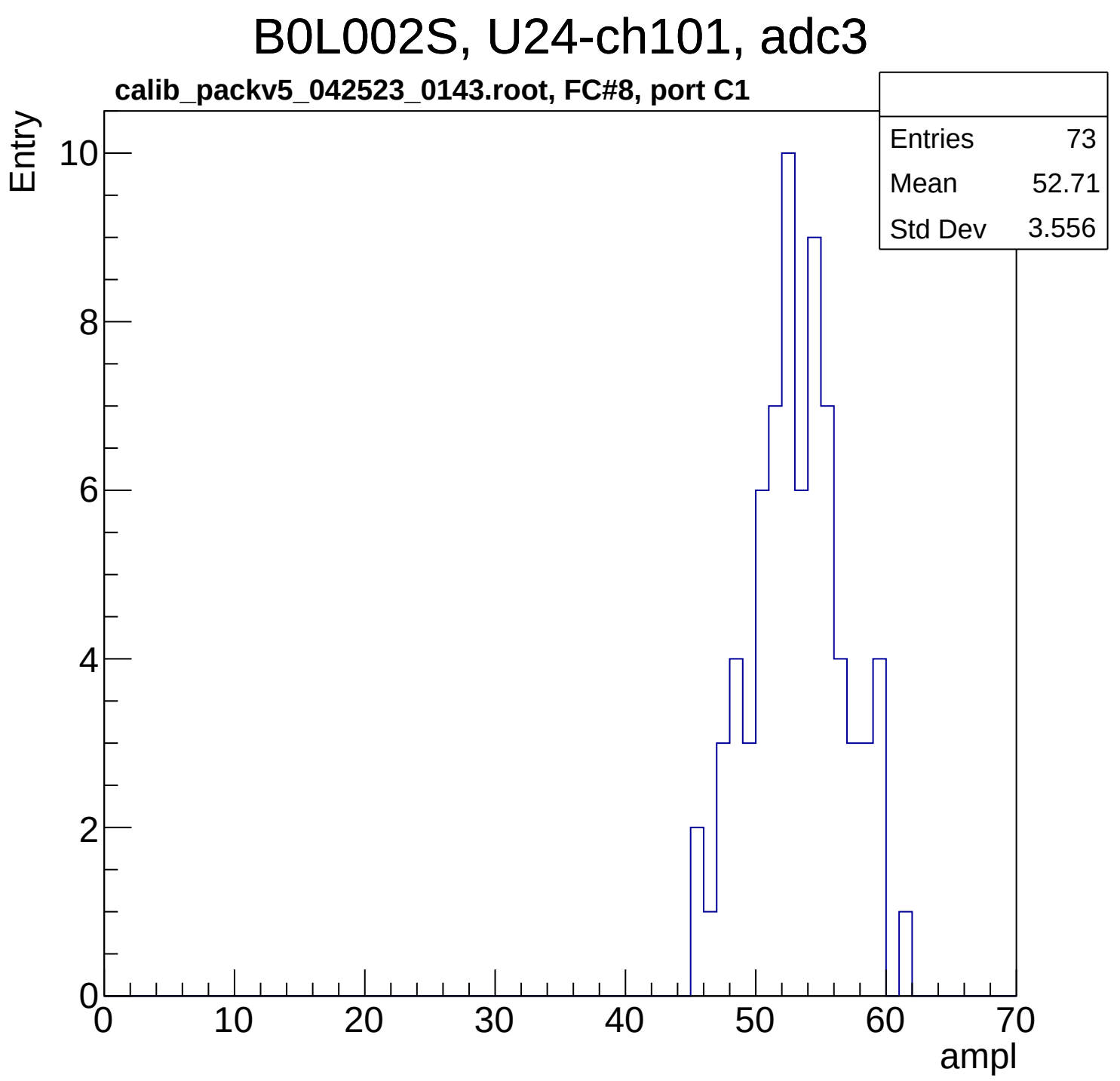
B0L002S, U24-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 52.71
Std Dev 3.556
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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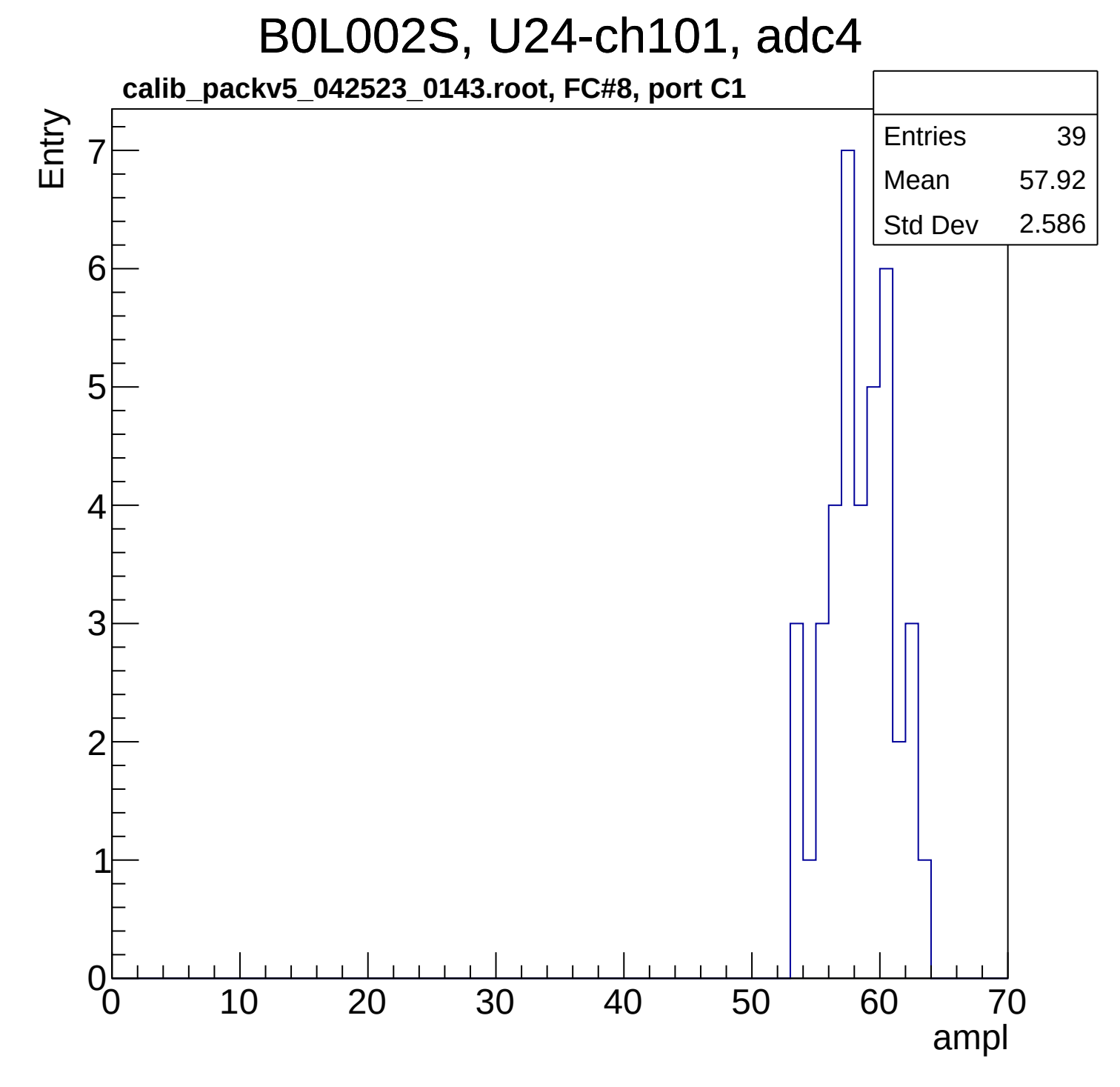
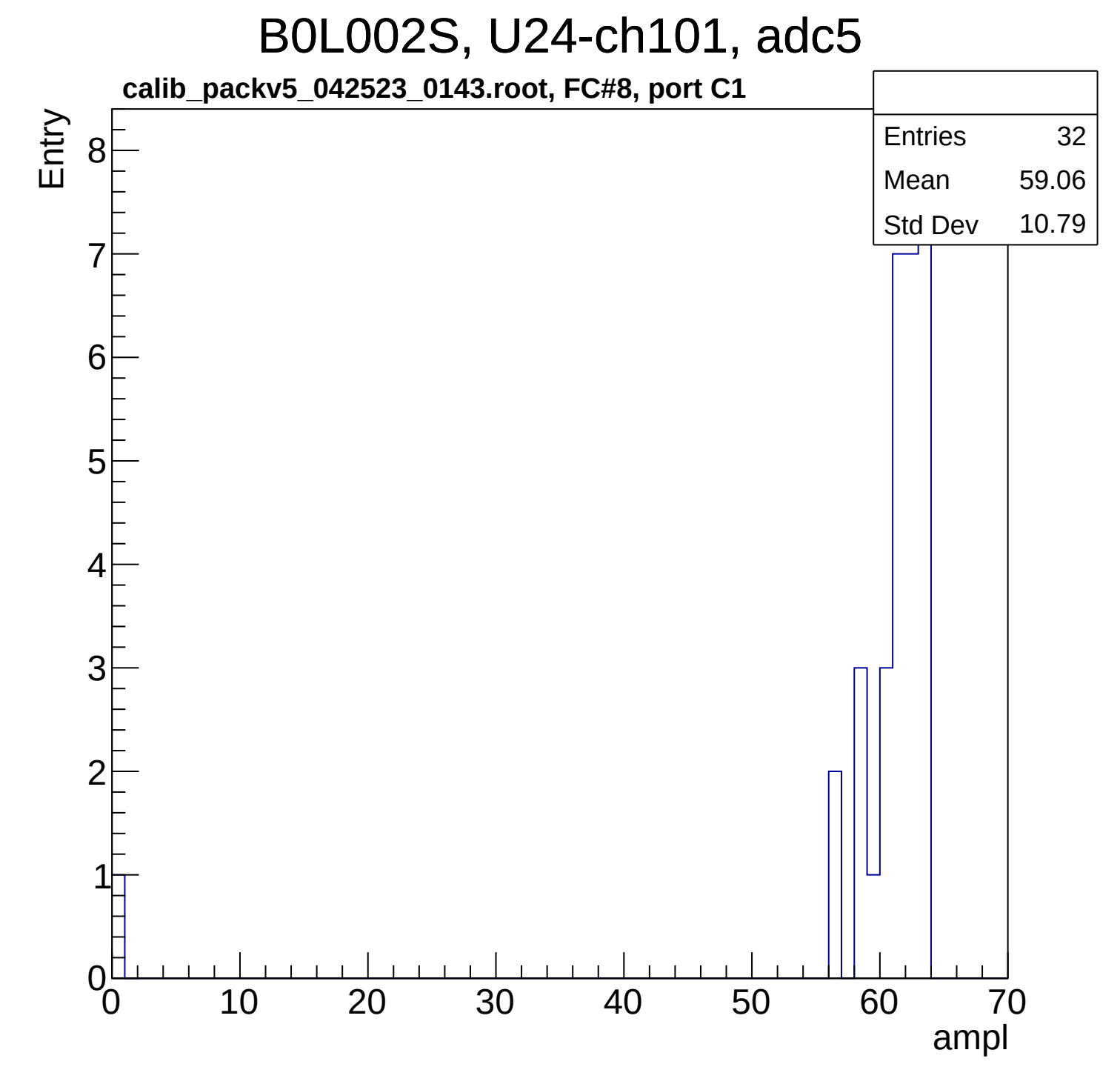

B0L002S, U24-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 57.92
Std Dev 2.586
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.06
Std Dev 10.79
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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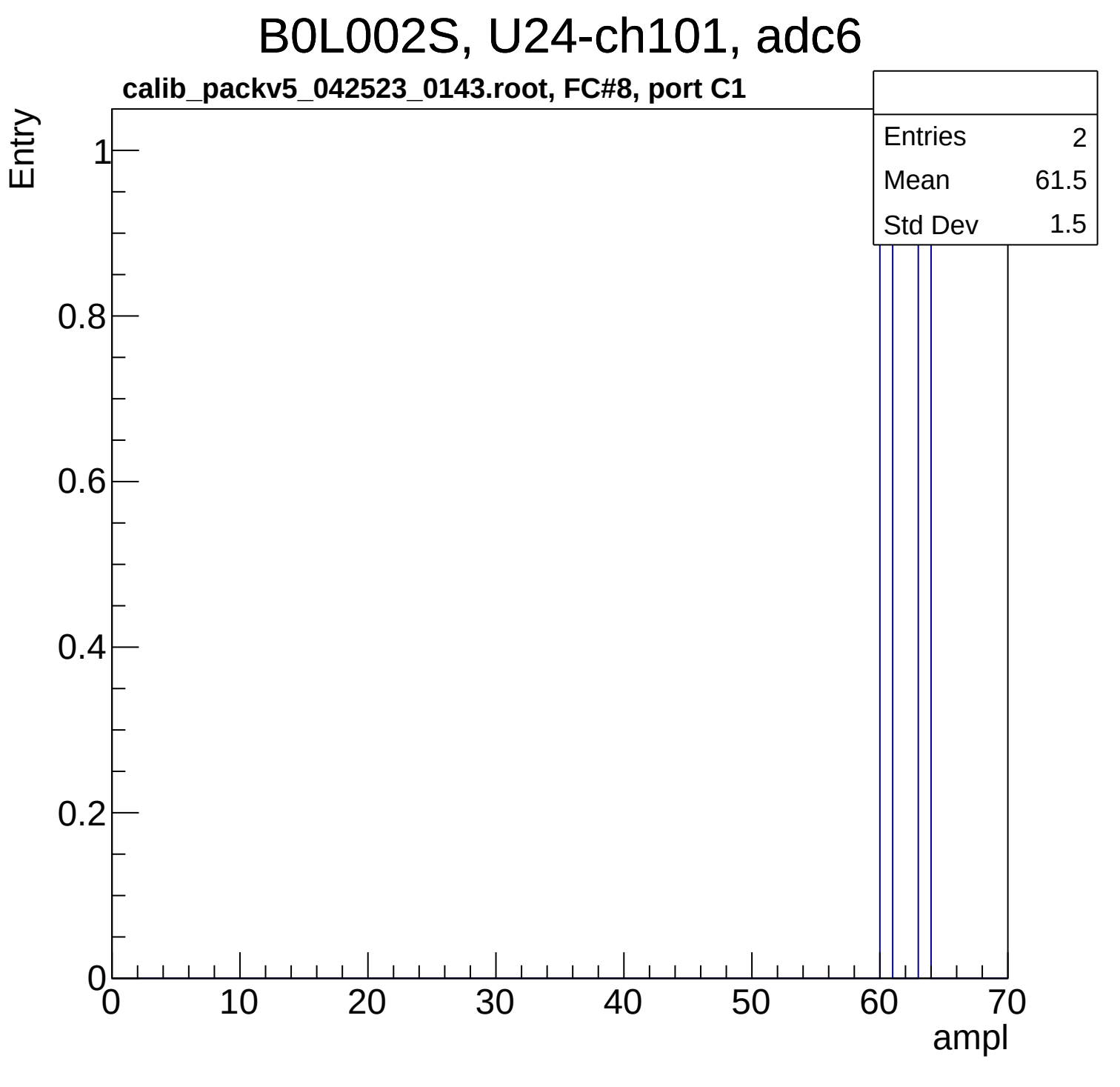
B0L002S, U24-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
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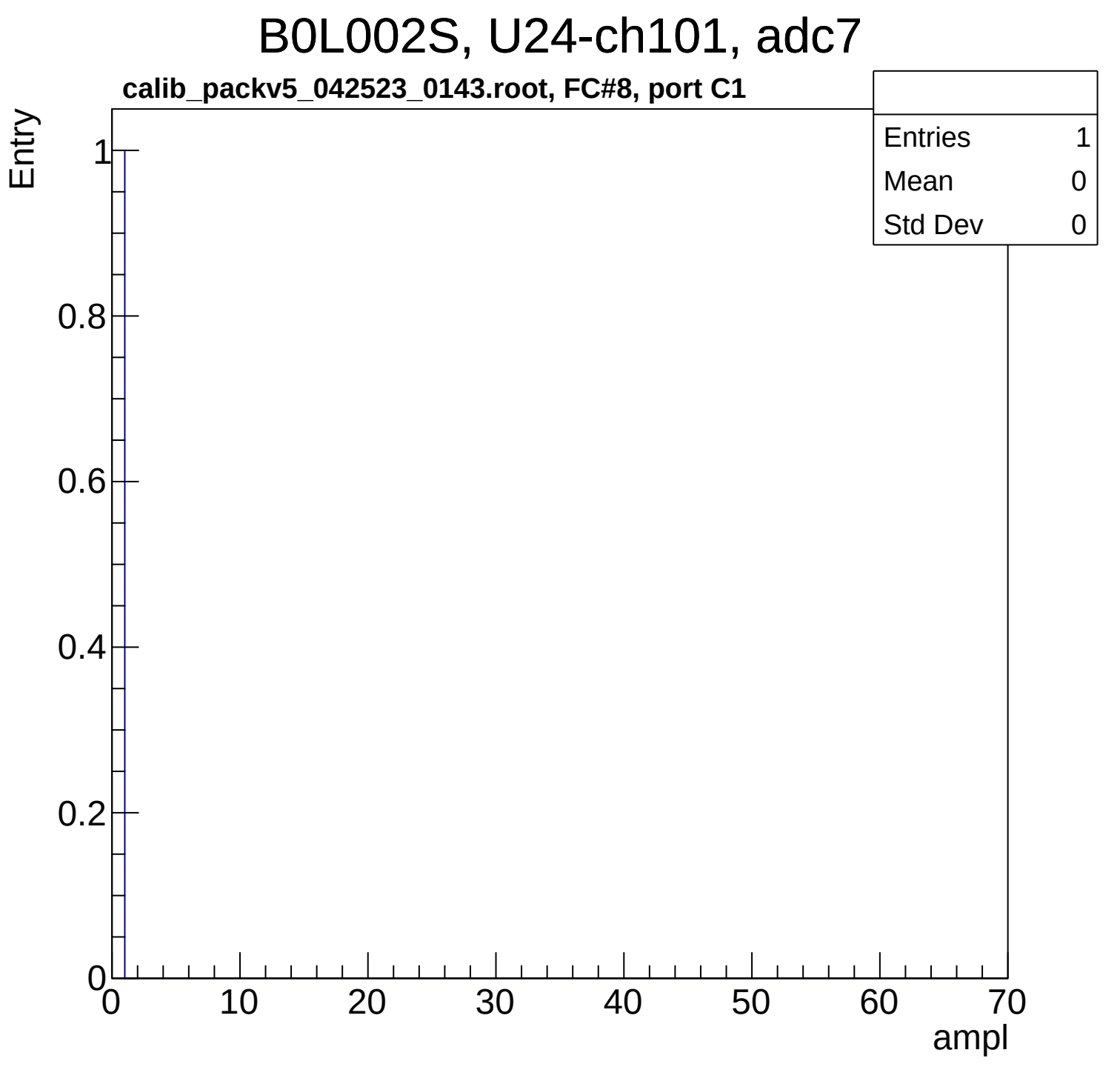
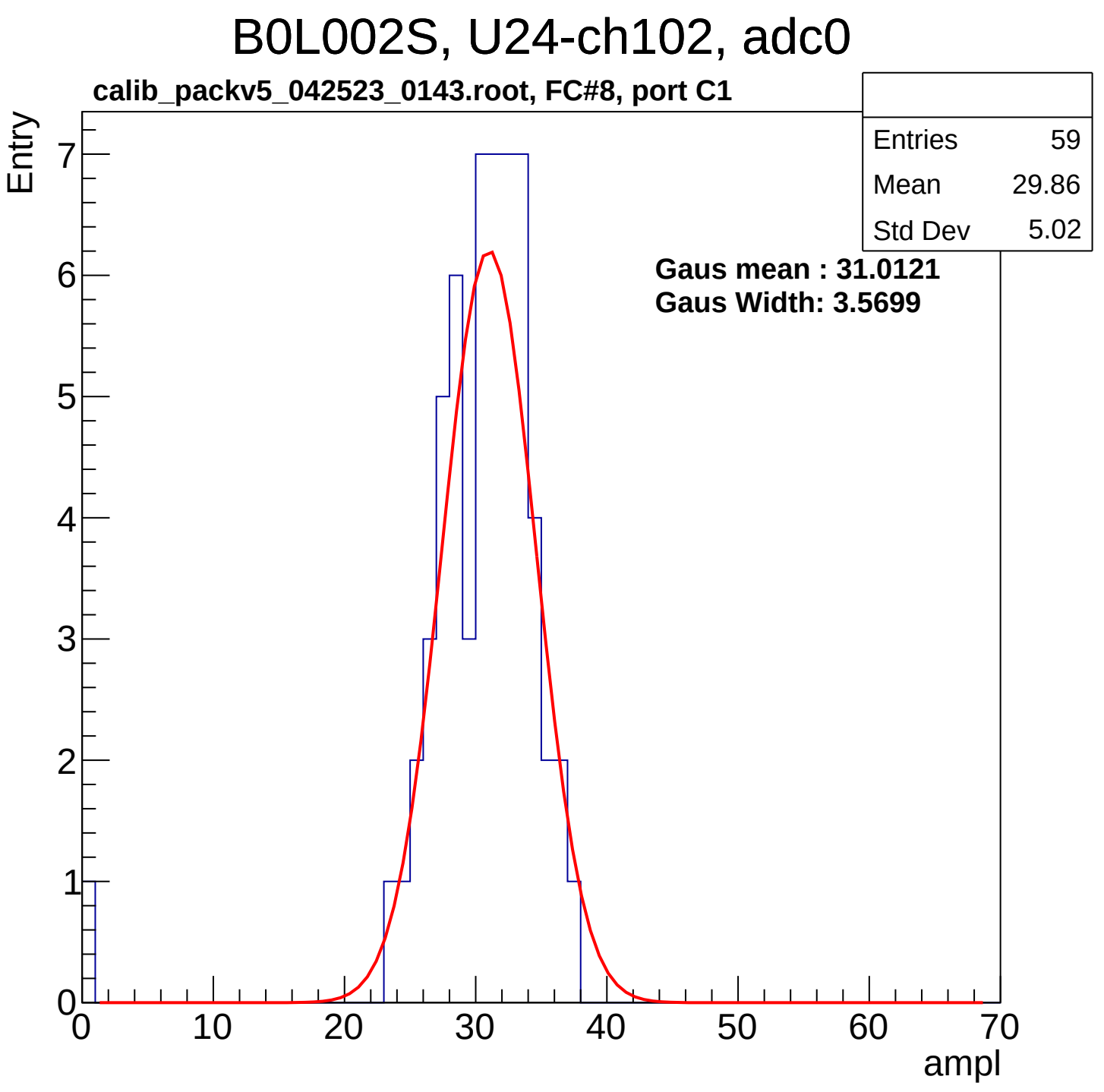
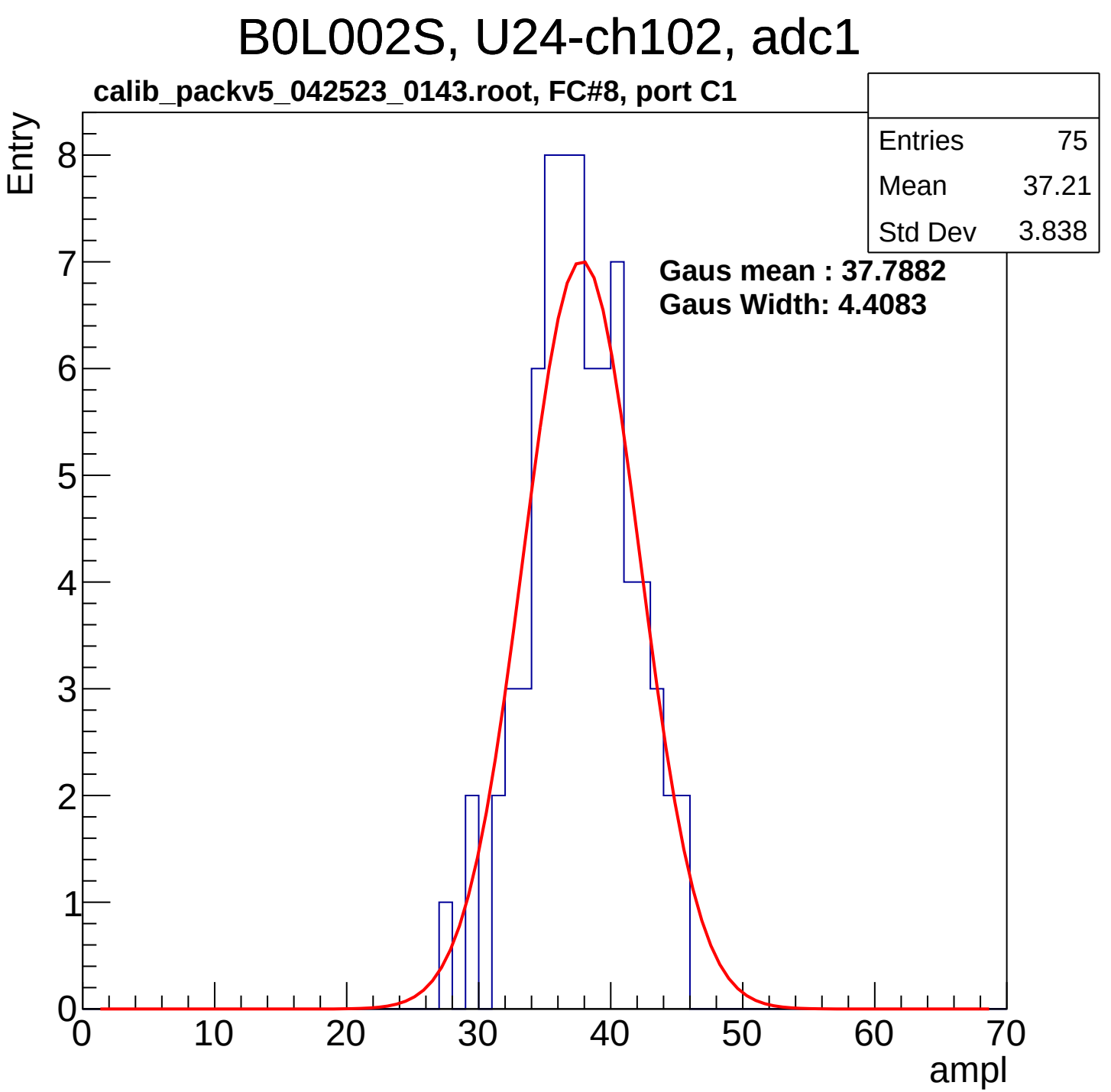
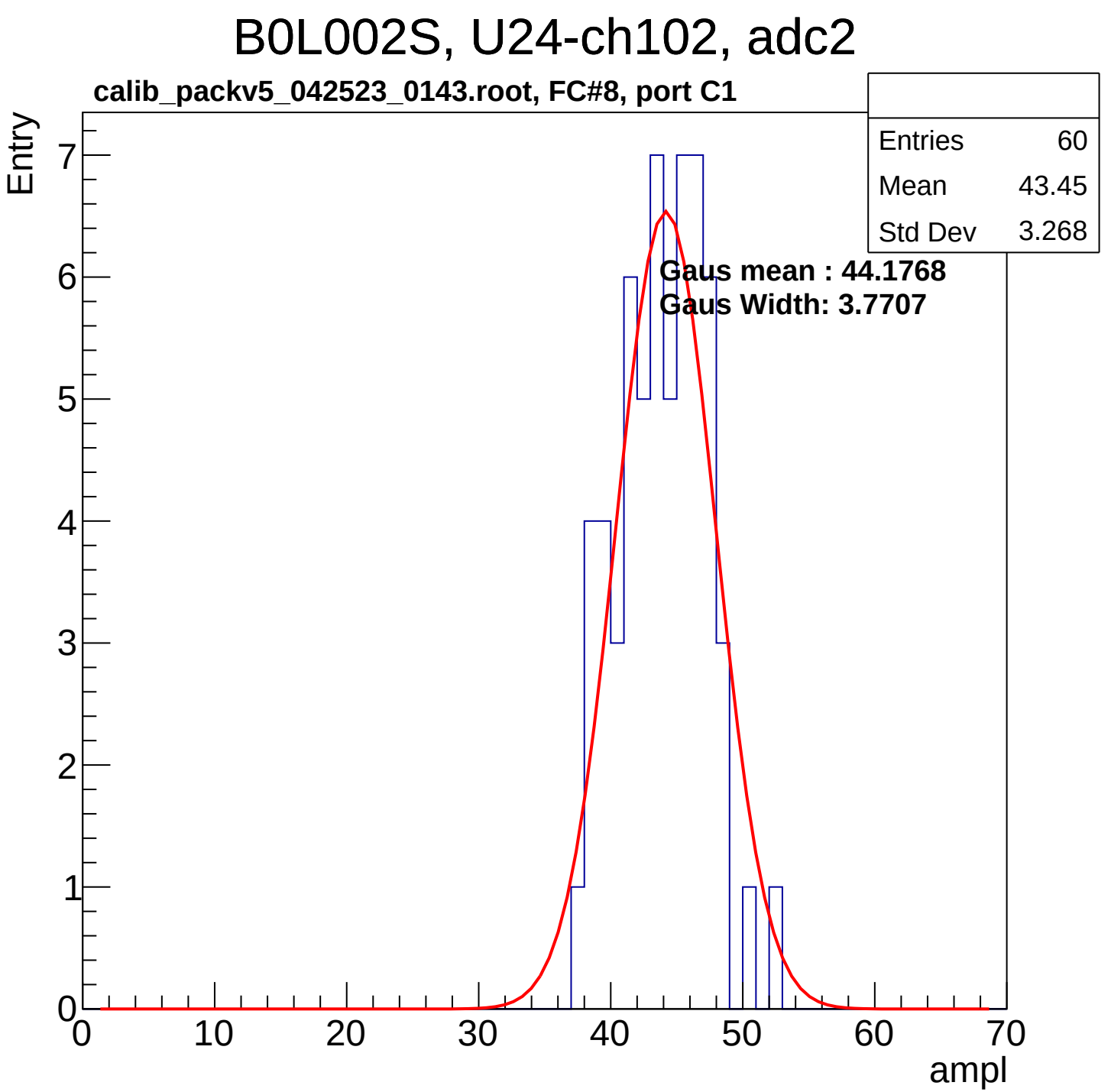
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 29.86
Std Dev 5.02
Gaus mean : 31.0121
Gaus Width: 3.5699
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.21
Std Dev 3.838
Gaus mean : 37.7882
Gaus Width: 4.4083
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.45
Std Dev 3.268
Gaus mean : 44.1768
Gaus Width: 3.7707
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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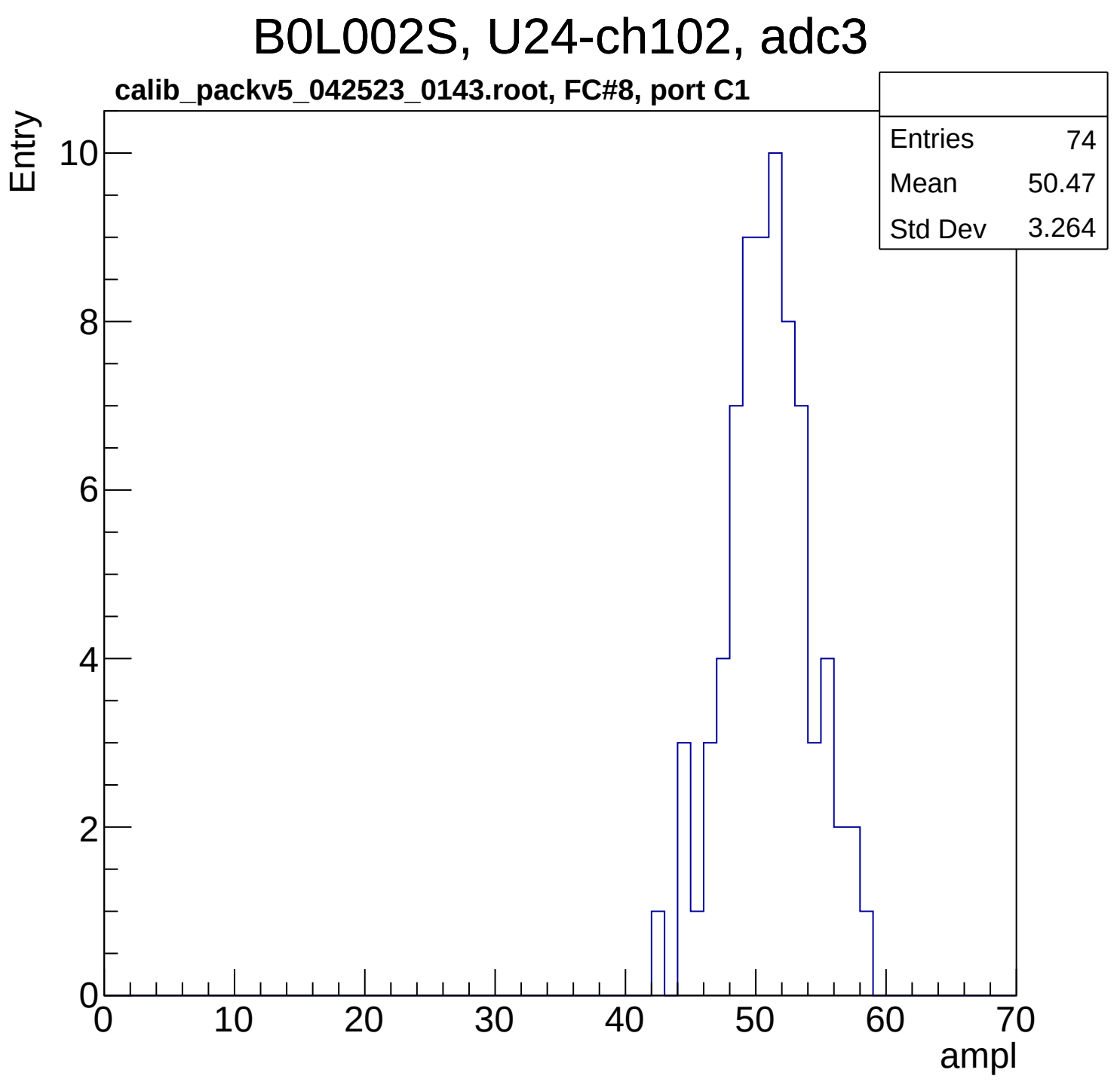
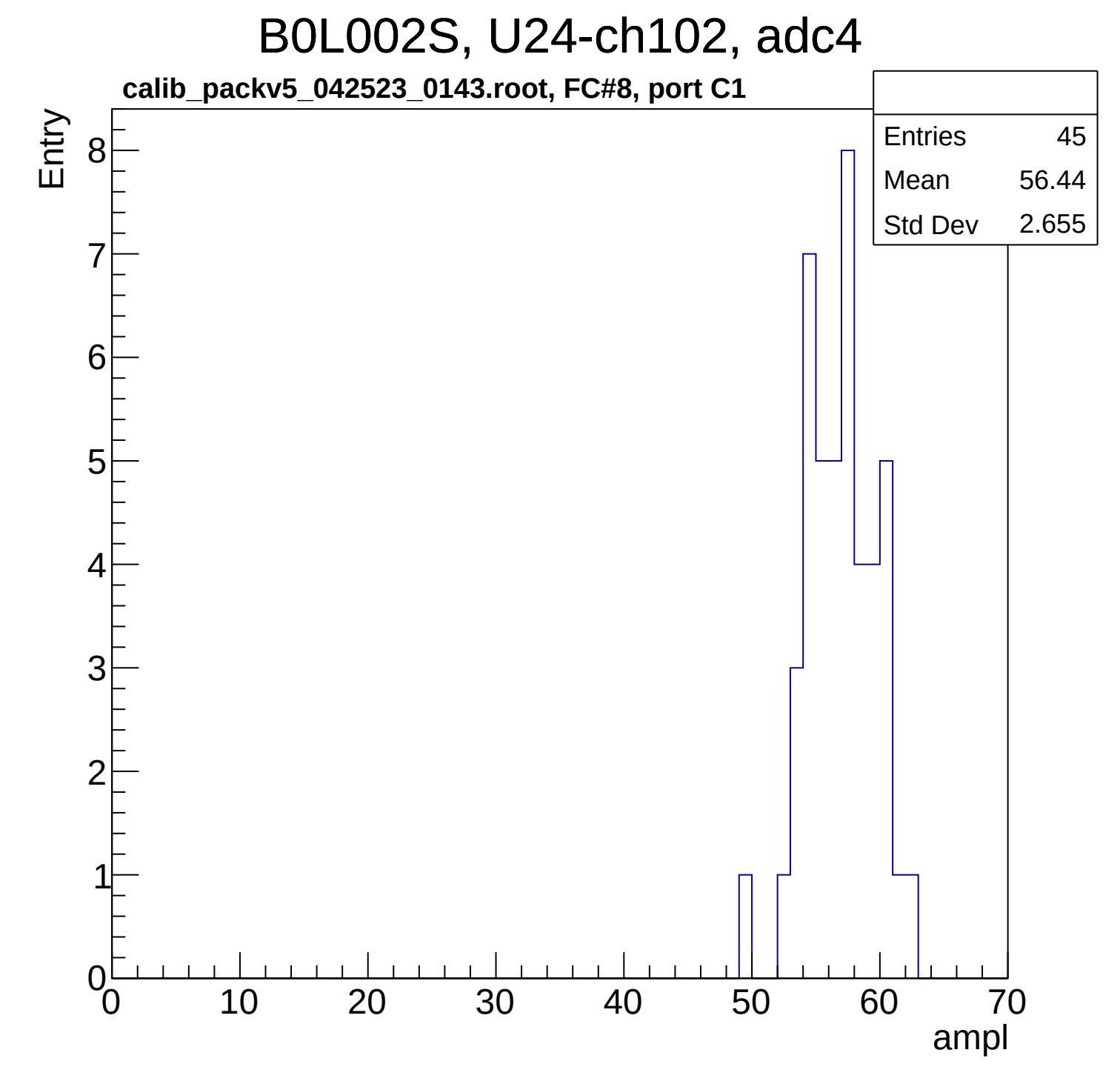

B0L002S, U24-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 50.47
Std Dev 3.264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.44
Std Dev 2.655
Entry
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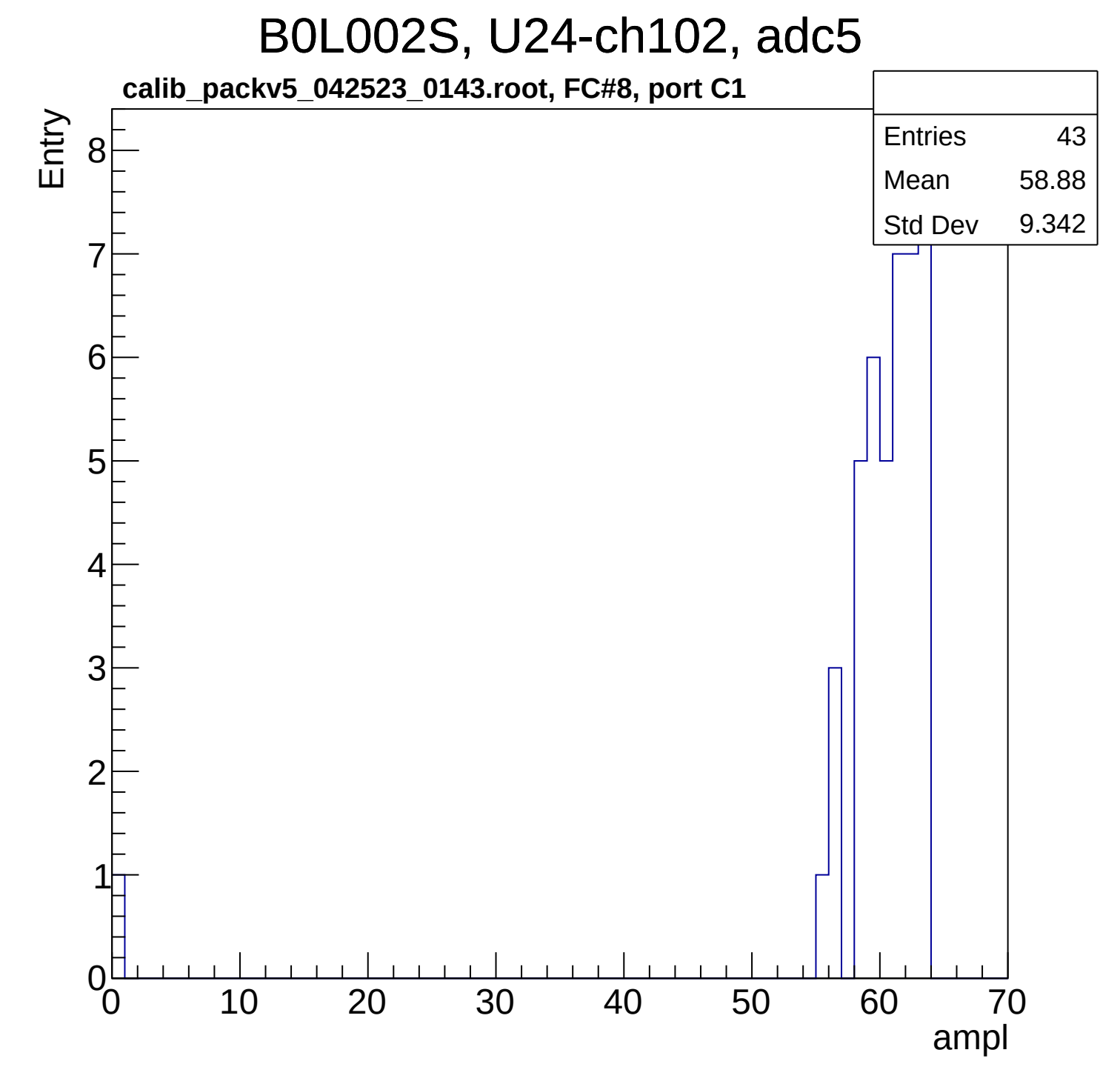
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 58.88
Std Dev 9.342
Entry
ampl
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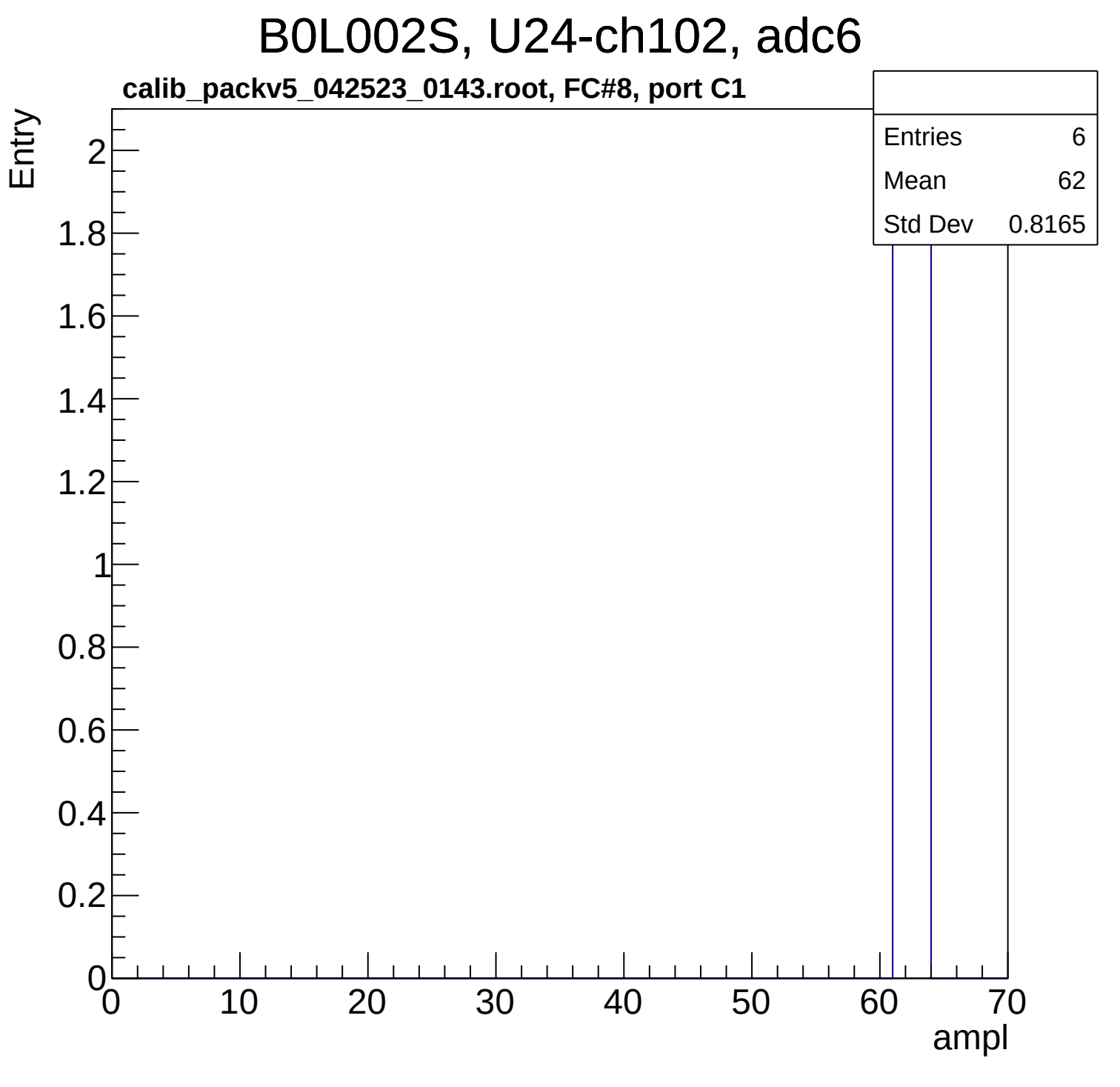
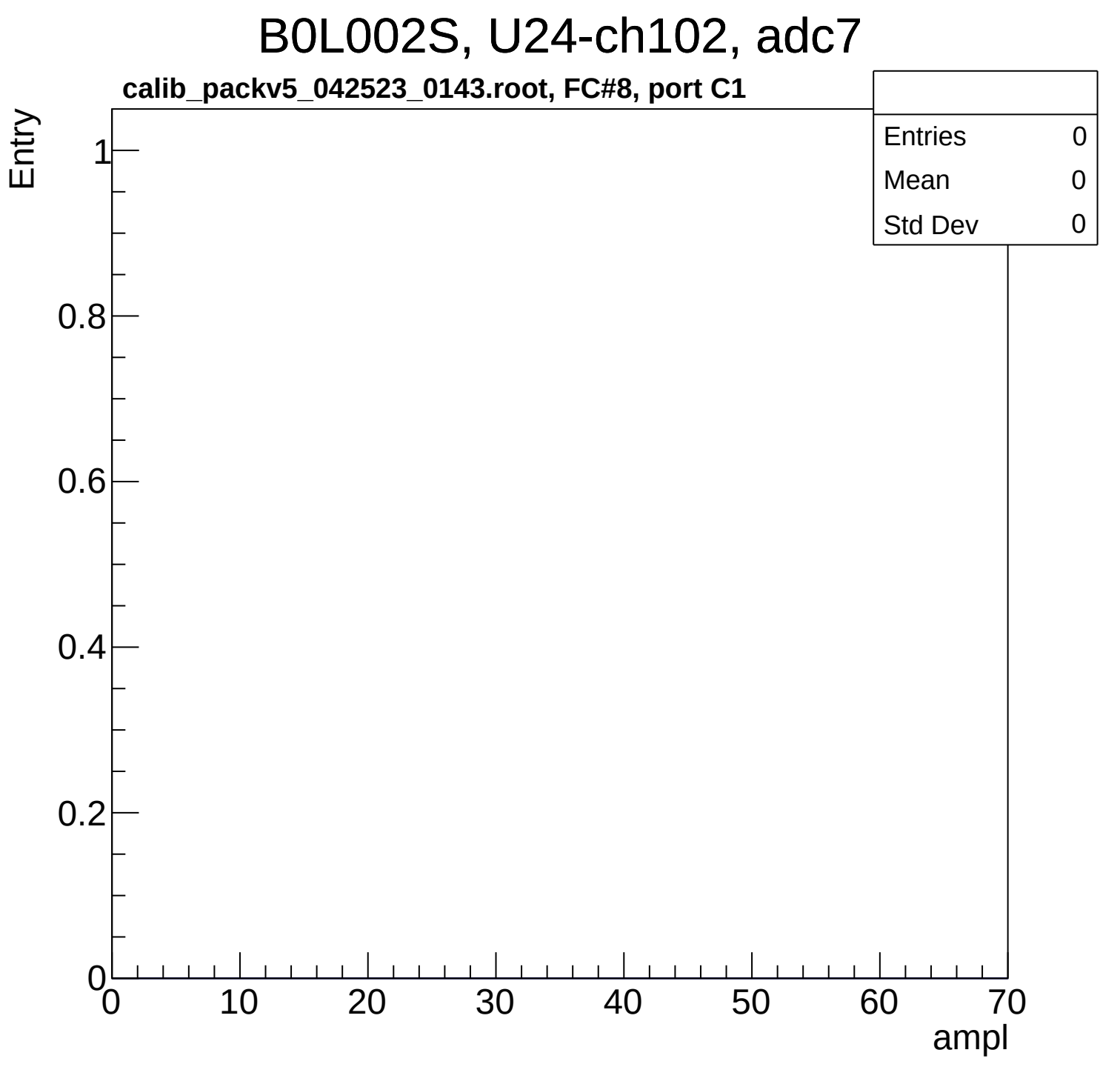
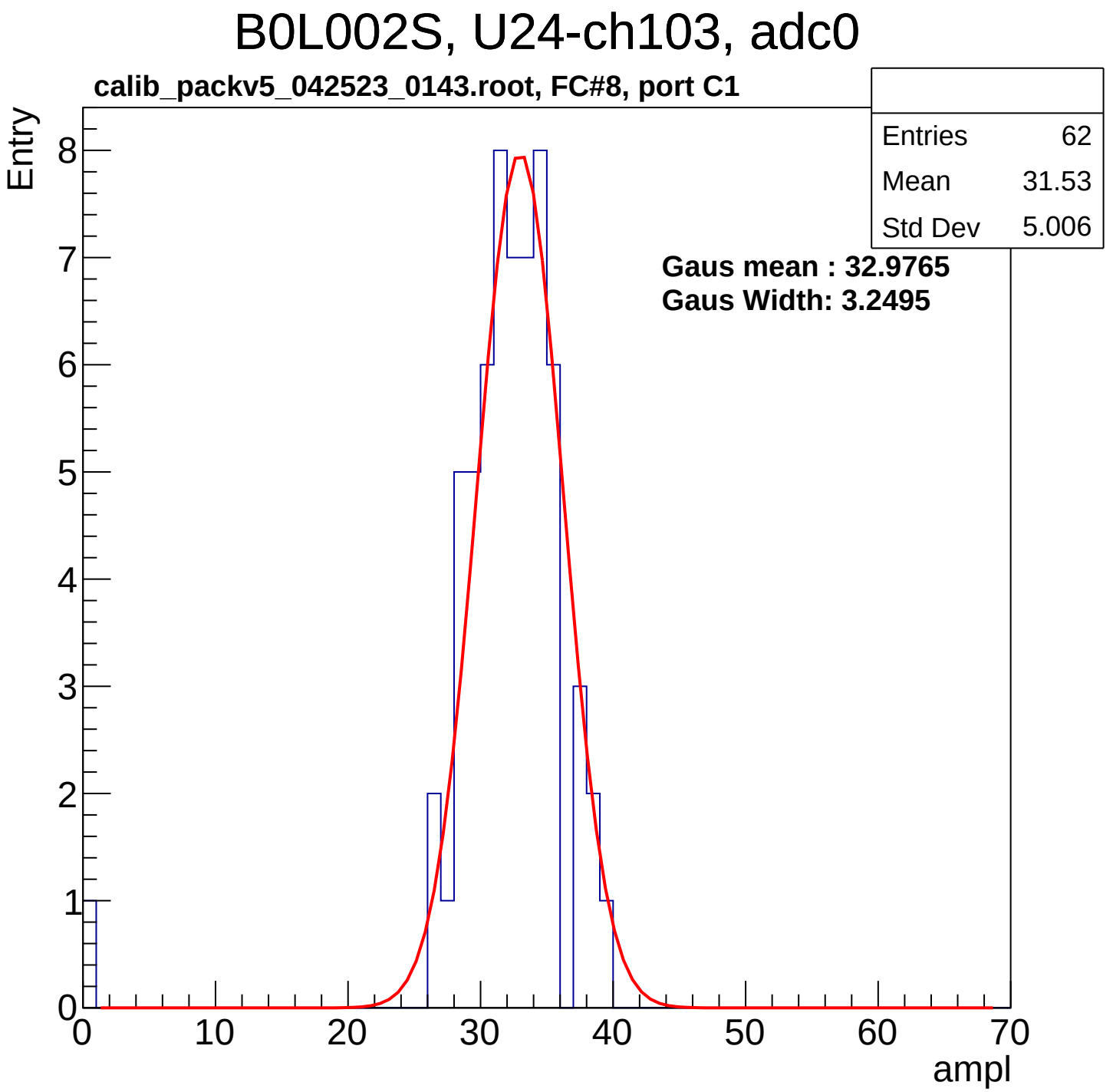
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.53
Std Dev 5.006
Gaus mean : 32.9765
Gaus Width: 3.2495
Entry
ampl
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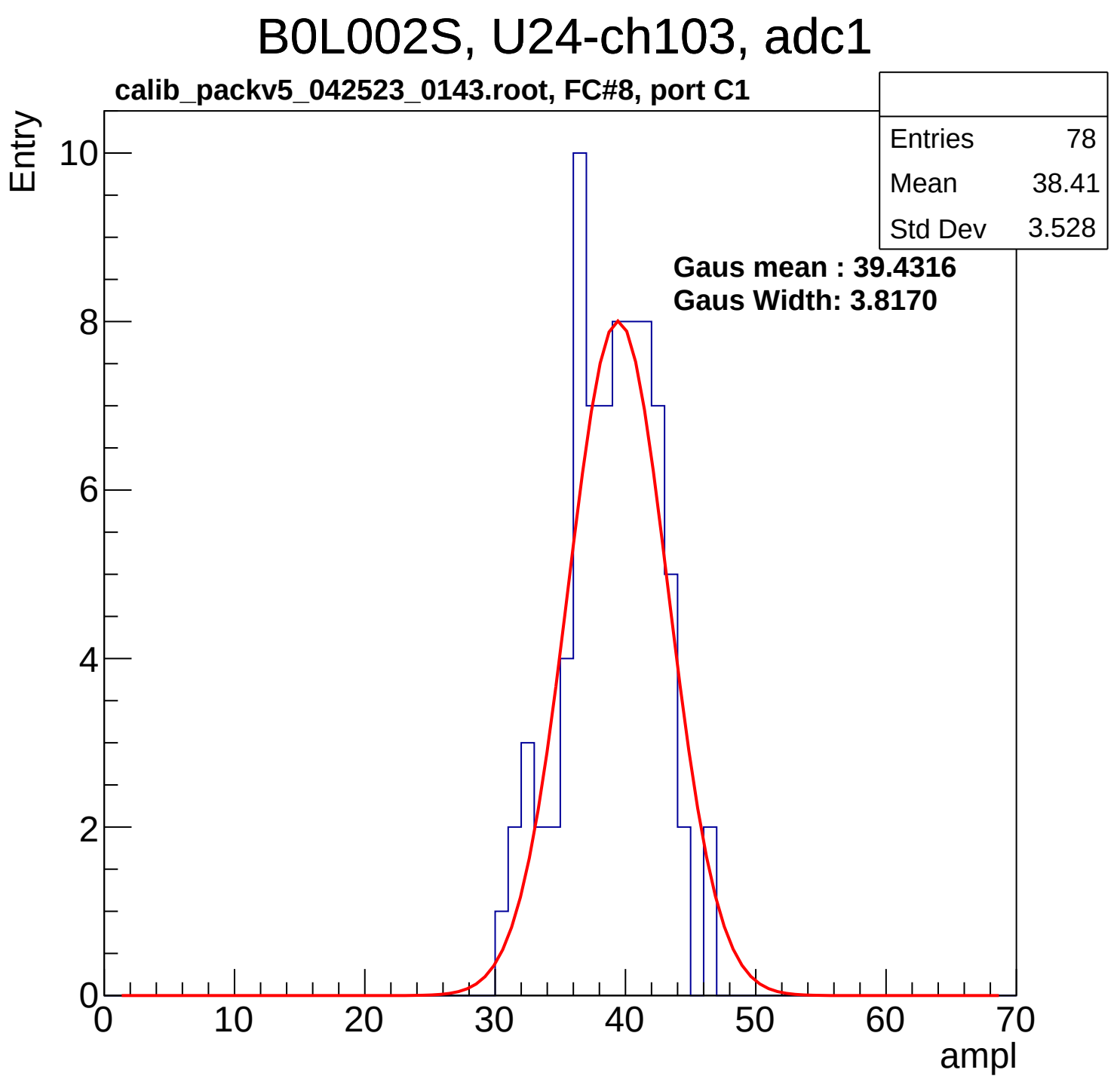
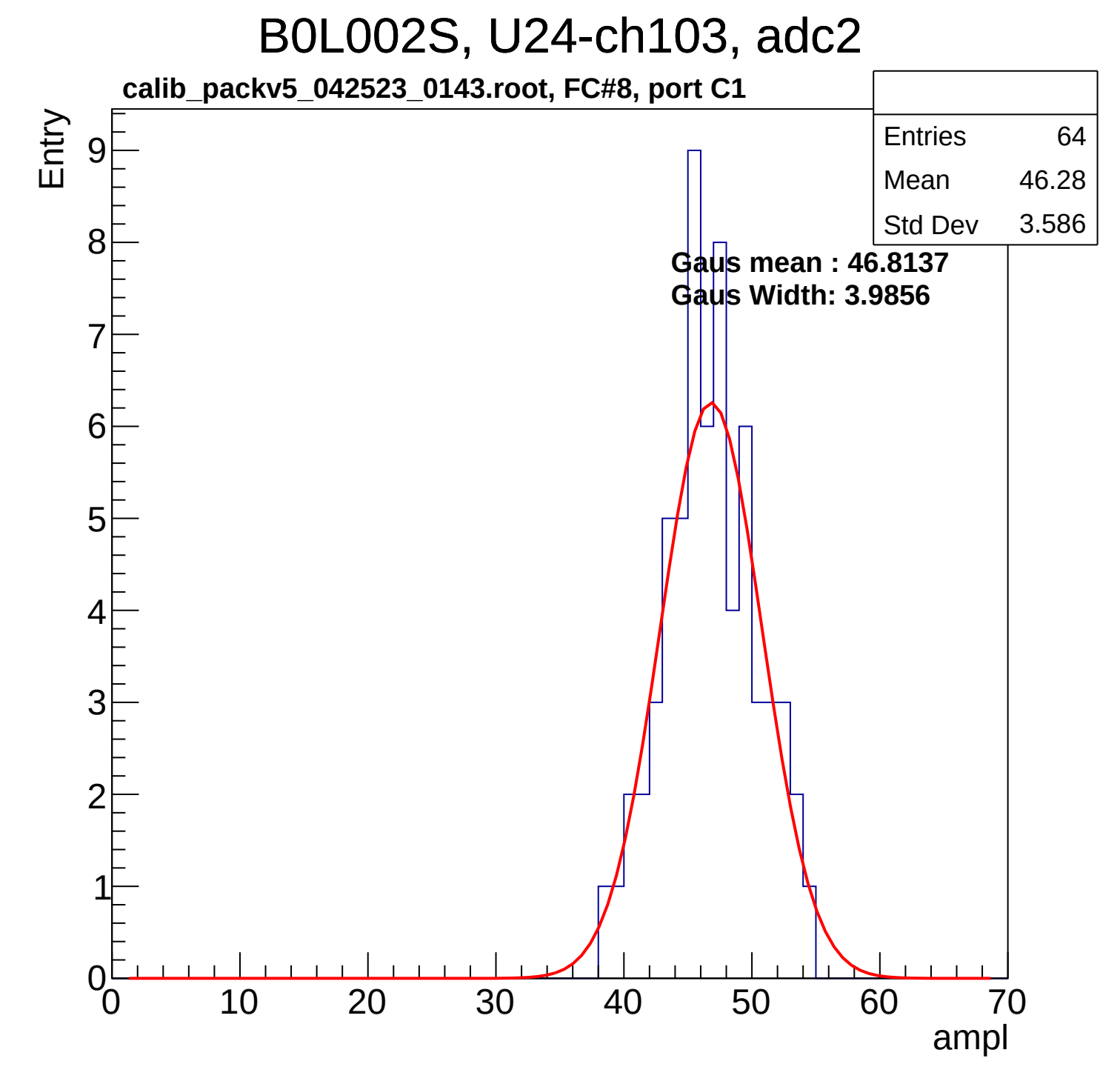
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.41
Std Dev 3.528
Gaus mean : 39.4316
Gaus Width: 3.8170
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 46.28
Std Dev 3.586
Gaus mean : 46.8137
Gaus Width: 3.9856
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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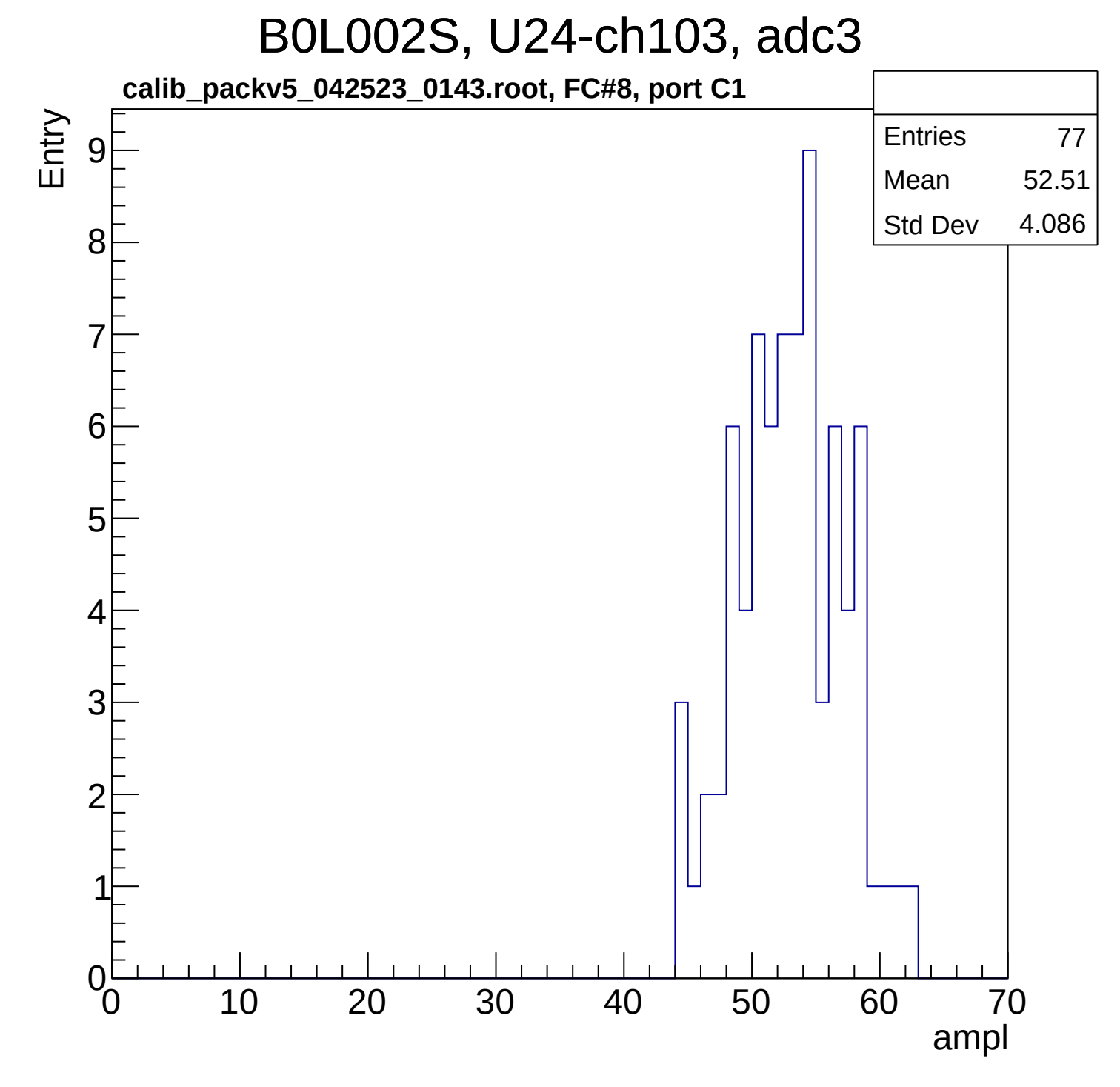
60
70


B0L002S, U24-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 52.51
Std Dev 4.086
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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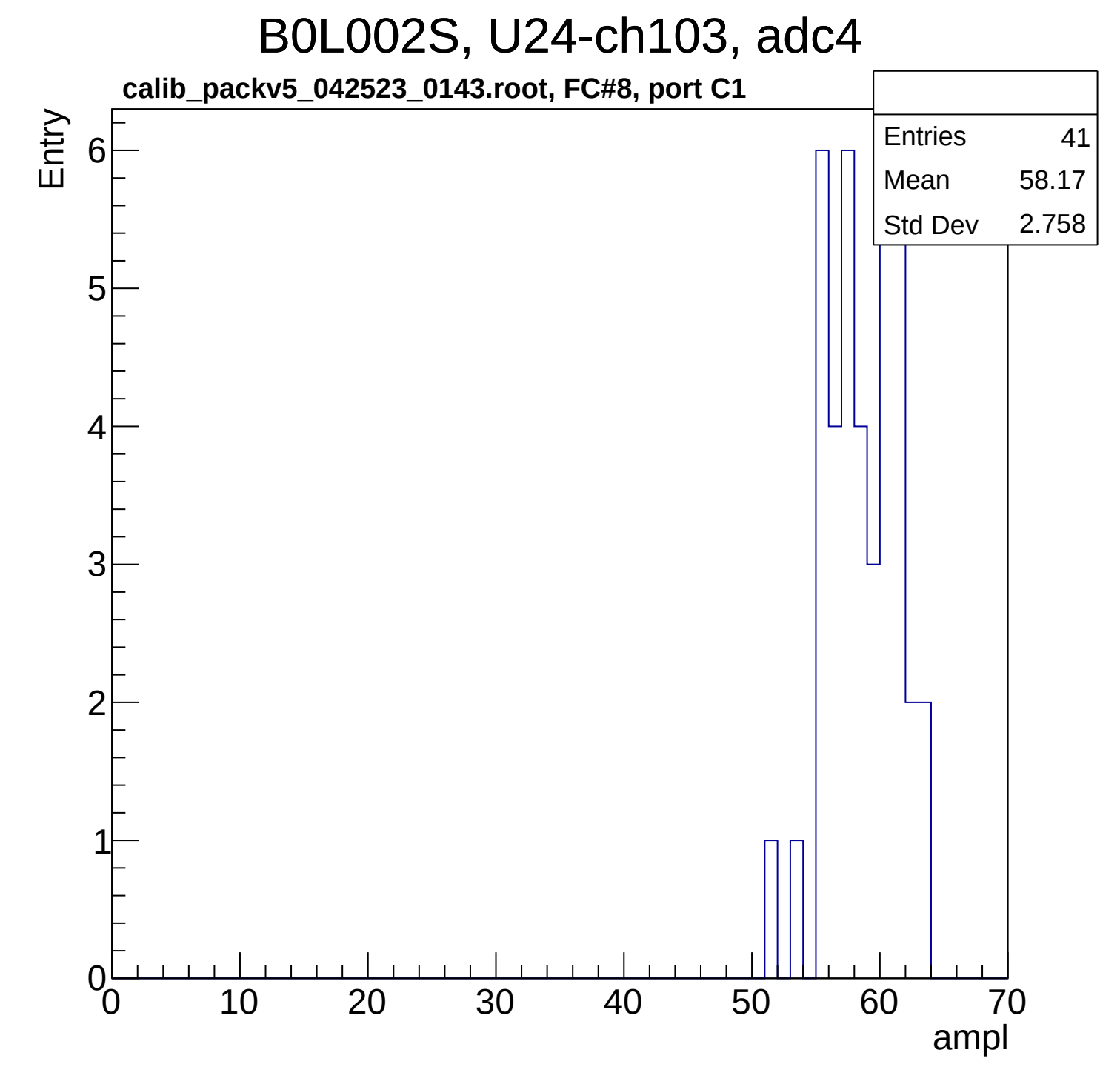
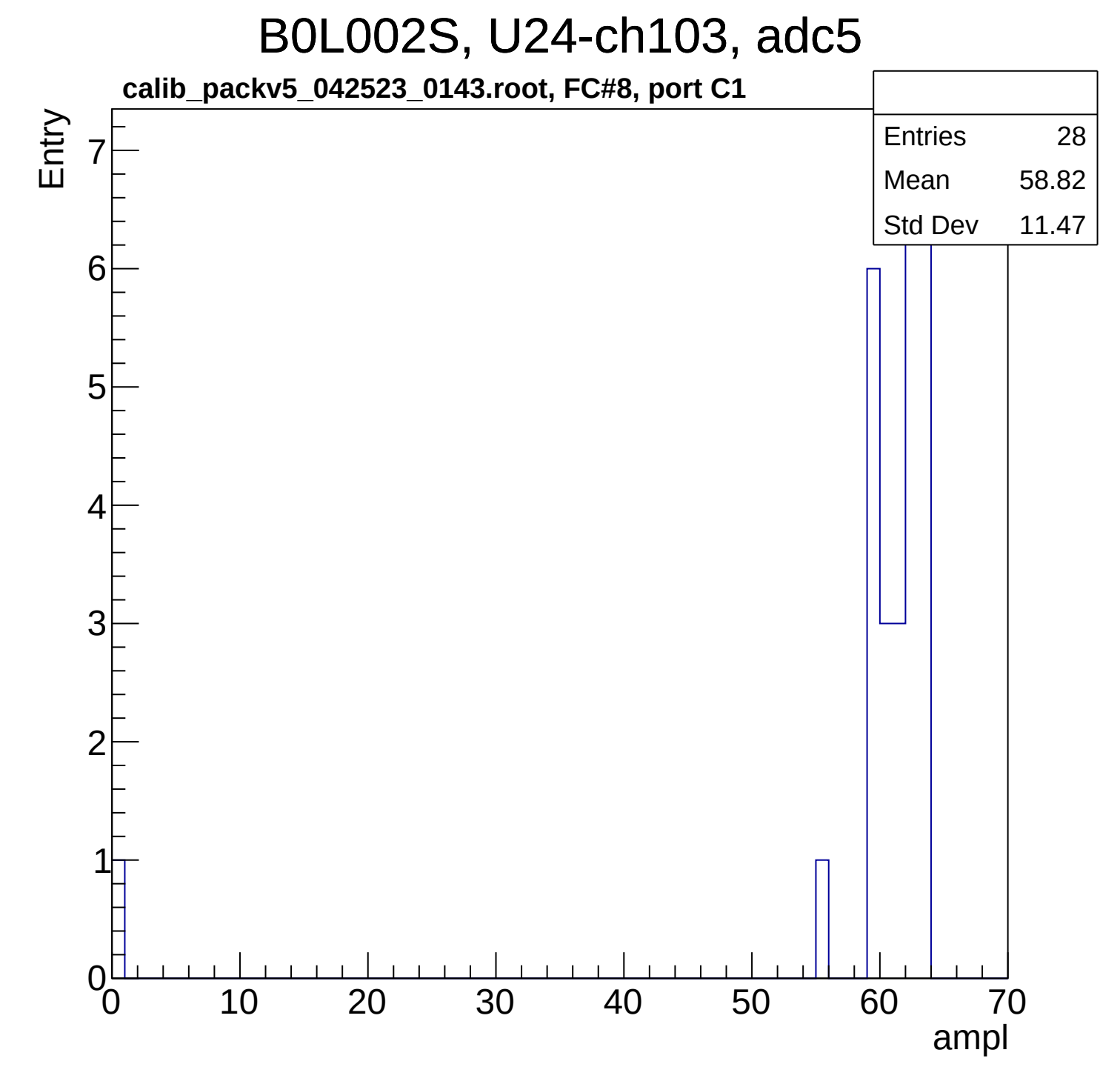
60
70


B0L002S, U24-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.17
Std Dev 2.758
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.82
Std Dev 11.47
Entry
ampl
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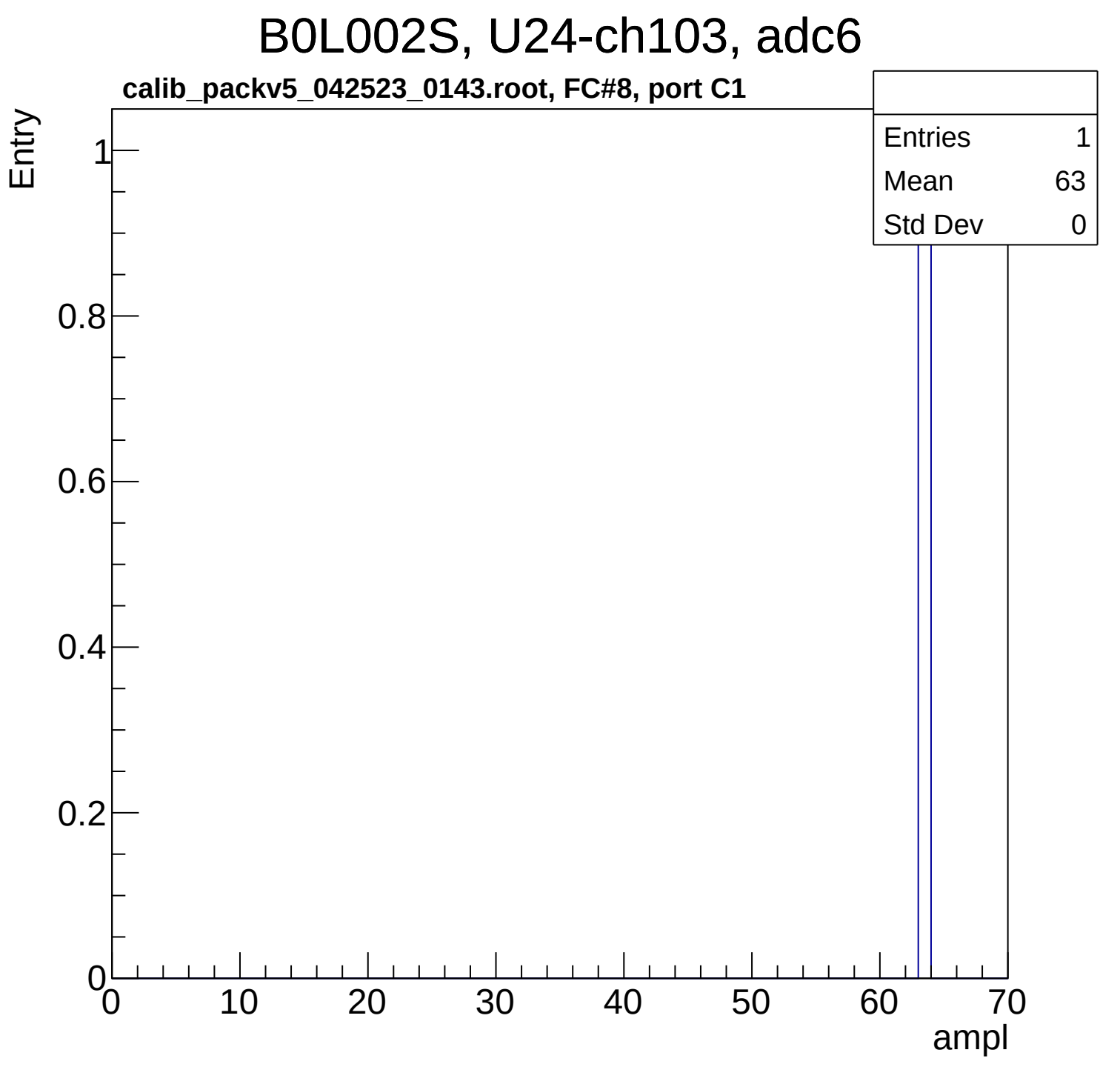
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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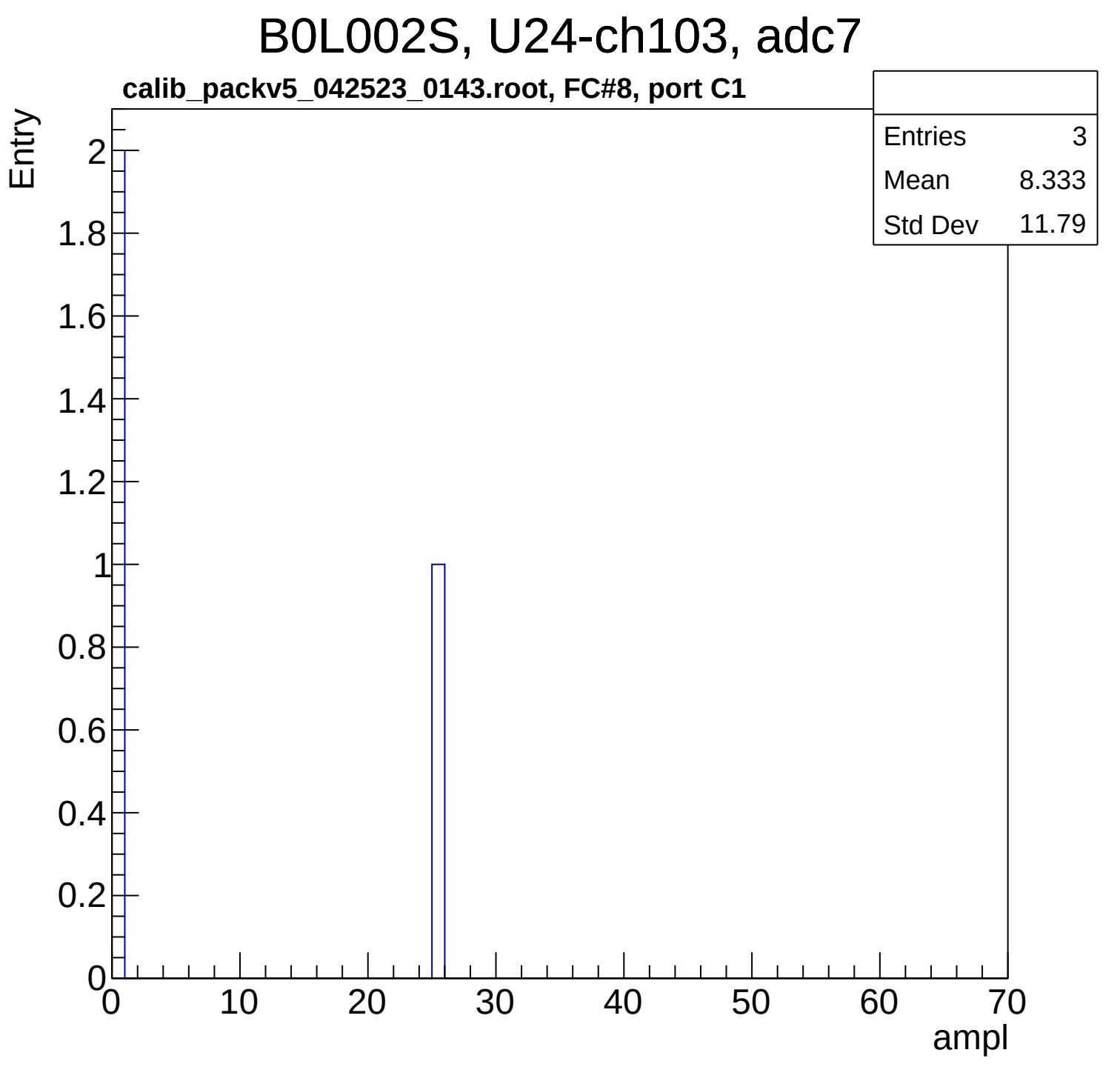
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
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60
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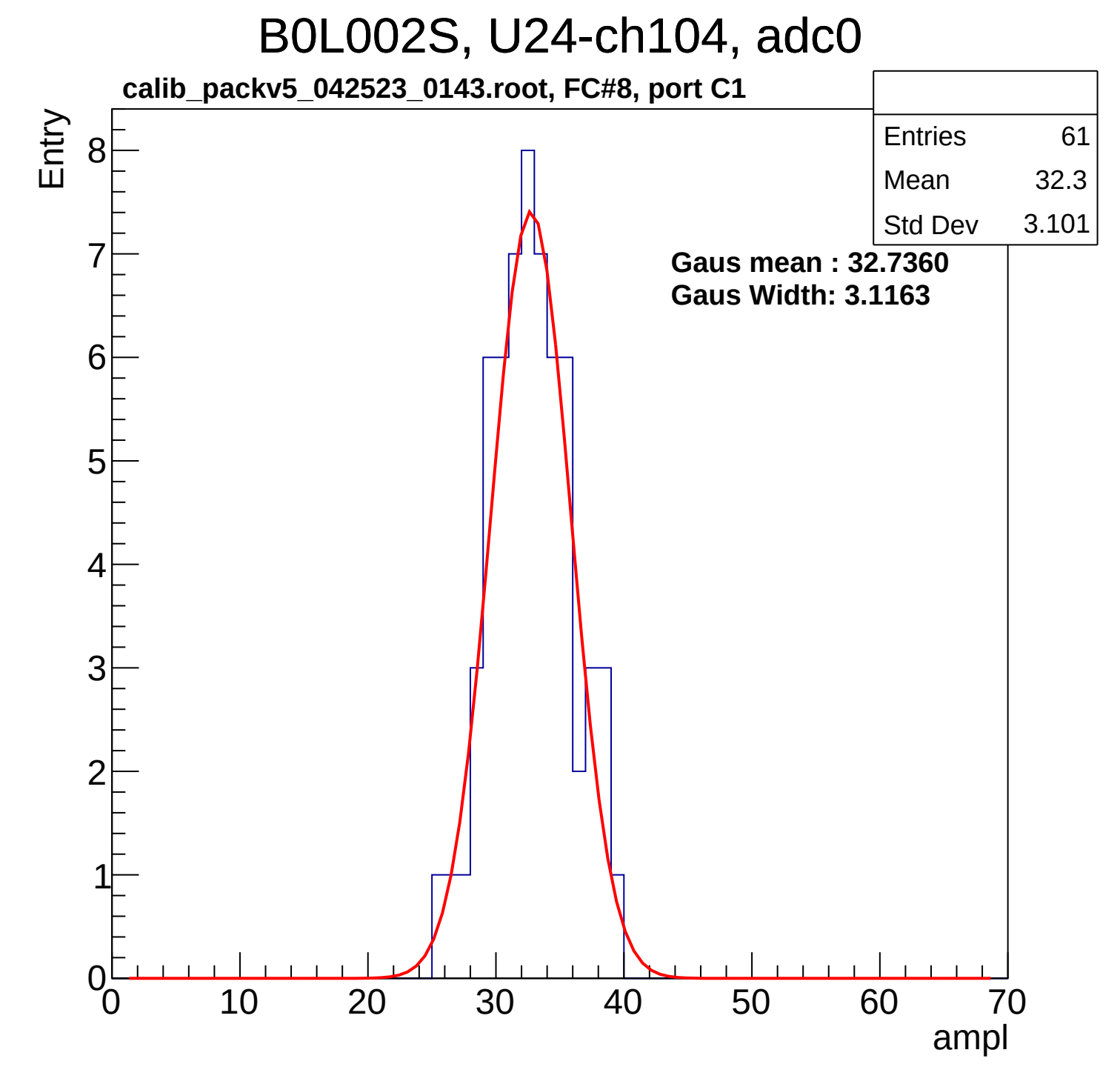
B0L002S, U24-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 32.3
Std Dev 3.101
Gaus mean : 32.7360
Gaus Width: 3.1163
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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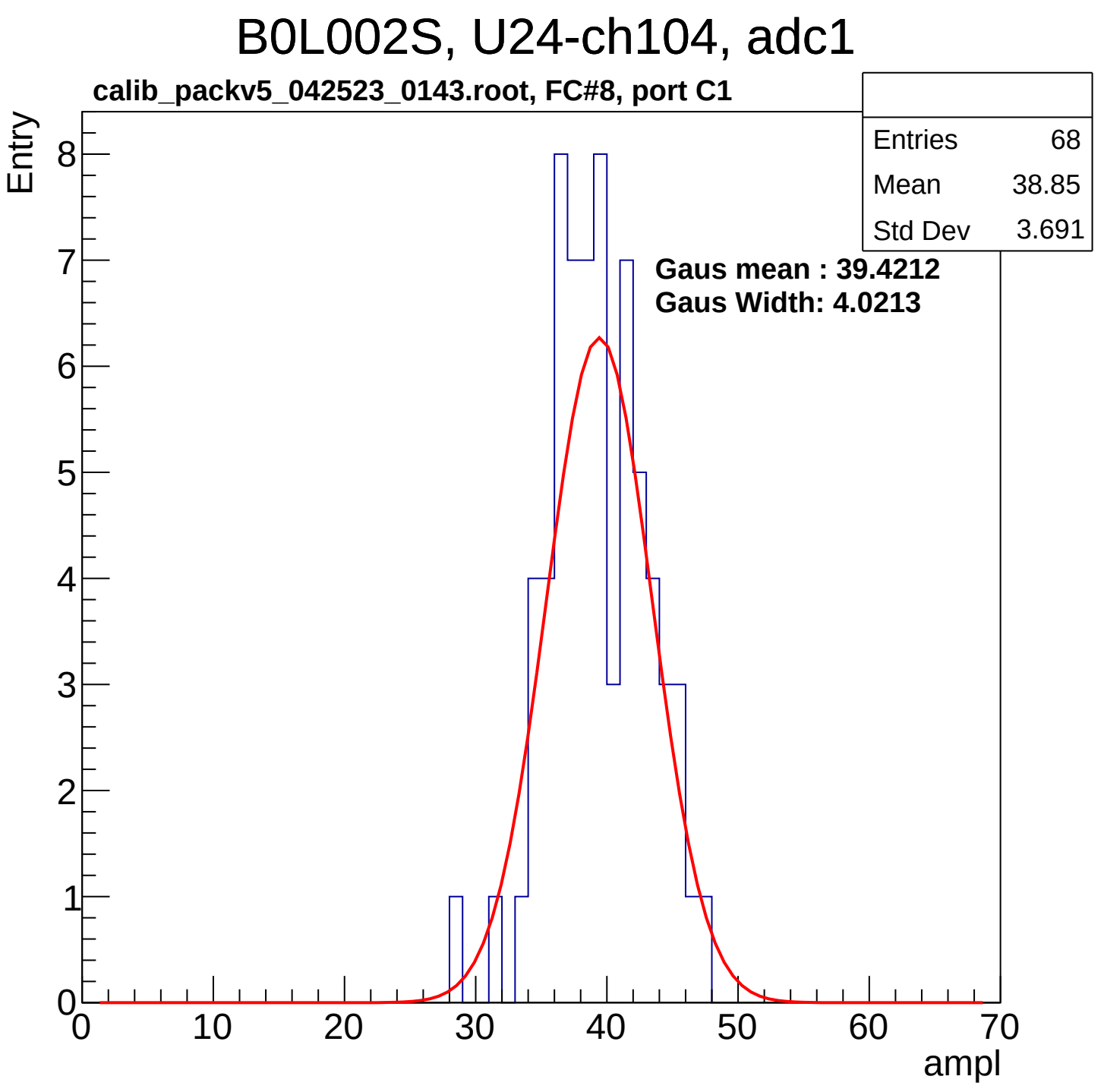
B0L002S, U24-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.85
Std Dev 3.691
Gaus mean : 39.4212
Gaus Width: 4.0213
Entry
ampl
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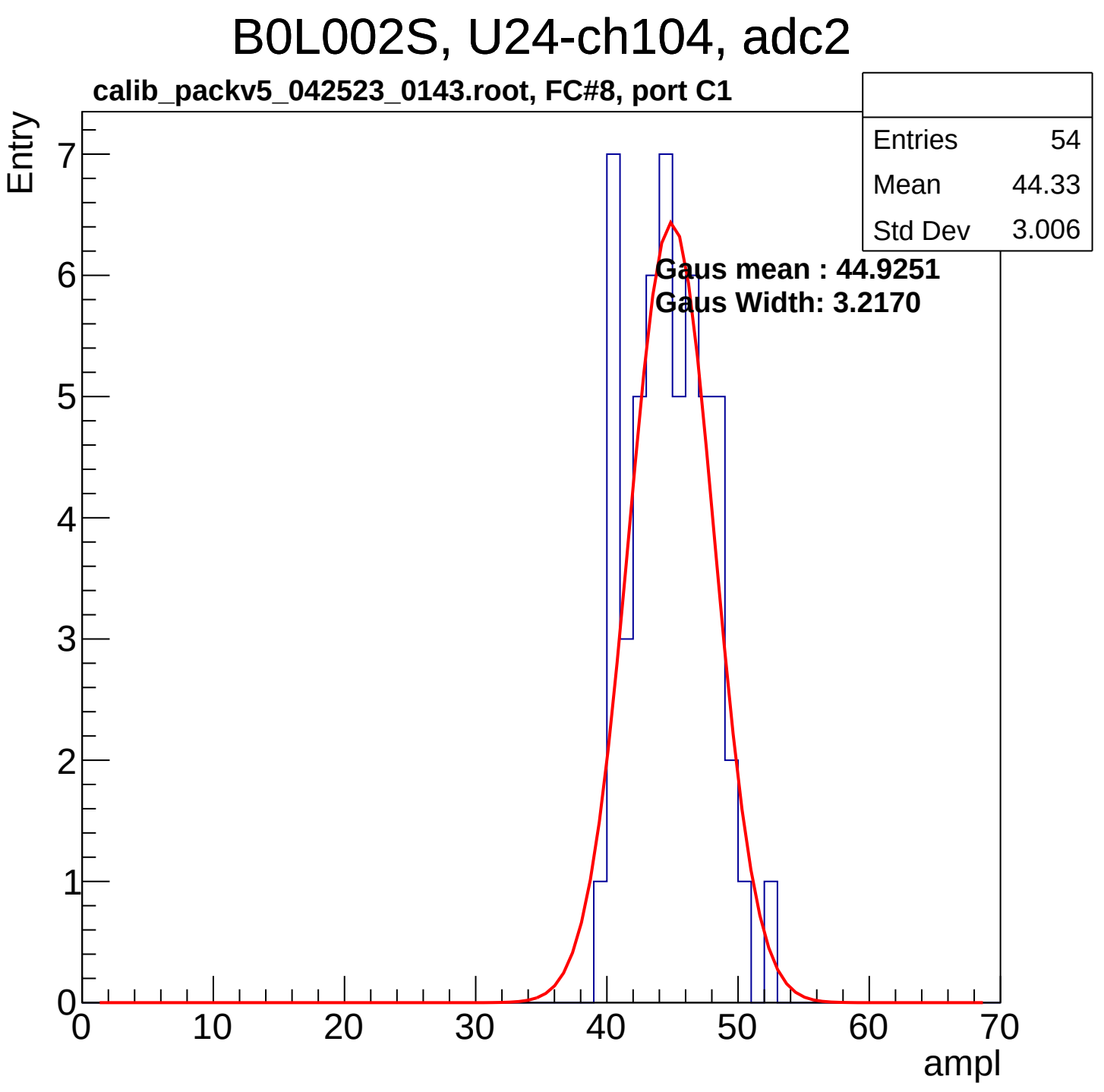
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.33
Std Dev 3.006
Gaus mean : 44.9251
Gaus Width: 3.2170
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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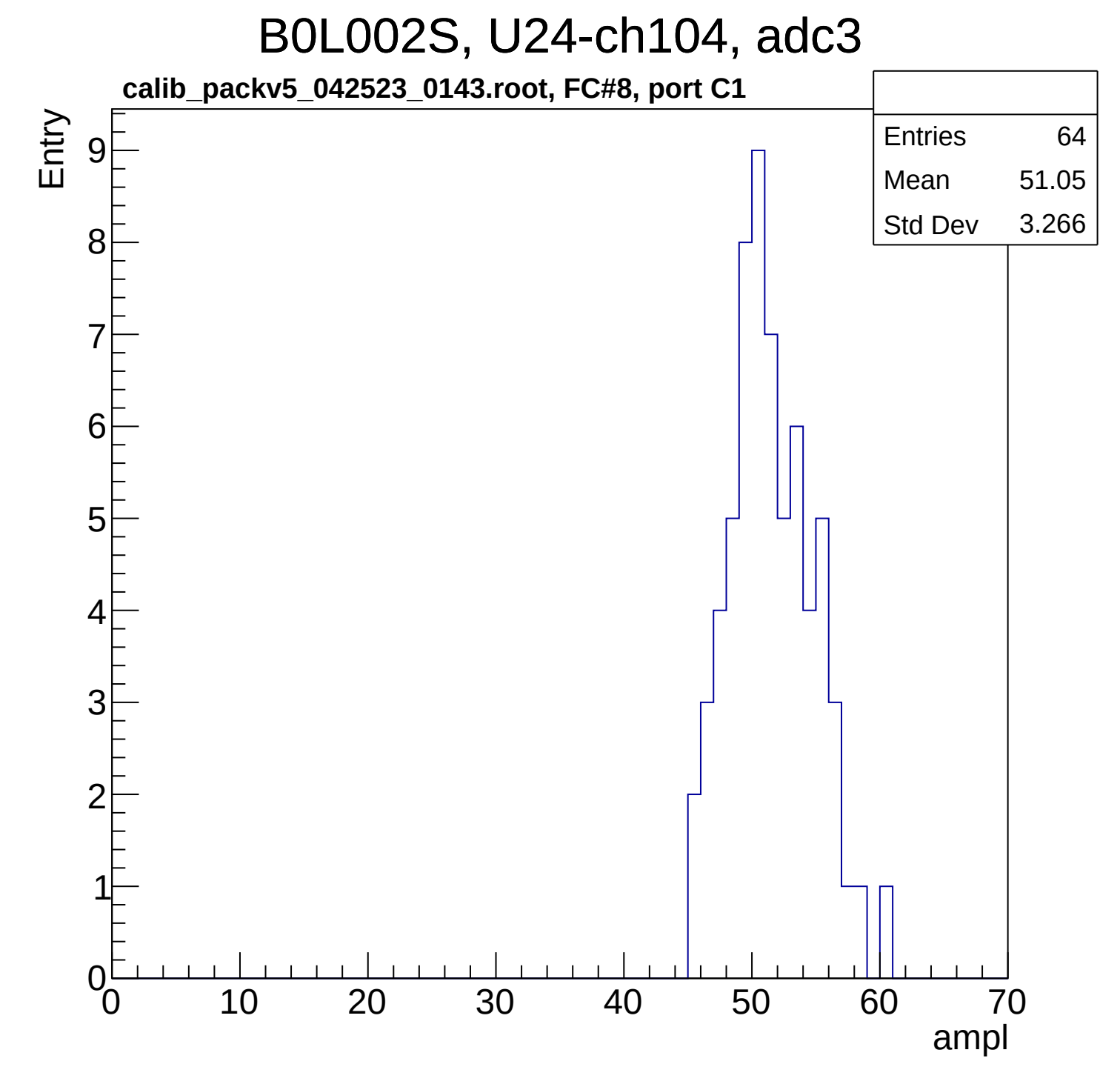
B0L002S, U24-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.05
Std Dev 3.266
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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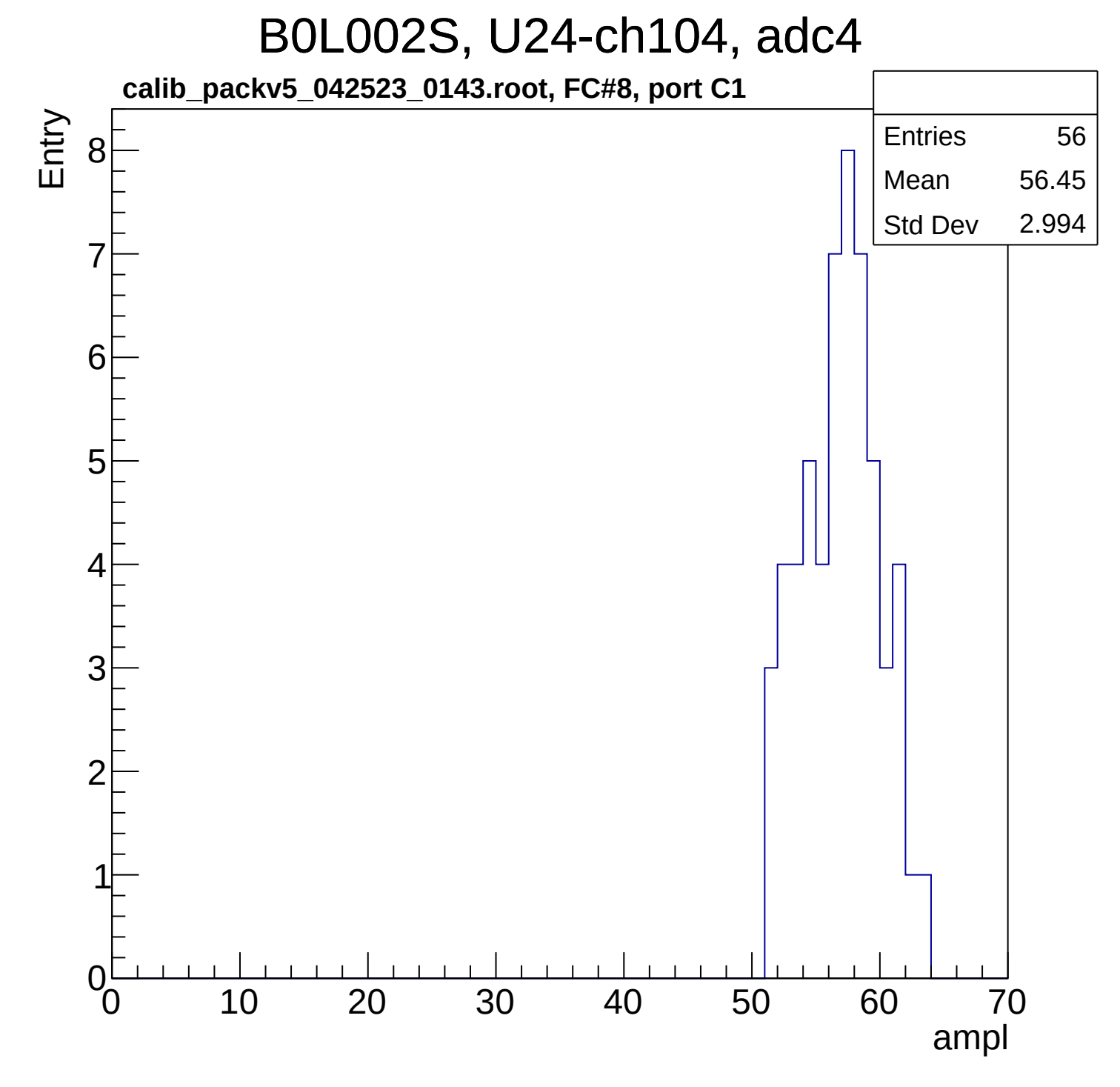
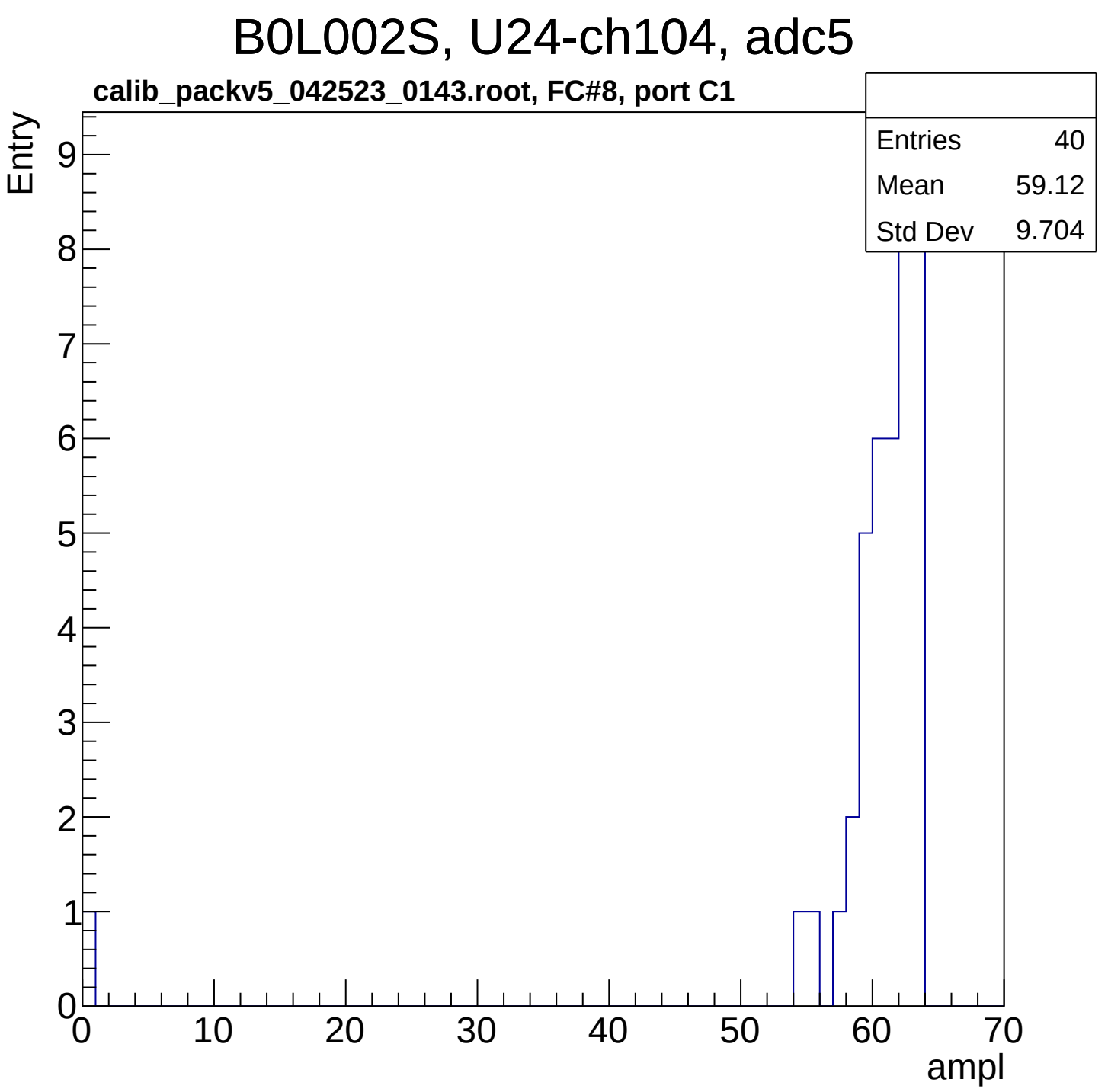

B0L002S, U24-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.45
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.12
Std Dev 9.704
Entry
ampl
0
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3
4
5
6
7
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0
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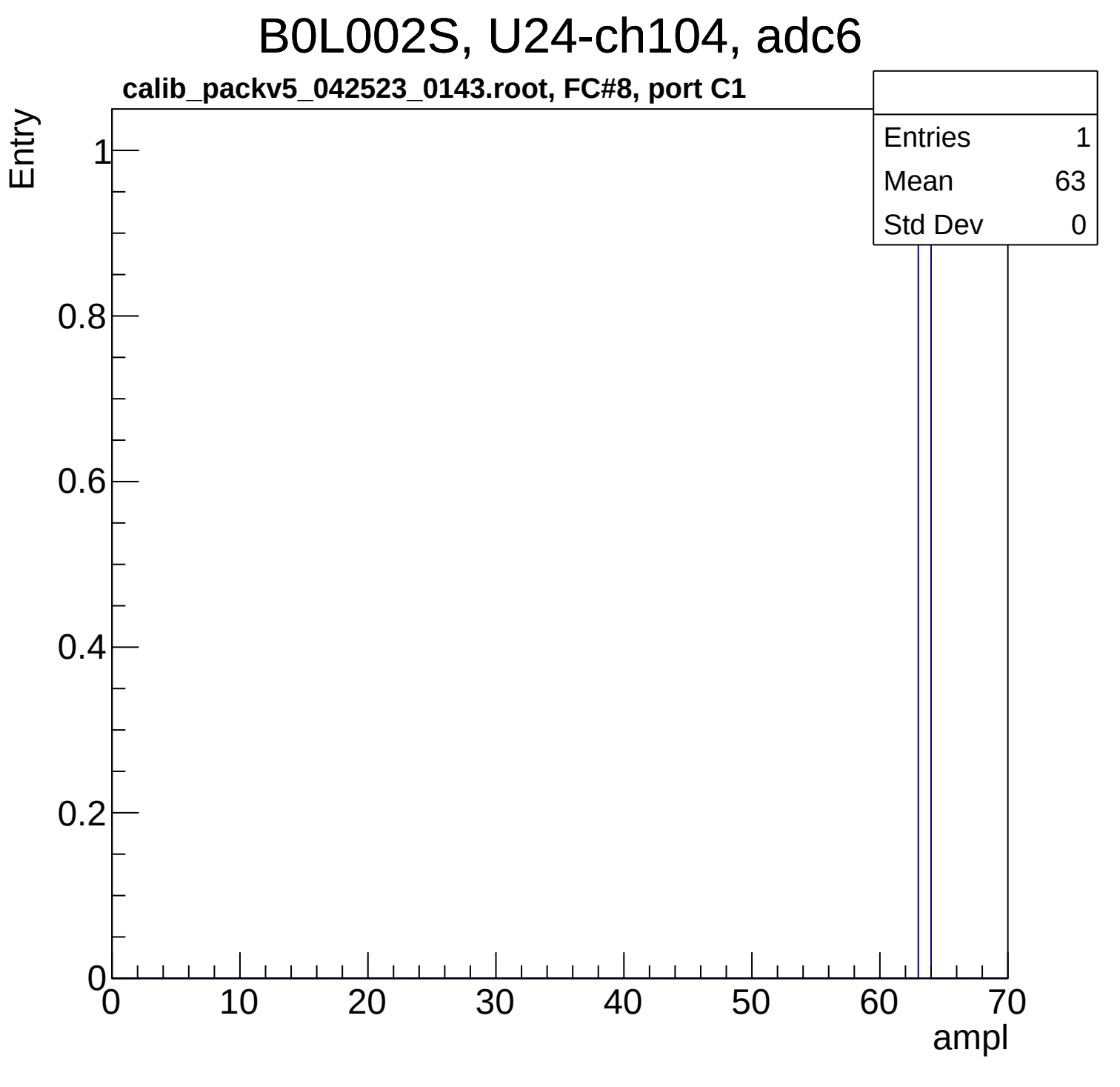
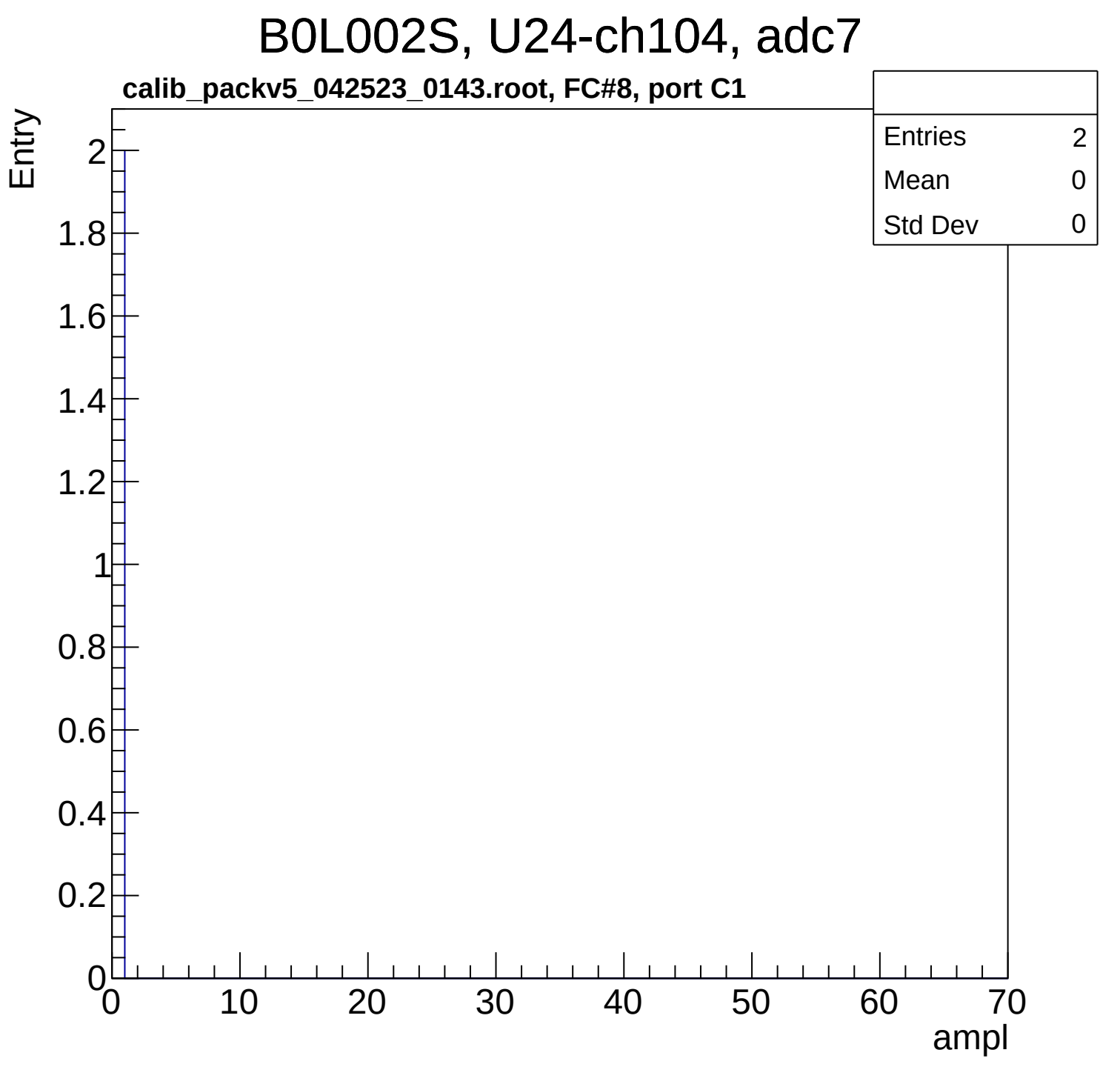

B0L002S, U24-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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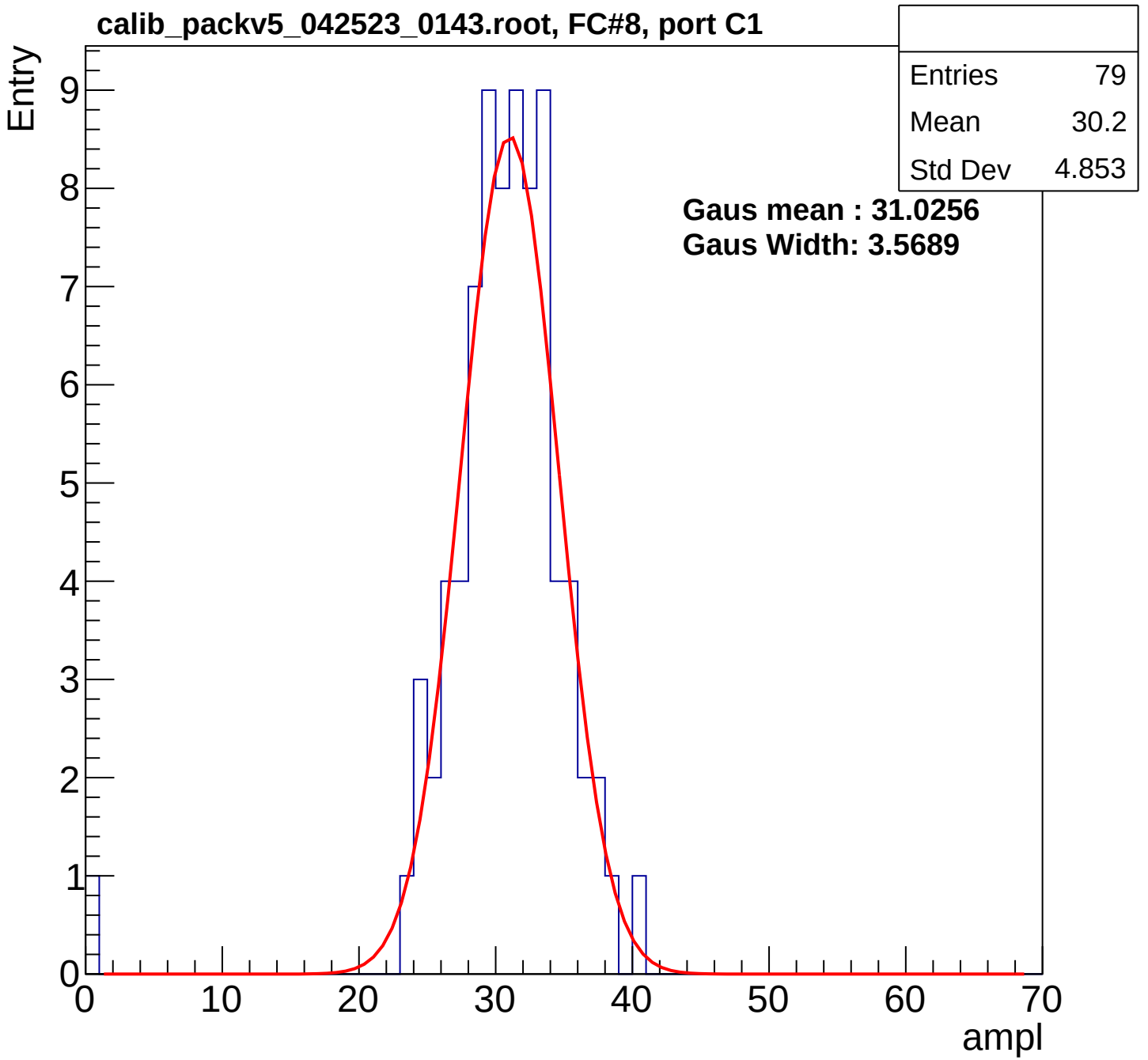
B0L002S, U24-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.2
Std Dev 4.853
Gaus mean : 31.0256
Gaus Width: 3.5689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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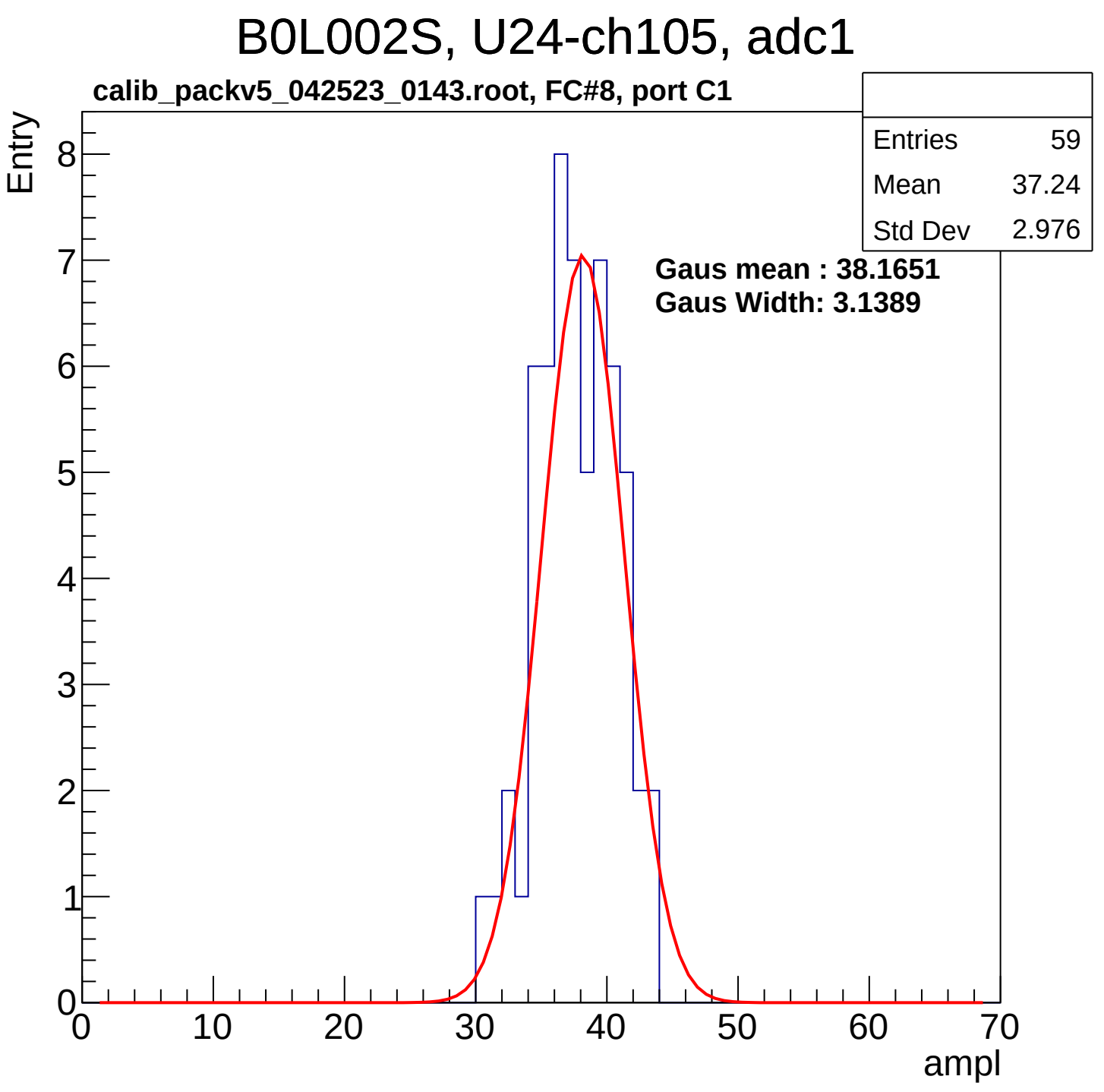
B0L002S, U24-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.24
Std Dev 2.976
Gaus mean : 38.1651
Gaus Width: 3.1389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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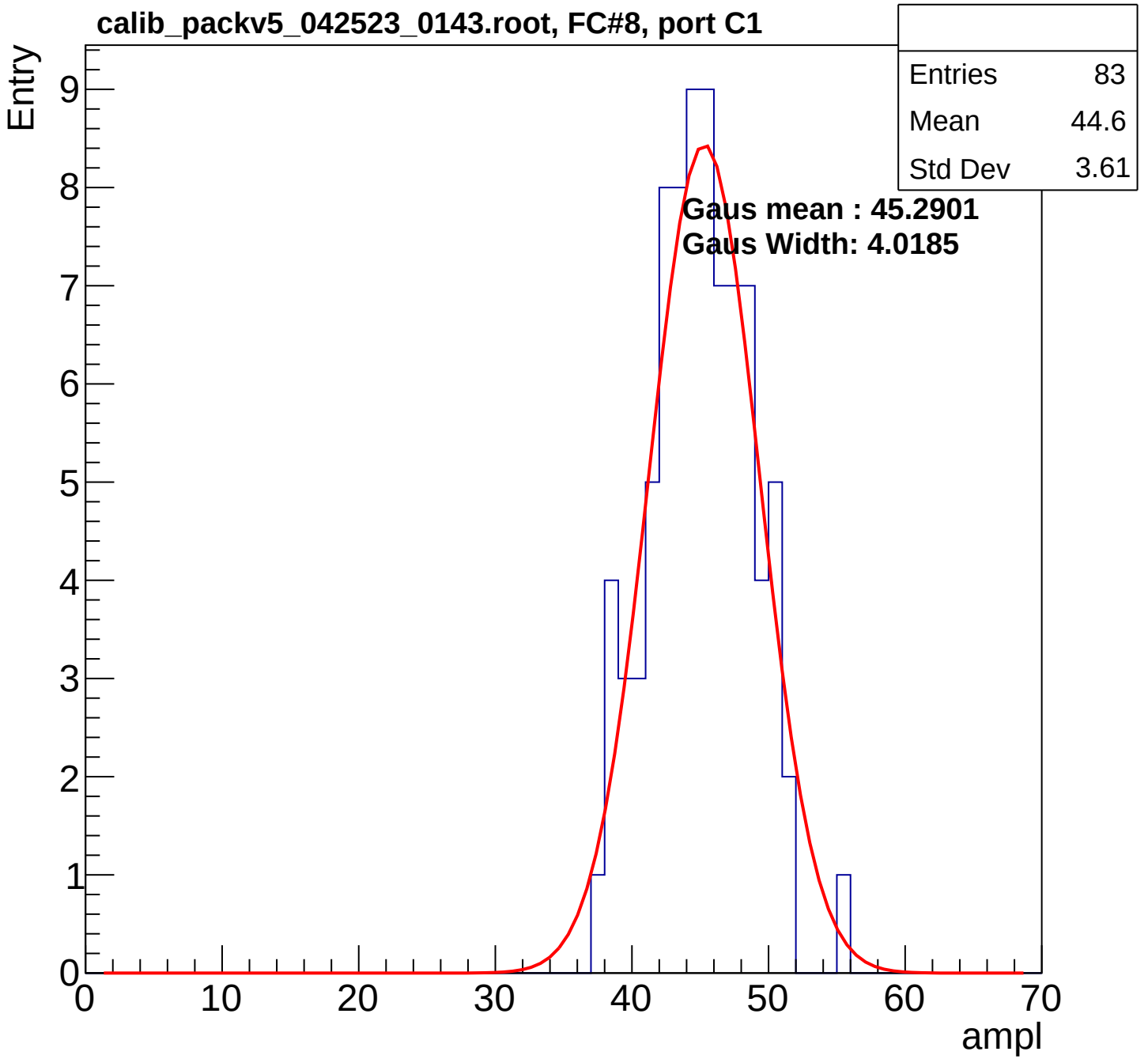
B0L002S, U24-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 44.6
Std Dev 3.61
Gaus mean : 45.2901
Gaus Width: 4.0185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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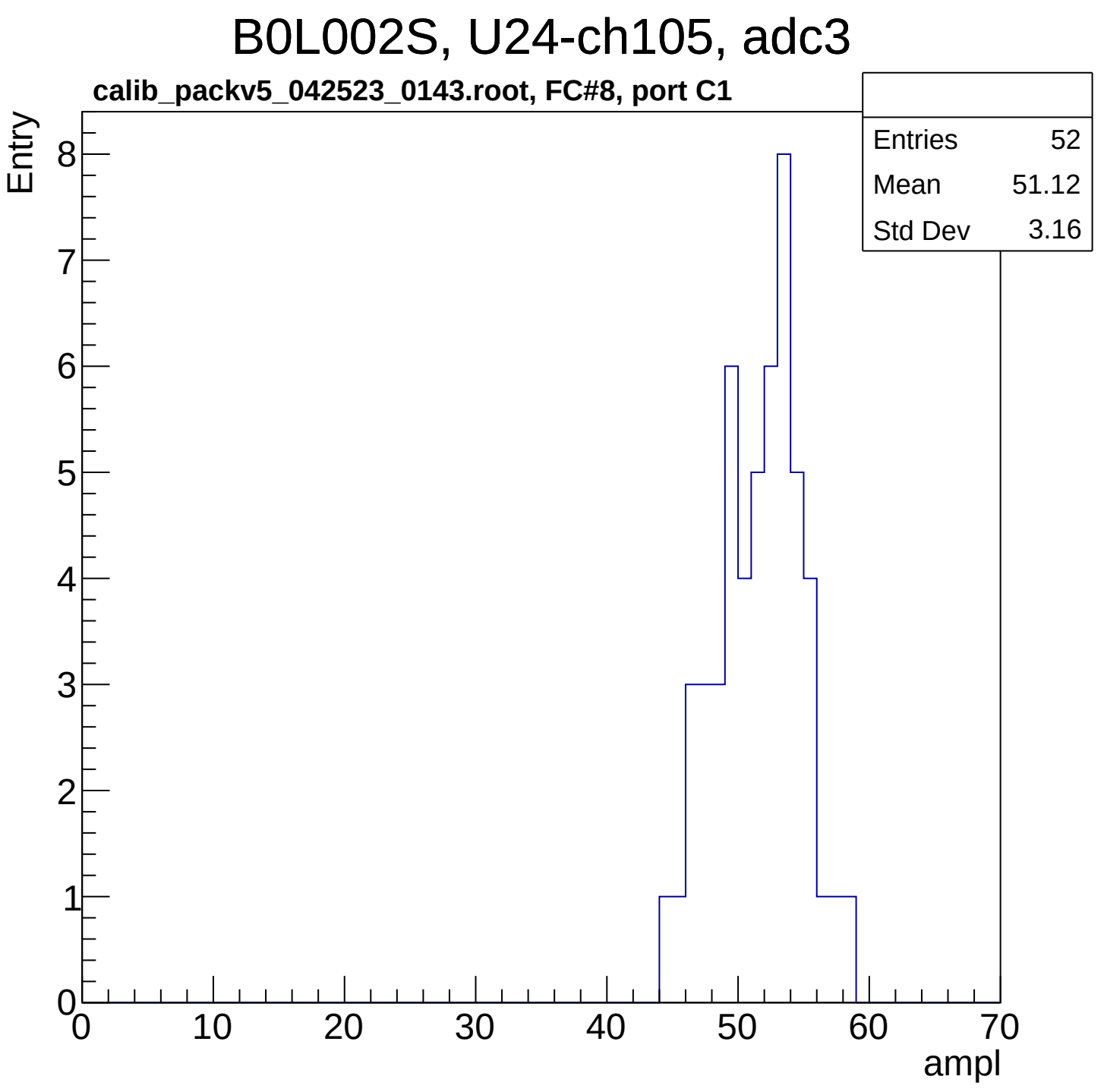
B0L002S, U24-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 51.12
Std Dev 3.16
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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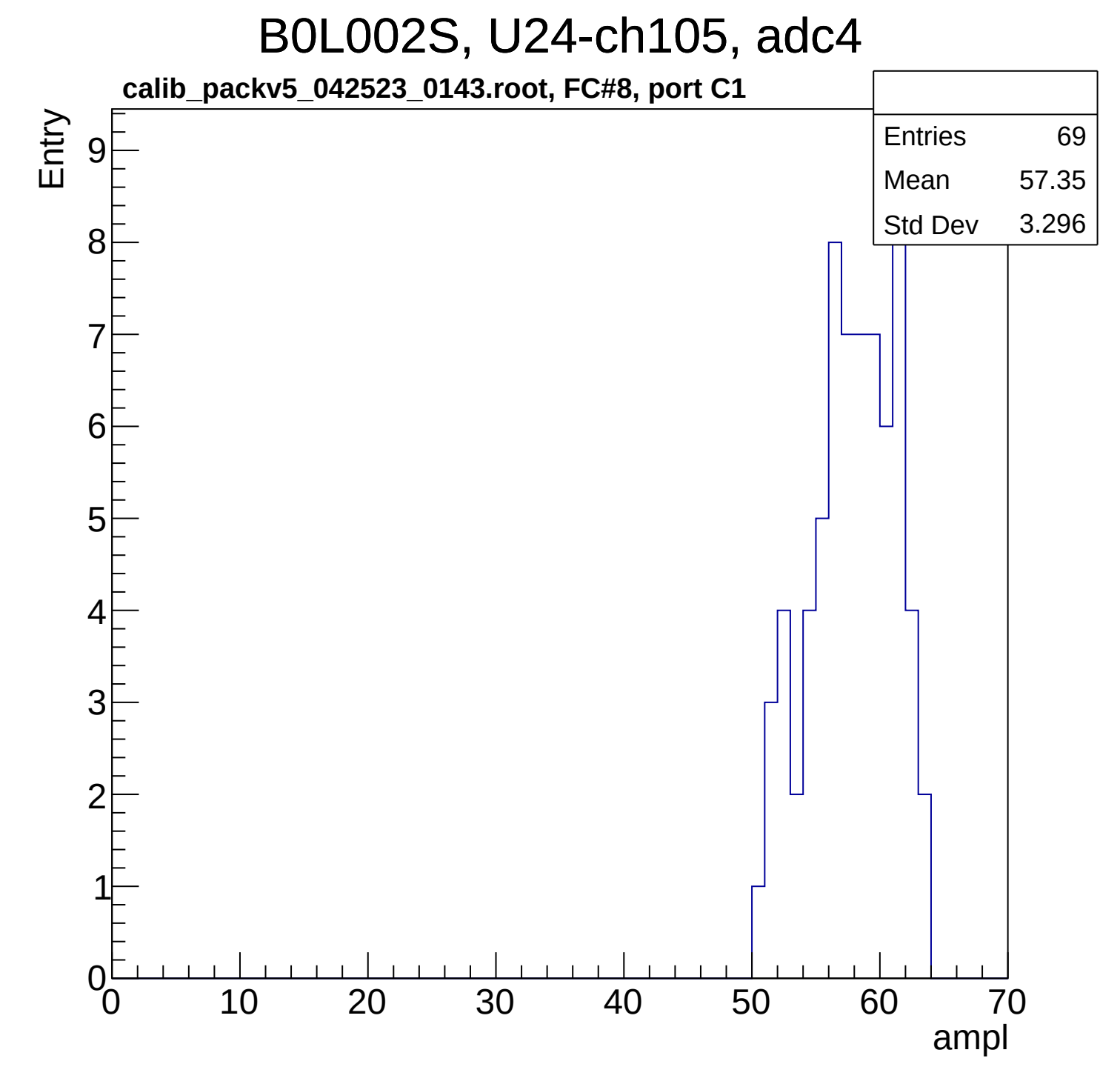
B0L002S, U24-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 57.35
Std Dev 3.296
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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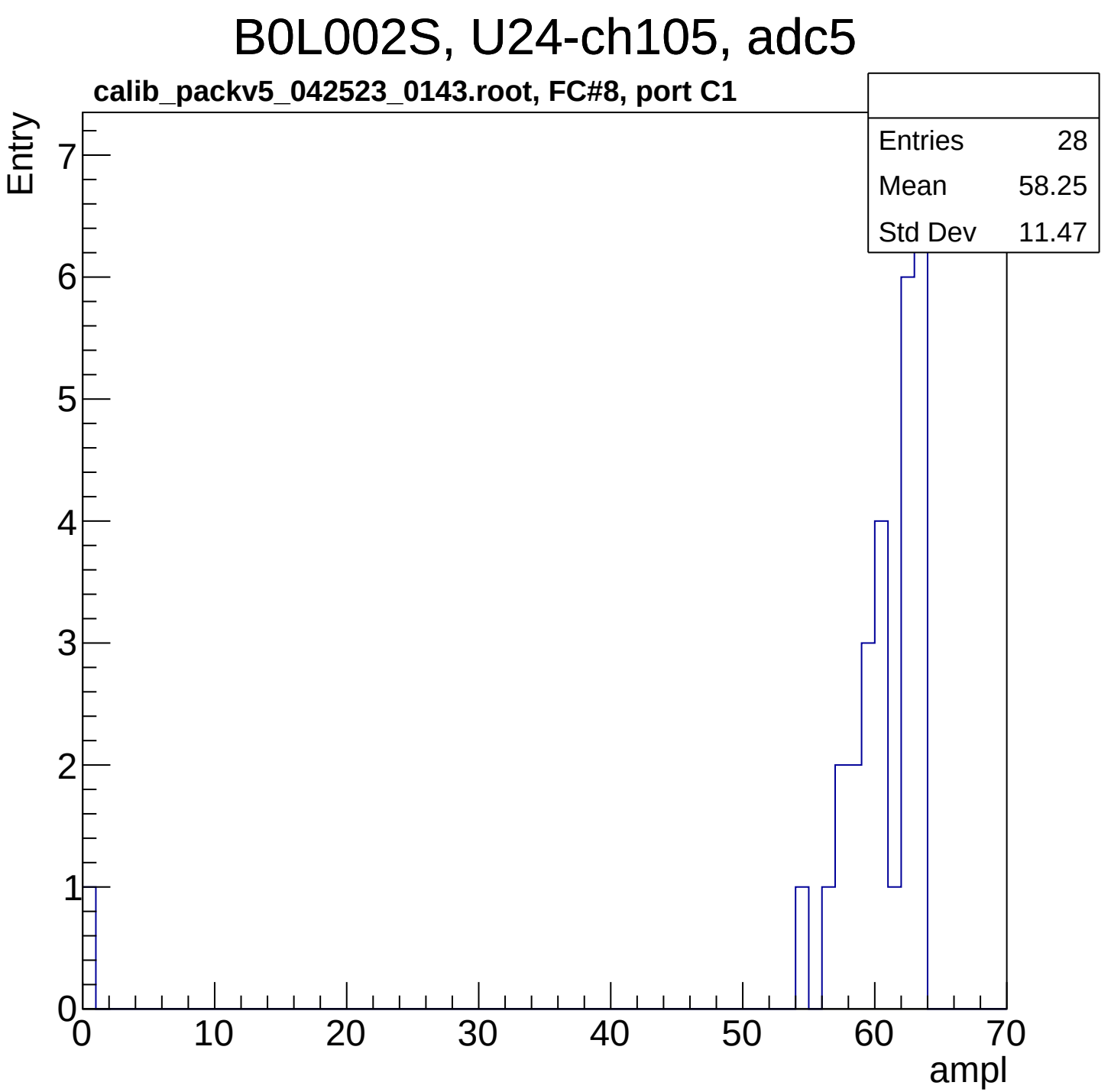
B0L002S, U24-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.25
Std Dev 11.47
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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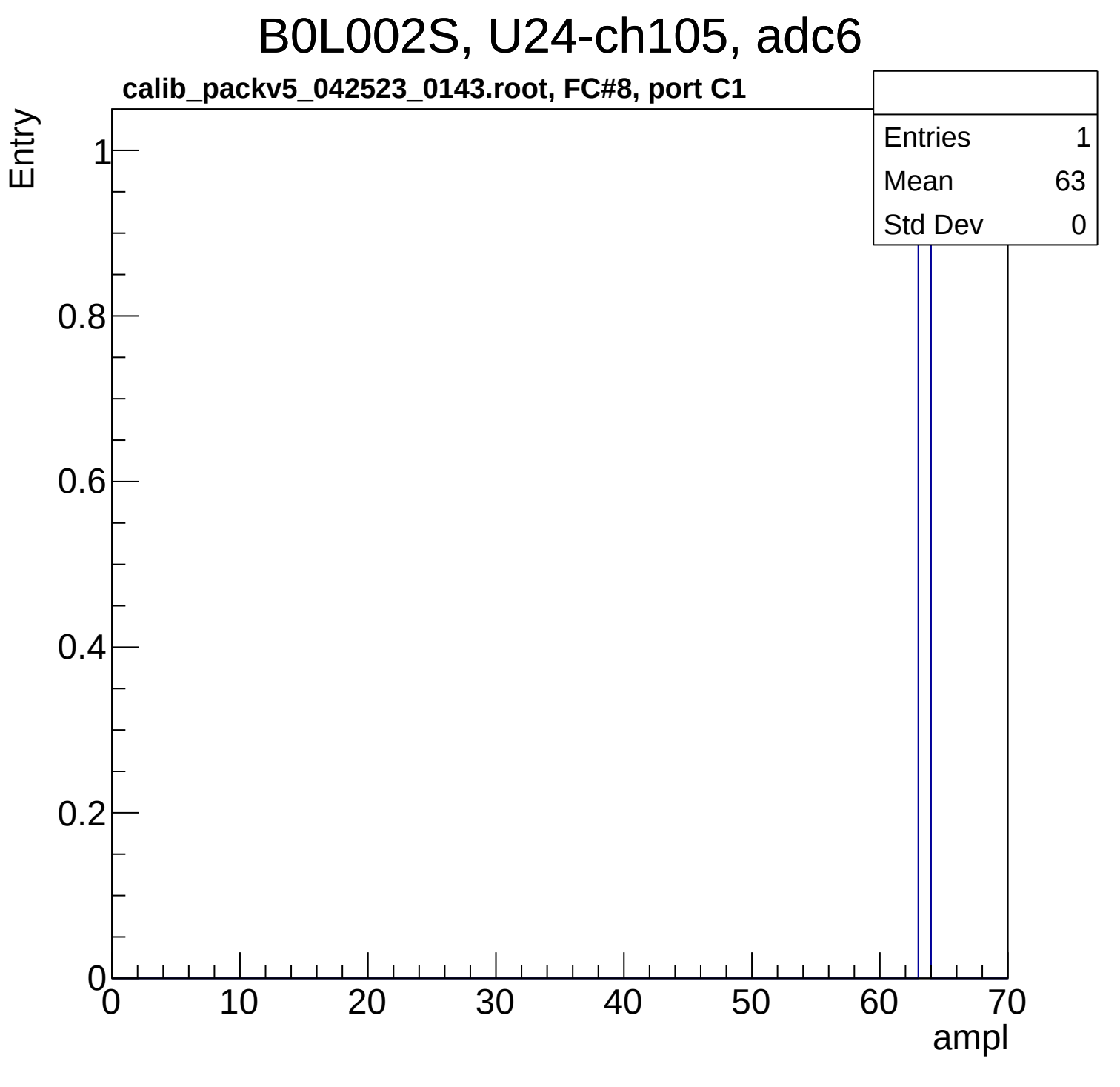
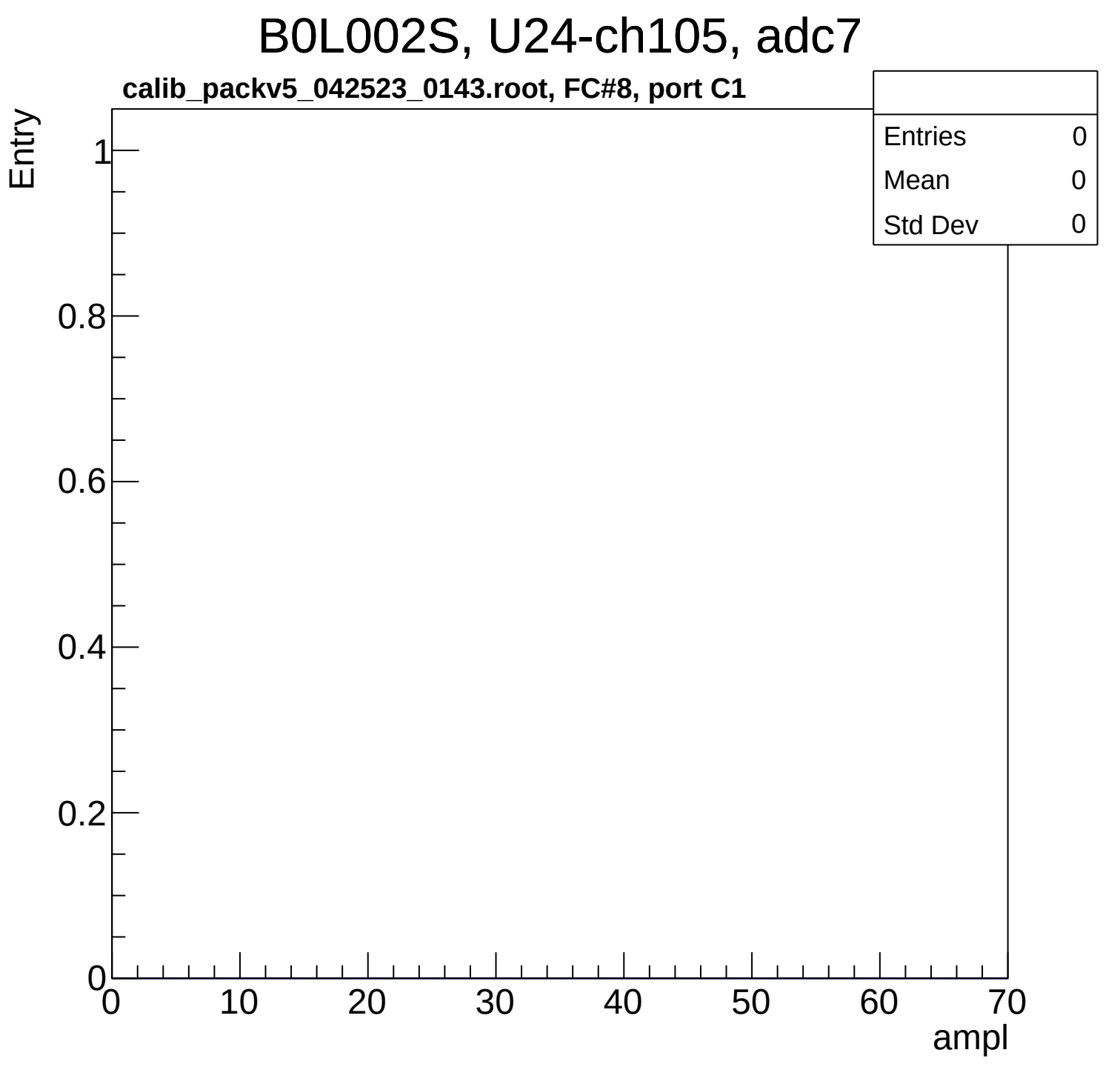

B0L002S, U24-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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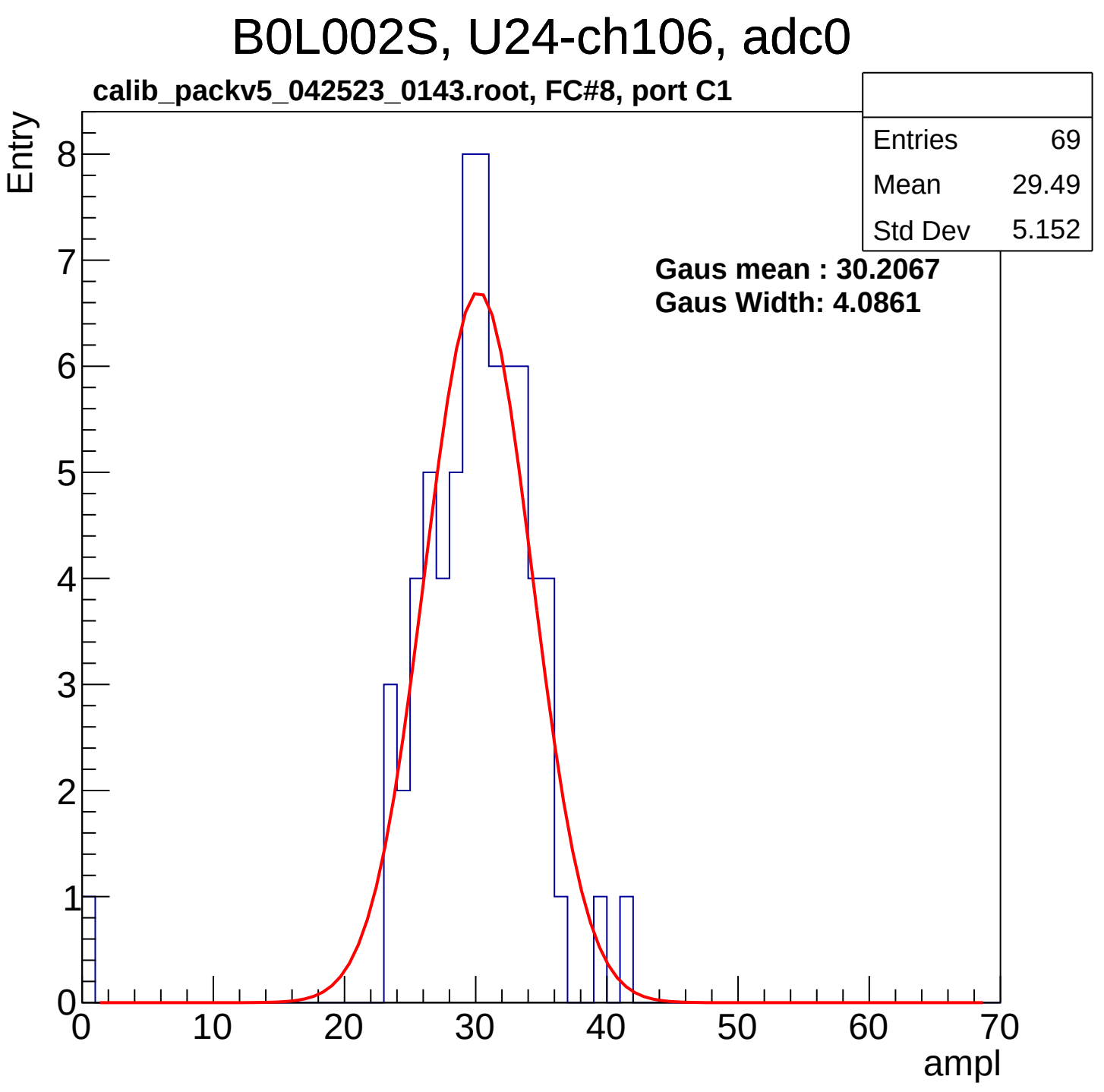
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.49
Std Dev 5.152
Gaus mean : 30.2067
Gaus Width: 4.0861
Entry
ampl
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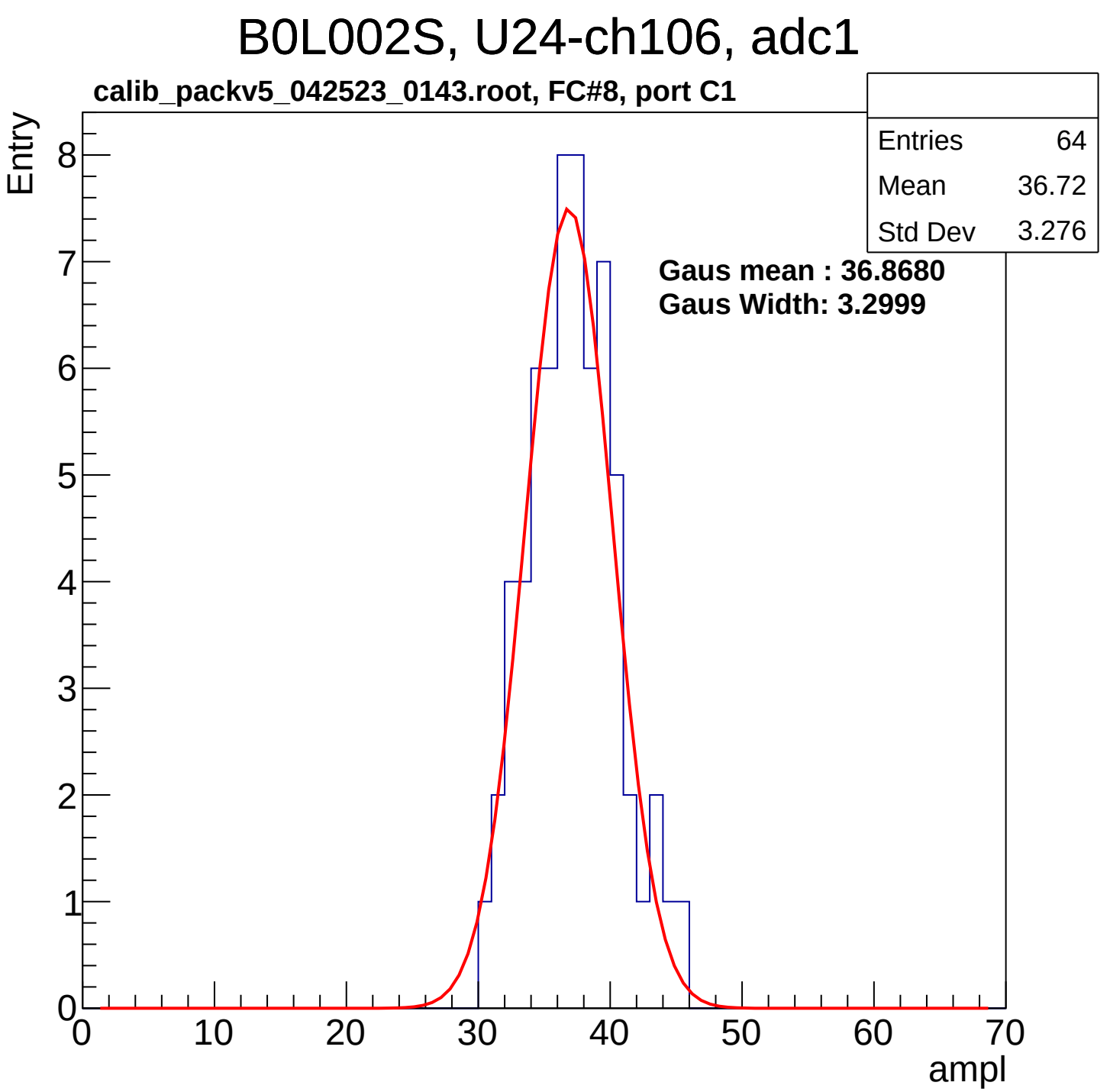
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 36.72
Std Dev 3.276
Gaus mean : 36.8680
Gaus Width: 3.2999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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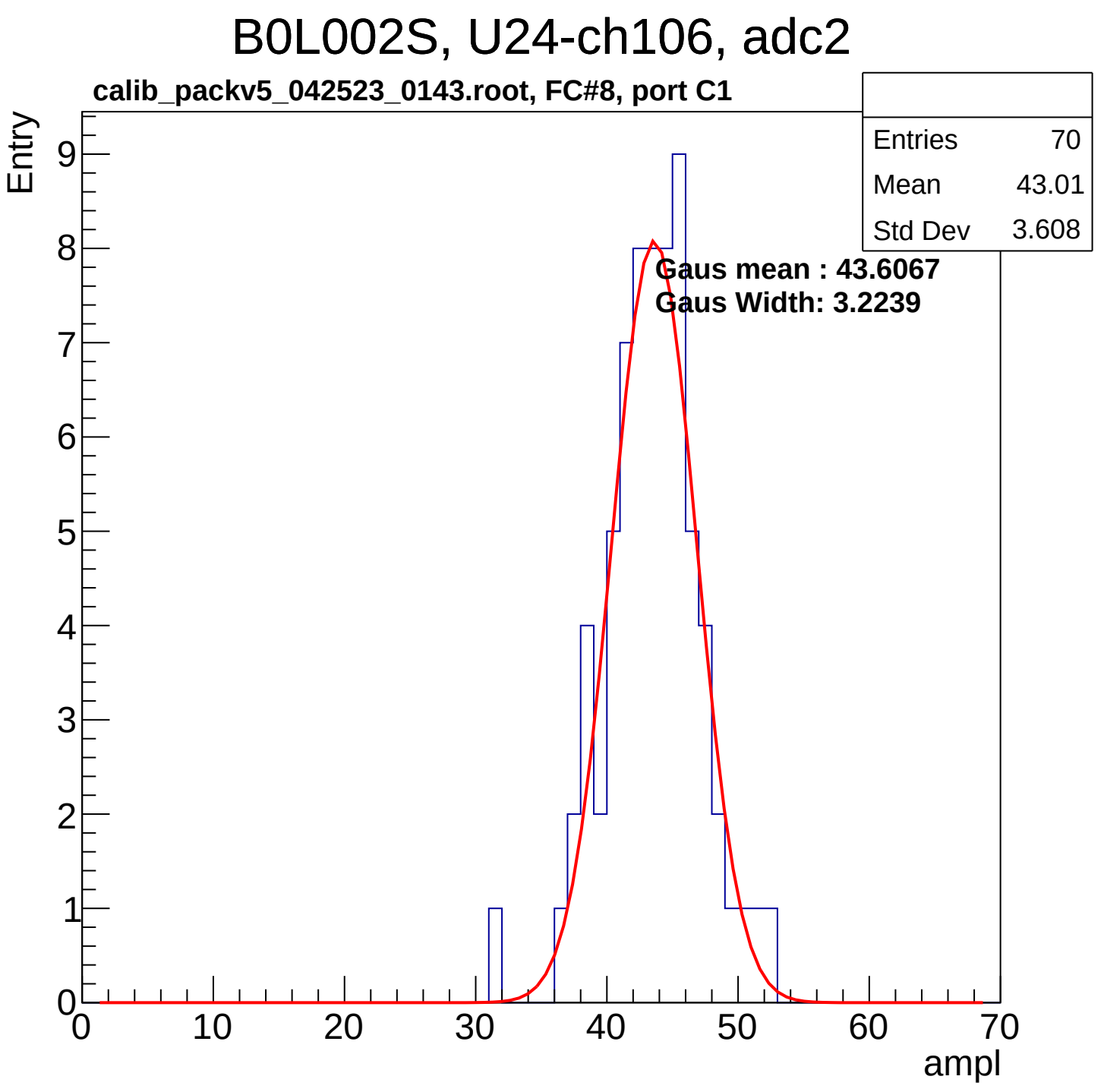
B0L002S, U24-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.01
Std Dev 3.608
Gaus mean : 43.6067
Gaus Width: 3.2239
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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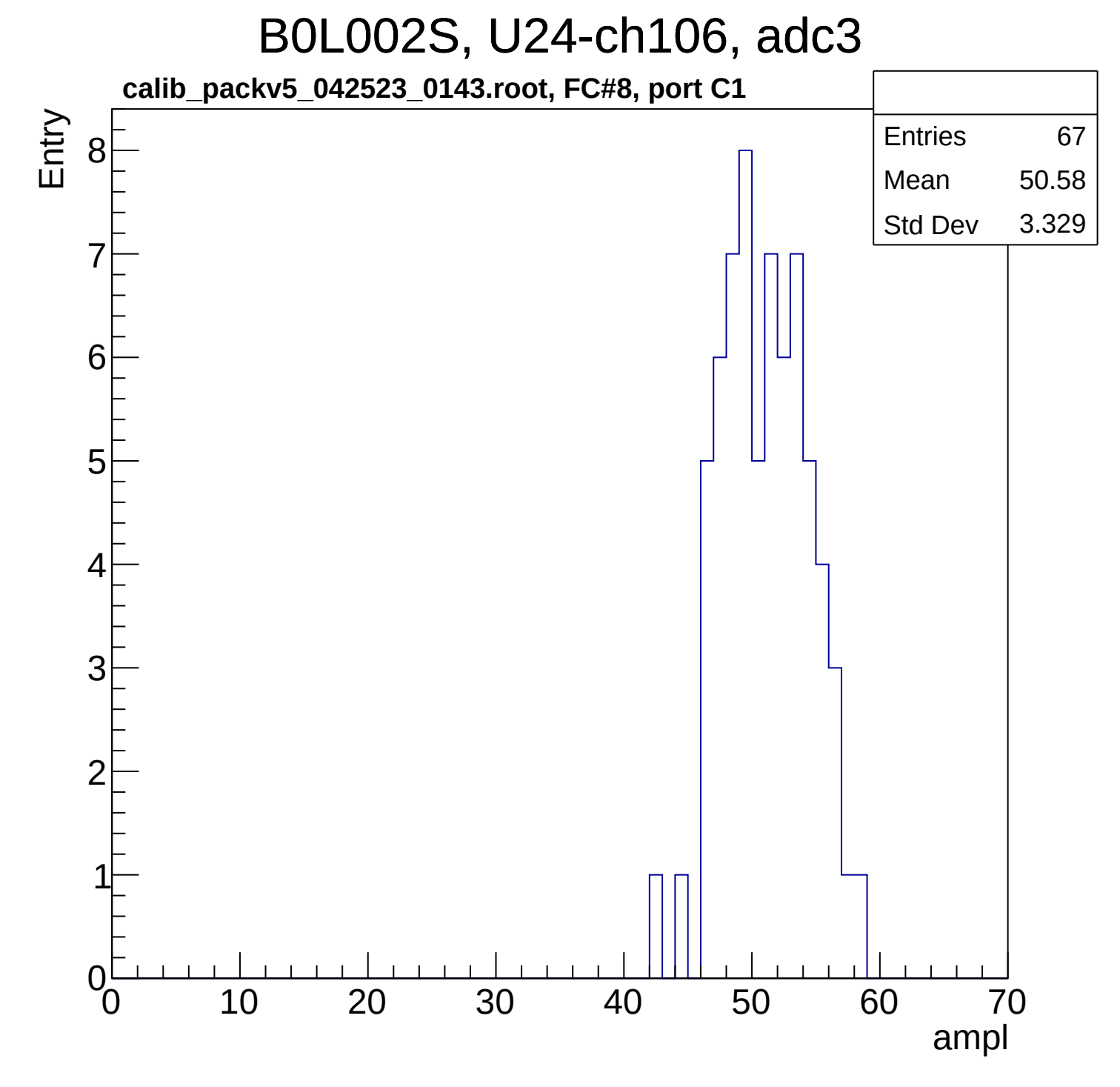
B0L002S, U24-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.58
Std Dev 3.329
Entry
ampl
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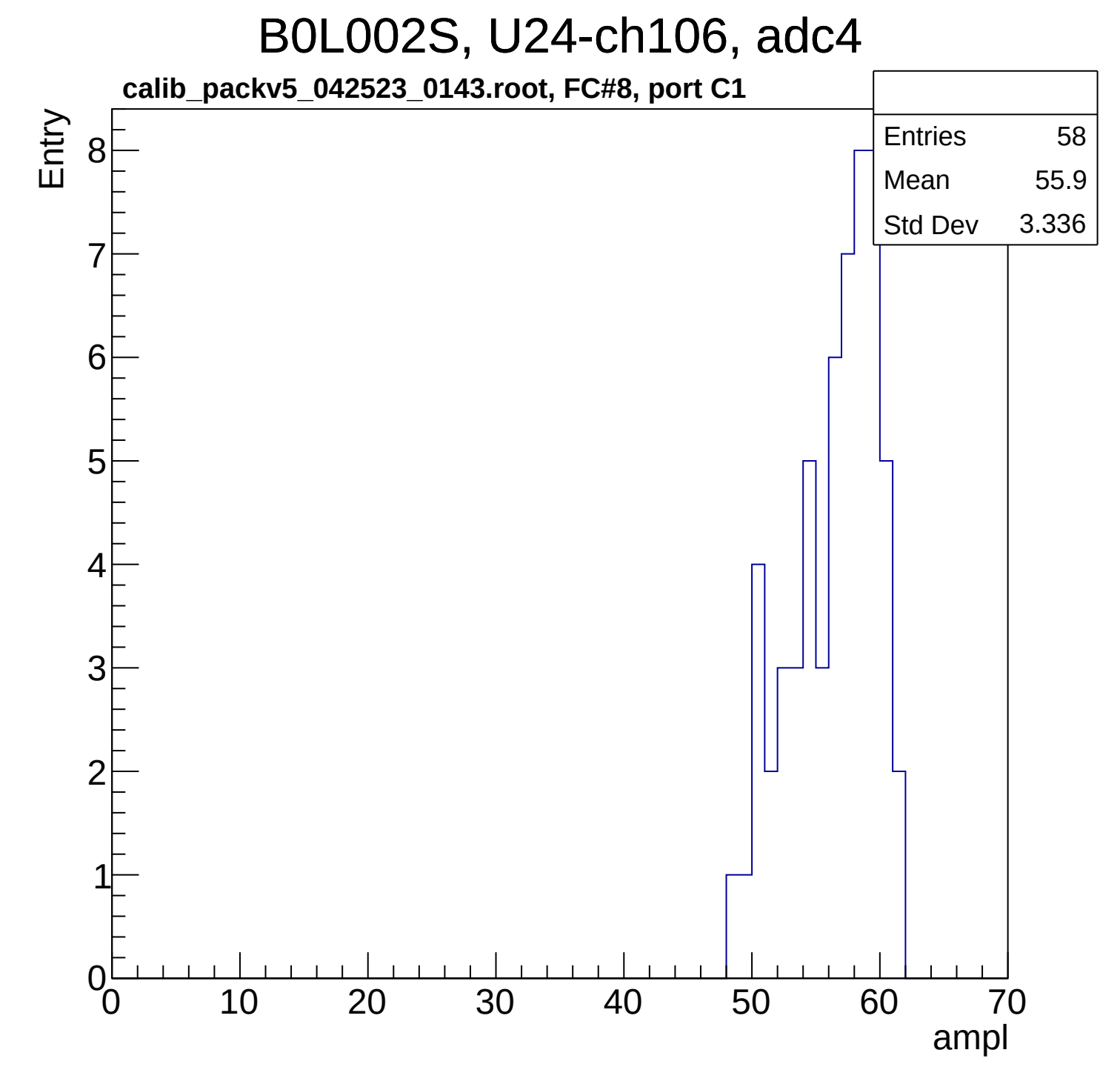
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.9
Std Dev 3.336
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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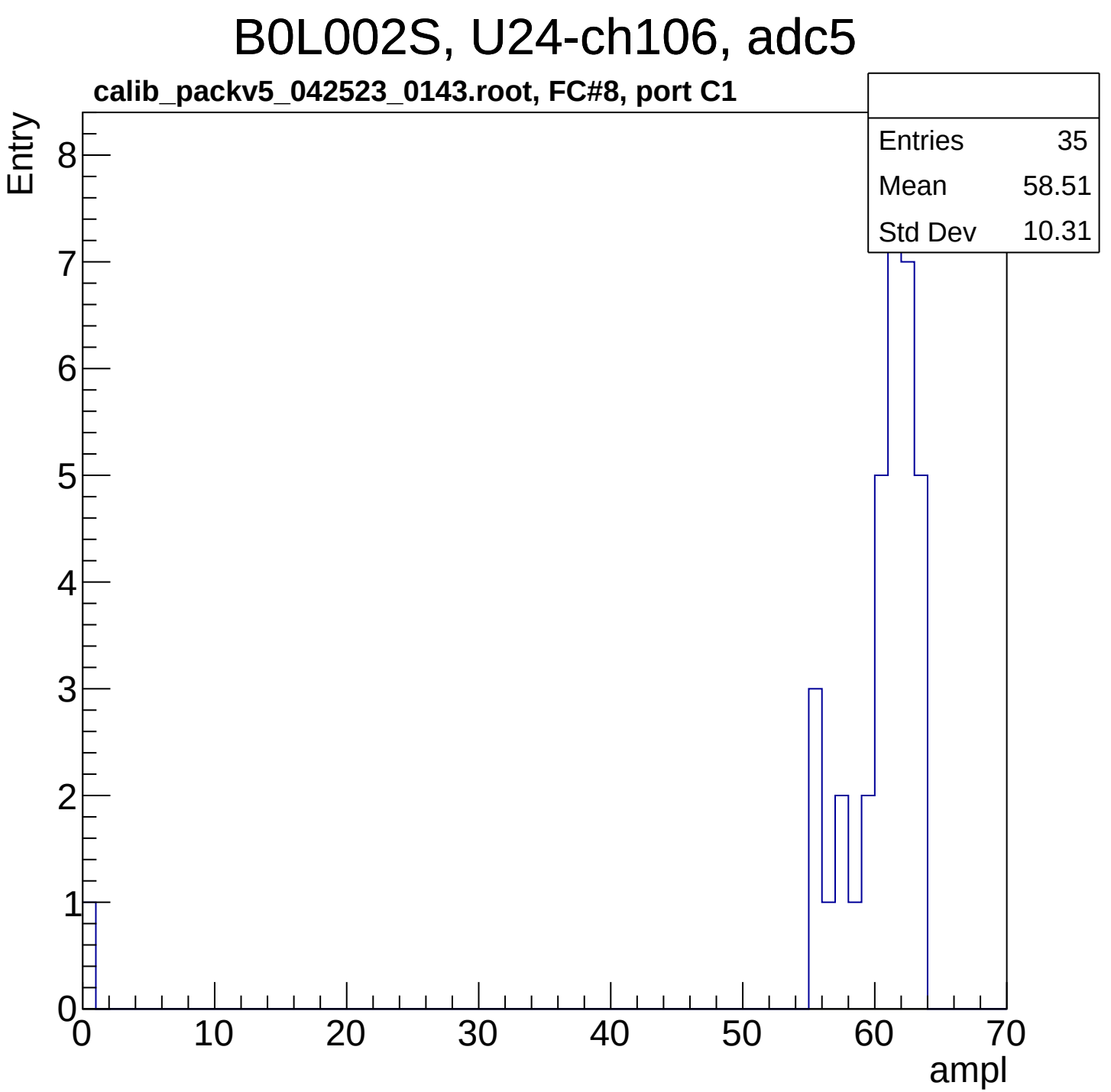
60
70


B0L002S, U24-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.51
Std Dev 10.31
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
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60
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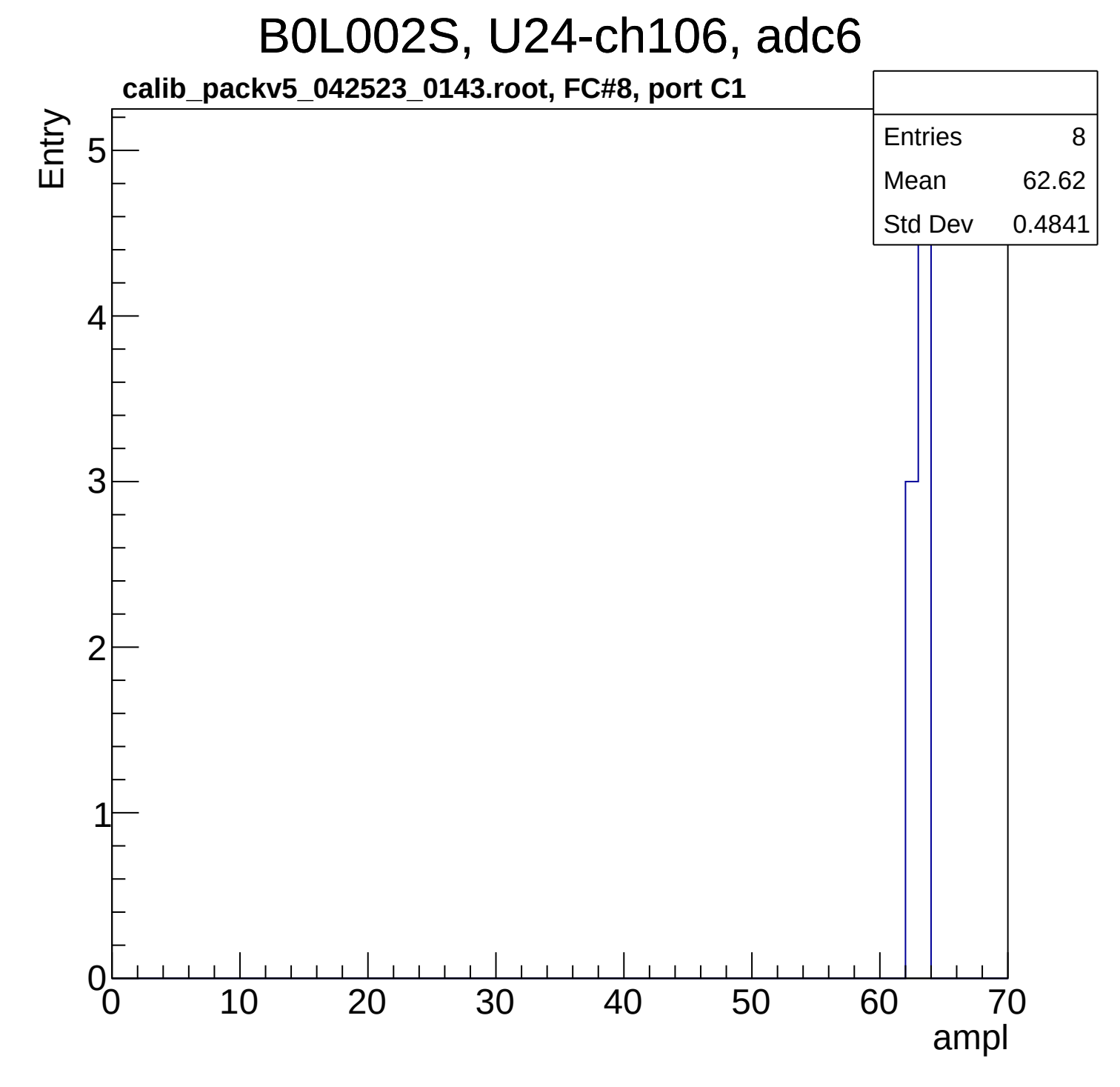
B0L002S, U24-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 62.62
Std Dev 0.4841
Entry
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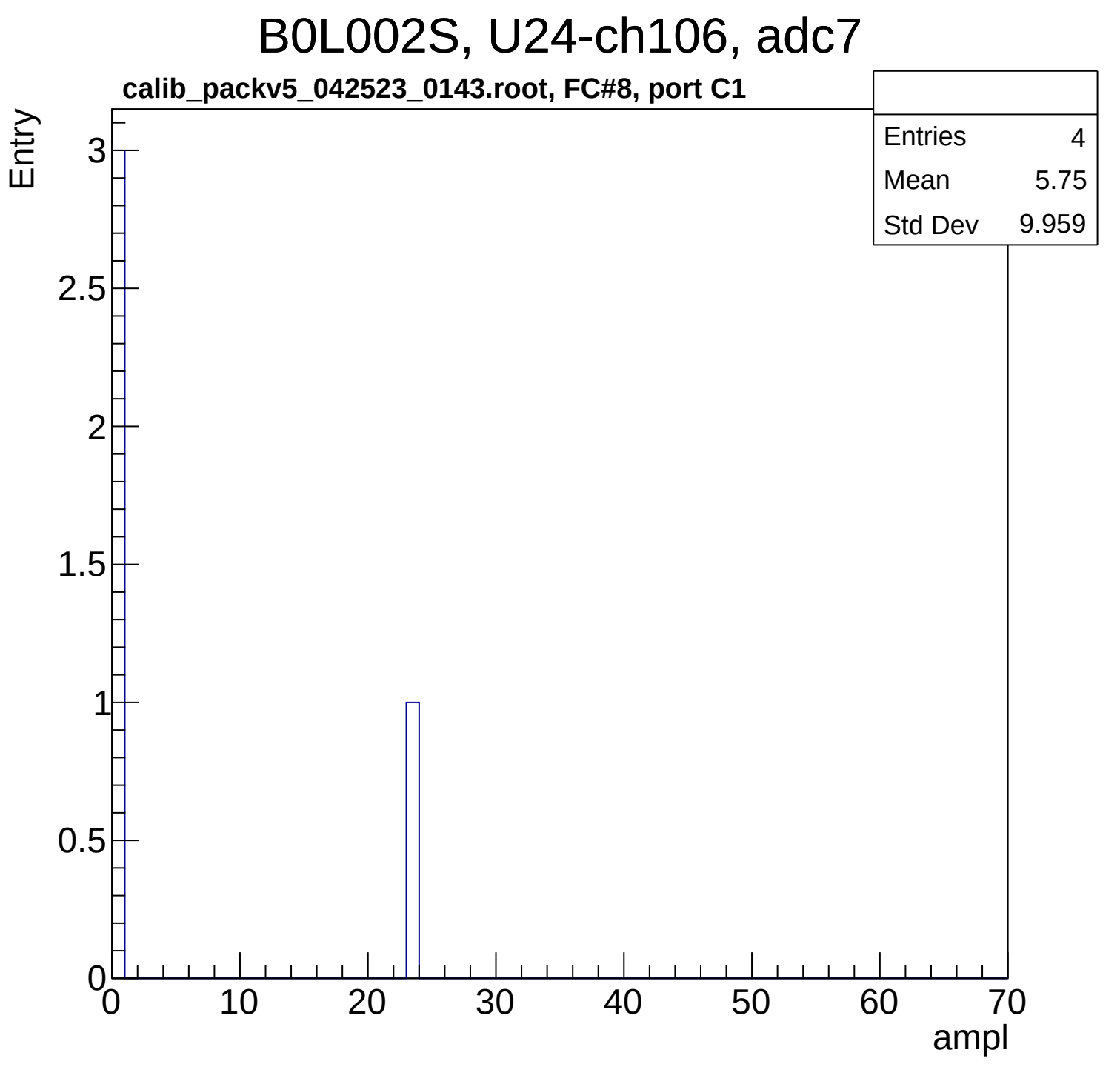
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
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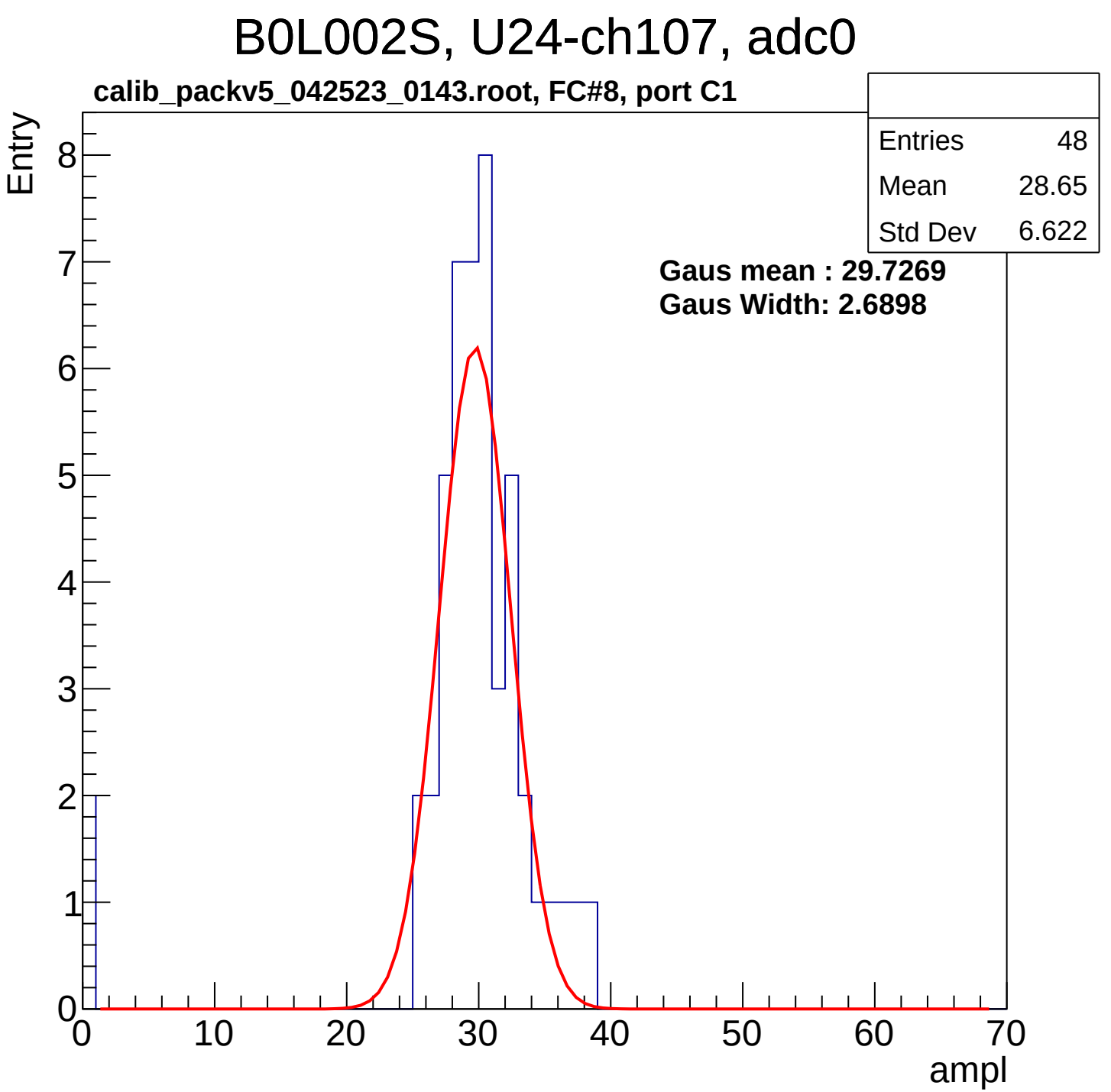
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 28.65
Std Dev 6.622
Gaus mean : 29.7269
Gaus Width: 2.6898
Entry
ampl
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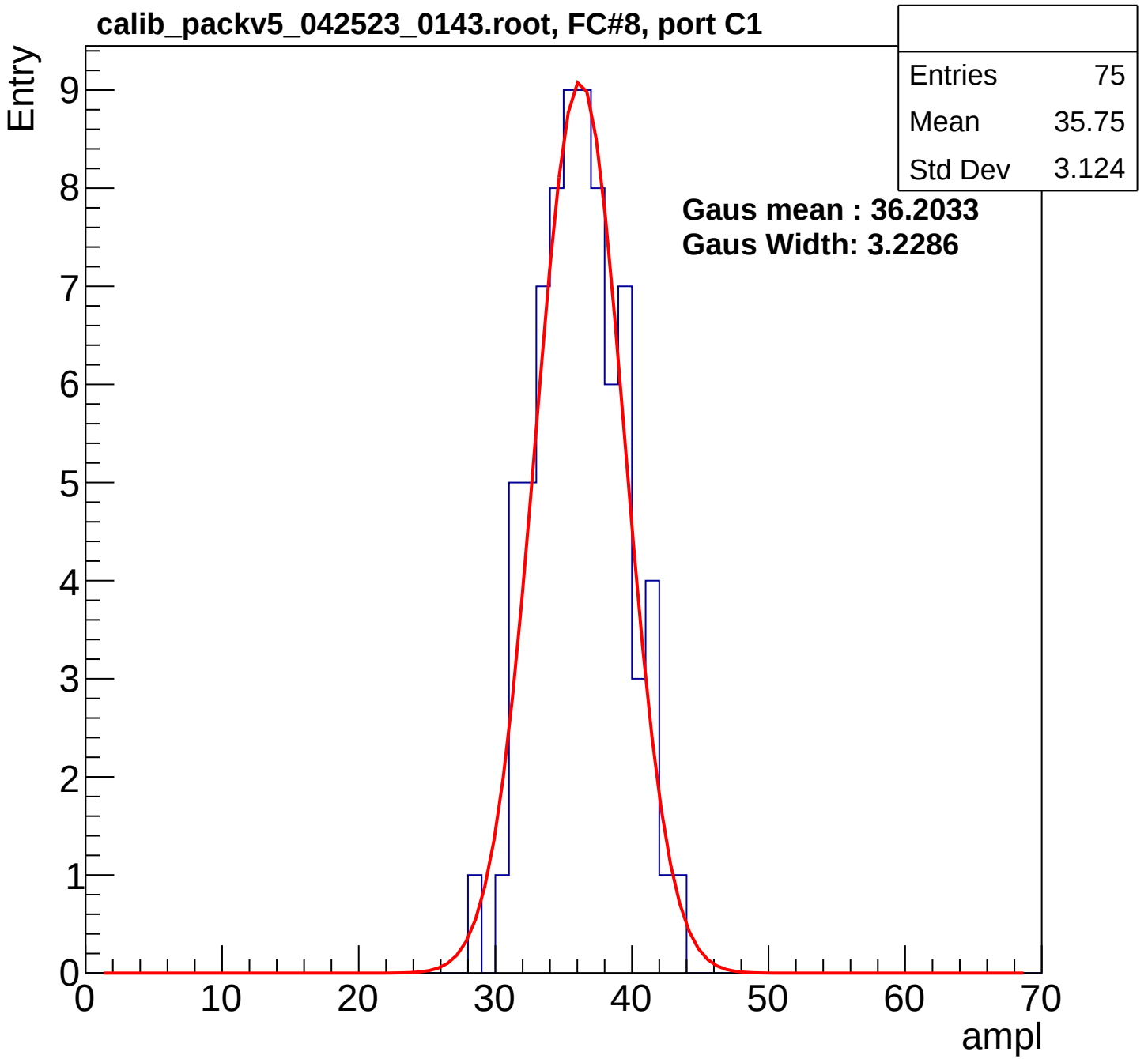
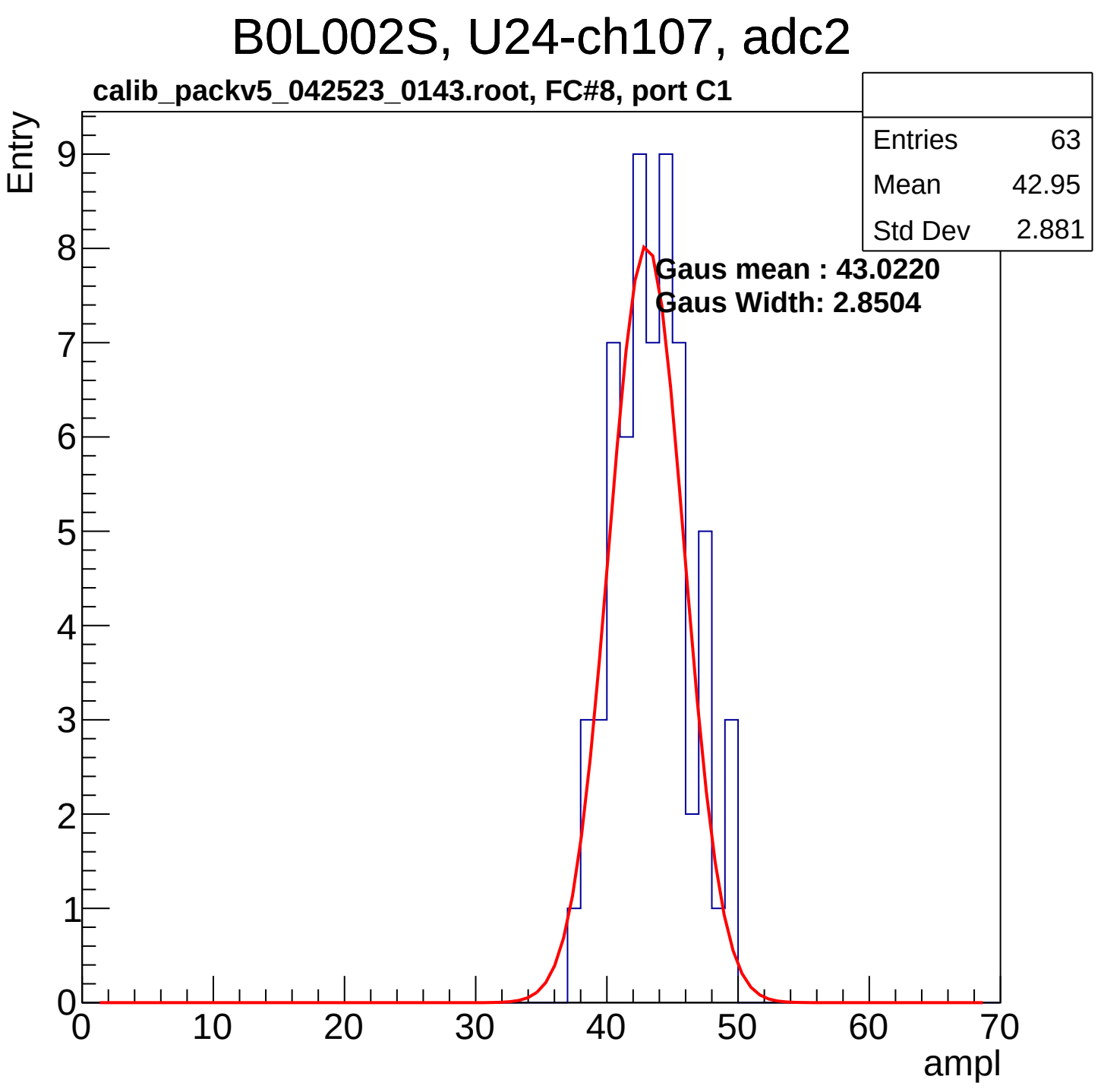
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 35.75
Std Dev 3.124
Gaus mean : 36.2033
Gaus Width: 3.2286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 42.95
Std Dev 2.881
Gaus mean : 43.0220
Gaus Width: 2.8504
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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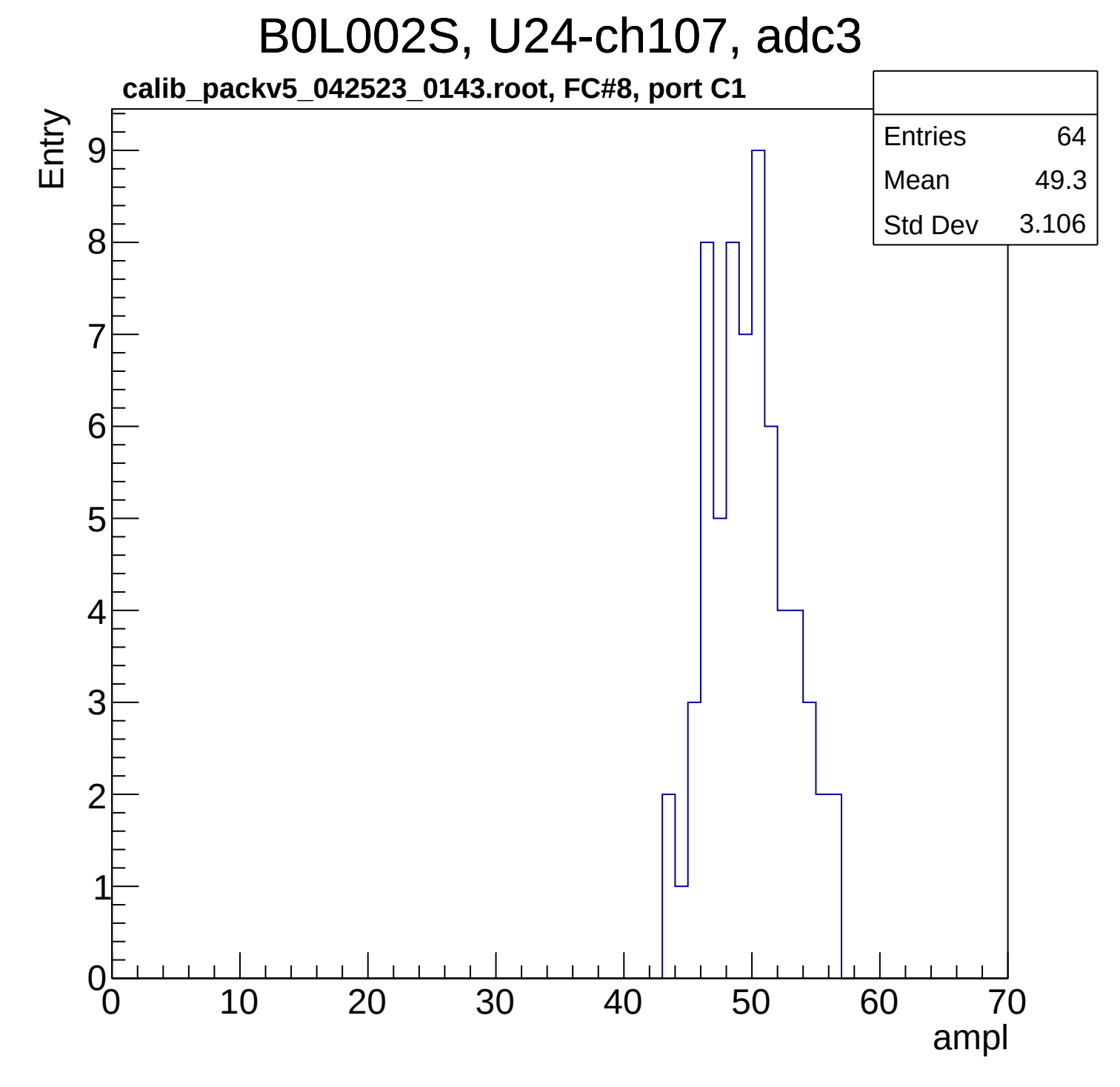
B0L002S, U24-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.3
Std Dev 3.106
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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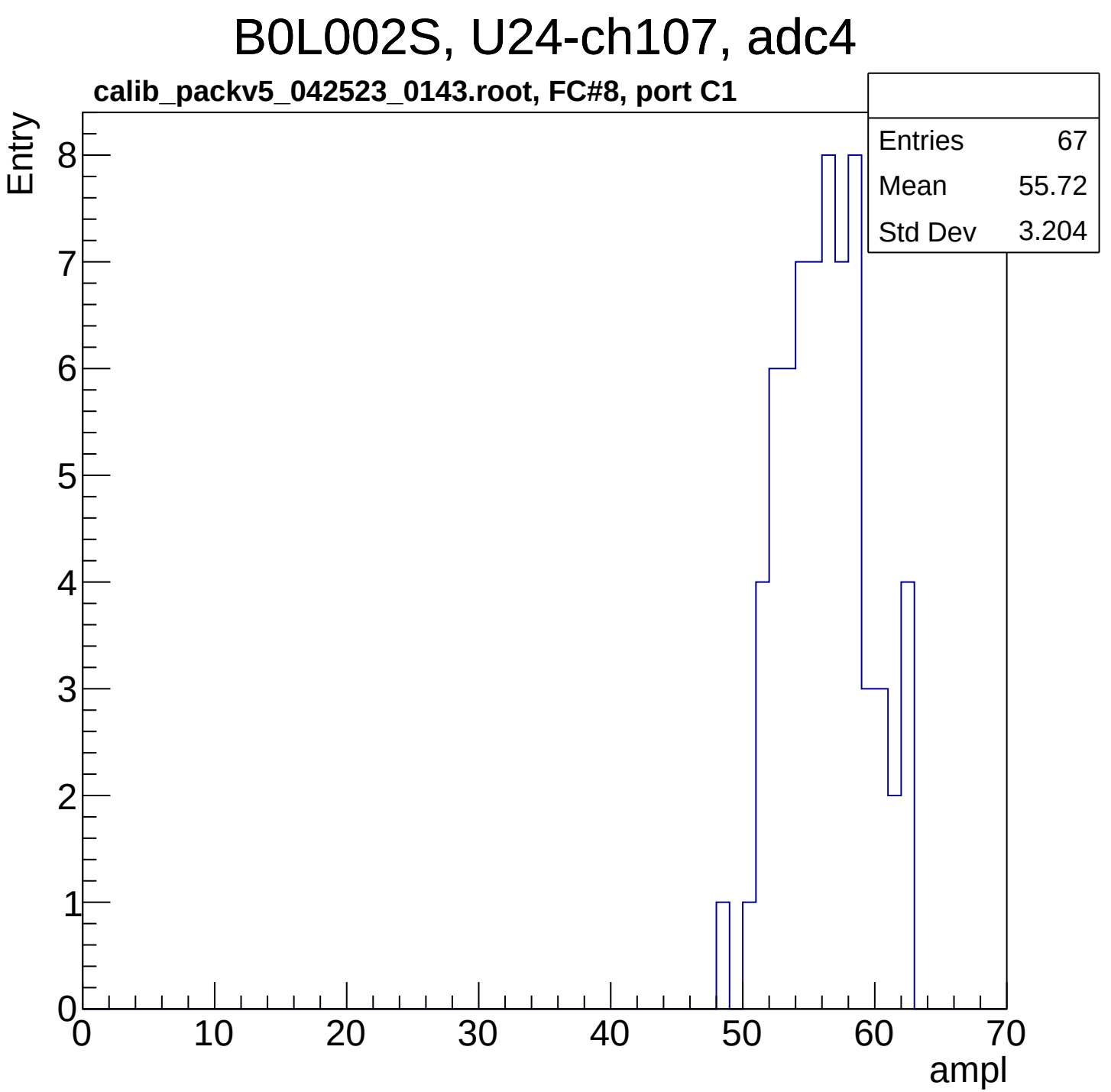
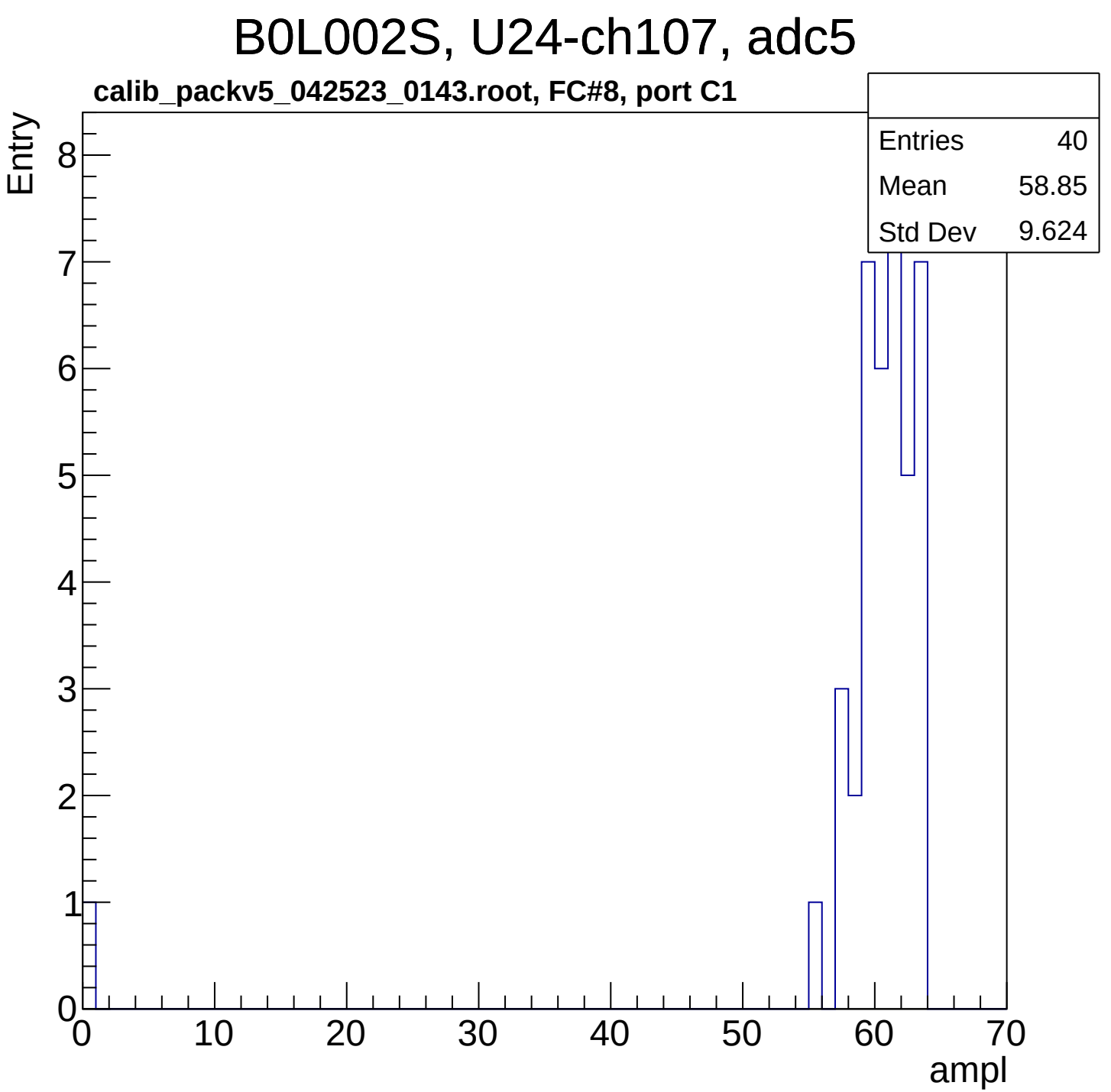

B0L002S, U24-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 55.72
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.85
Std Dev 9.624
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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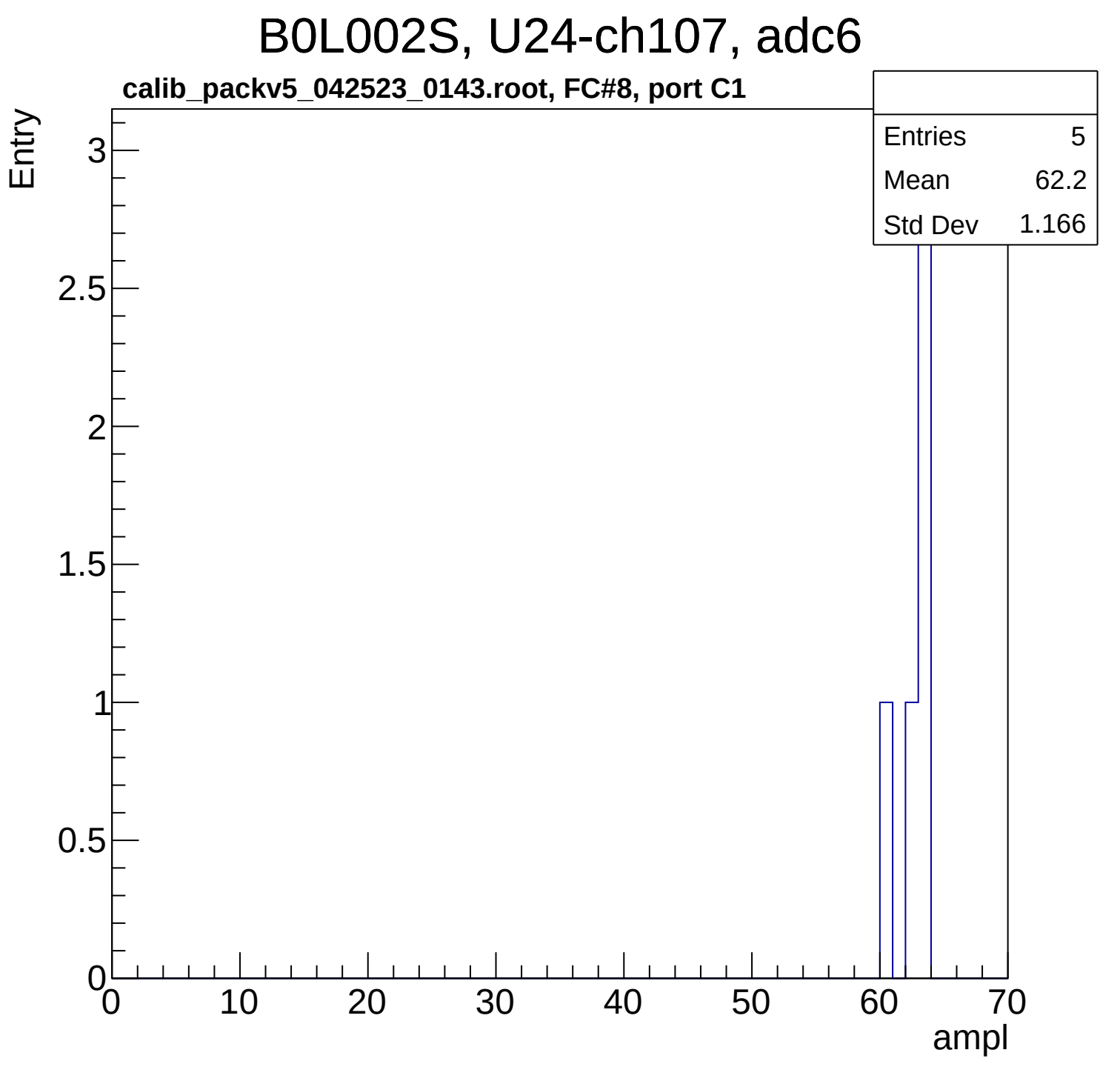
B0L002S, U24-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
ampl
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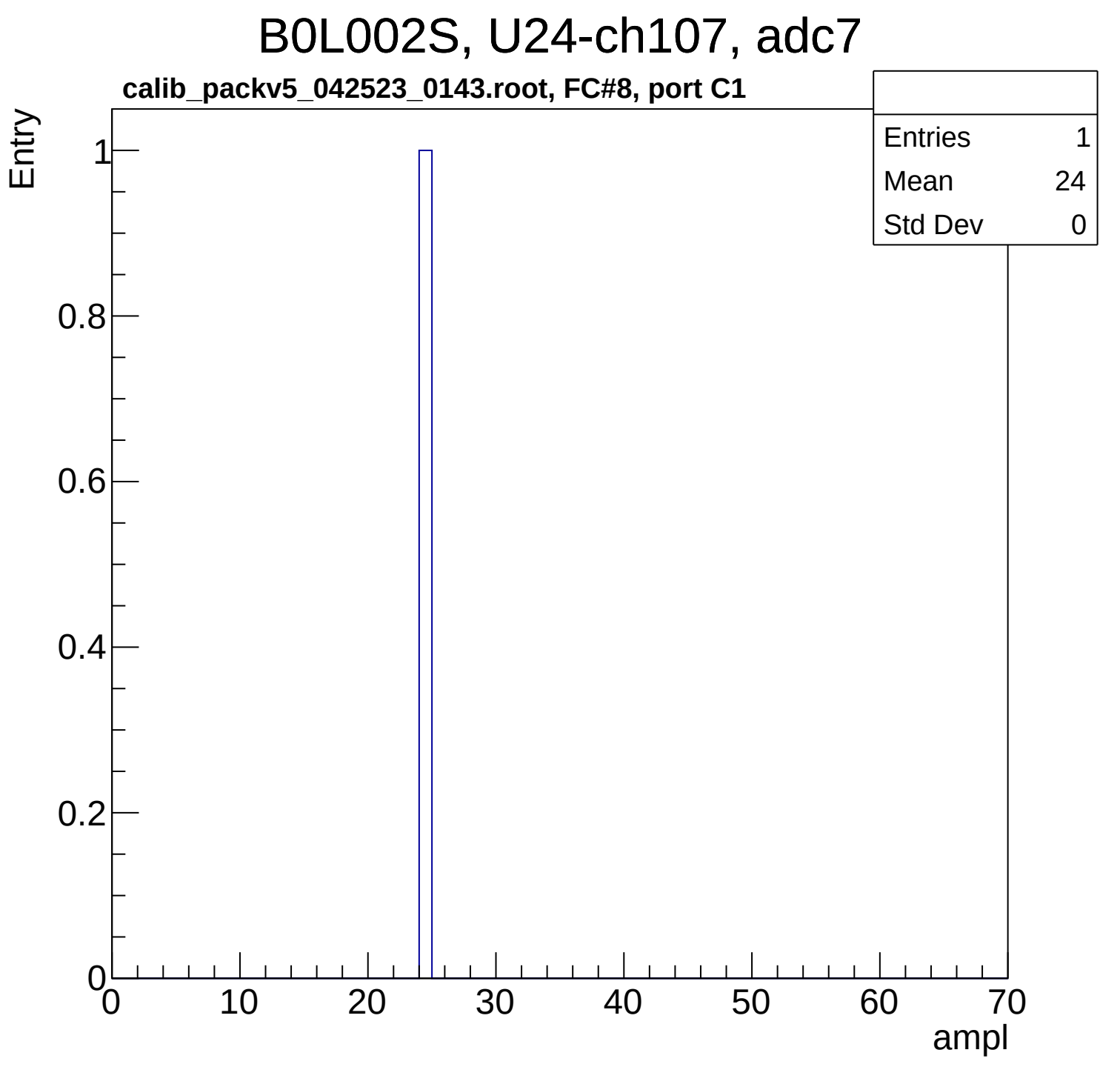
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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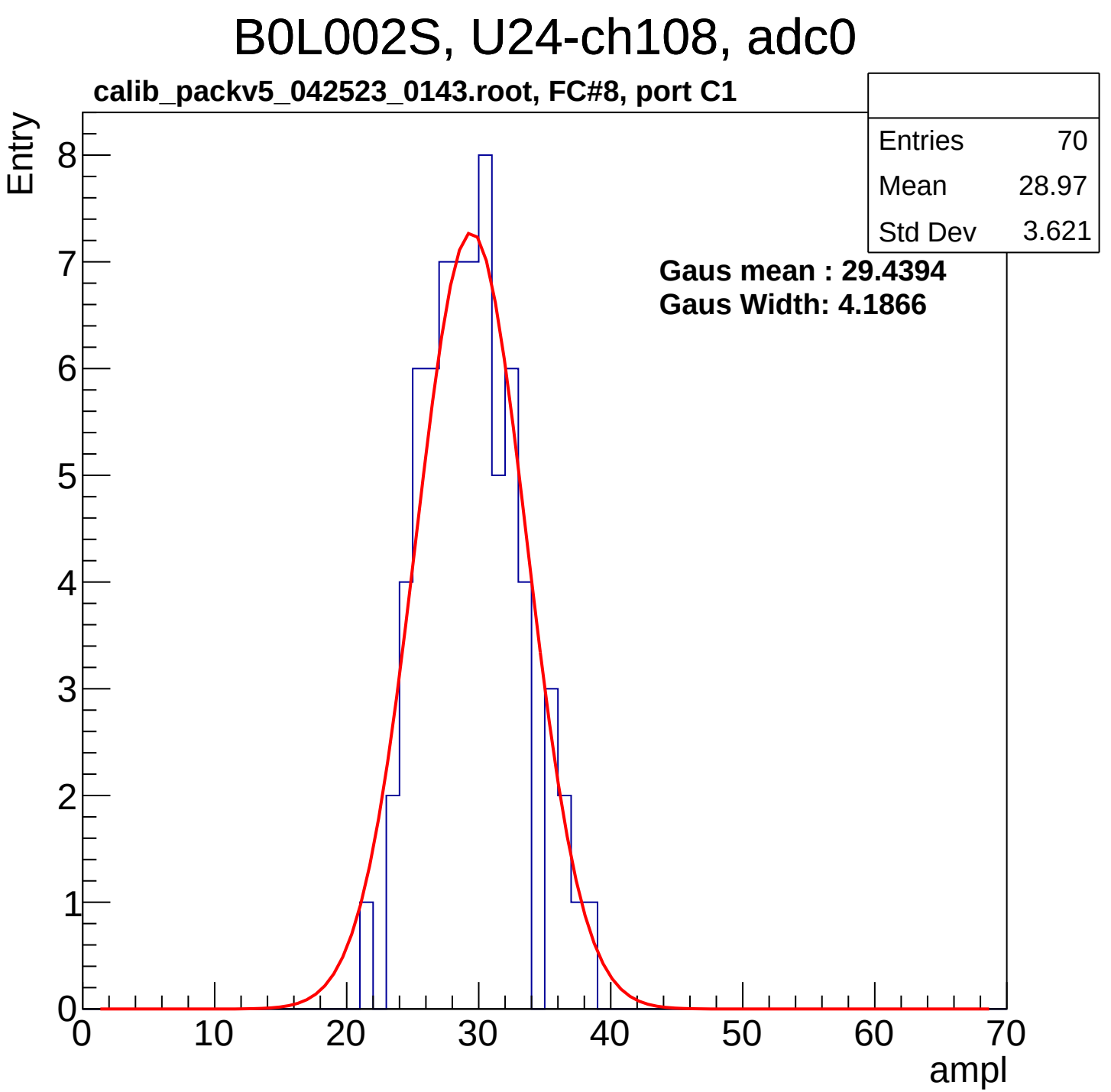
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 28.97
Std Dev 3.621
Gaus mean : 29.4394
Gaus Width: 4.1866
Entry
ampl
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1
2
3
4
5
6
7
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0
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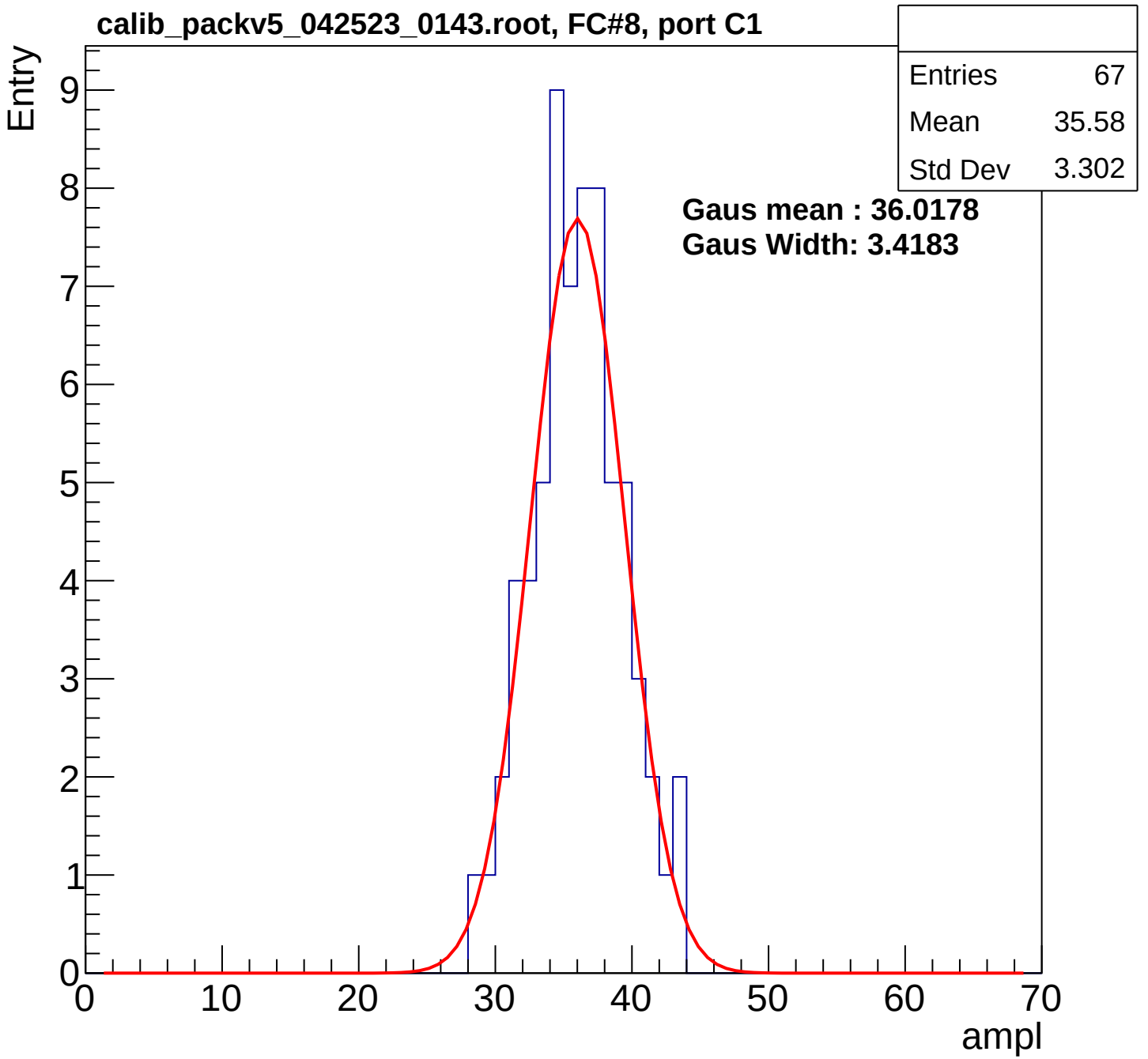
B0L002S, U24-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 35.58
Std Dev 3.302
Gaus mean : 36.0178
Gaus Width: 3.4183
Entry
ampl
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2
3
4
5
6
7
8
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0
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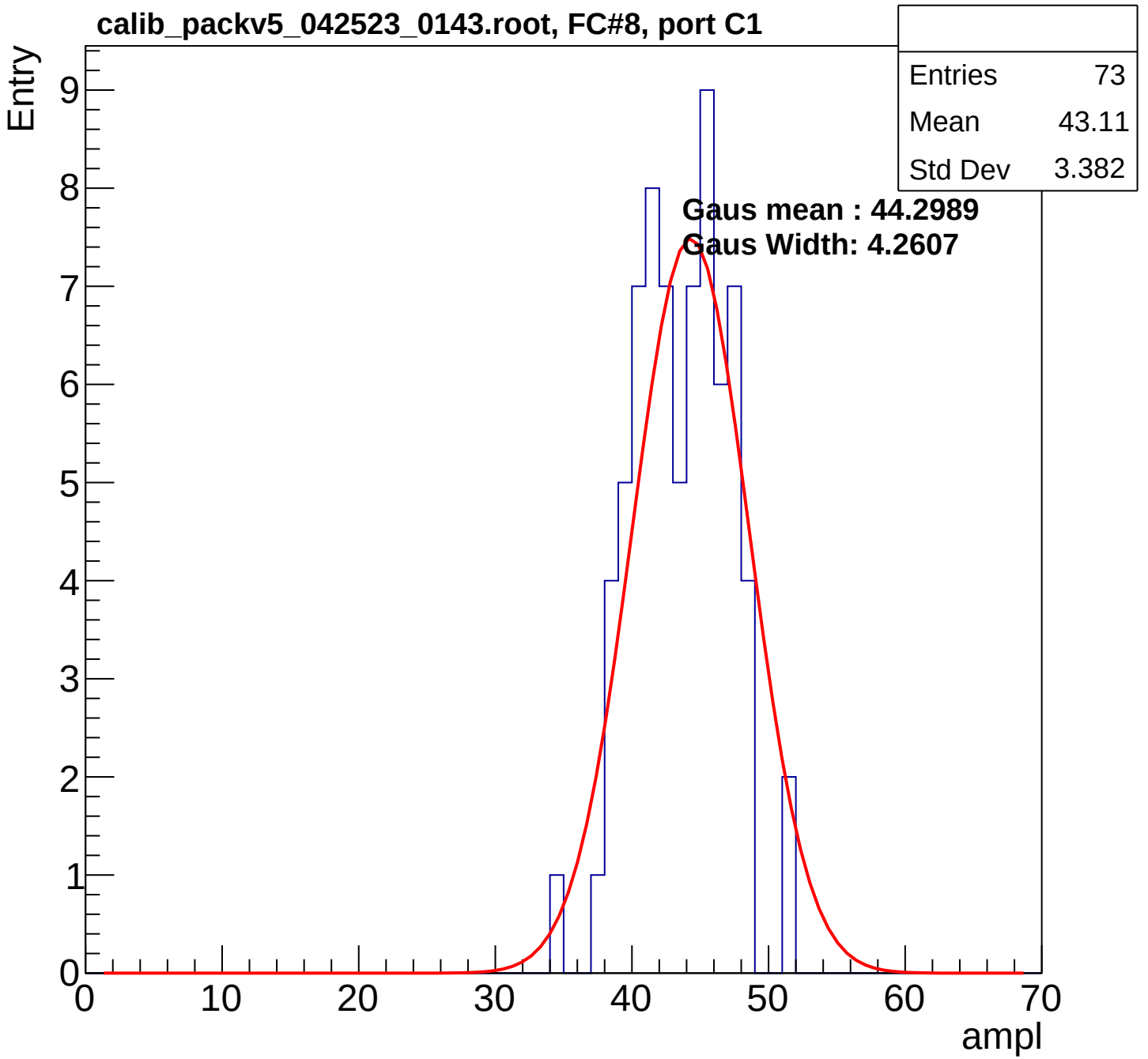
B0L002S, U24-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.11
Std Dev 3.382
Gaus mean : 44.2989
Gaus Width: 4.2607
Entry
ampl
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3
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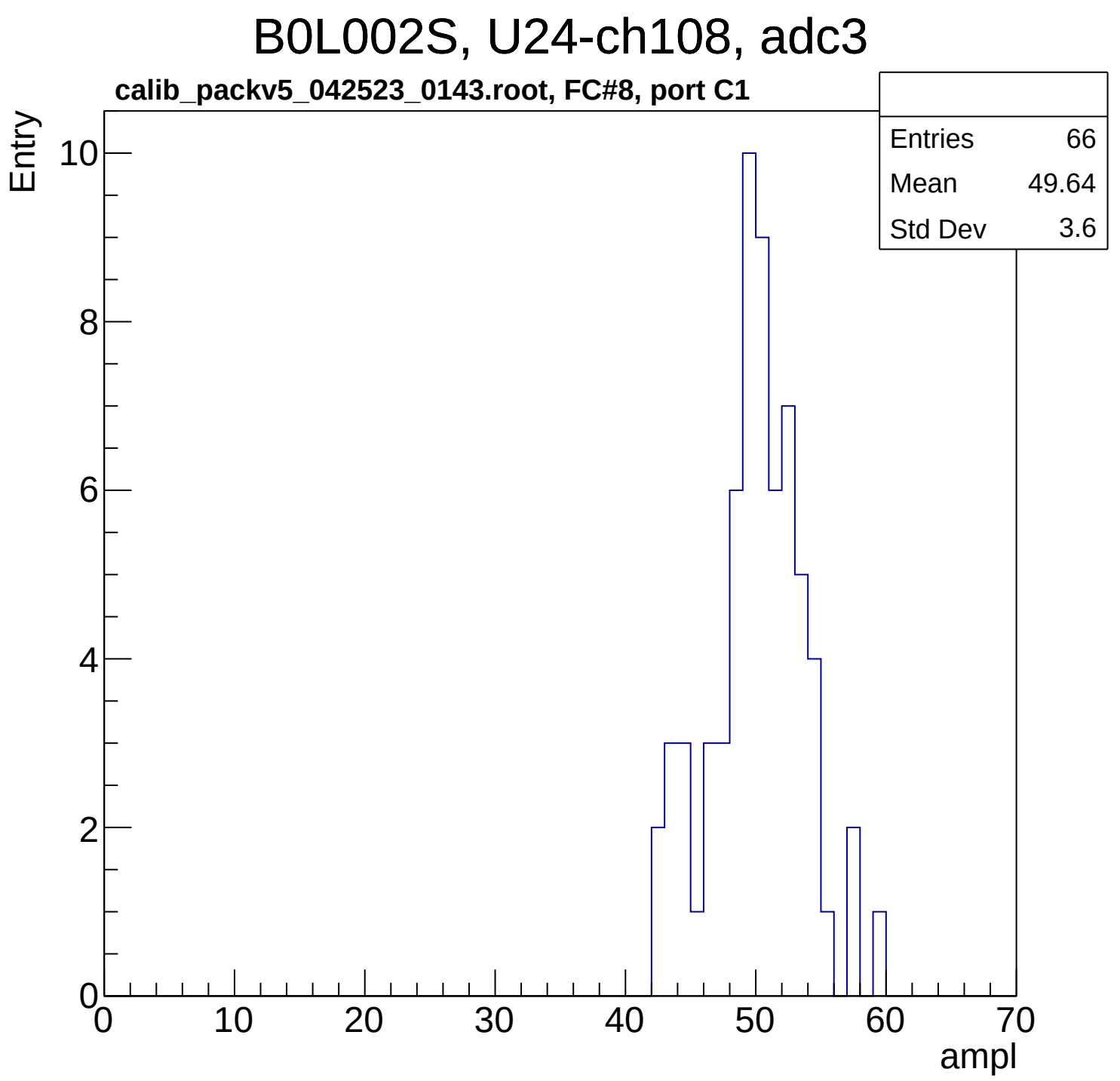
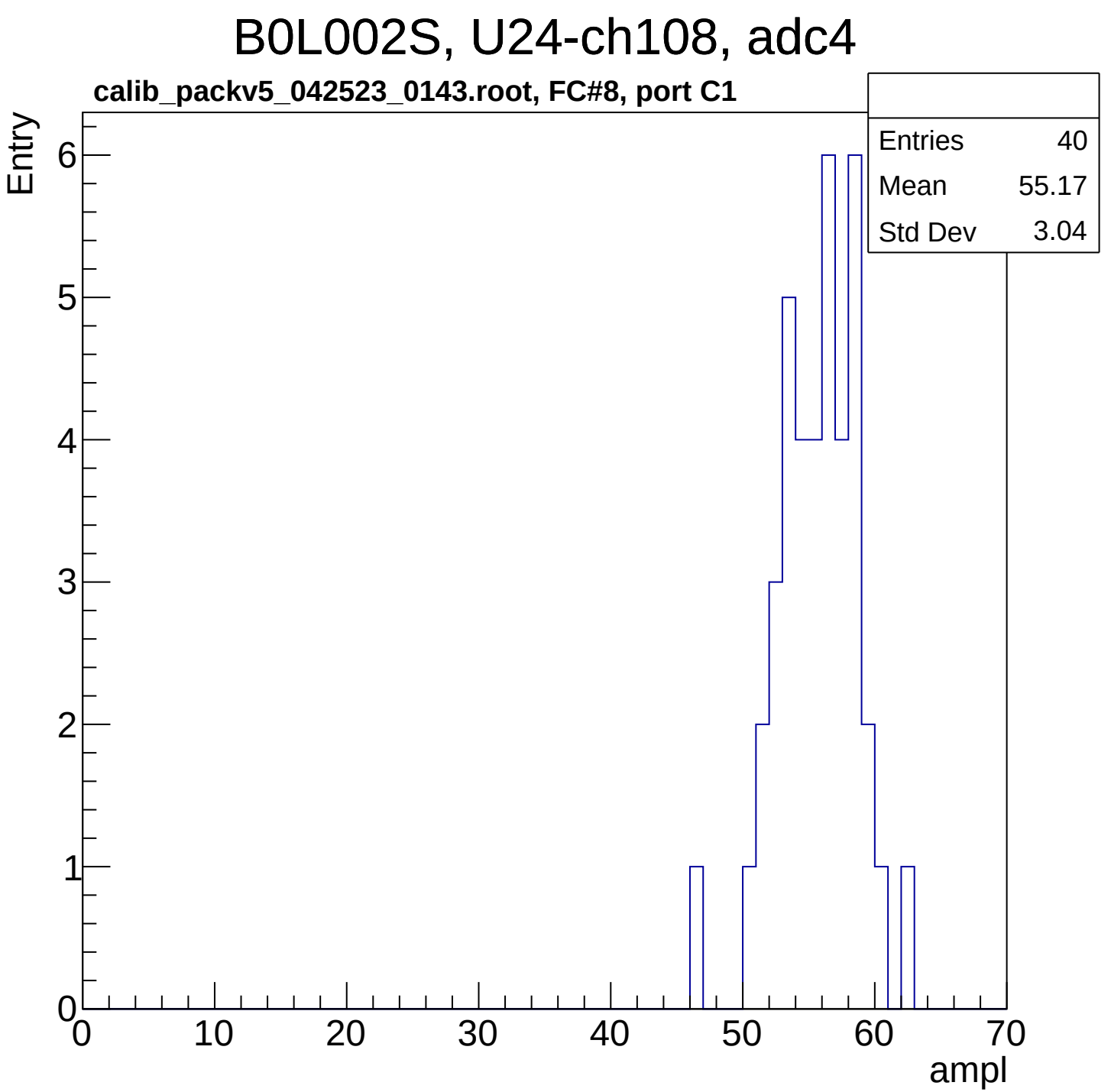

B0L002S, U24-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.64
Std Dev 3.6
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 55.17
Std Dev 3.04
Entry
0
1
2
3
4
5
6
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10
20
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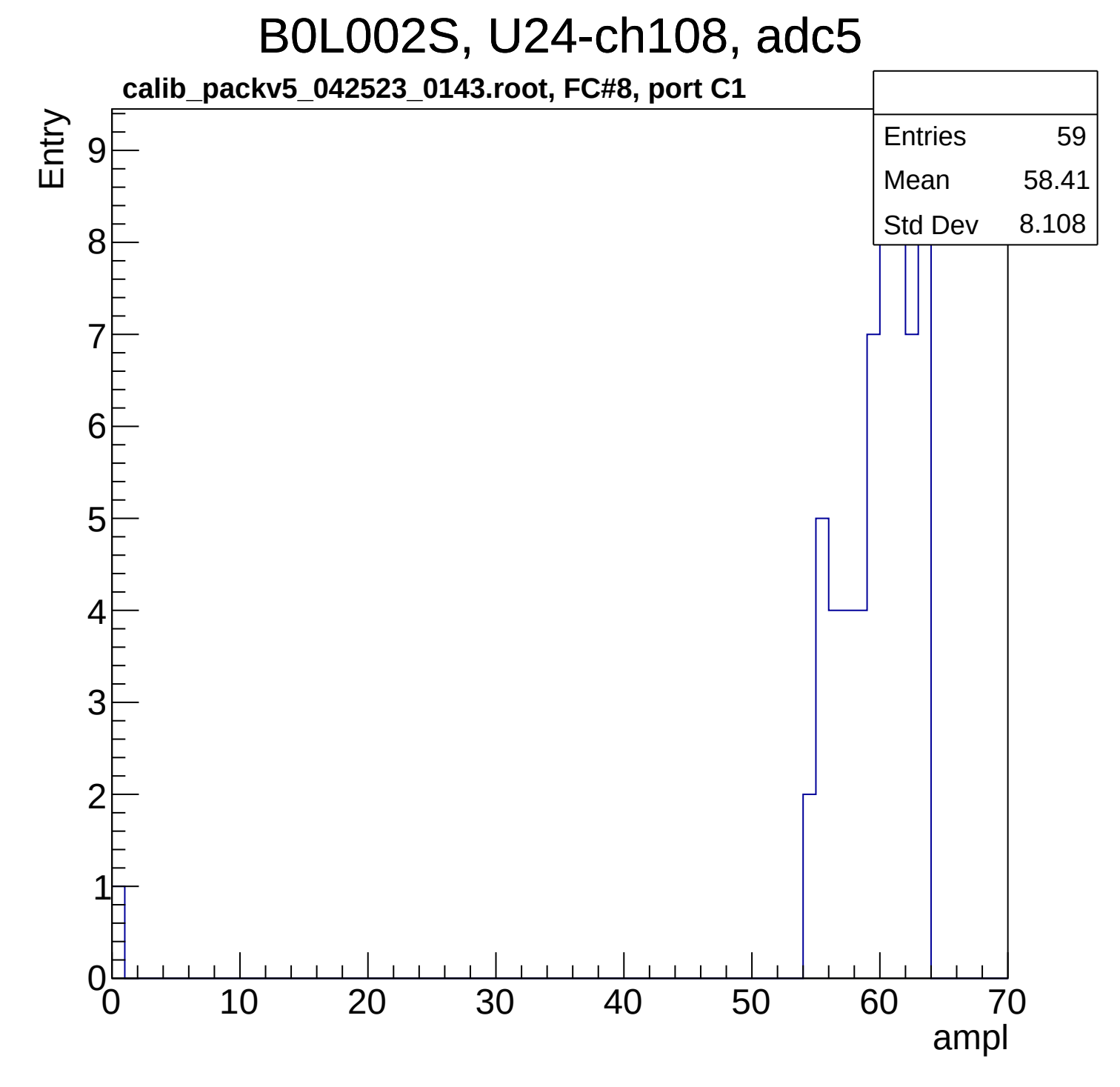
50
60
70
ampl


B0L002S, U24-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 58.41
Std Dev 8.108
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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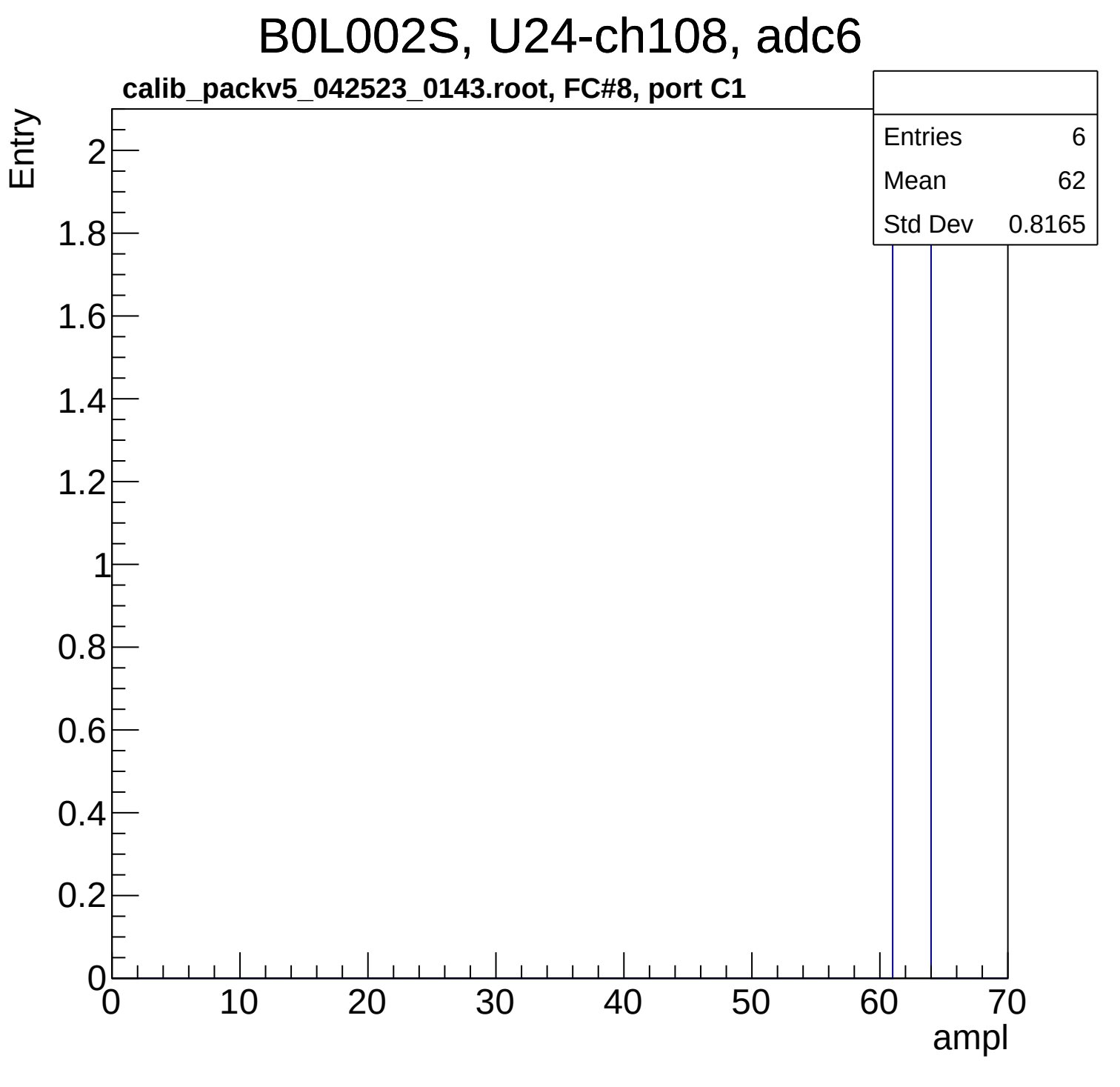
B0L002S, U24-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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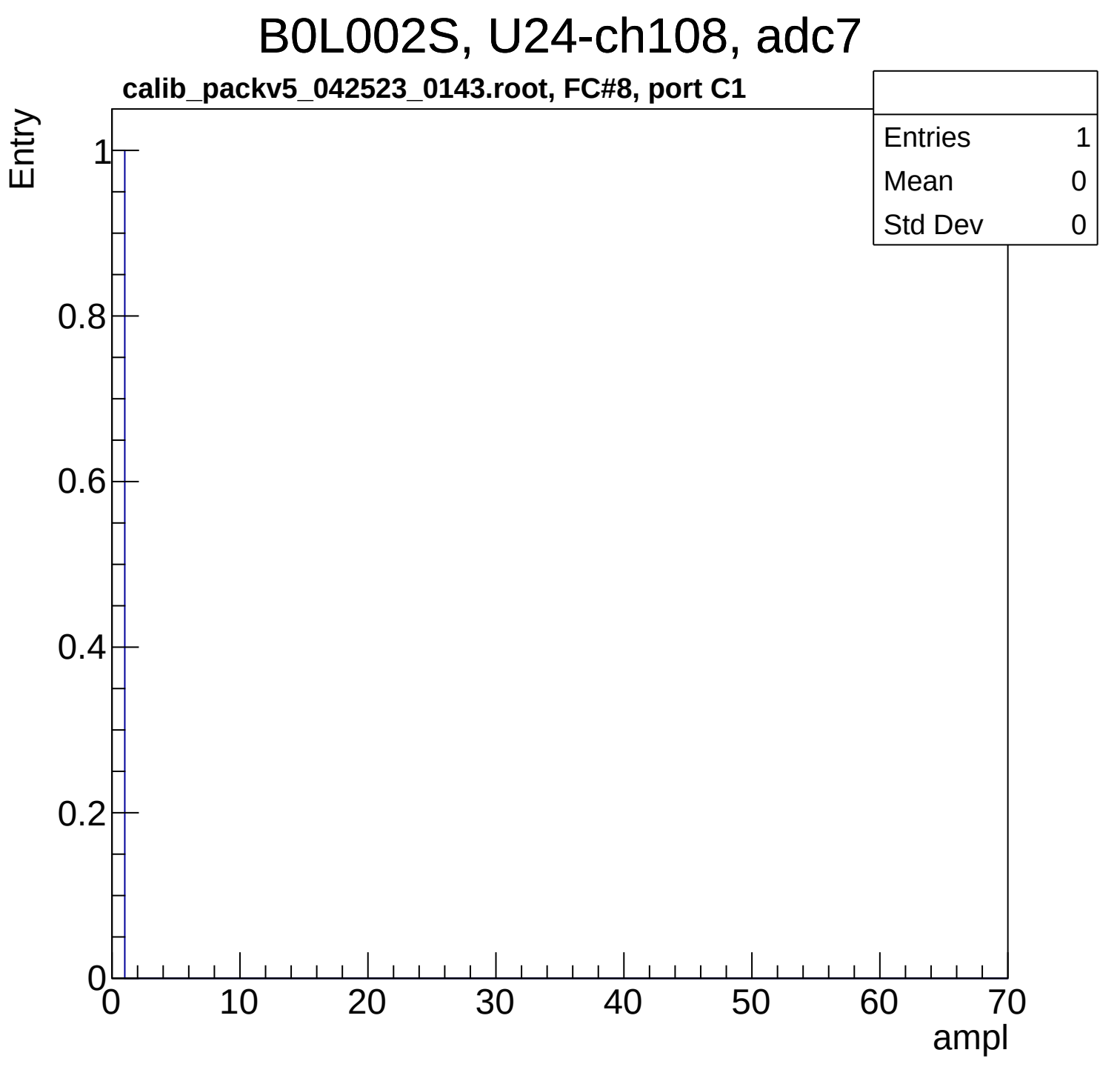
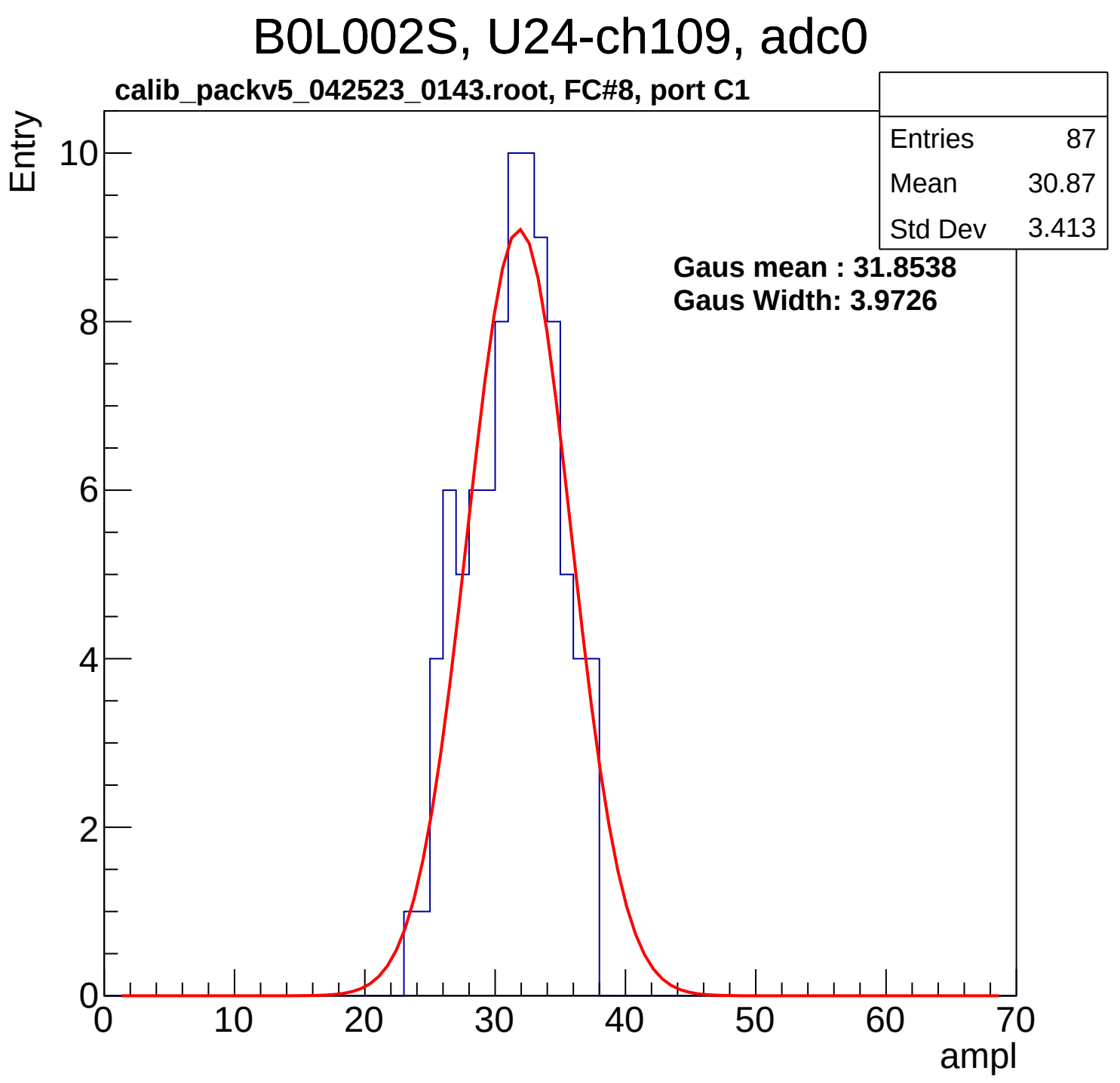
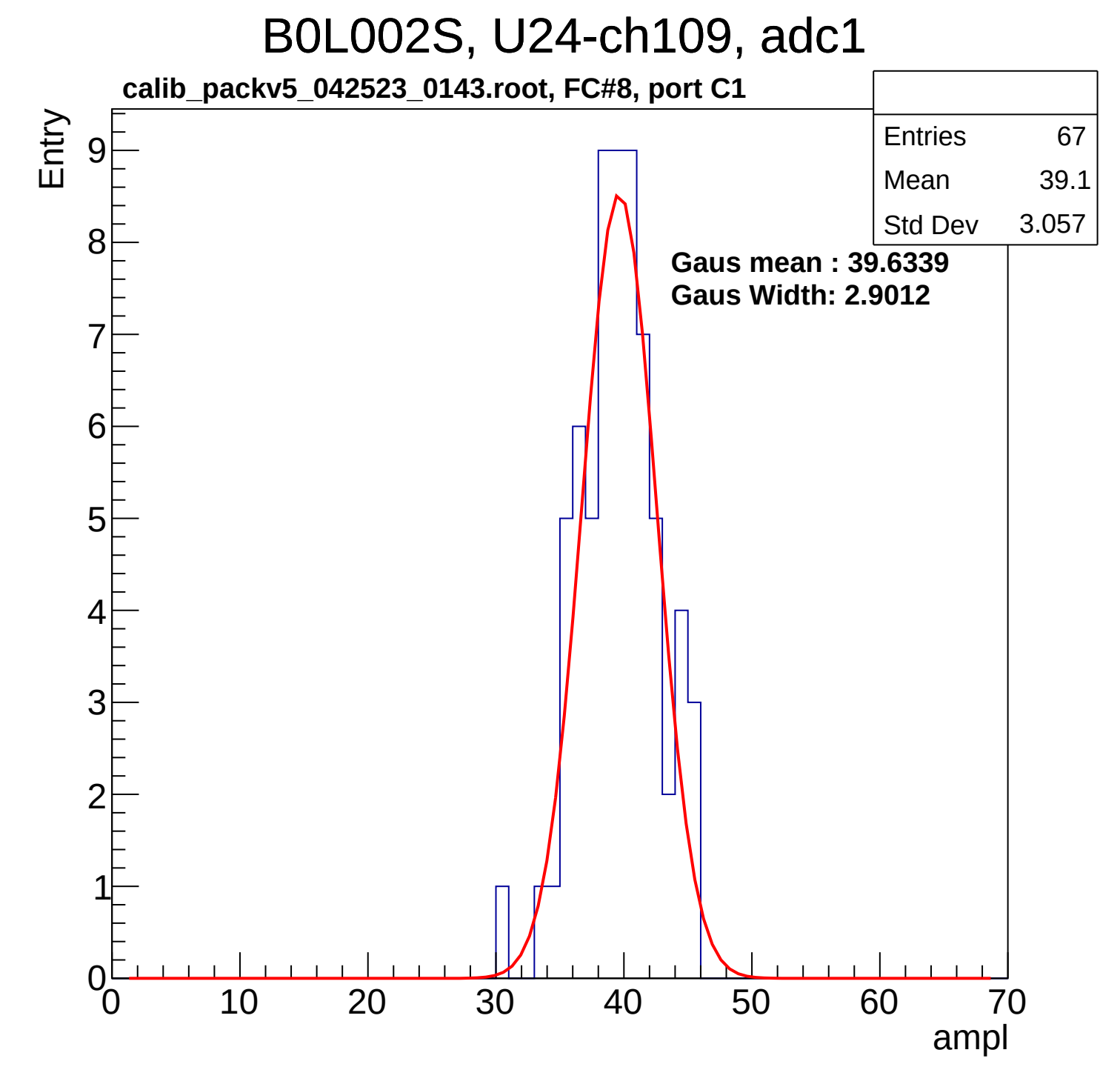
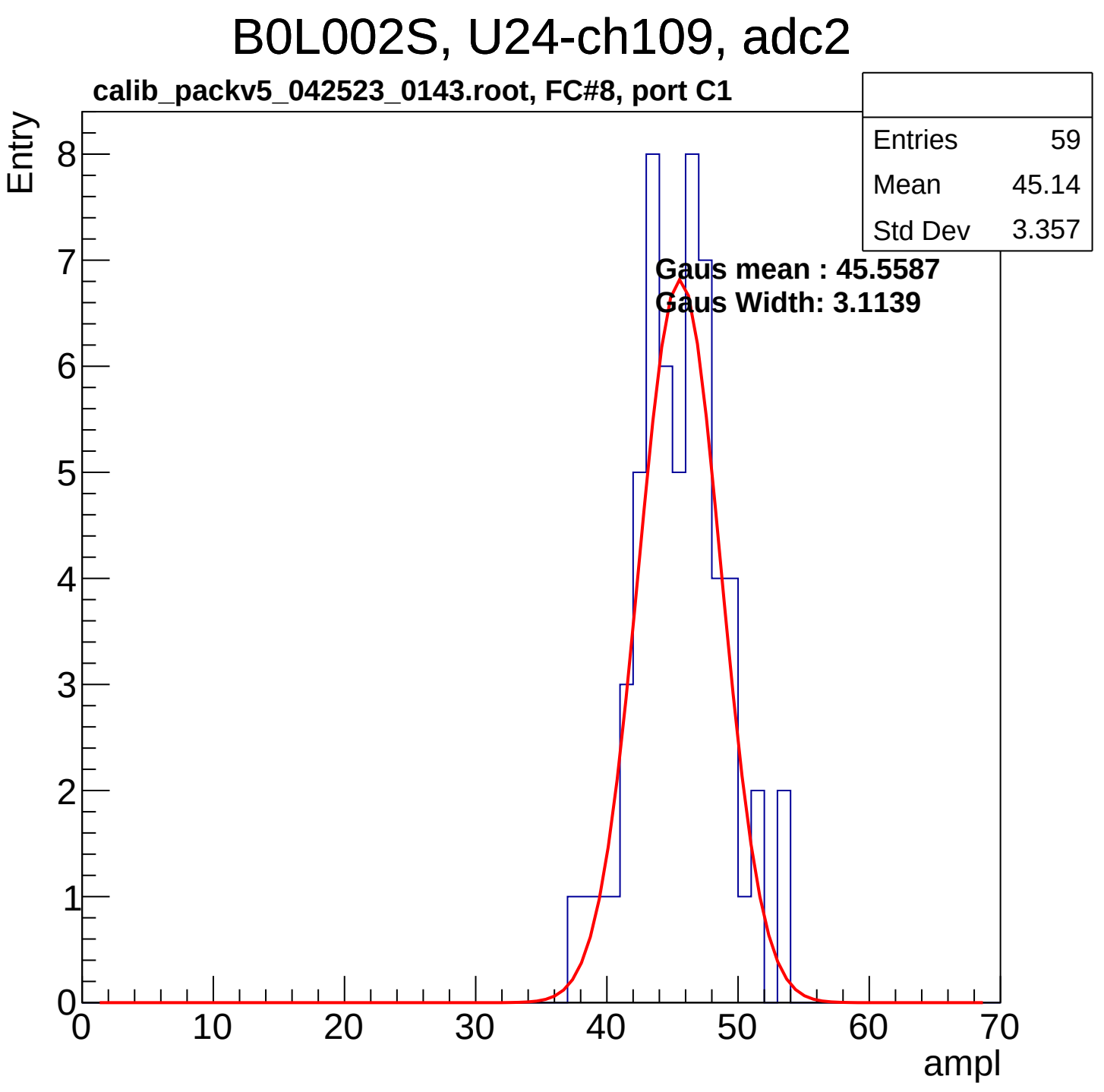

B0L002S, U24-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 30.87
Std Dev 3.413
Gaus mean : 31.8538
Gaus Width: 3.9726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 39.1
Std Dev 3.057
Gaus mean : 39.6339
Gaus Width: 2.9012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 45.14
Std Dev 3.357
Gaus mean : 45.5587
Gaus Width: 3.1139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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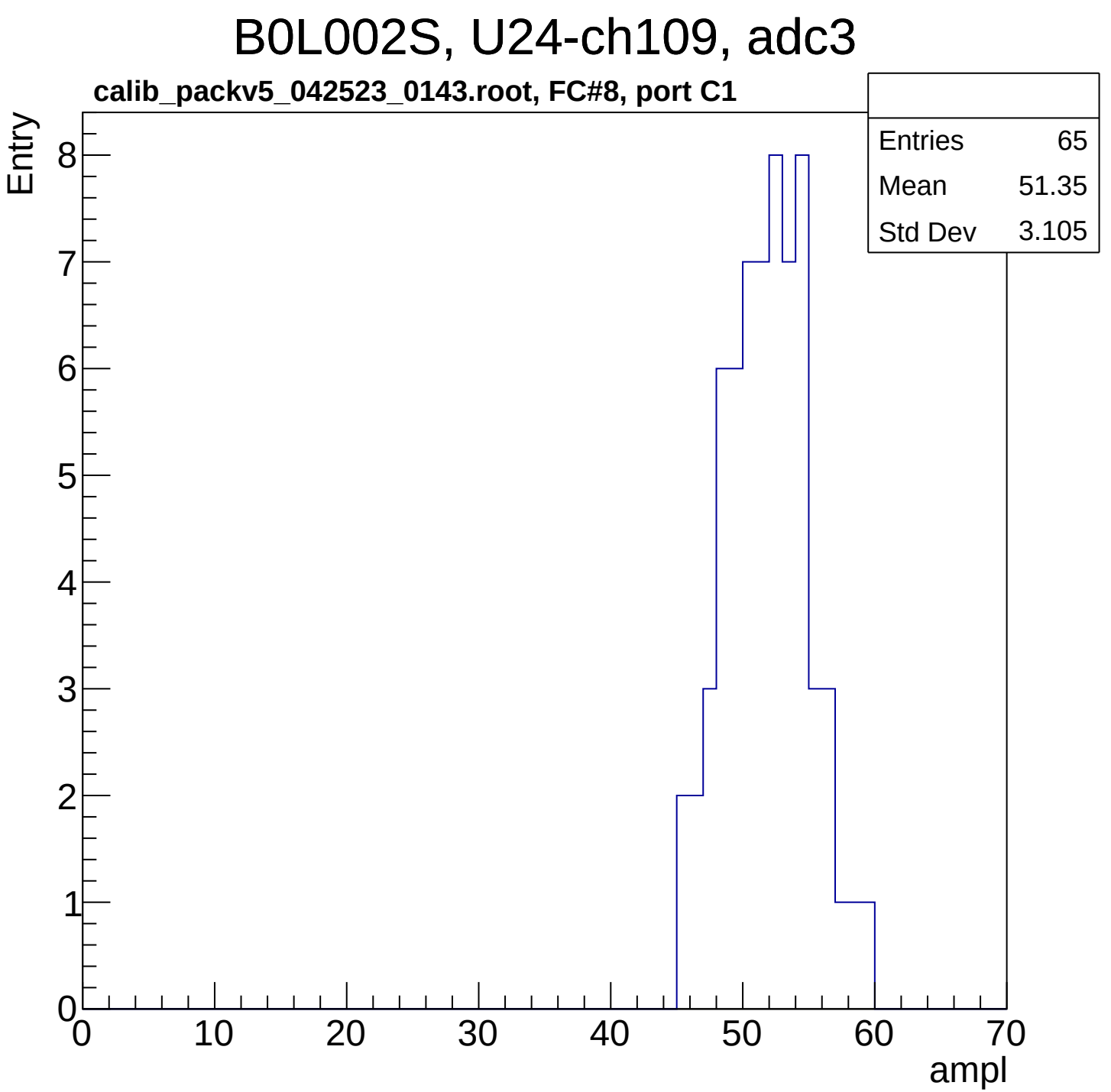
B0L002S, U24-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.35
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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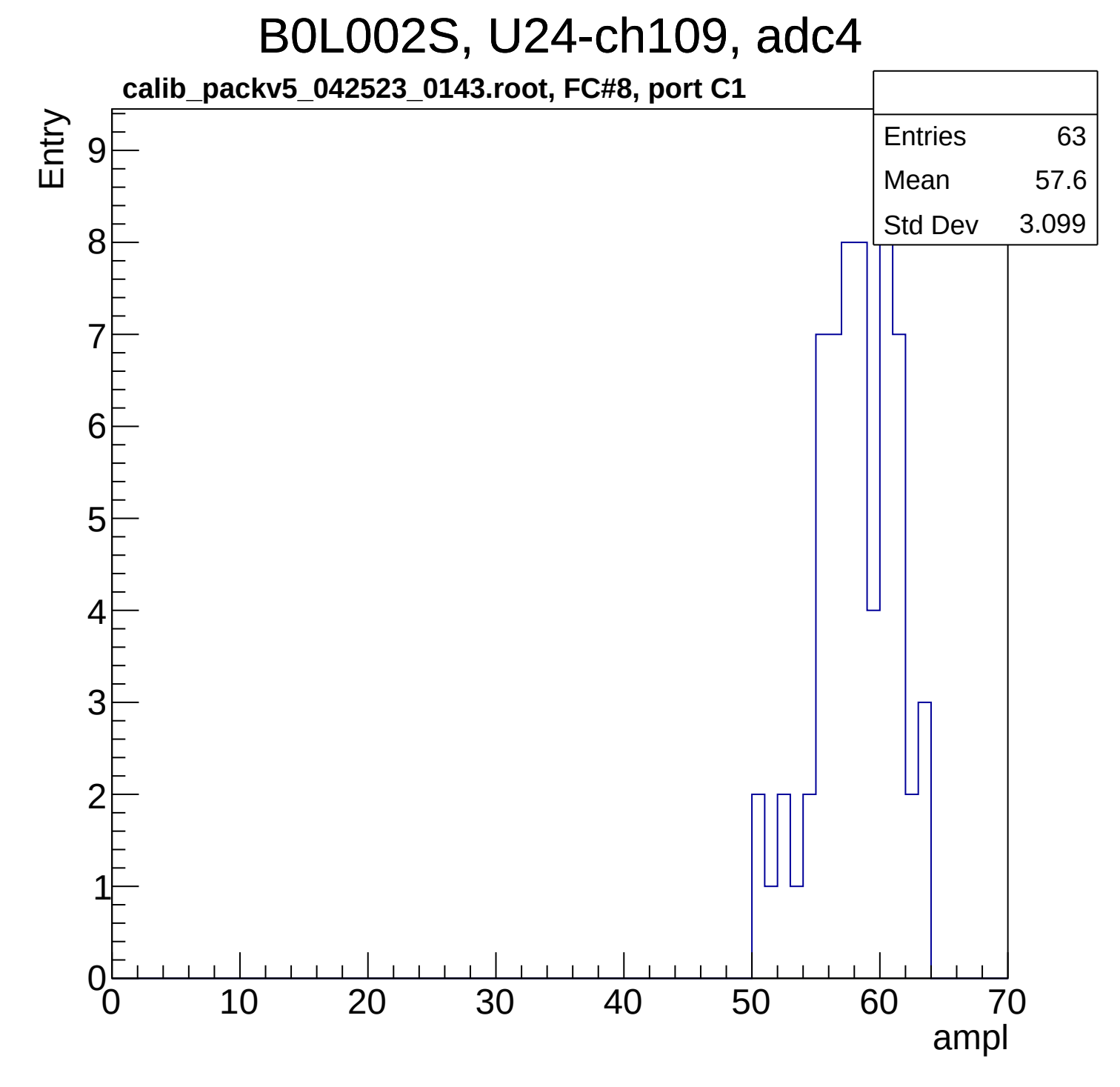
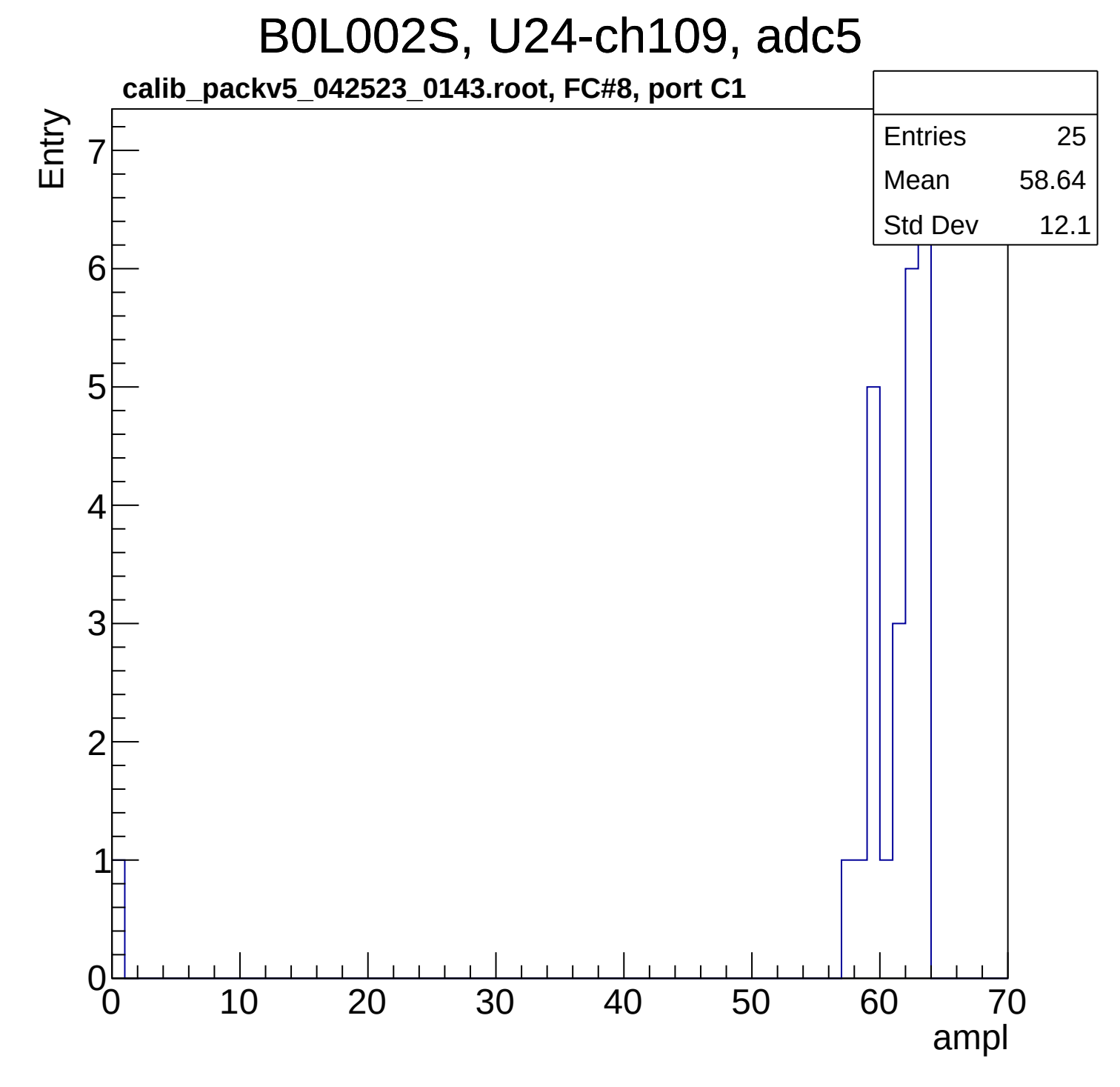
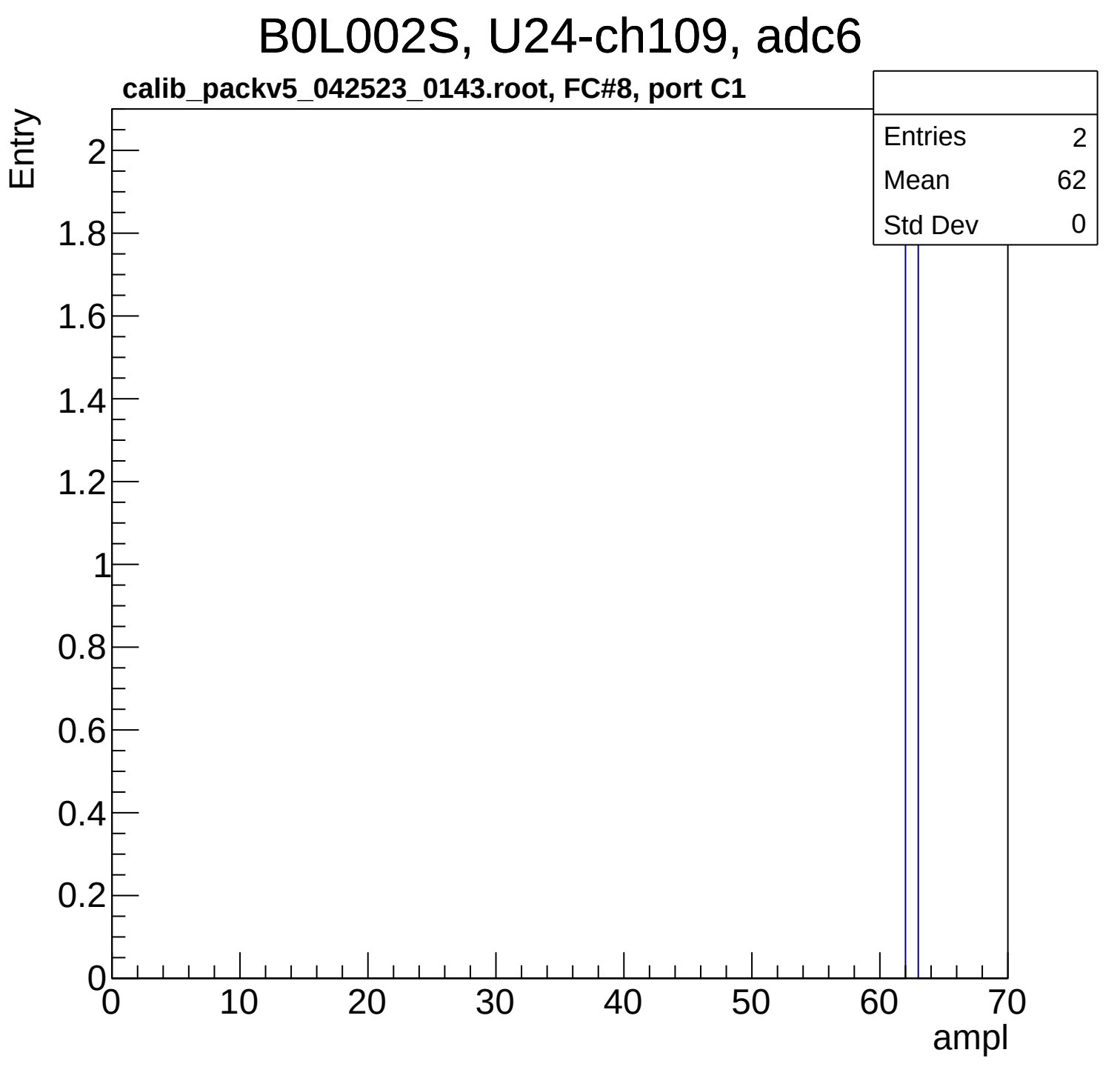
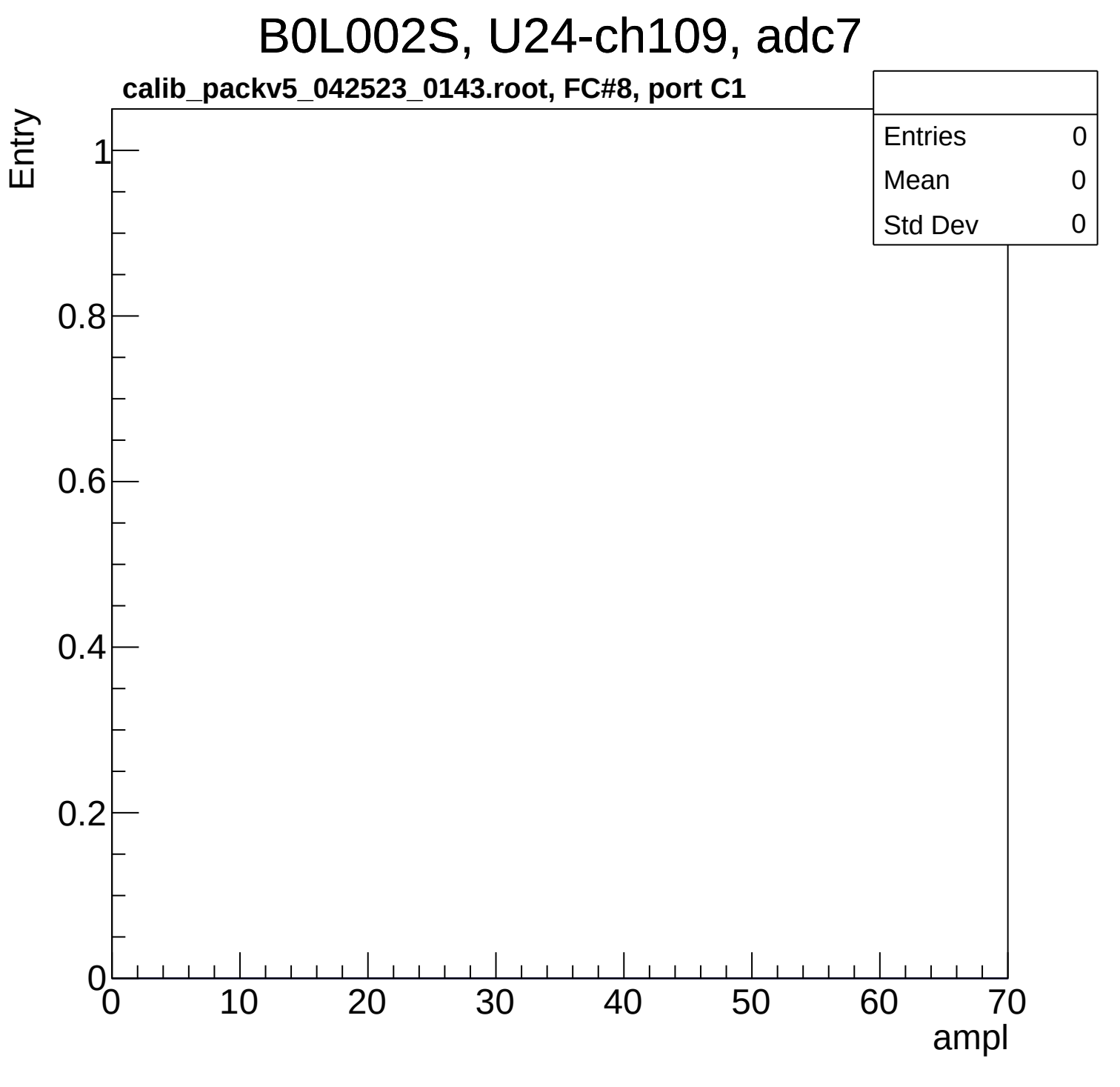
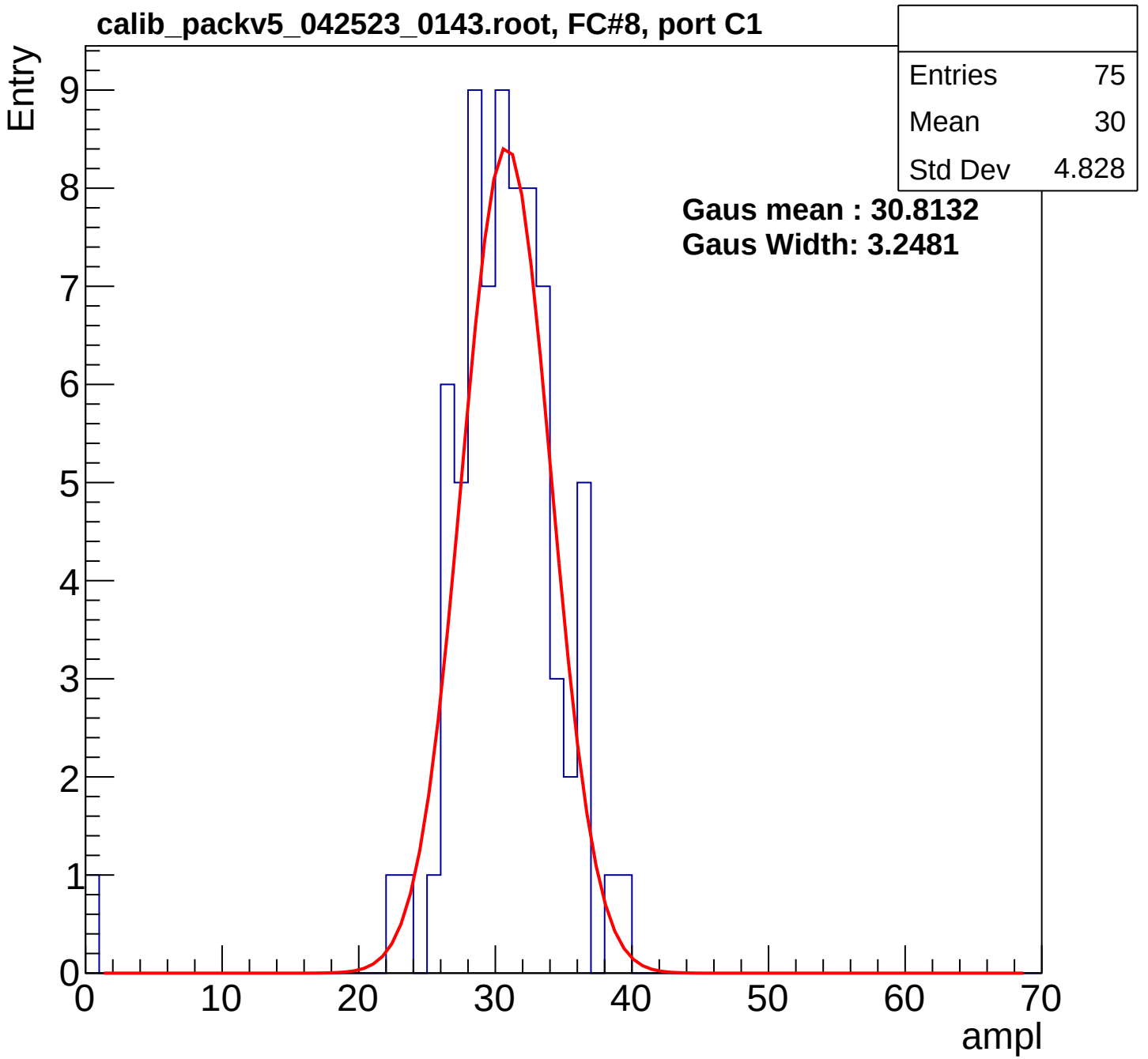
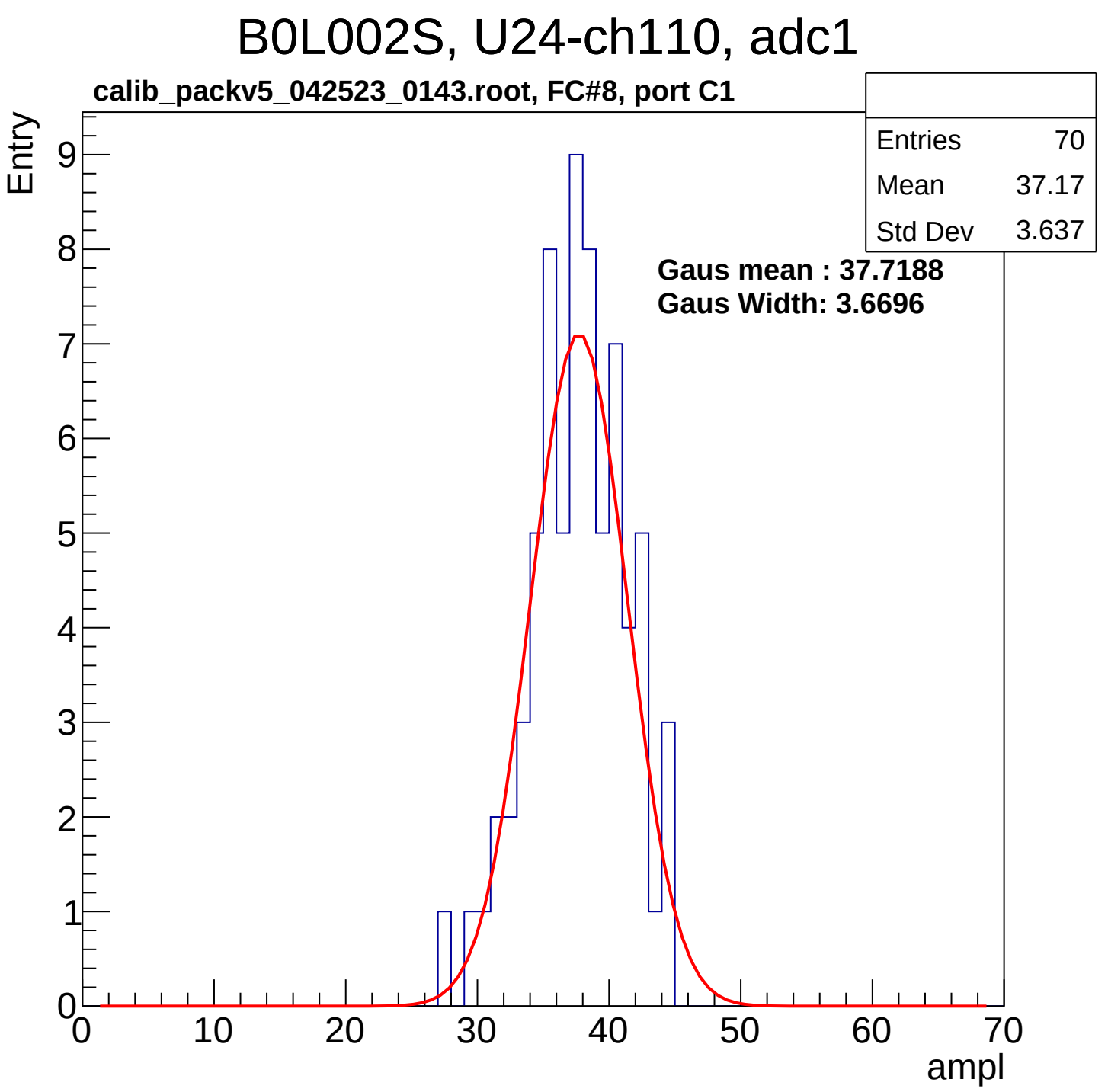

B0L002S, U24-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.6
Std Dev 3.099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.64
Std Dev 12.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30
Std Dev 4.828
Gaus mean : 30.8132
Gaus Width: 3.2481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.17
Std Dev 3.637
Gaus mean : 37.7188
Gaus Width: 3.6696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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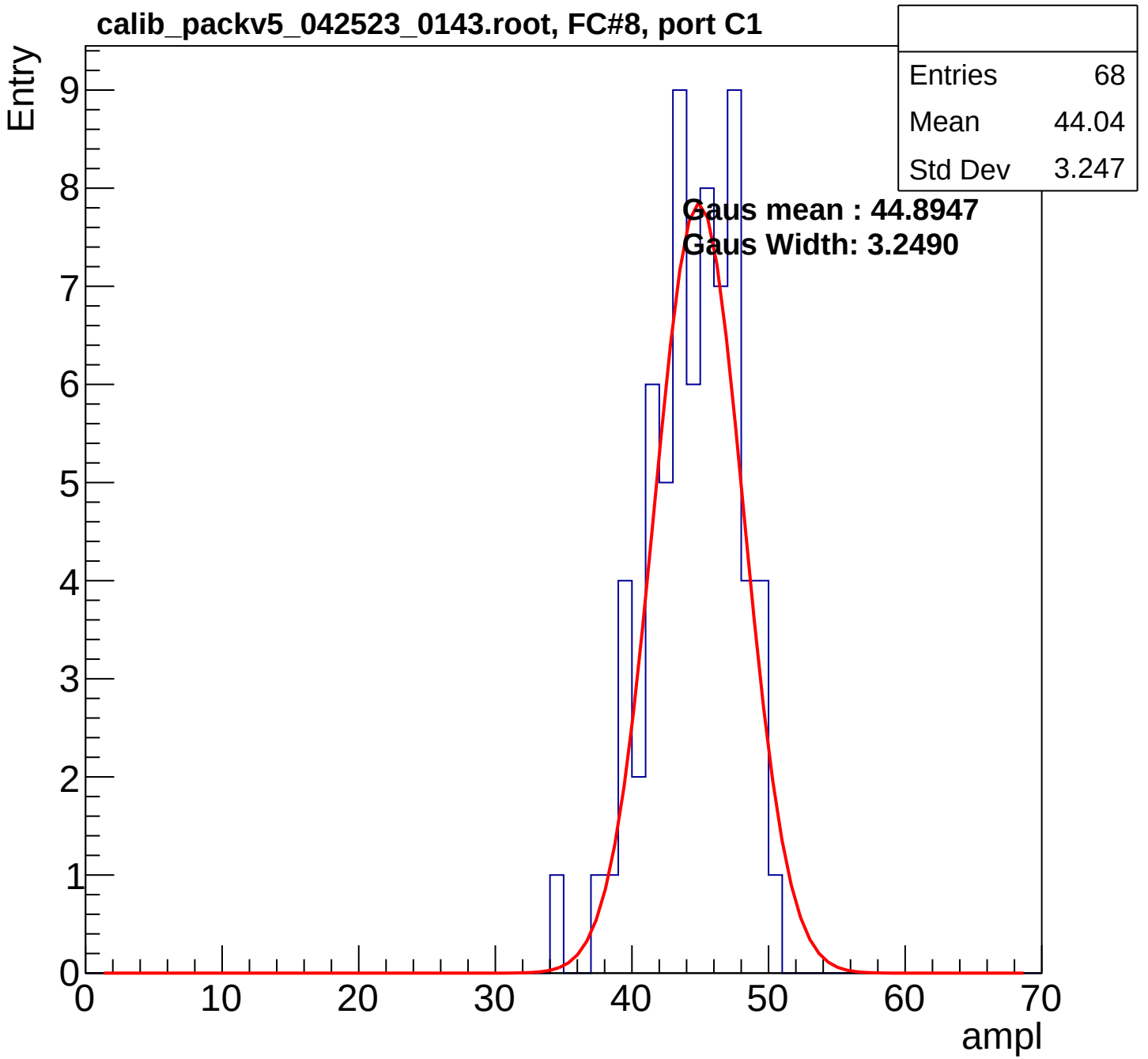
B0L002S, U24-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.04
Std Dev 3.247
Gaus mean : 44.8947
Gaus Width: 3.2490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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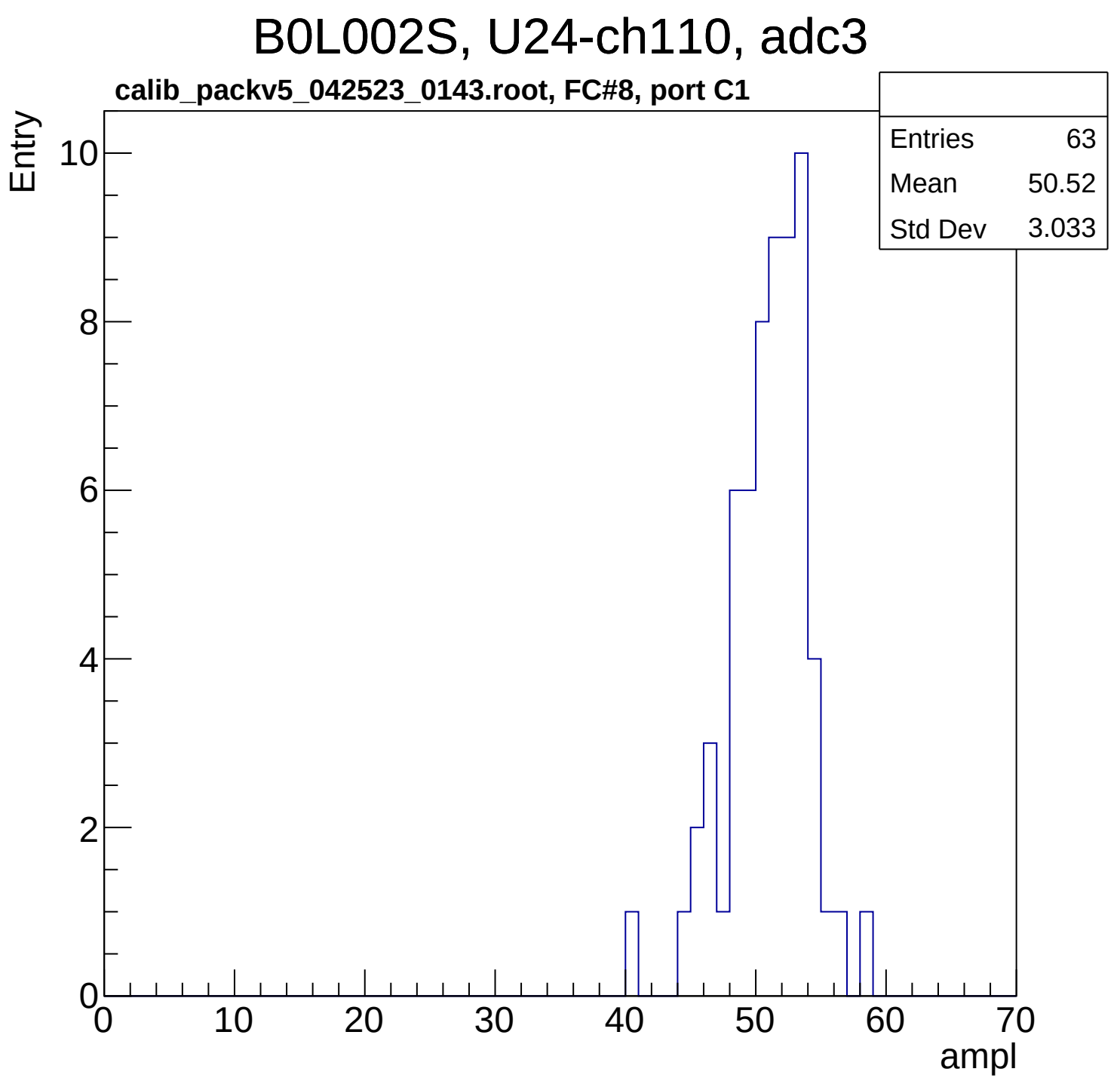
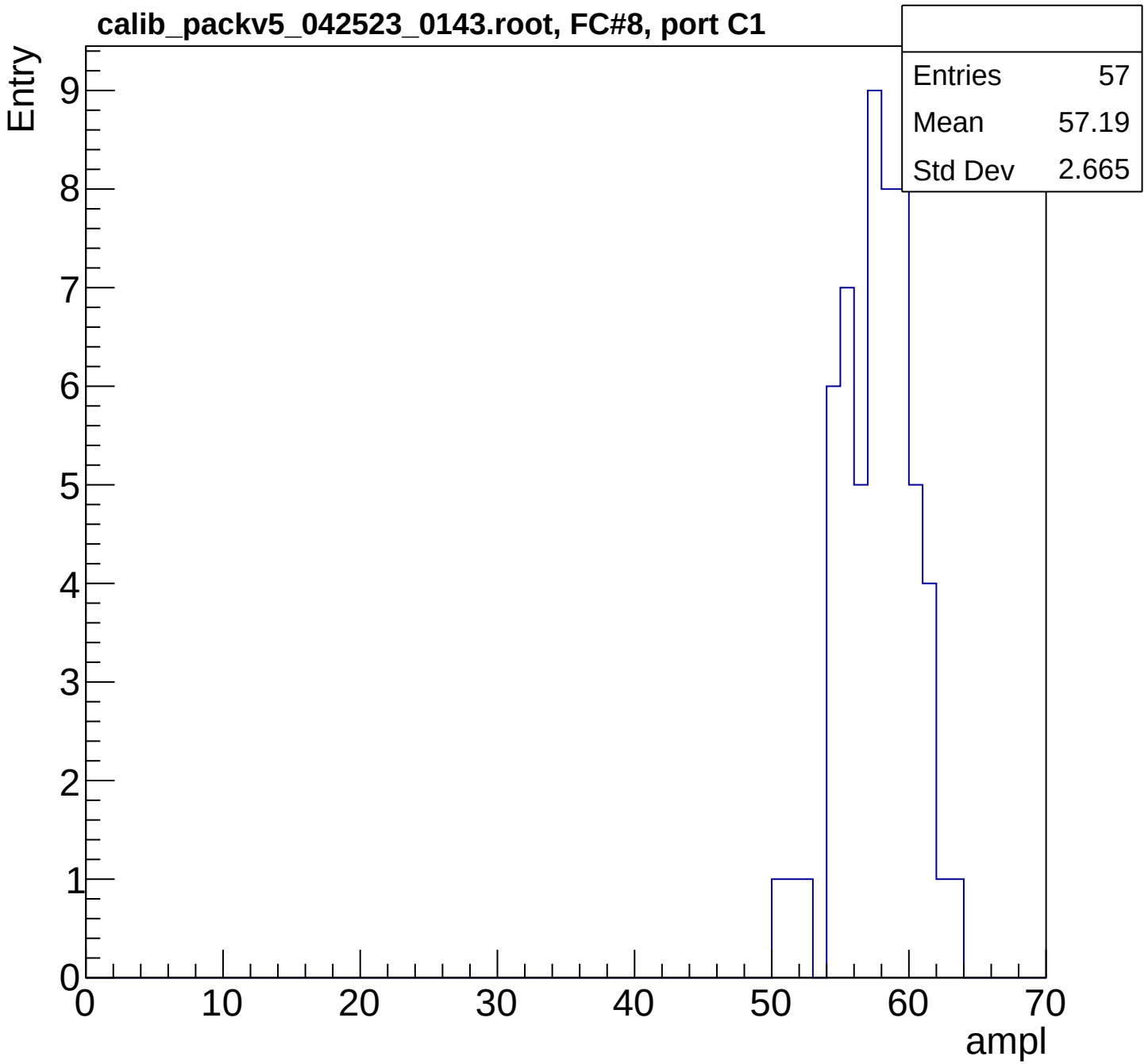

B0L002S, U24-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.52
Std Dev 3.033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.19
Std Dev 2.665
Entry
ampl
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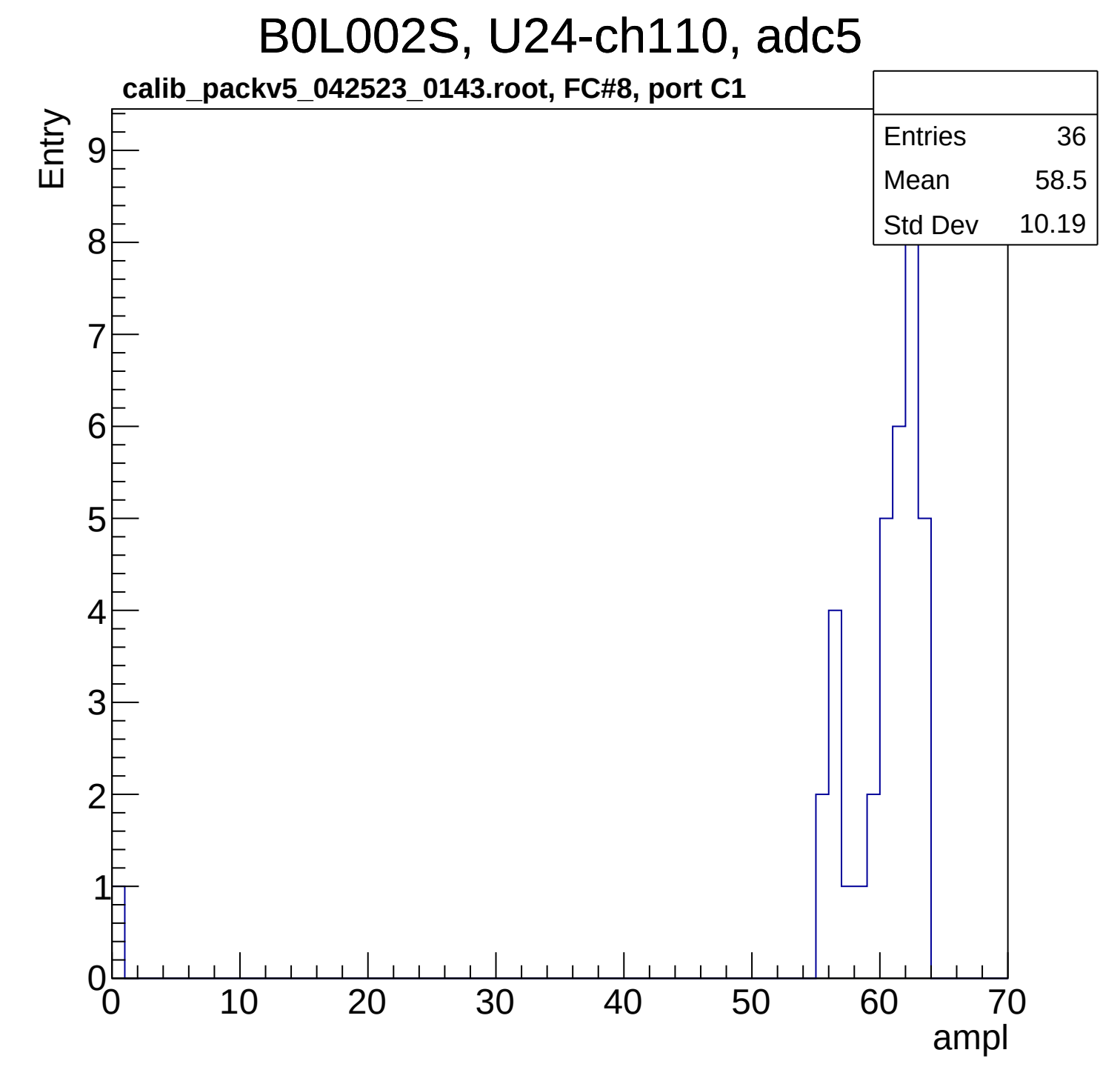
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.5
Std Dev 10.19
Entry
ampl
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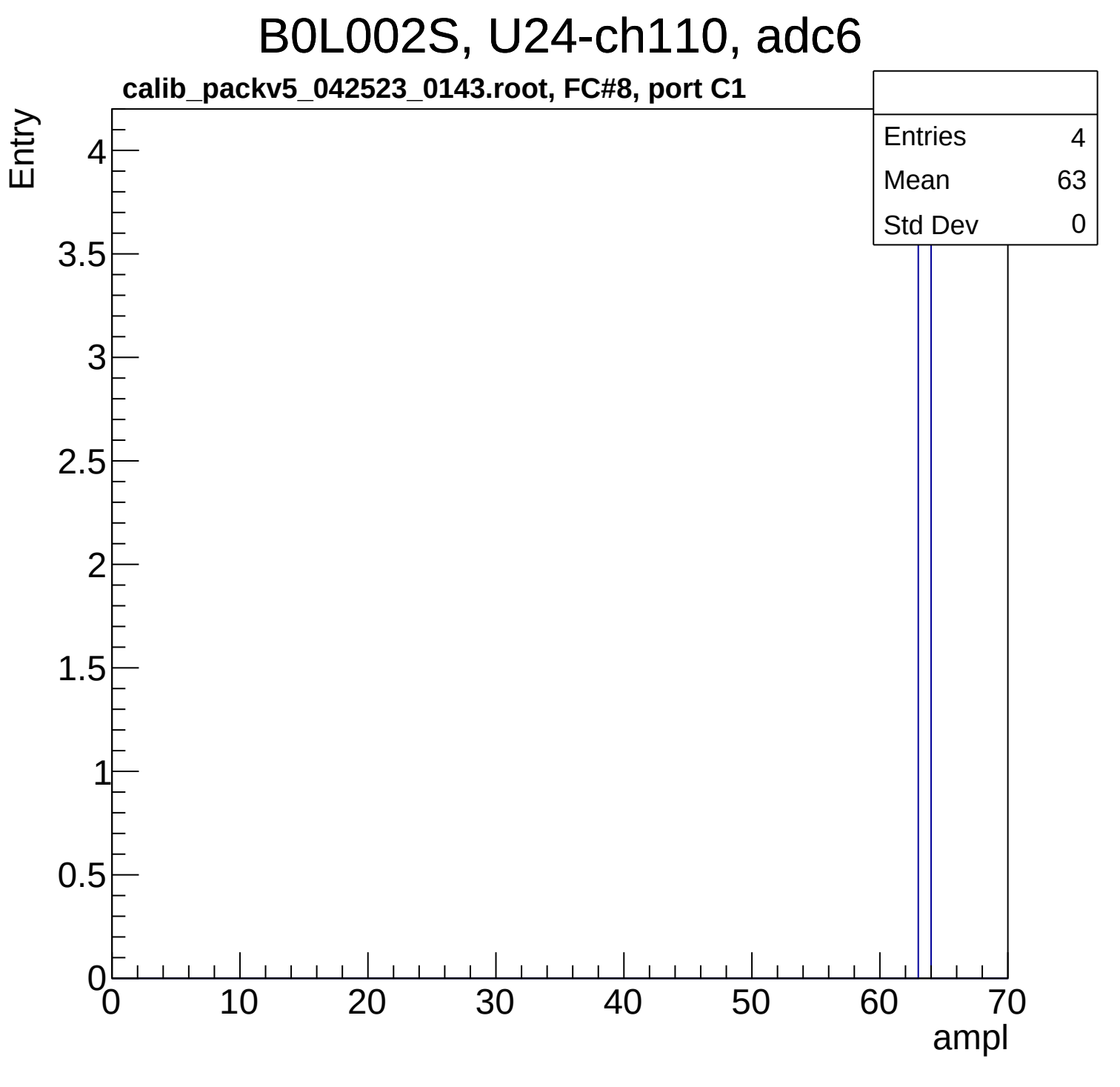
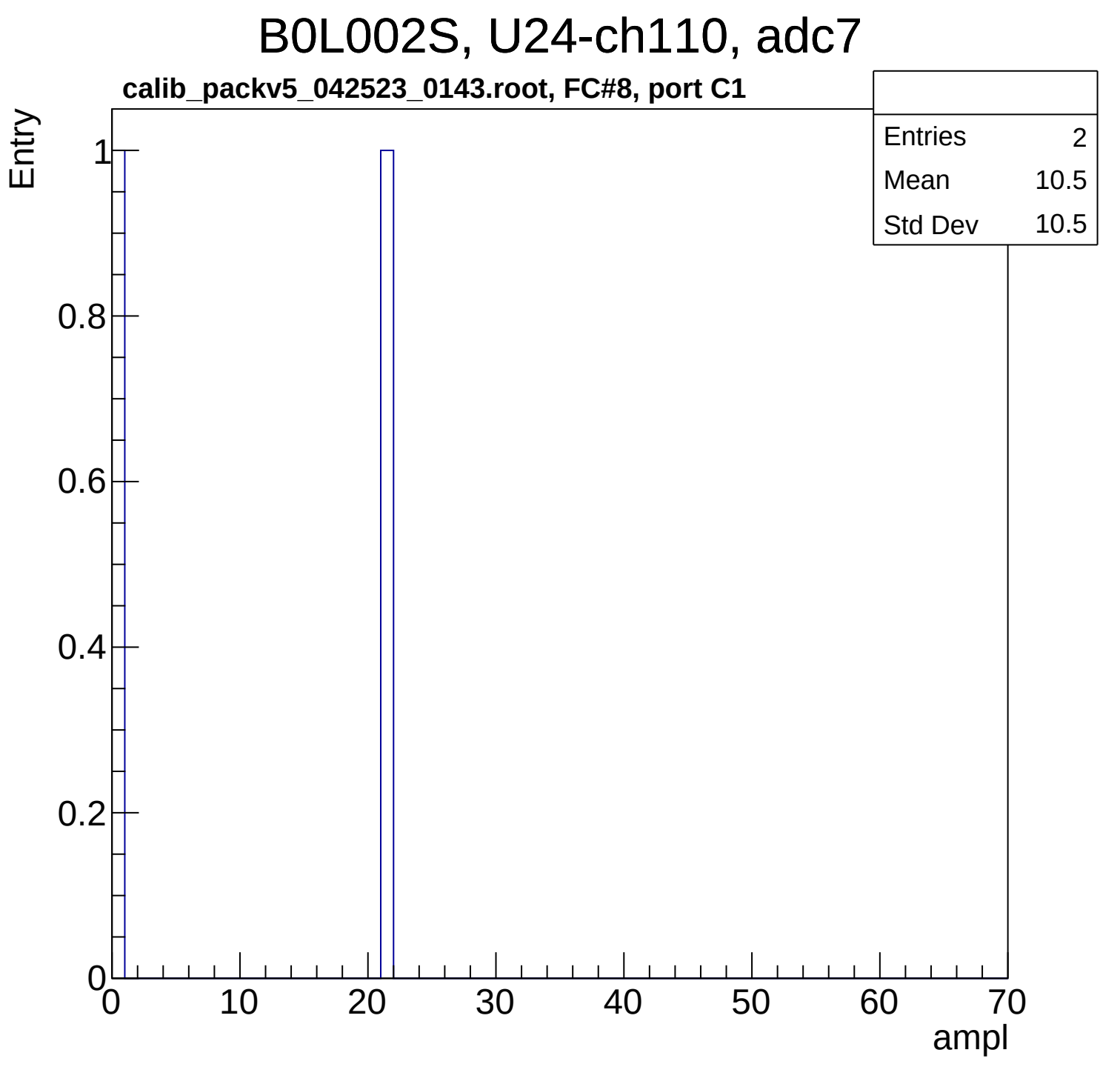
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
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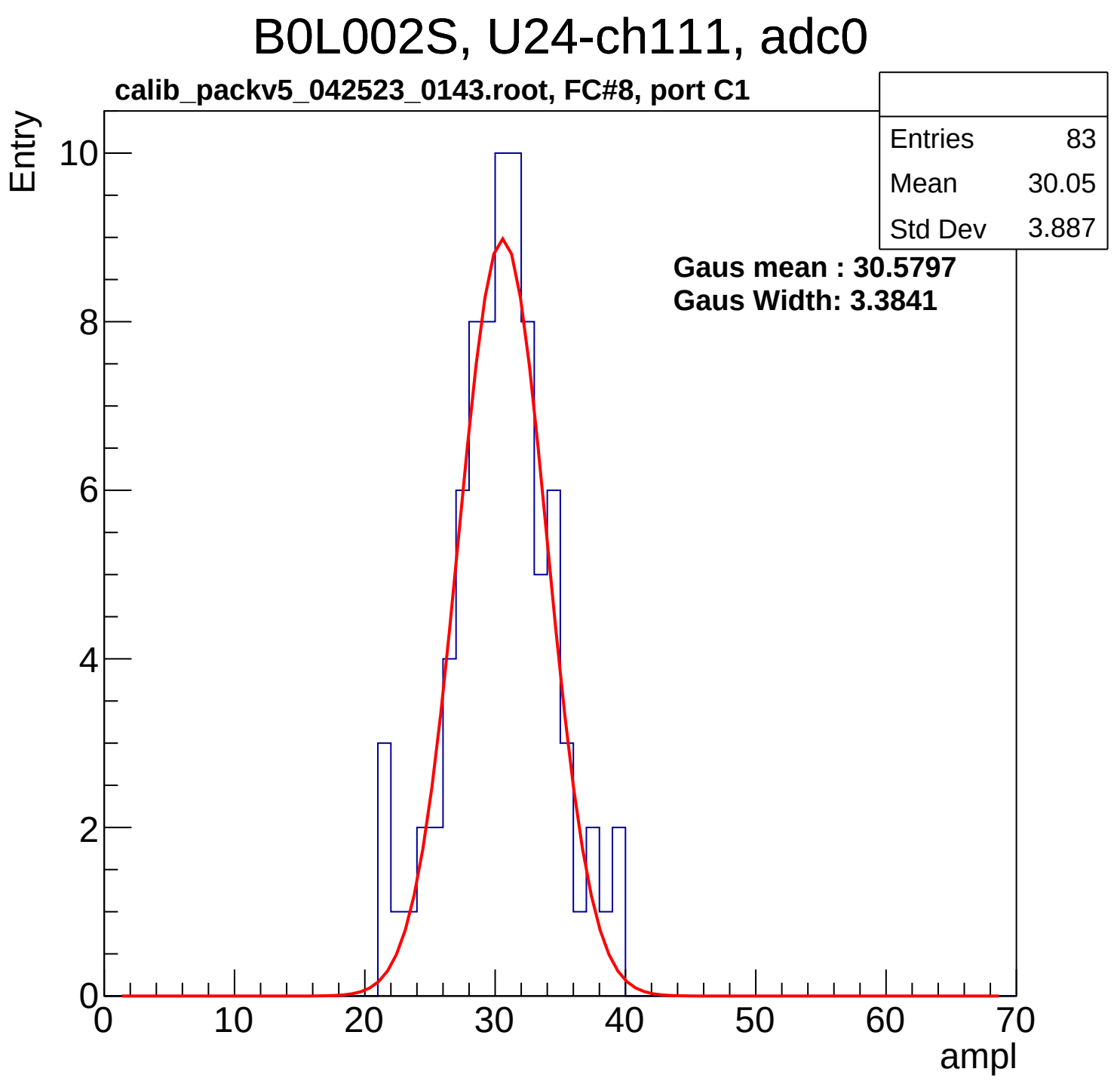
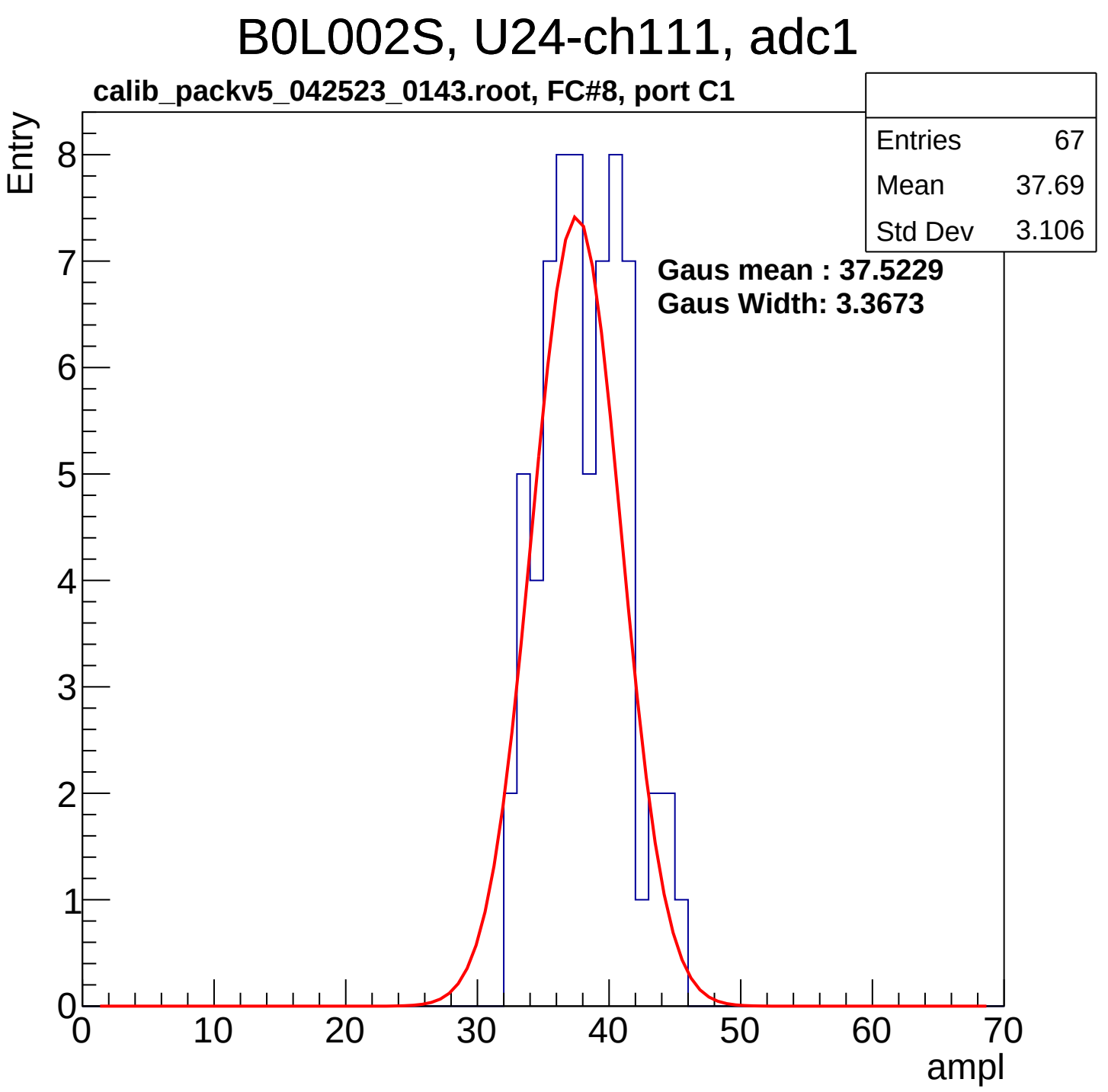
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L002S, U24-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 30.05
Std Dev 3.887
Gaus mean : 30.5797
Gaus Width: 3.3841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.69
Std Dev 3.106
Gaus mean : 37.5229
Gaus Width: 3.3673
Entry
ampl
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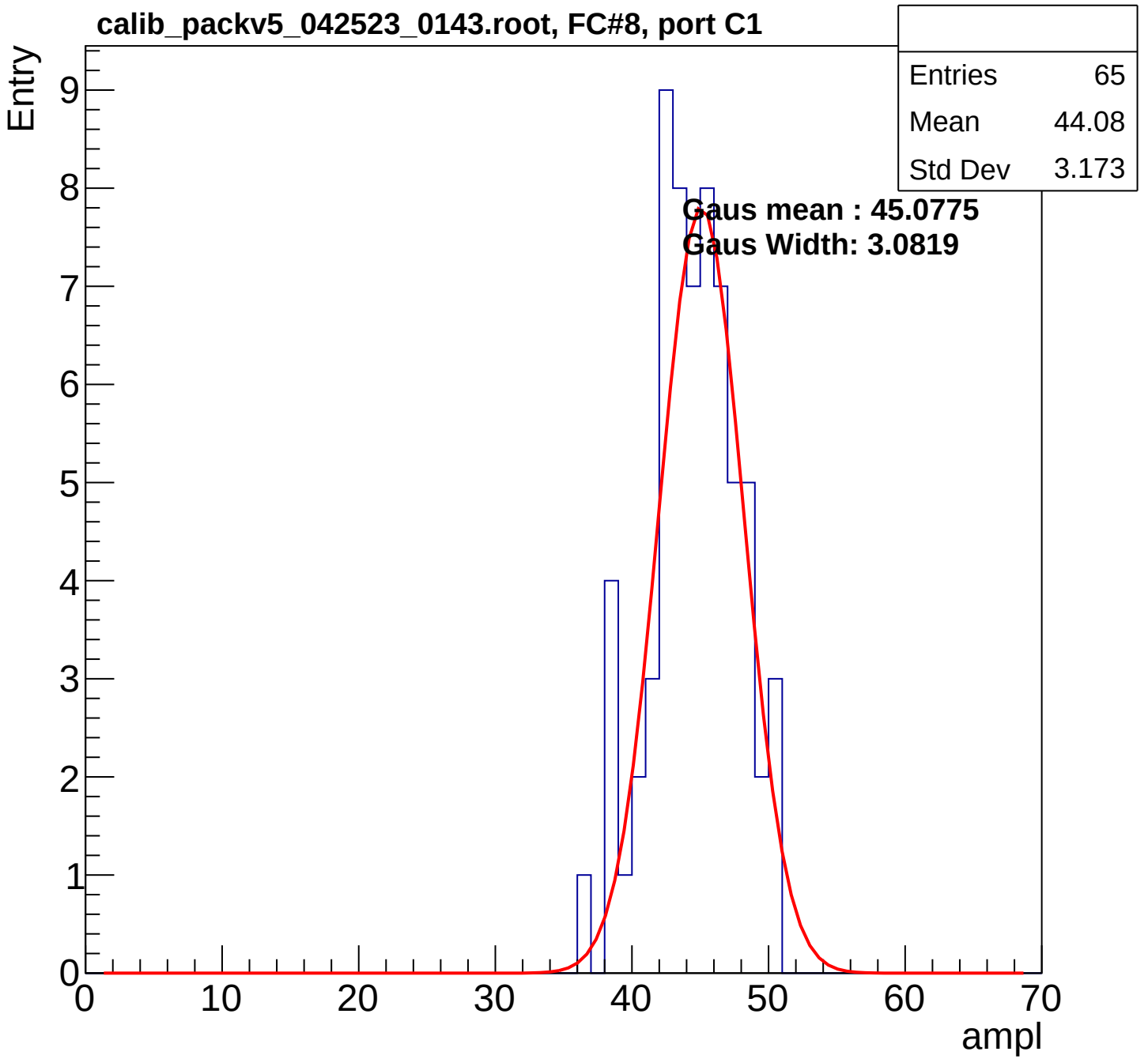
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.08
Std Dev 3.173
Gaus mean : 45.0775
Gaus Width: 3.0819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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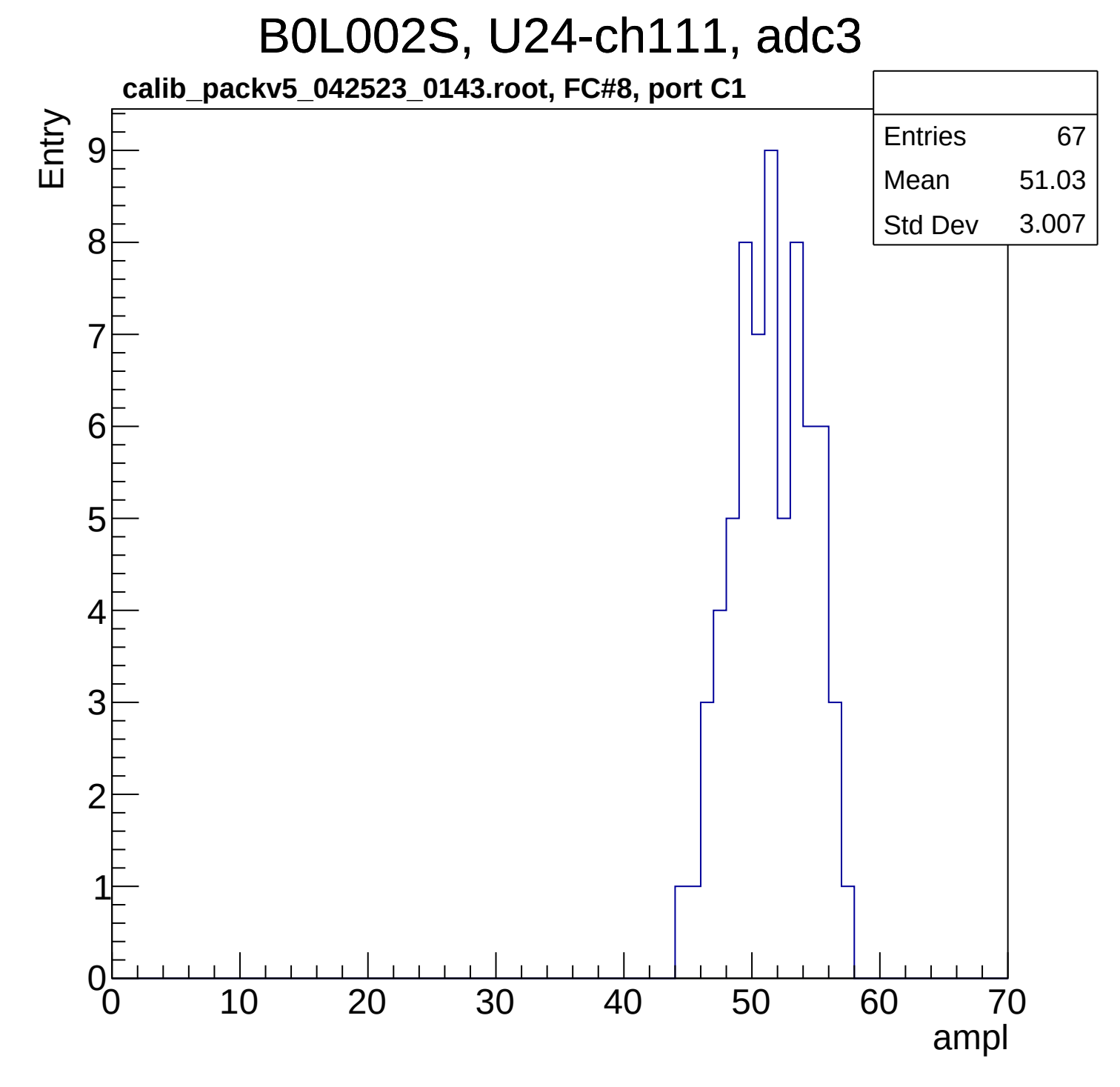
B0L002S, U24-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.03
Std Dev 3.007
Entry
ampl
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3
4
5
6
7
8
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0
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60
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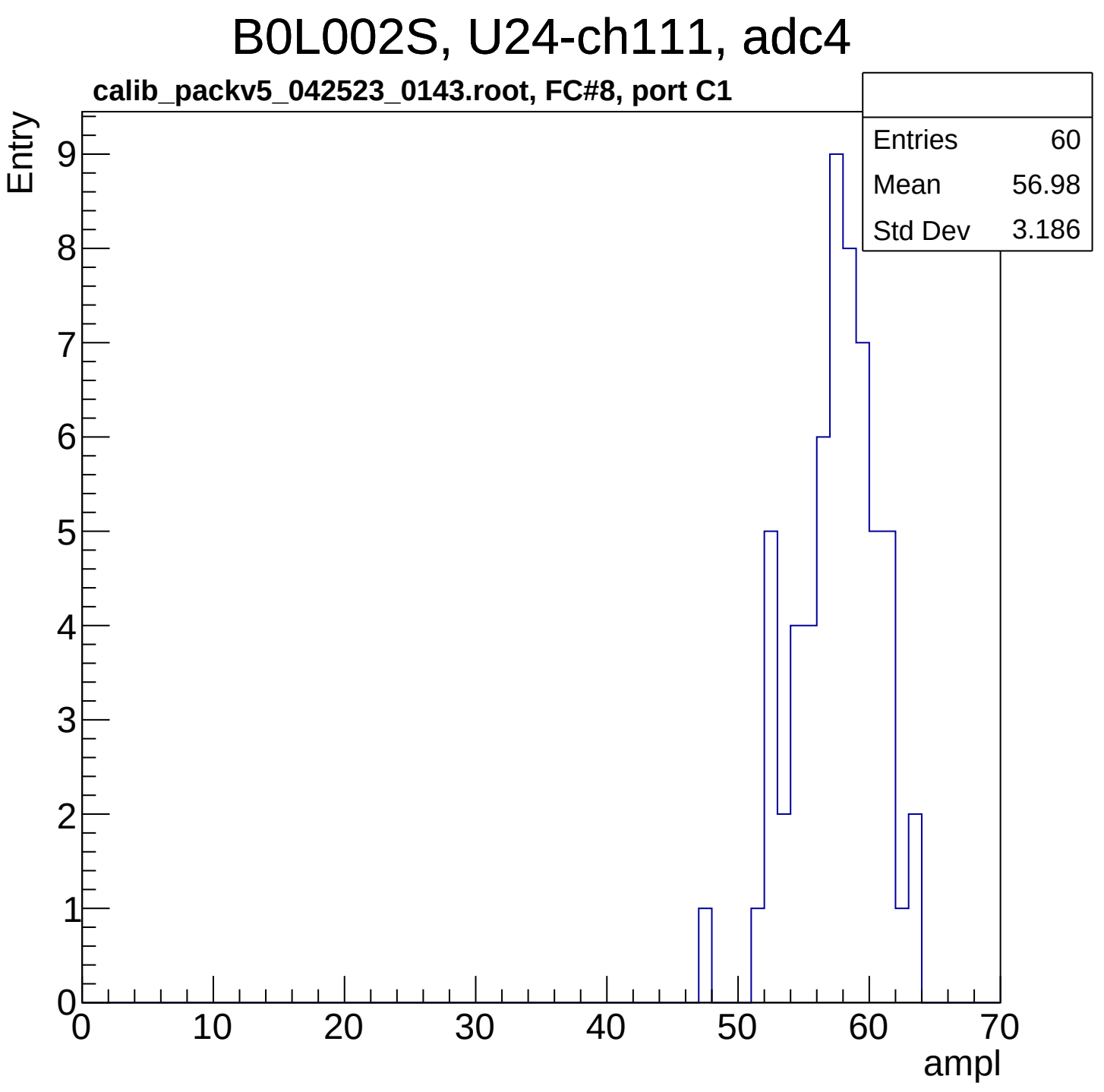
B0L002S, U24-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.98
Std Dev 3.186
Entry
ampl
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2
3
4
5
6
7
8
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0
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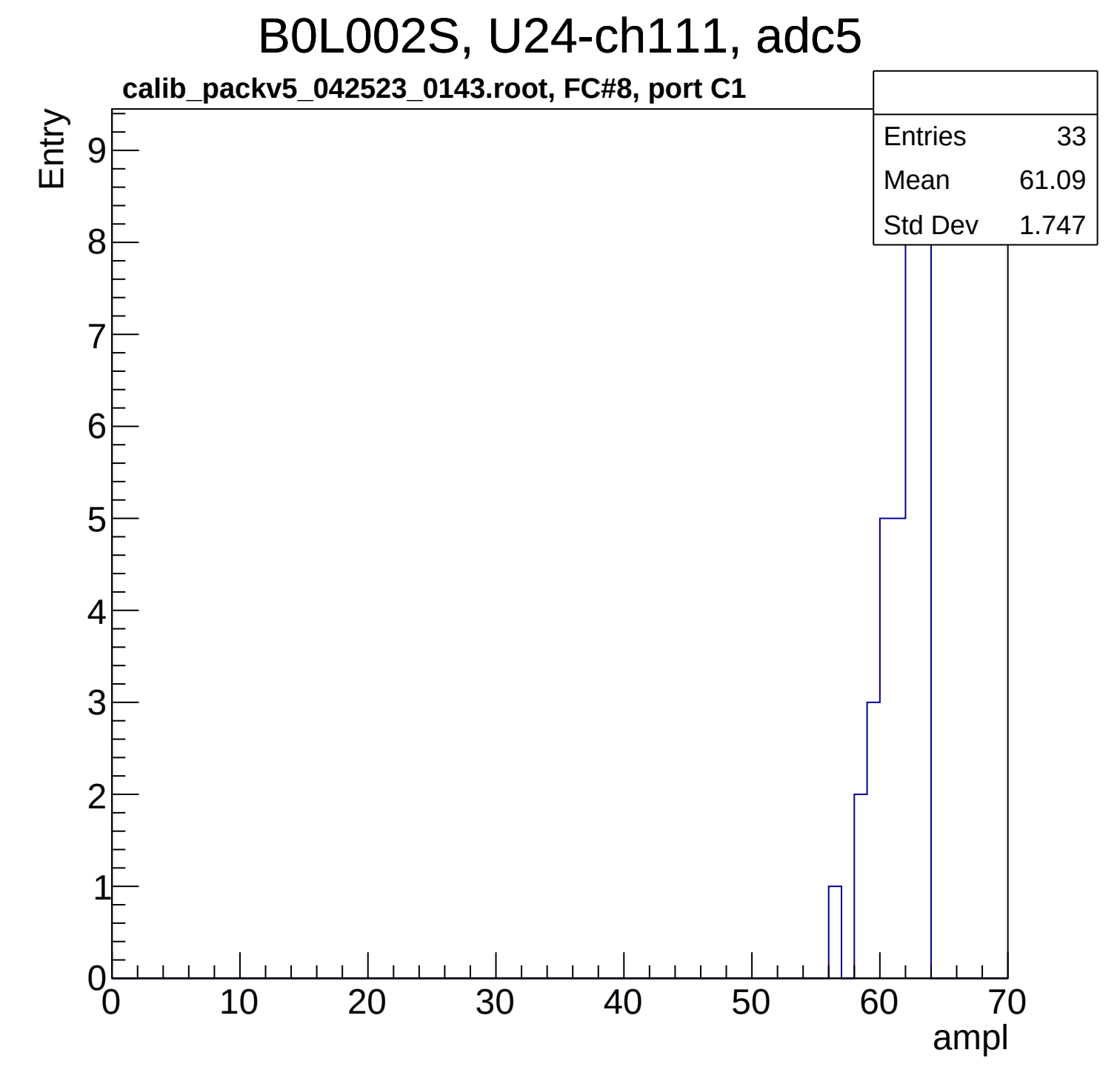
B0L002S, U24-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 61.09
Std Dev 1.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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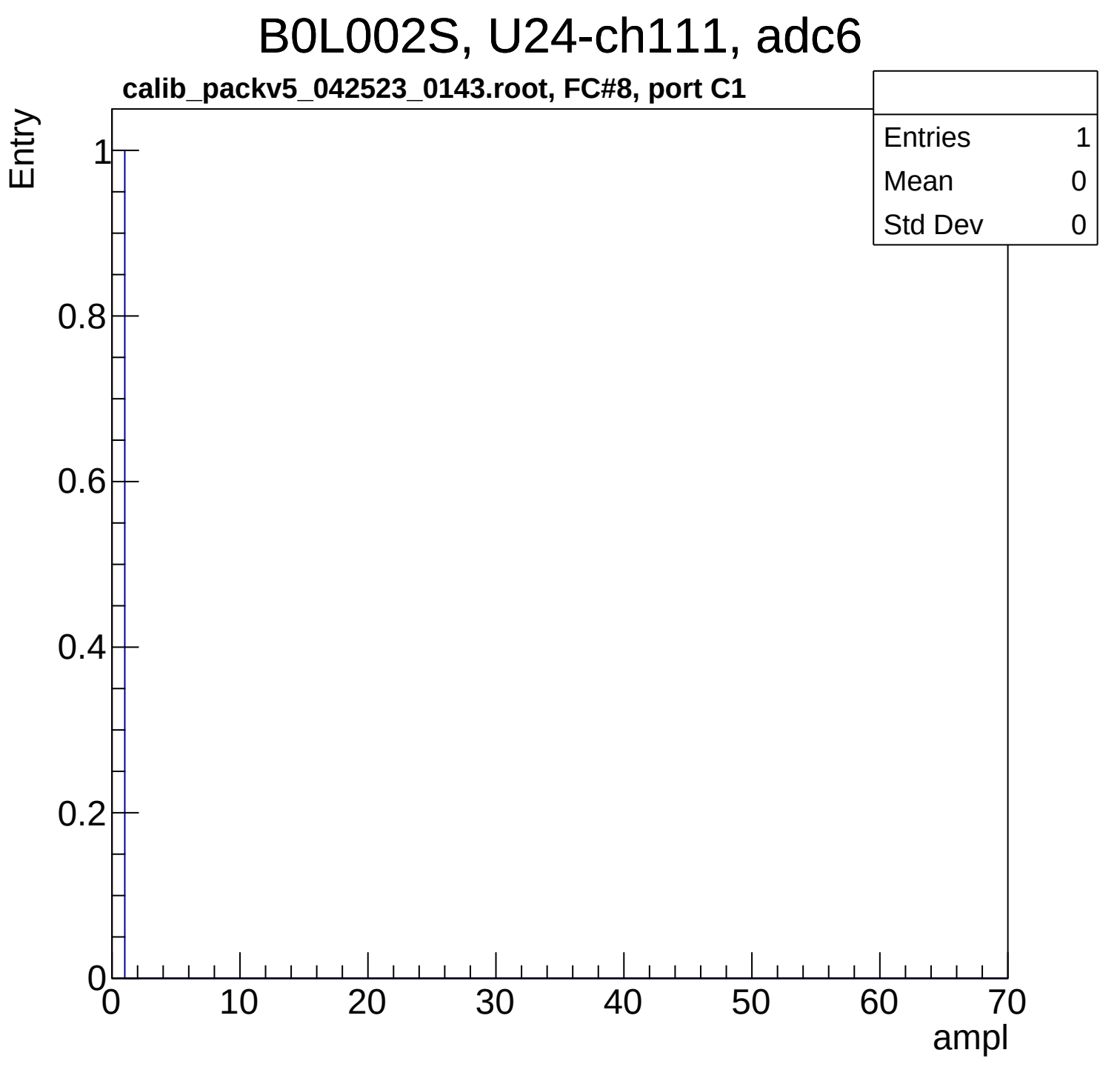
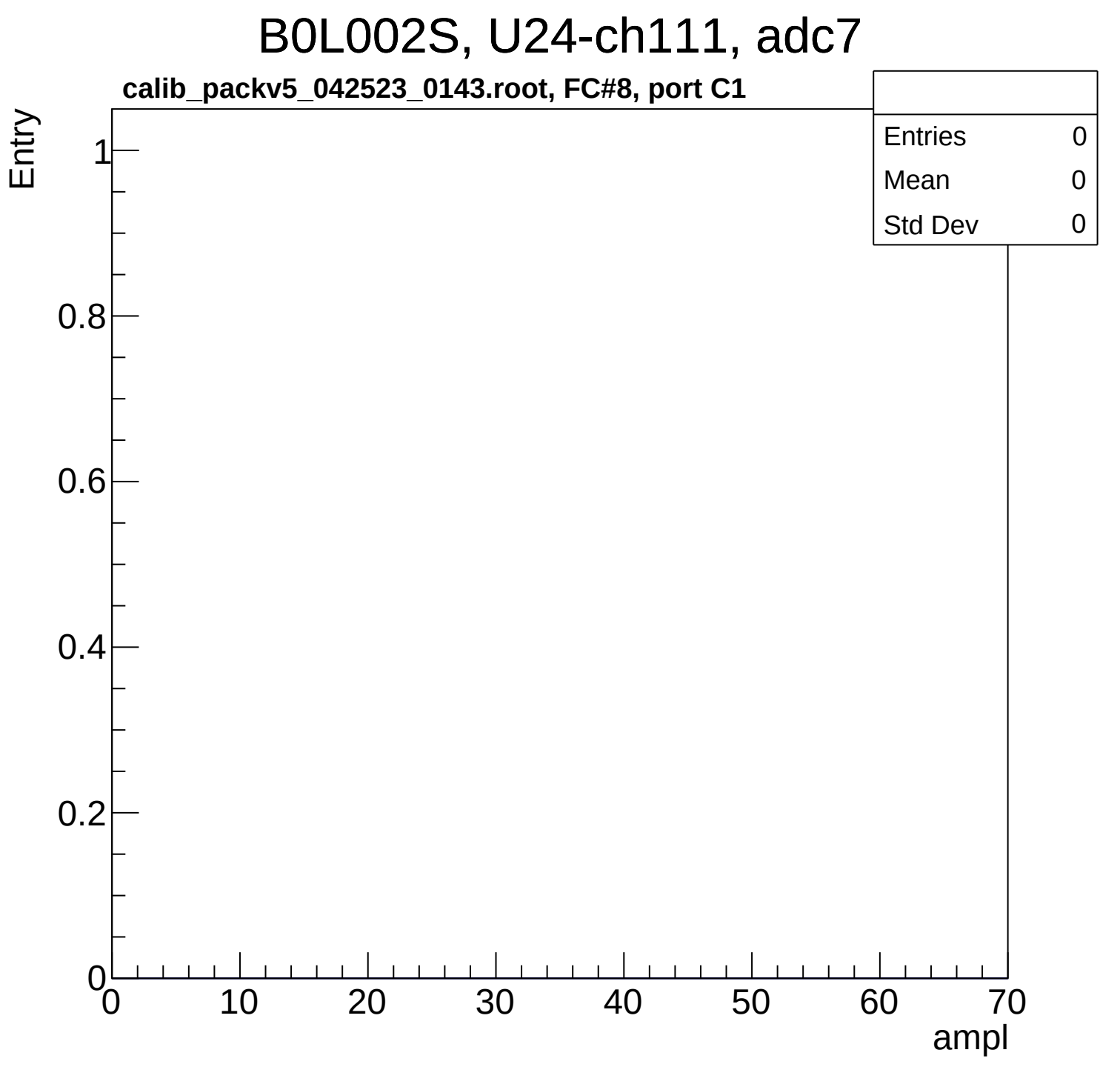

B0L002S, U24-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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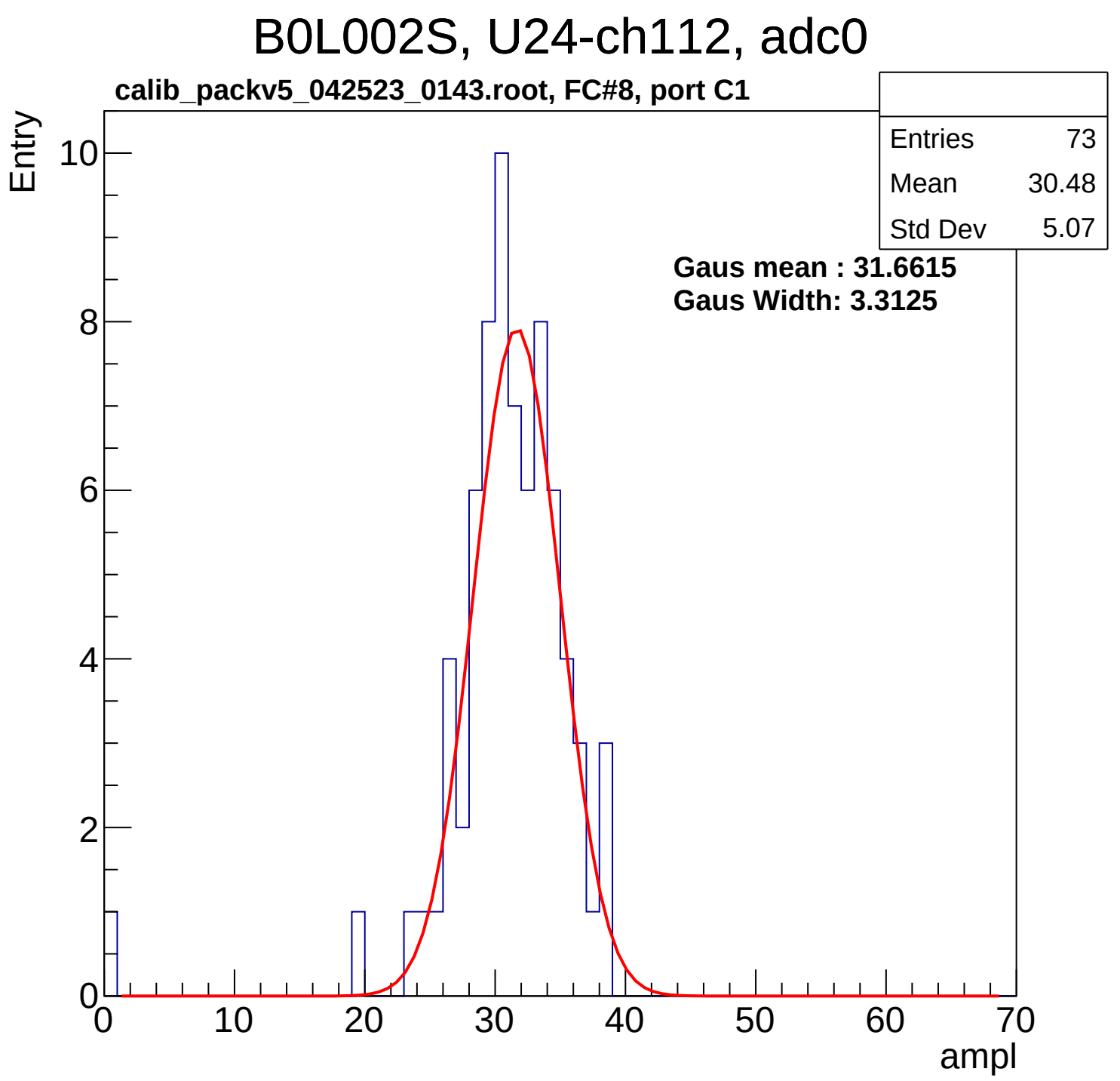
60
70
ampl


B0L002S, U24-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 30.48
Std Dev 5.07
Gaus mean : 31.6615
Gaus Width: 3.3125
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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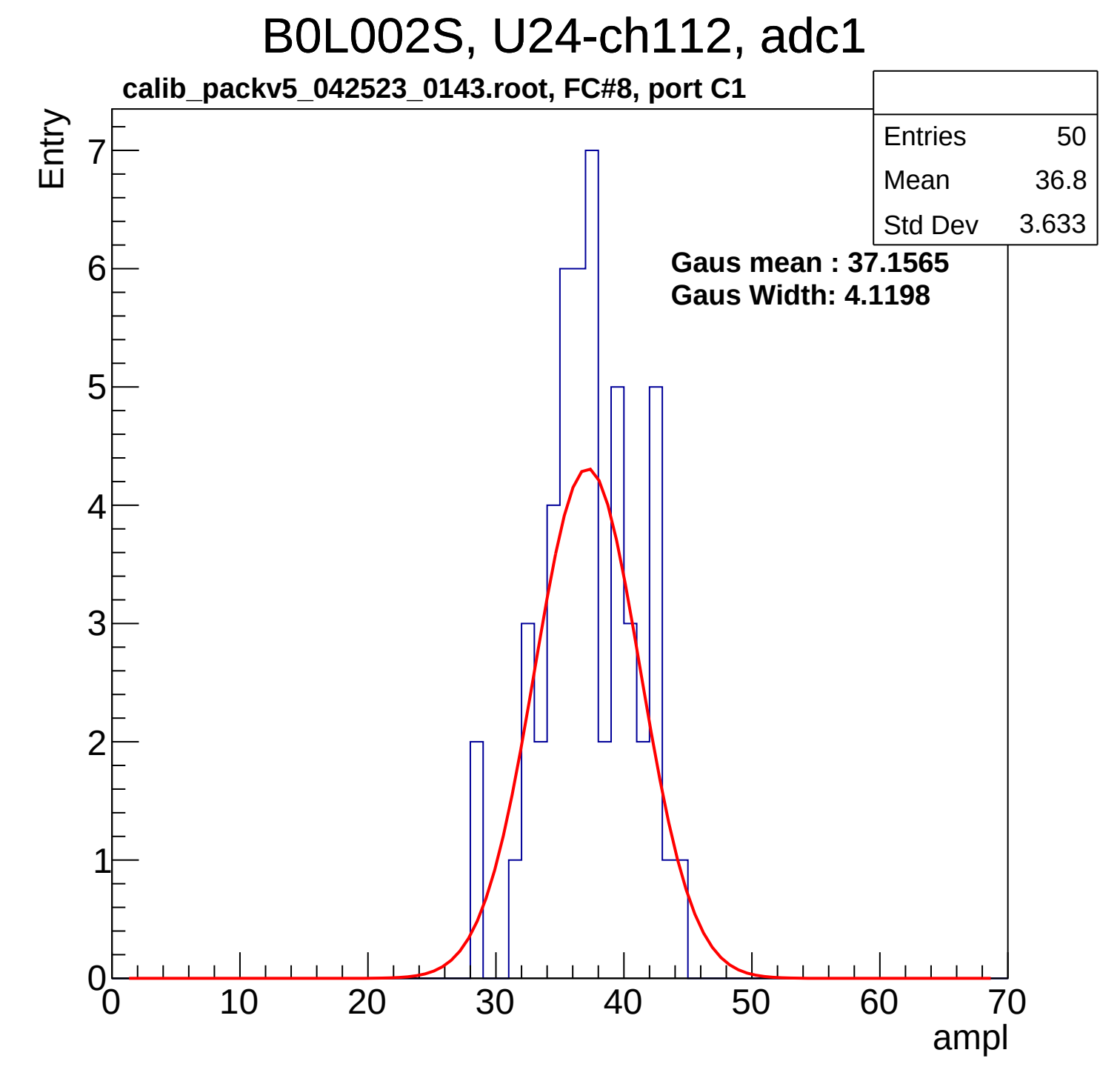
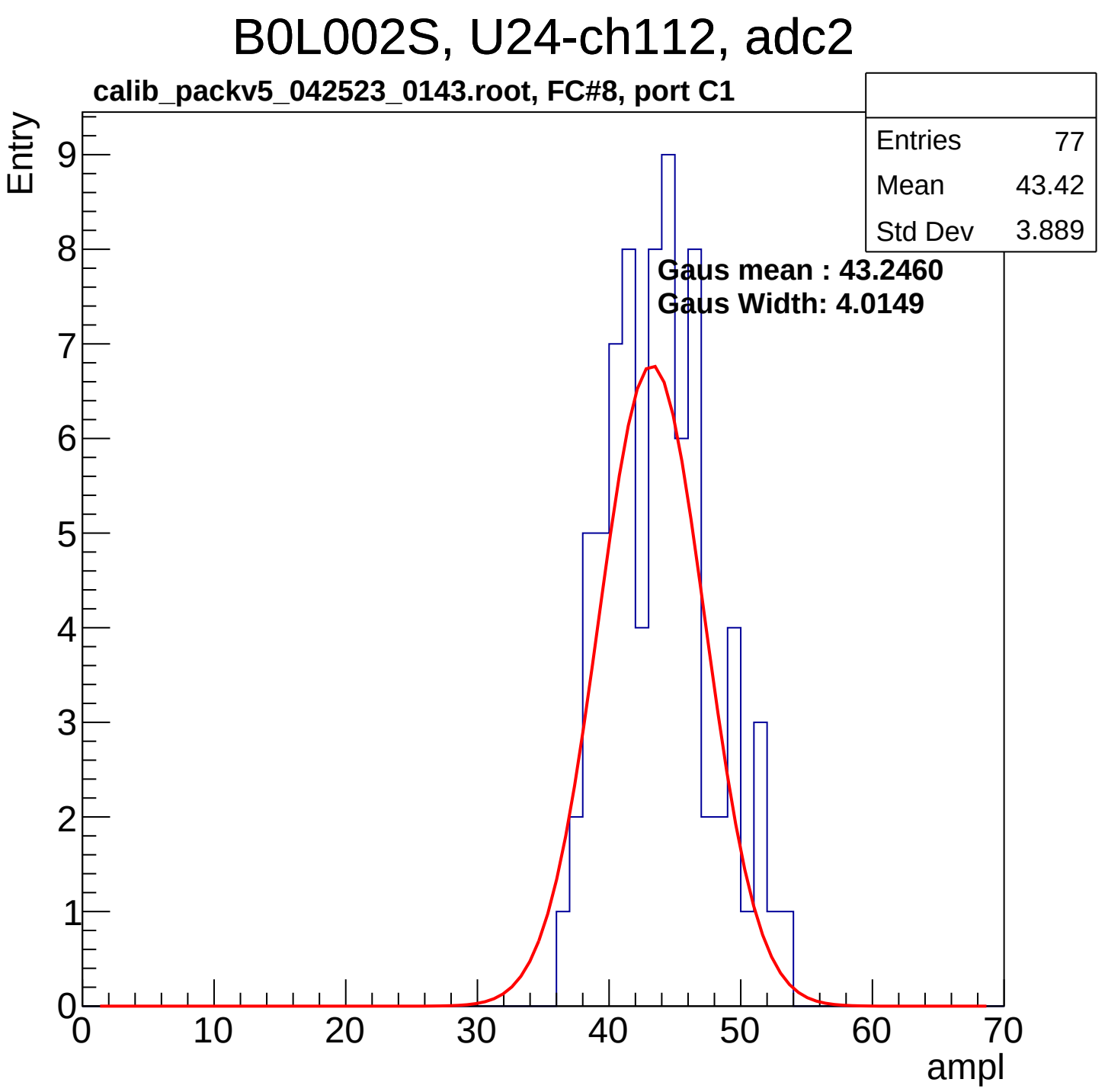
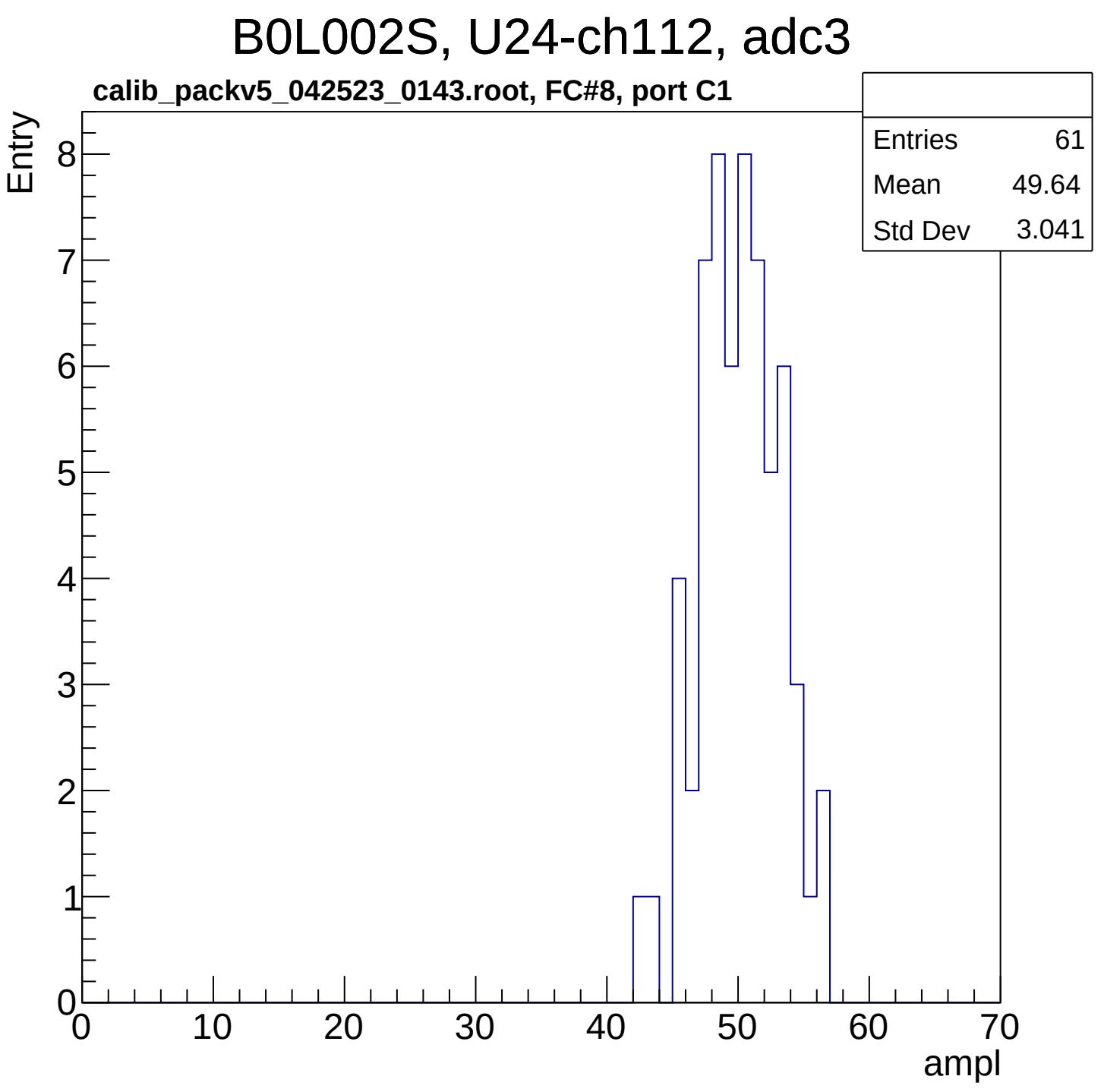

B0L002S, U24-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 36.8
Std Dev 3.633
Gaus mean : 37.1565
Gaus Width: 4.1198
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 43.42
Std Dev 3.889
Gaus mean : 43.2460
Gaus Width: 4.0149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 49.64
Std Dev 3.041
Entry
ampl
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1
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5
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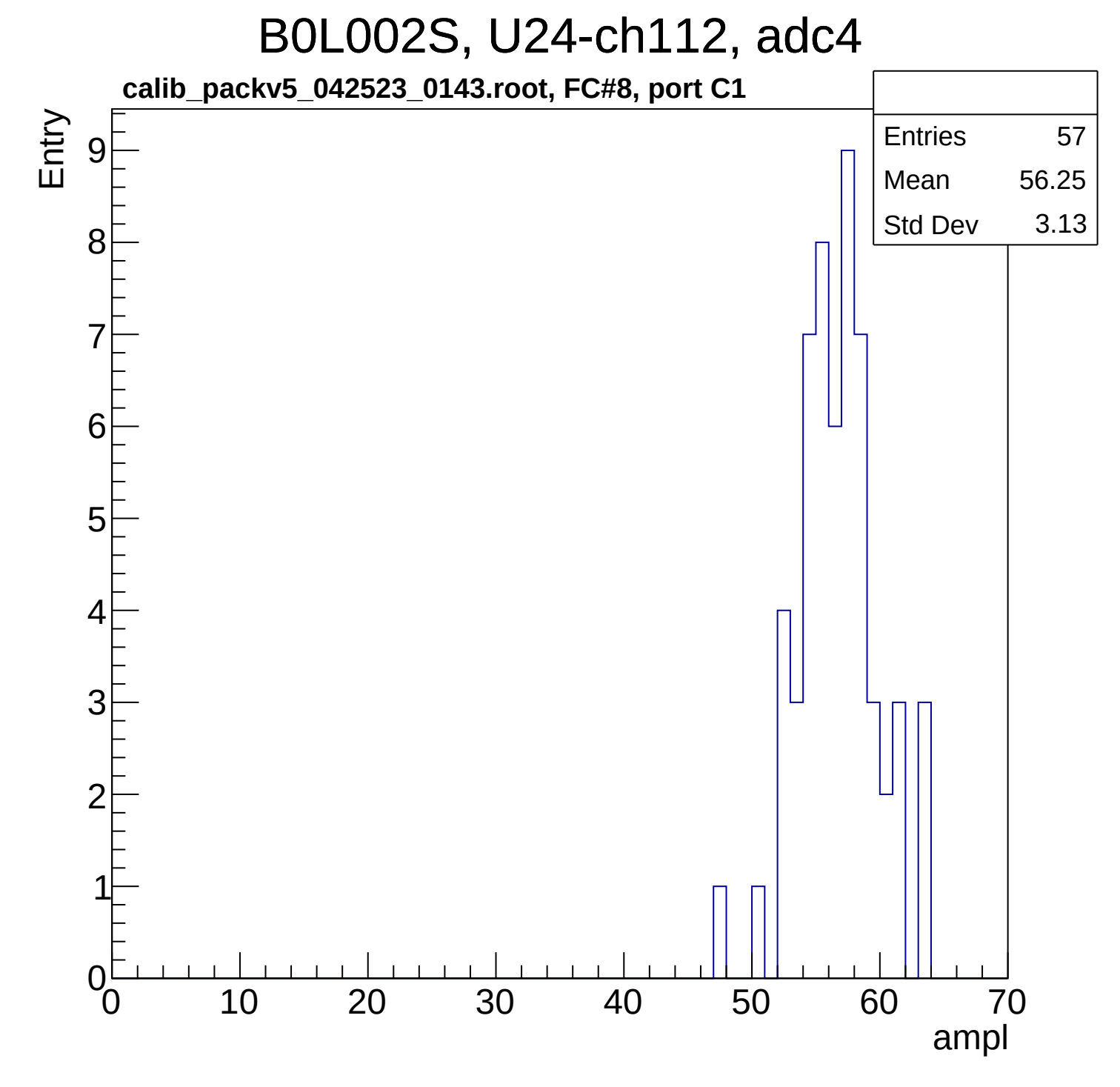
B0L002S, U24-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.25
Std Dev 3.13
Entry
ampl
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2
3
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5
6
7
8
9
0
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60
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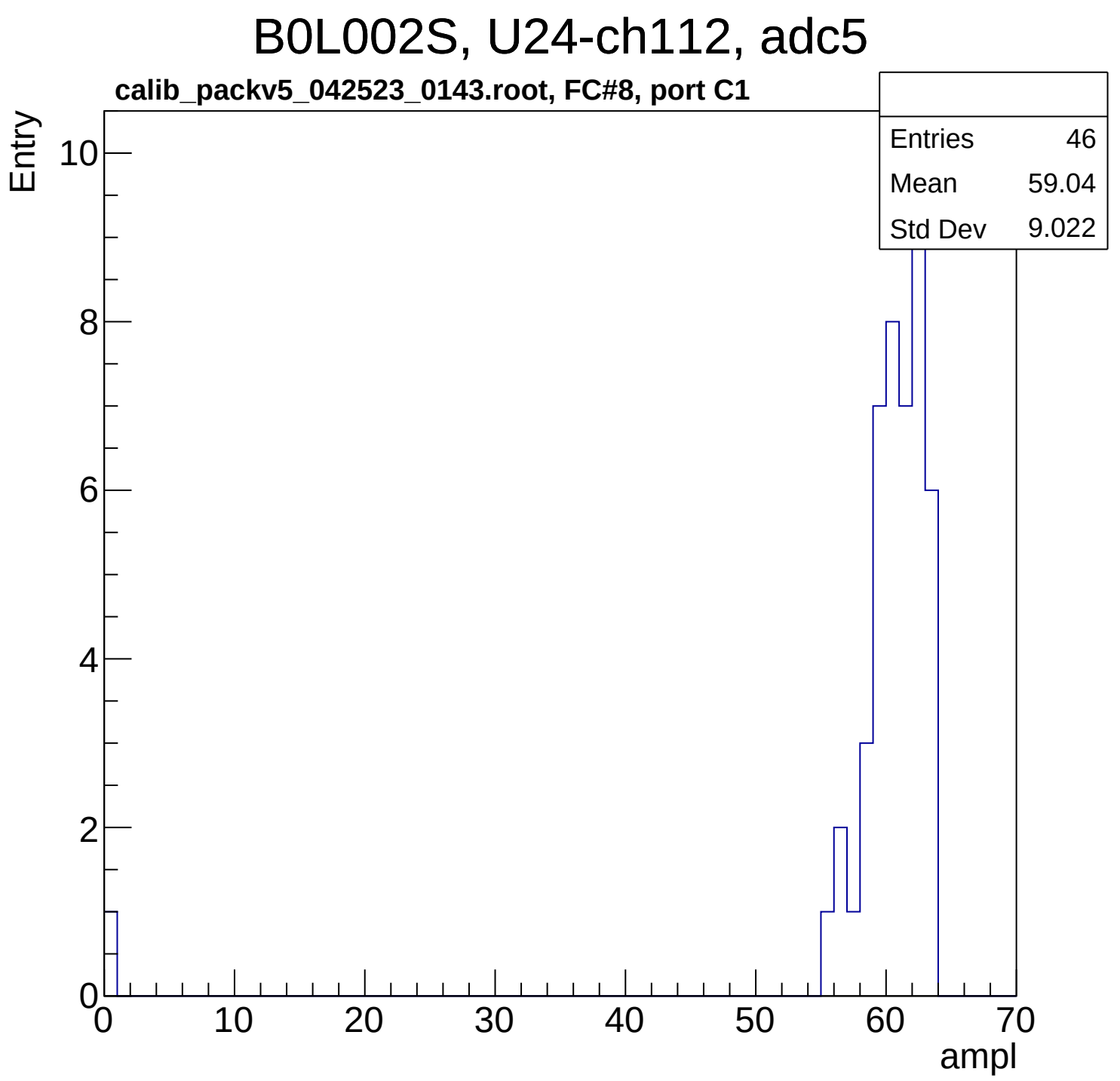
B0L002S, U24-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.04
Std Dev 9.022
Entry
ampl
0
2
4
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0
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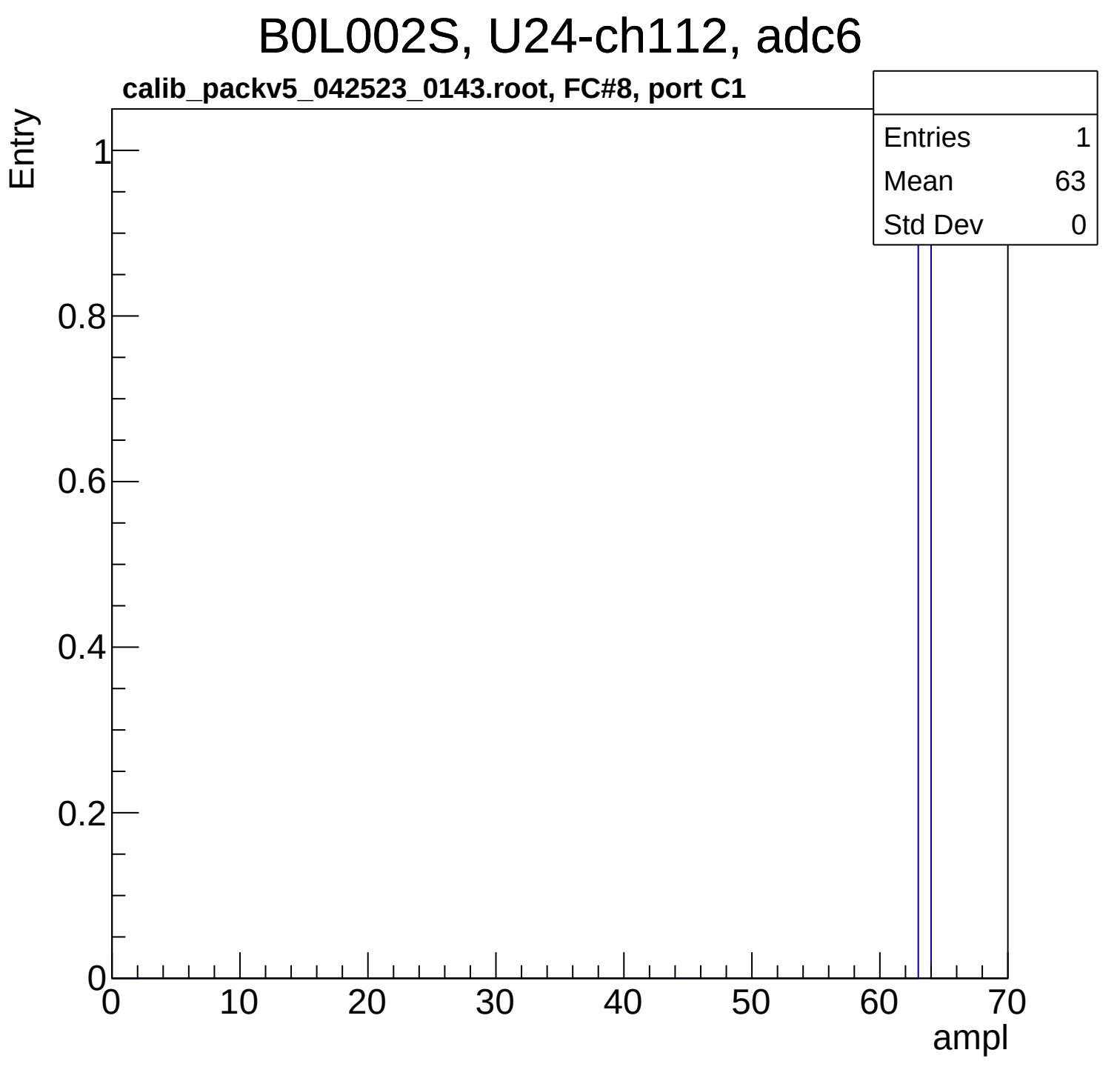
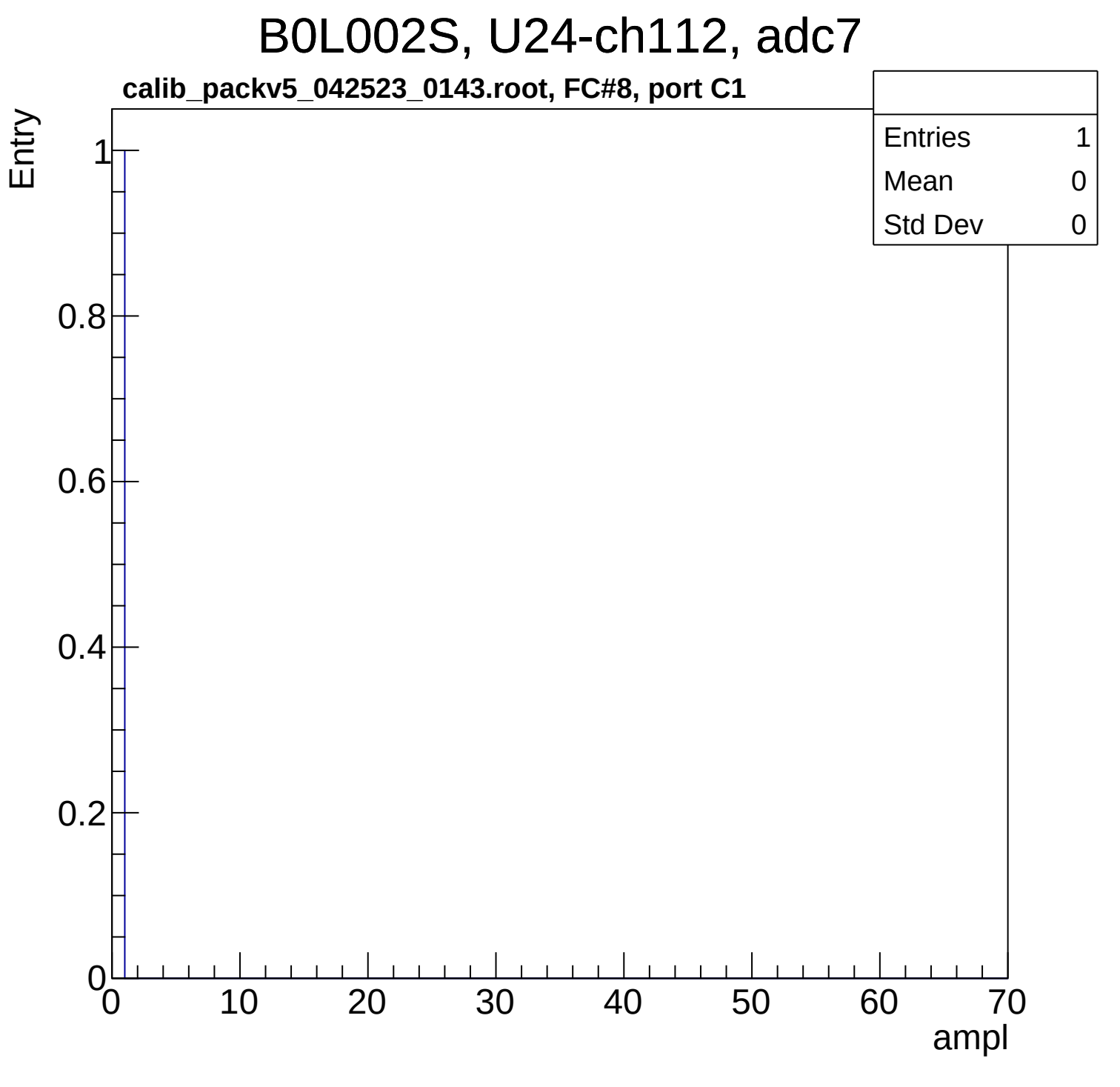
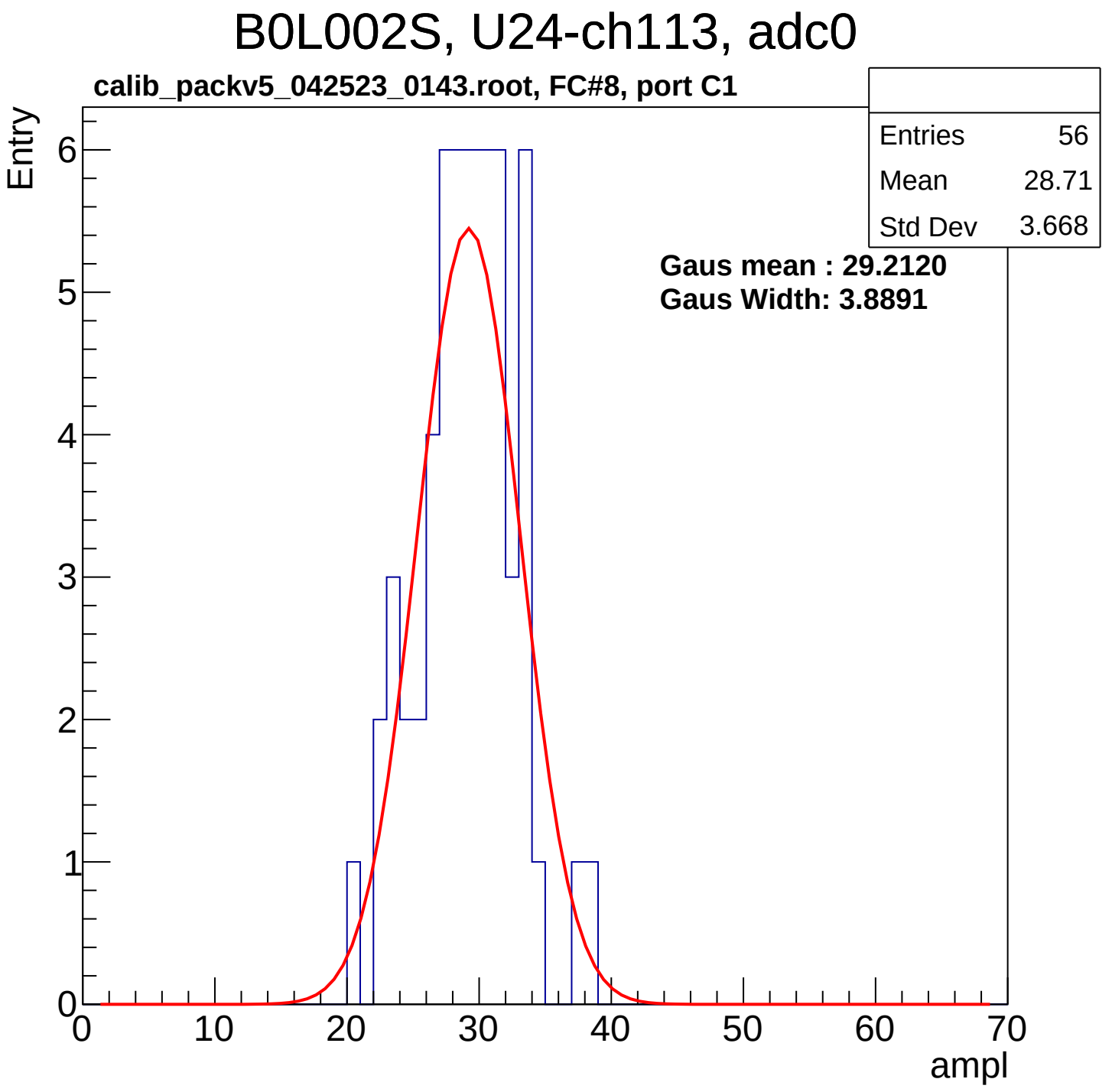

B0L002S, U24-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 28.71
Std Dev 3.668
Gaus mean : 29.2120
Gaus Width: 3.8891
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
50
60
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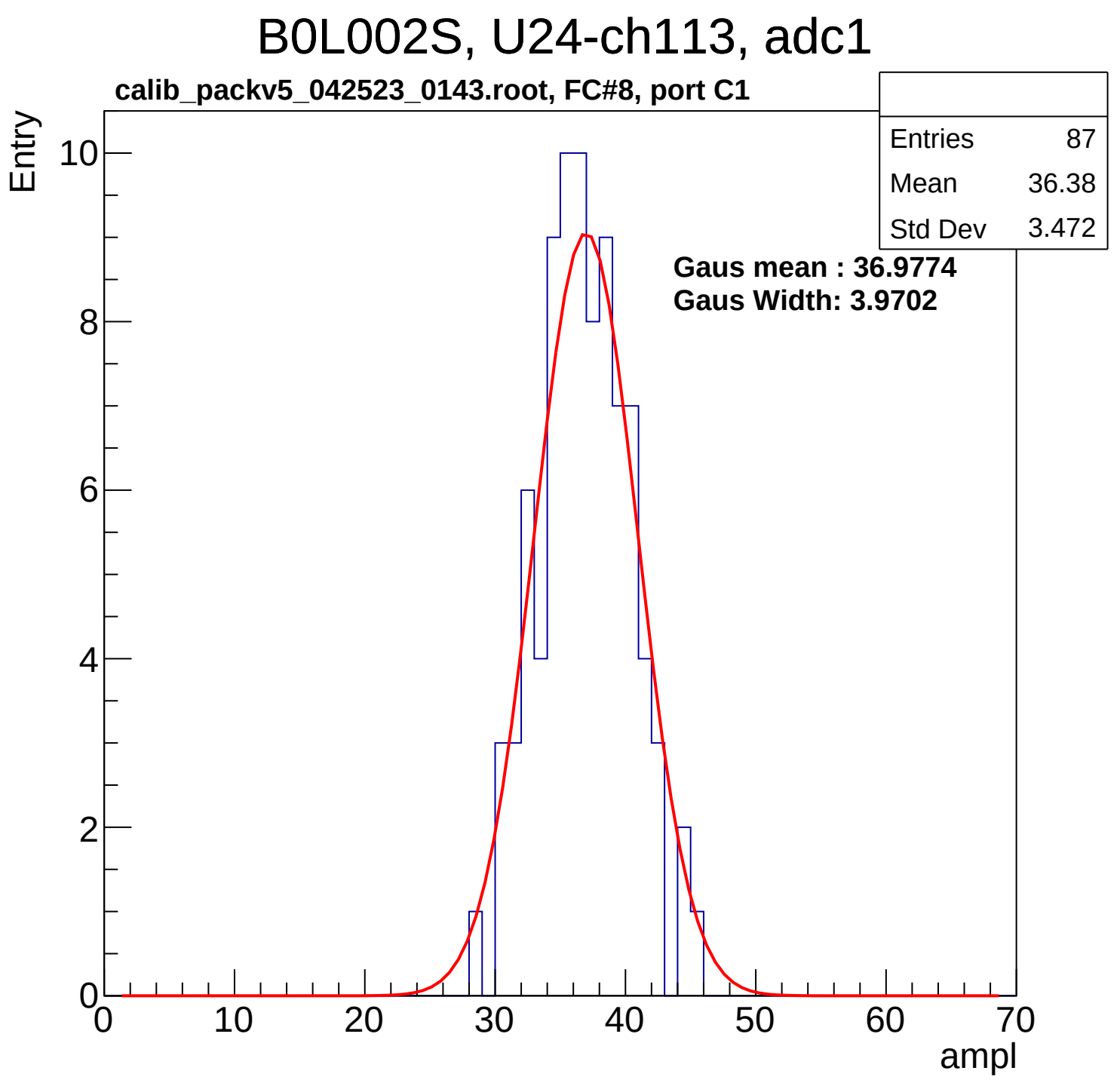
B0L002S, U24-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 36.38
Std Dev 3.472
Gaus mean : 36.9774
Gaus Width: 3.9702
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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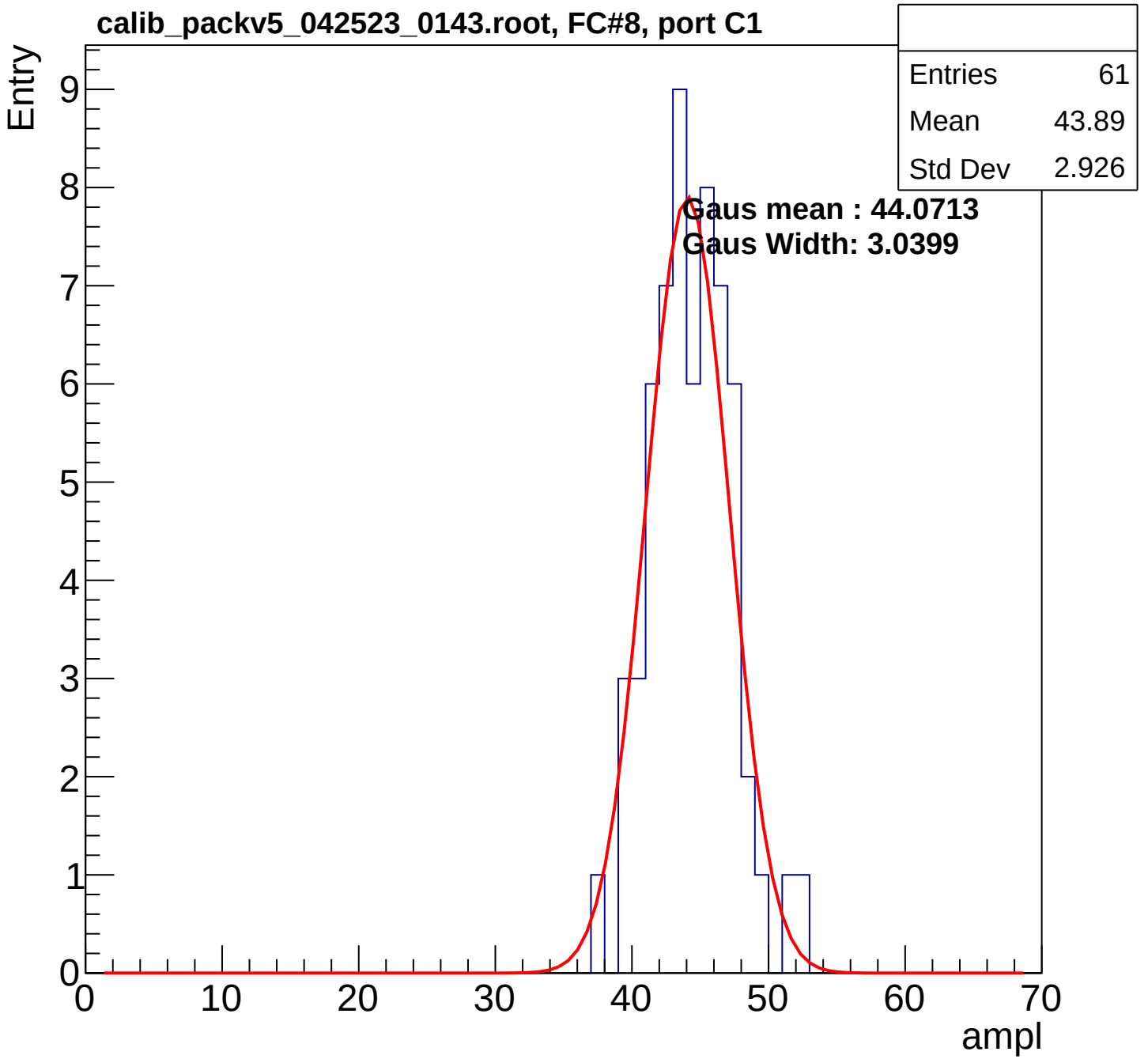
B0L002S, U24-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.89
Std Dev 2.926
Gaus mean : 44.0713
Gaus Width: 3.0399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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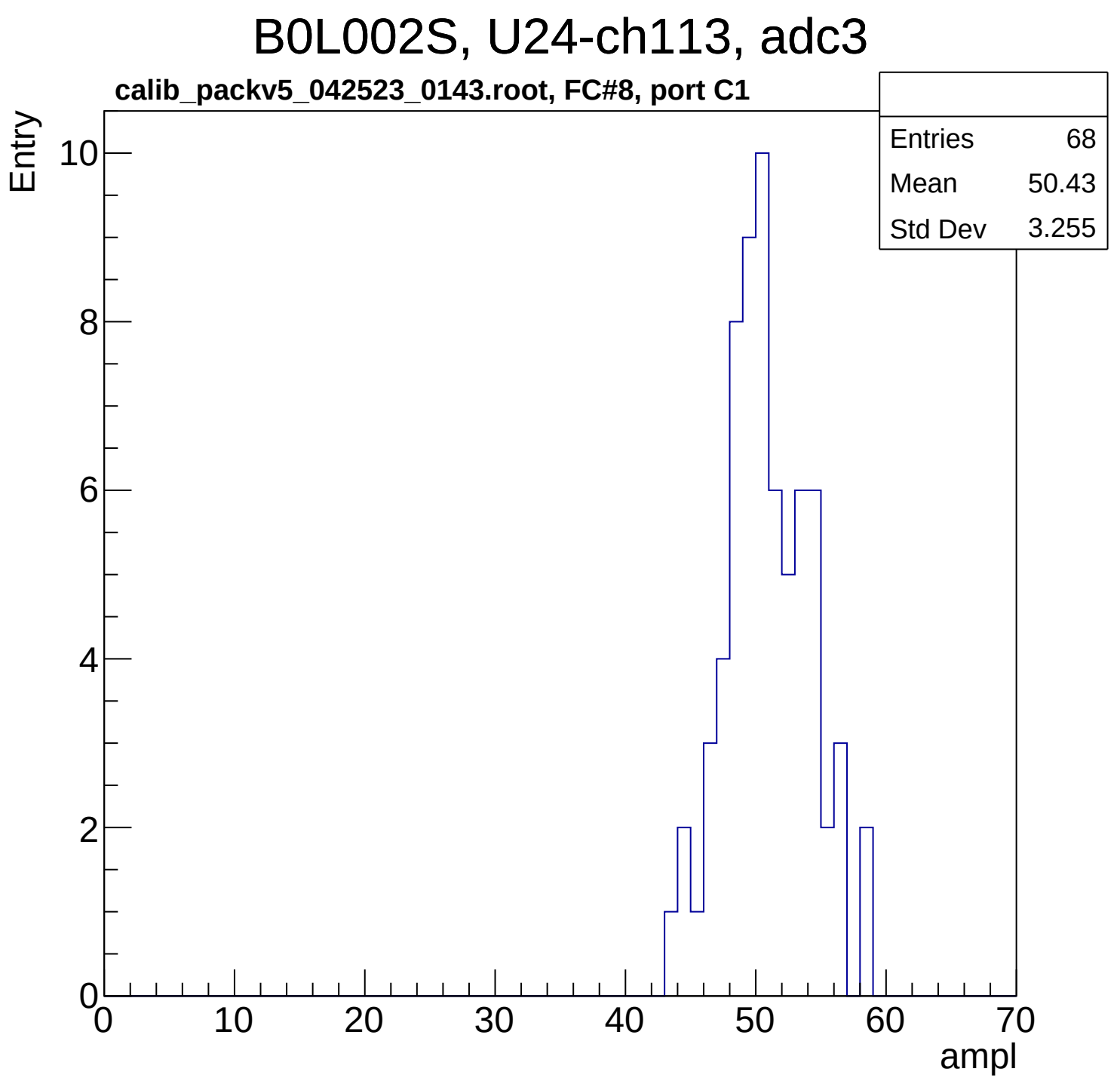
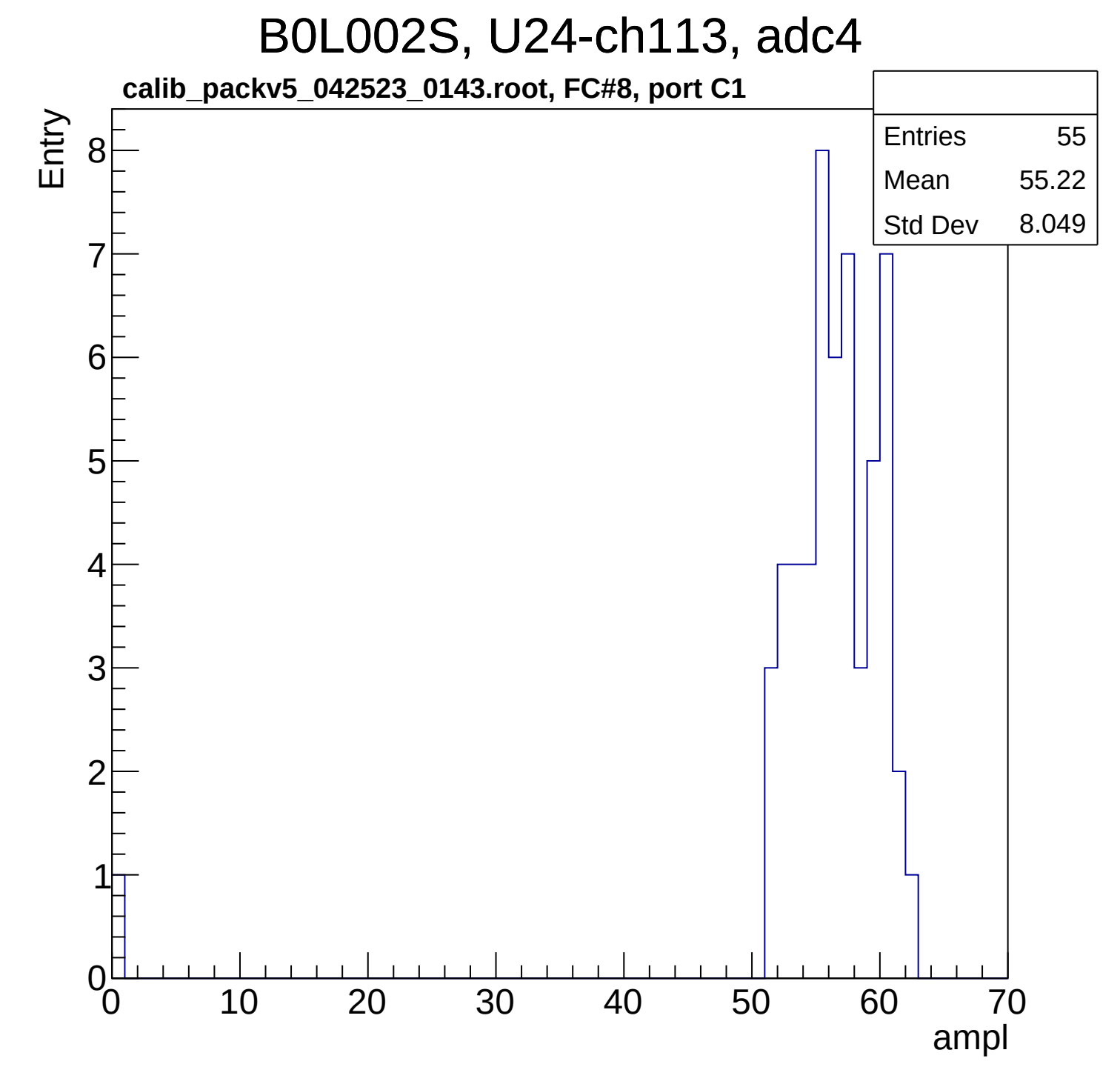
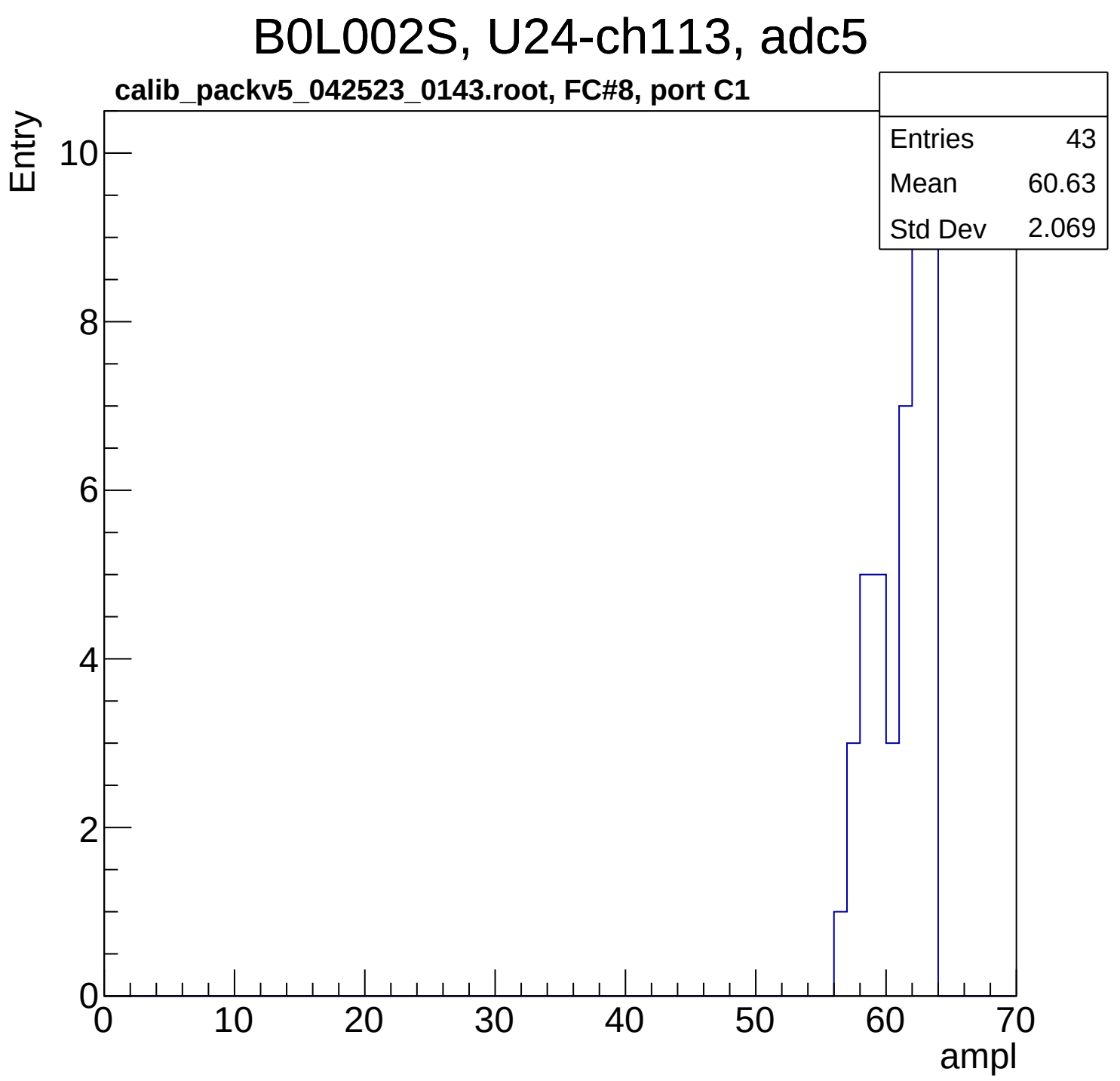
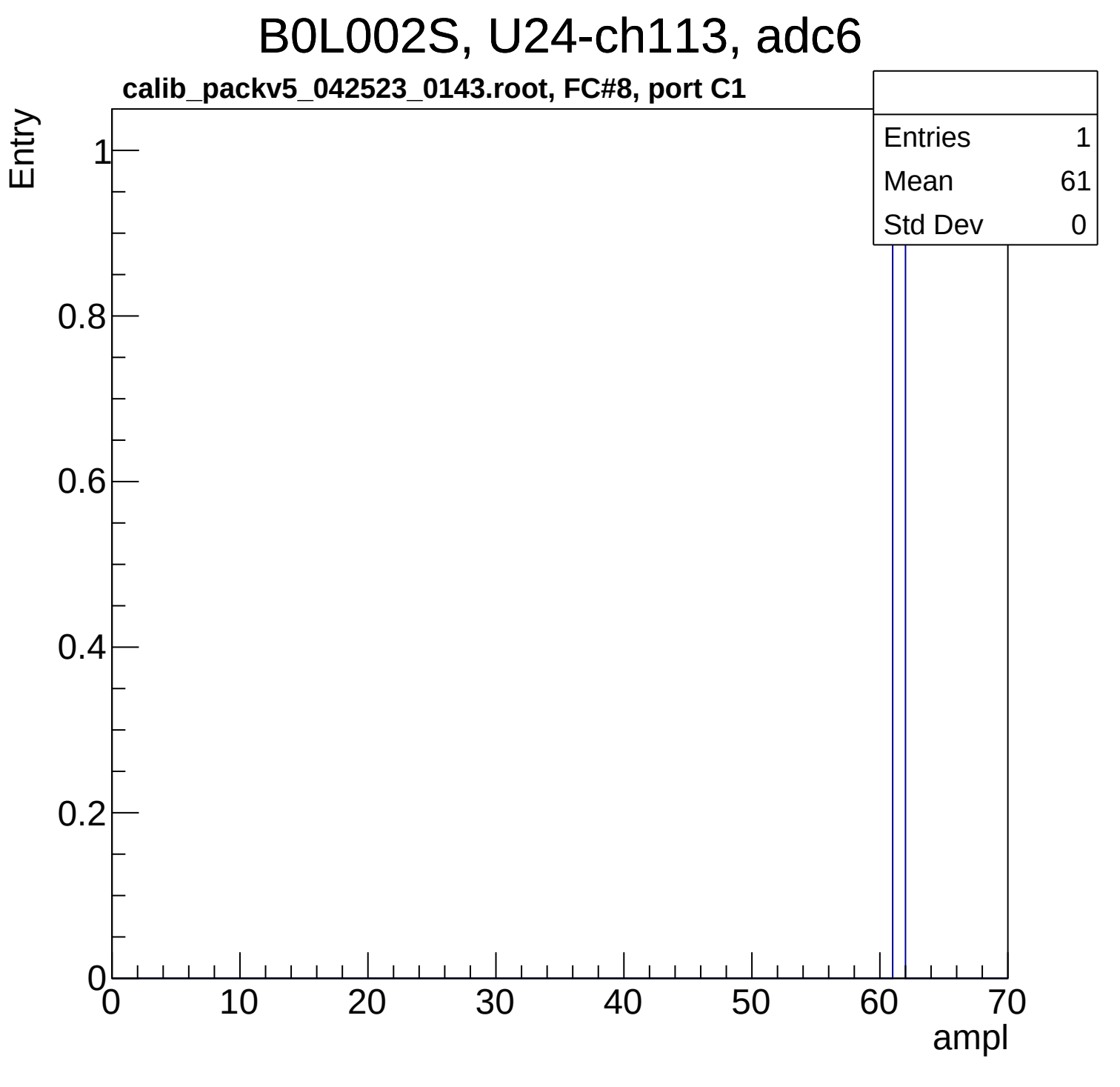
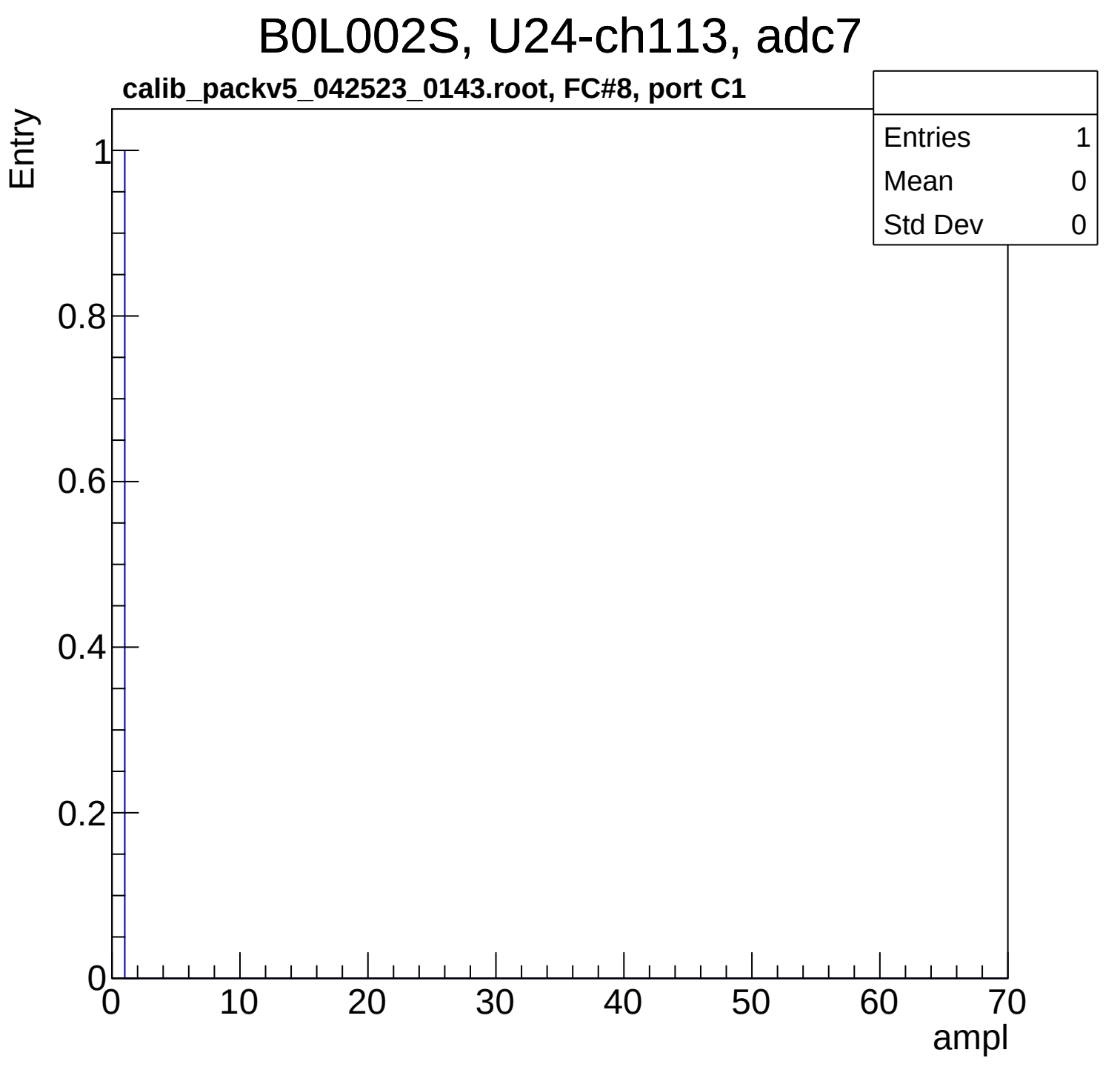
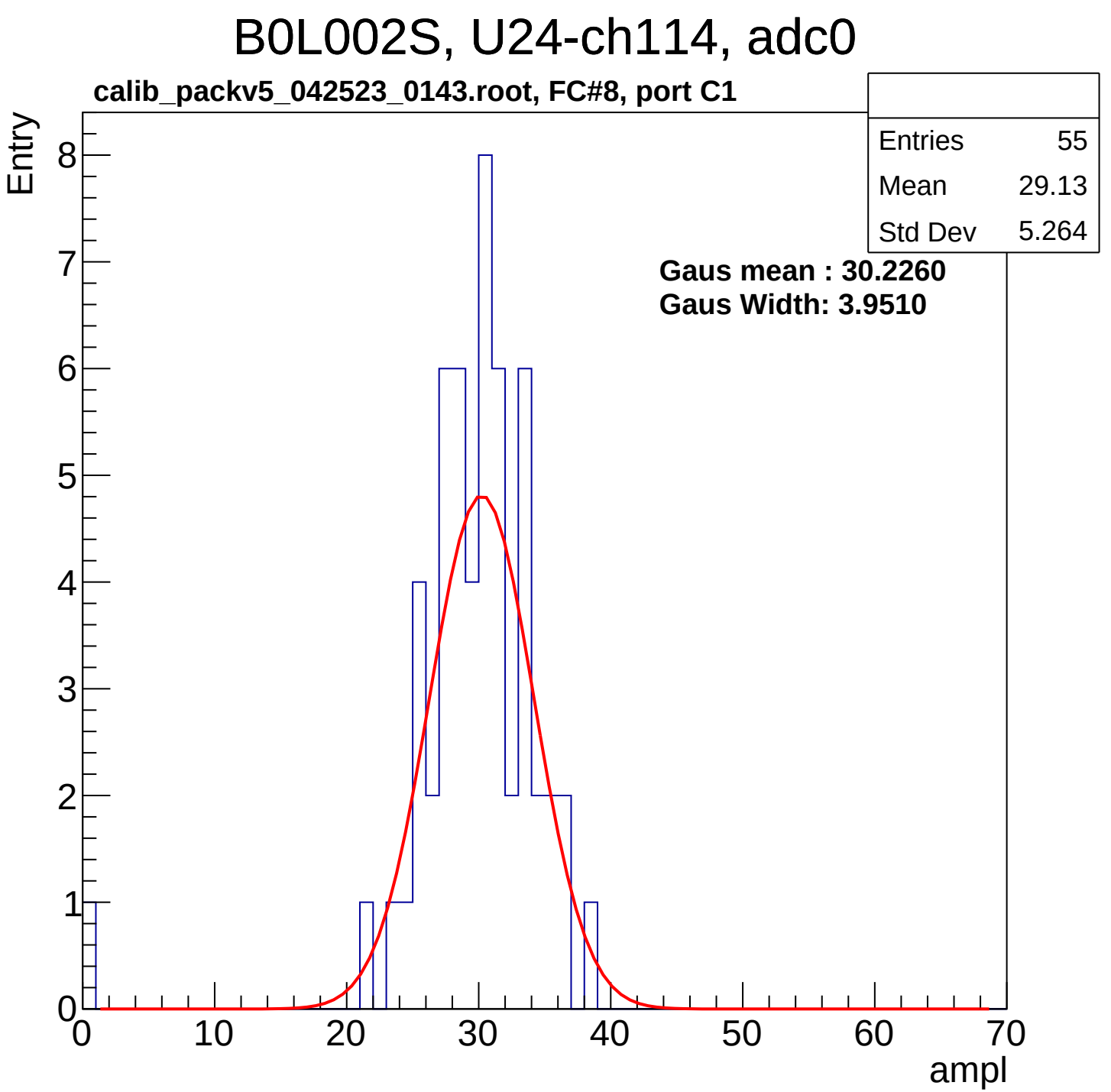

B0L002S, U24-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.43
Std Dev 3.255
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 55.22
Std Dev 8.049
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 60.63
Std Dev 2.069
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 29.13
Std Dev 5.264
Gaus mean : 30.2260
Gaus Width: 3.9510
Entry
ampl
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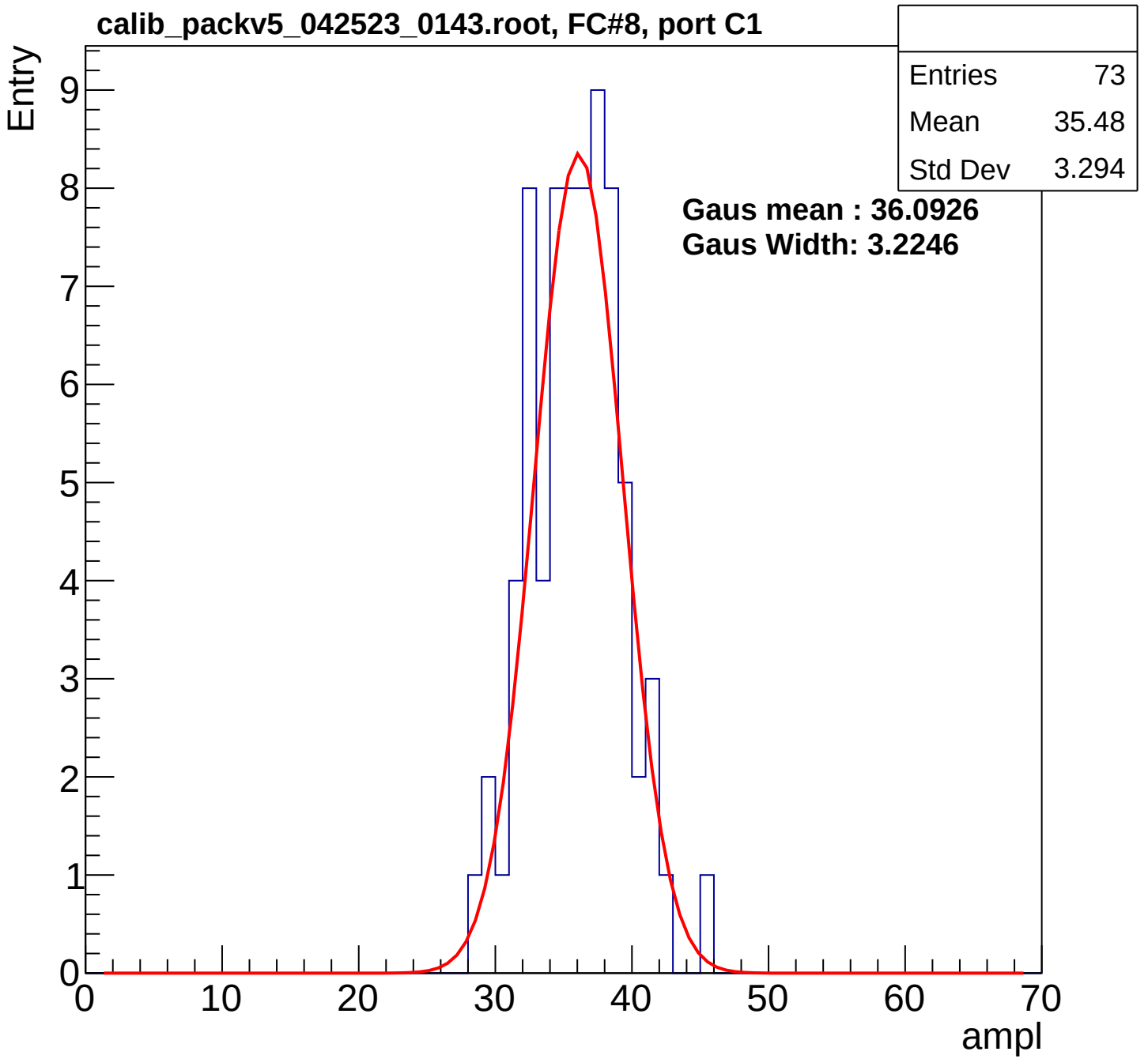
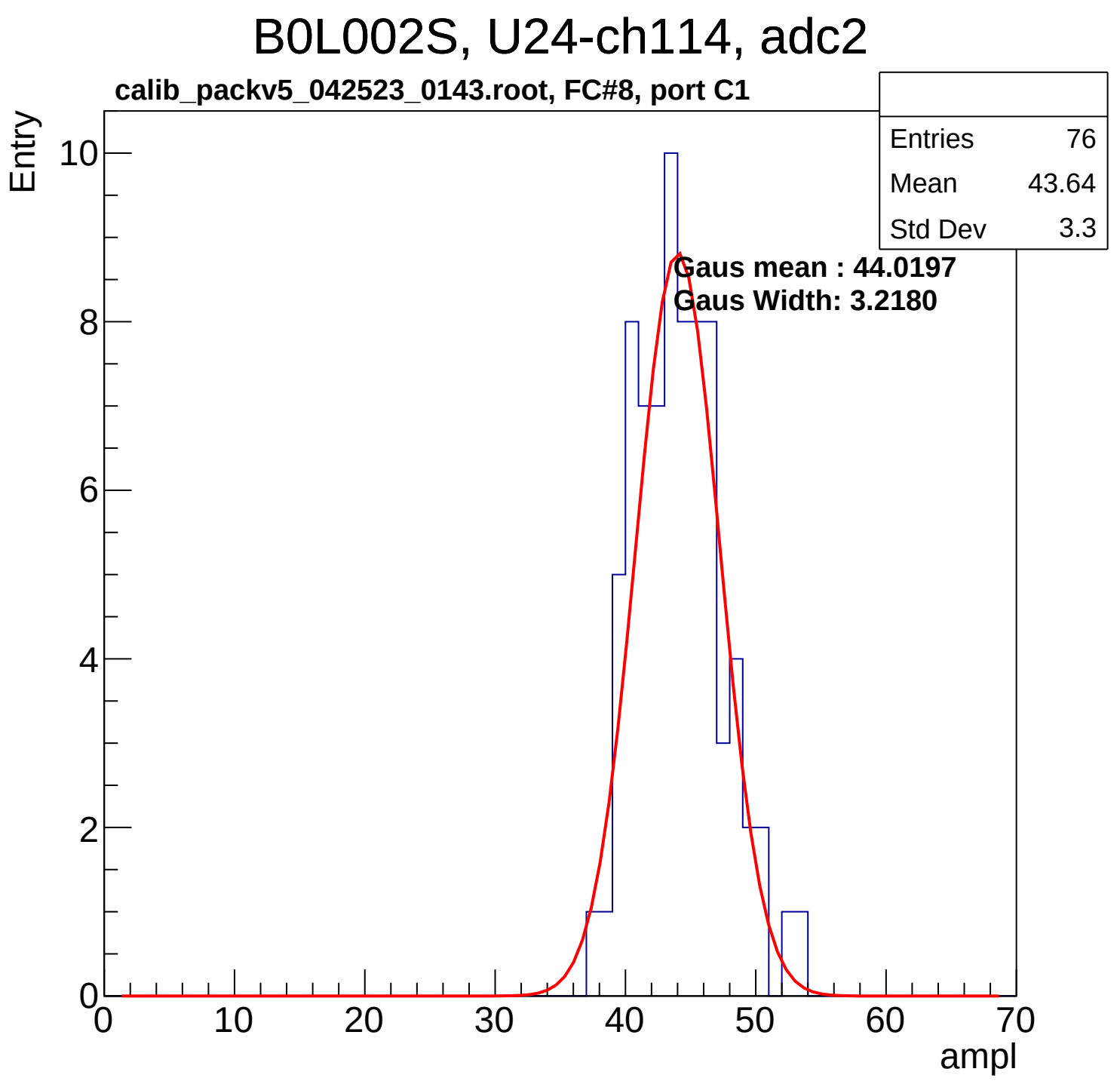
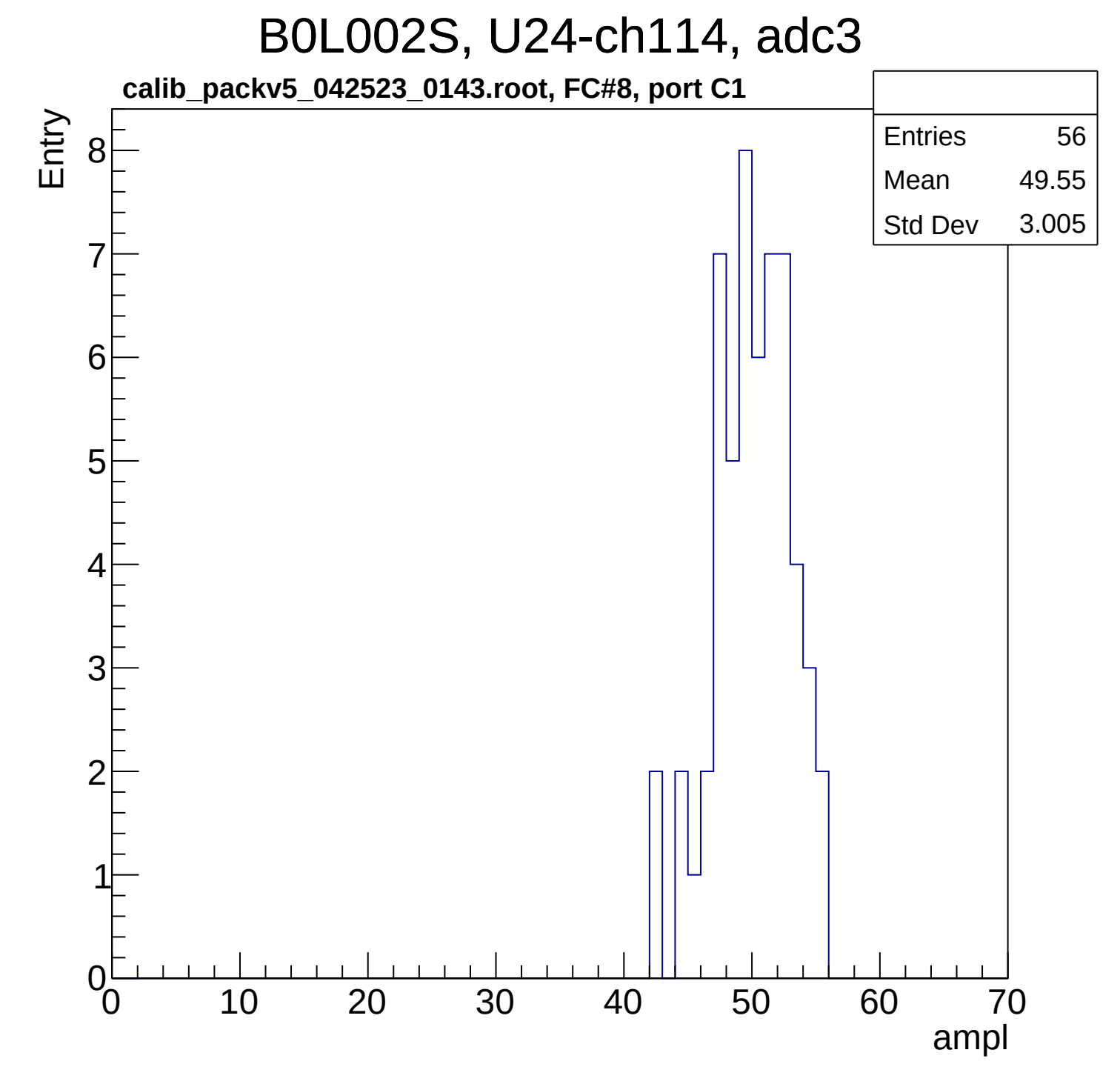
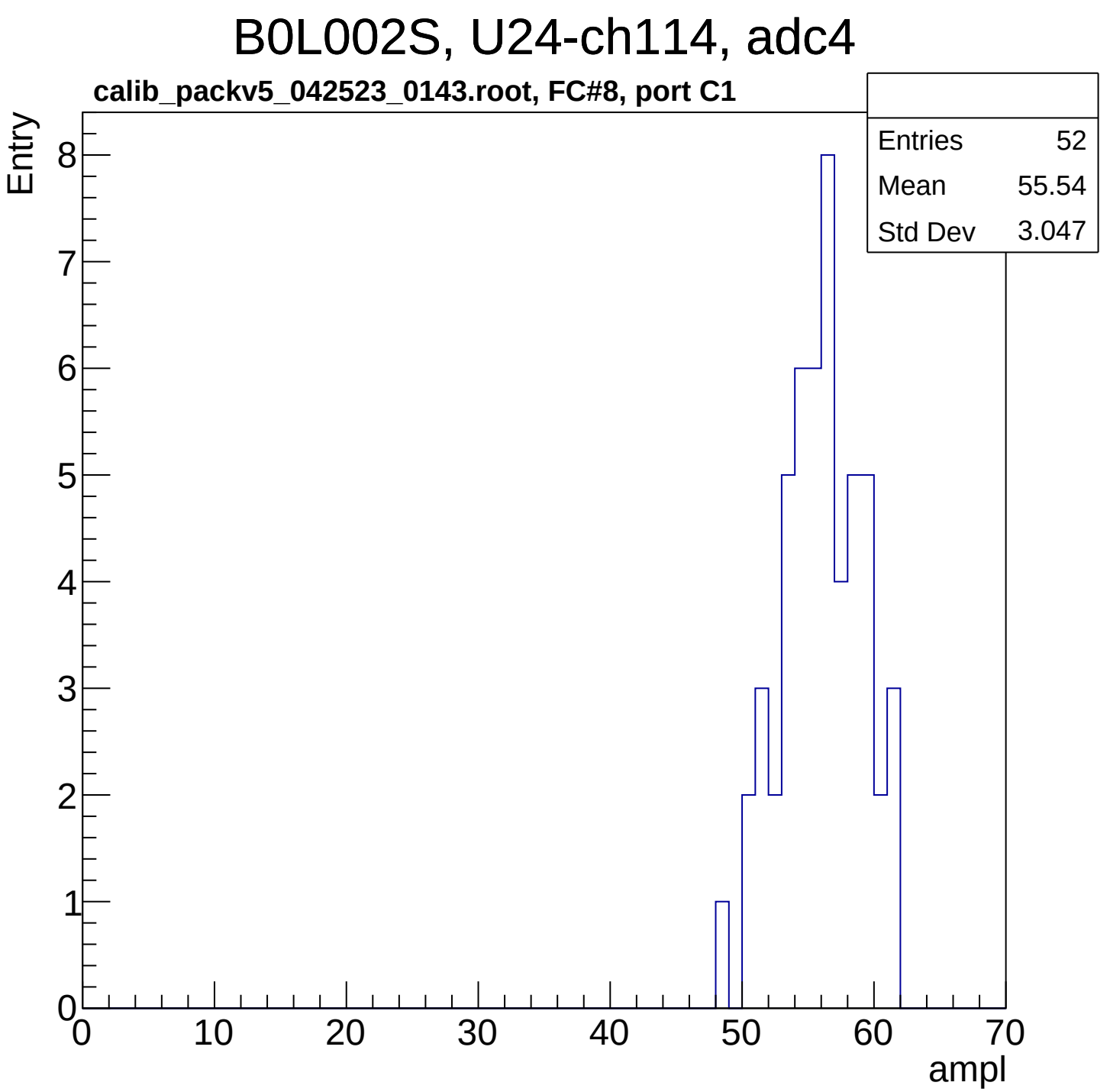
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 35.48
Std Dev 3.294
Gaus mean : 36.0926
Gaus Width: 3.2246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 43.64
Std Dev 3.3
Gaus mean : 44.0197
Gaus Width: 3.2180
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 49.55
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 55.54
Std Dev 3.047
Entry
ampl
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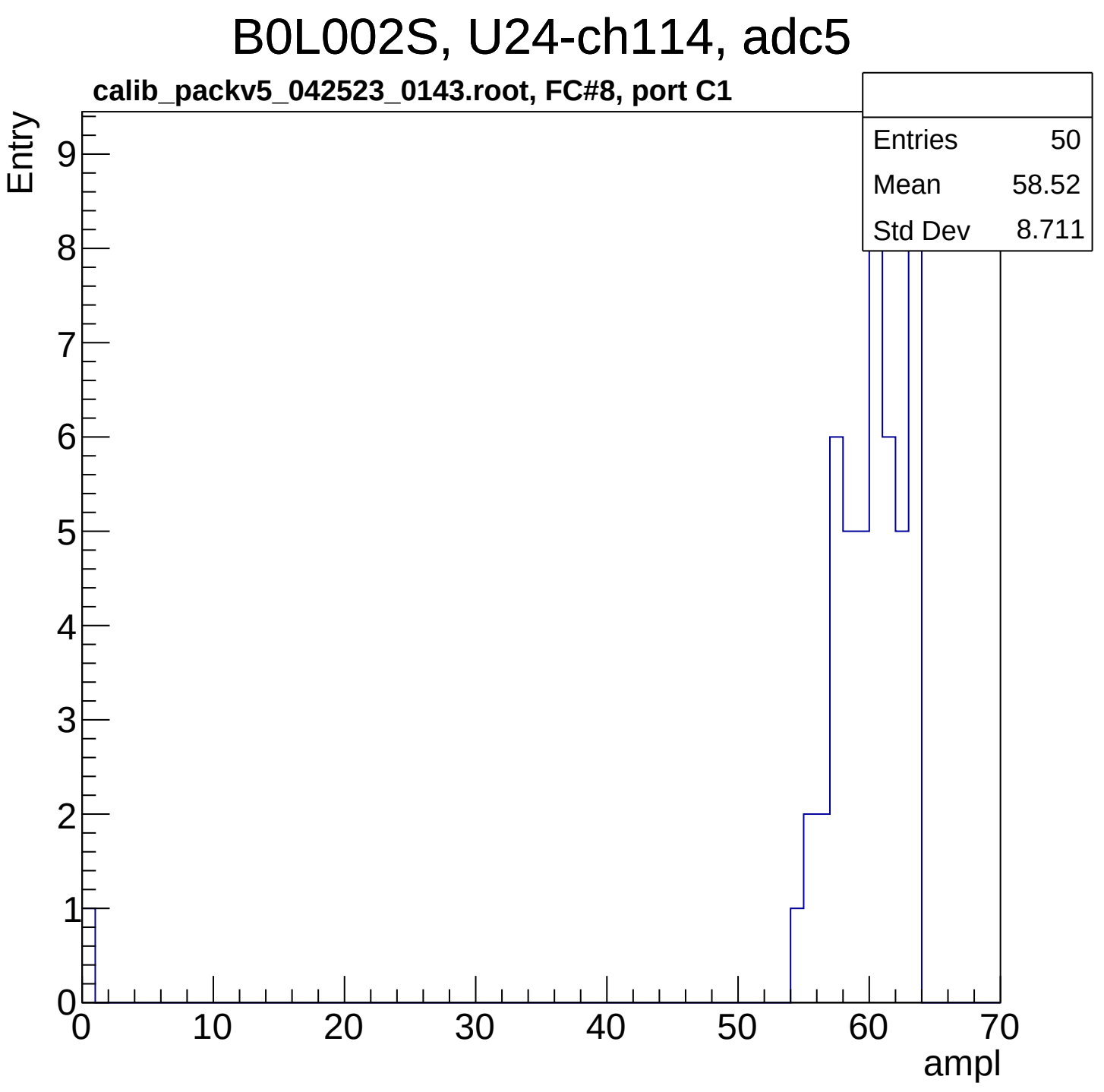
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.52
Std Dev 8.711
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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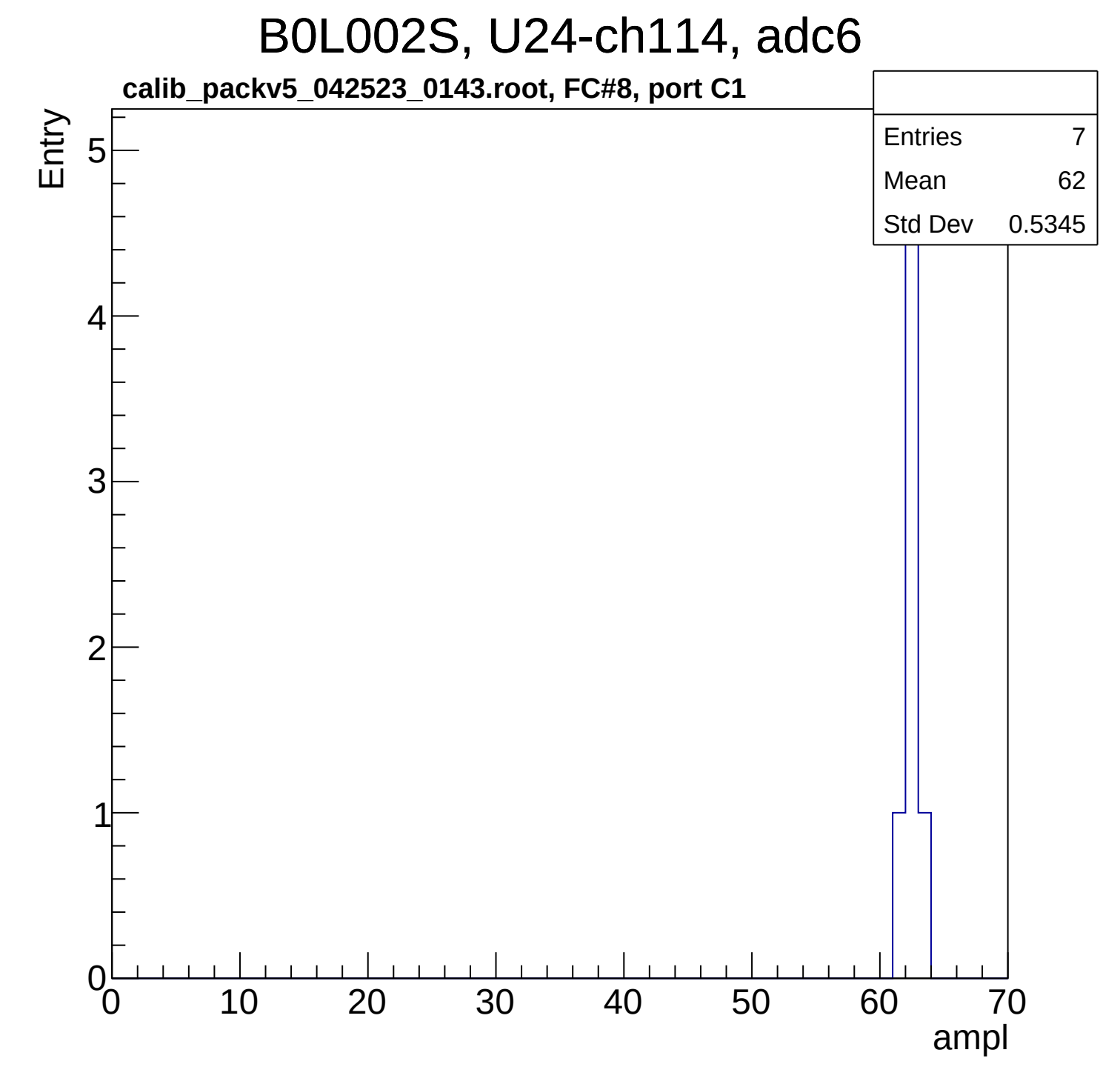
B0L002S, U24-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62
Std Dev 0.5345
Entry
0
1
2
3
4
5
0
10
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30
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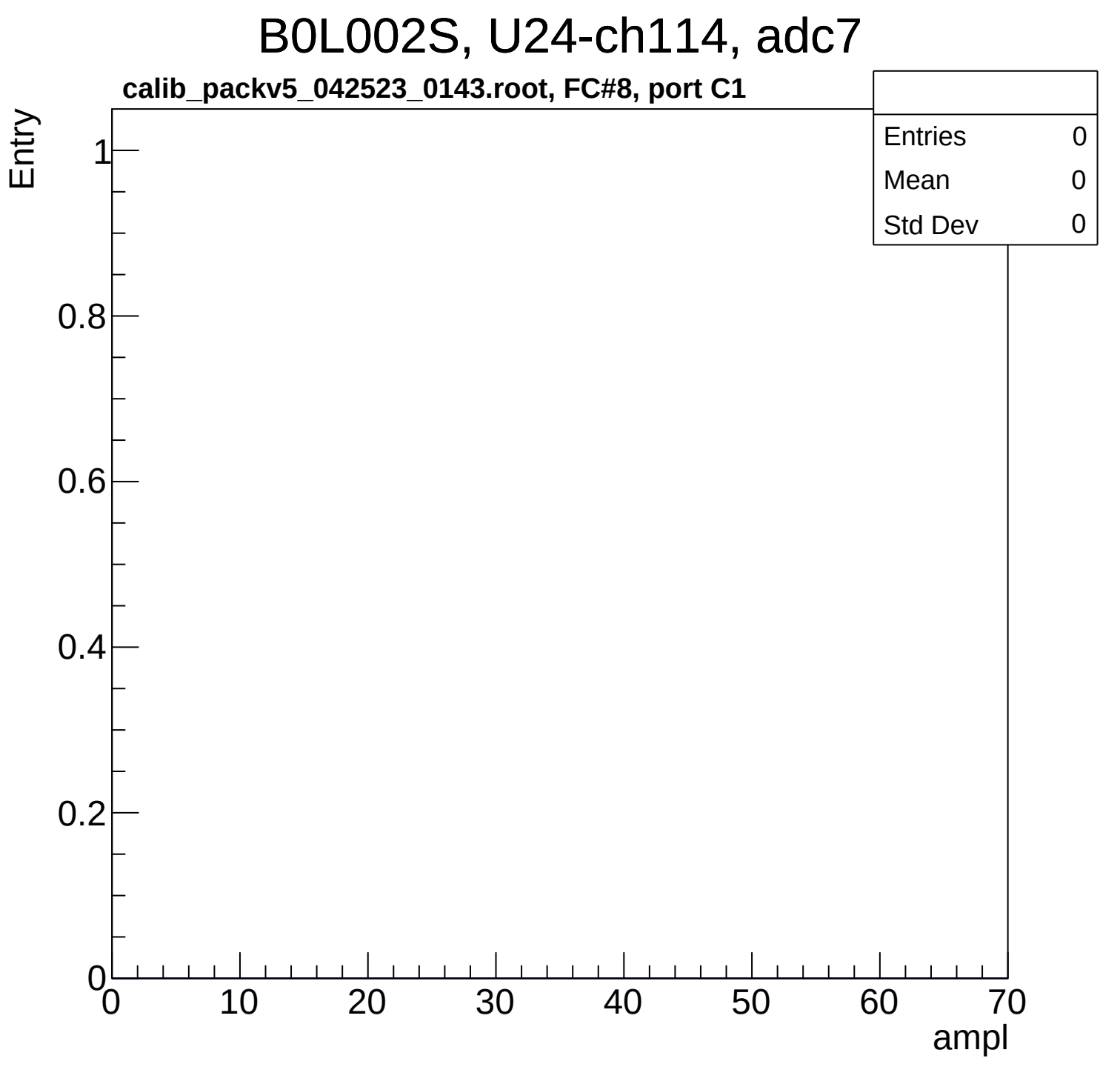
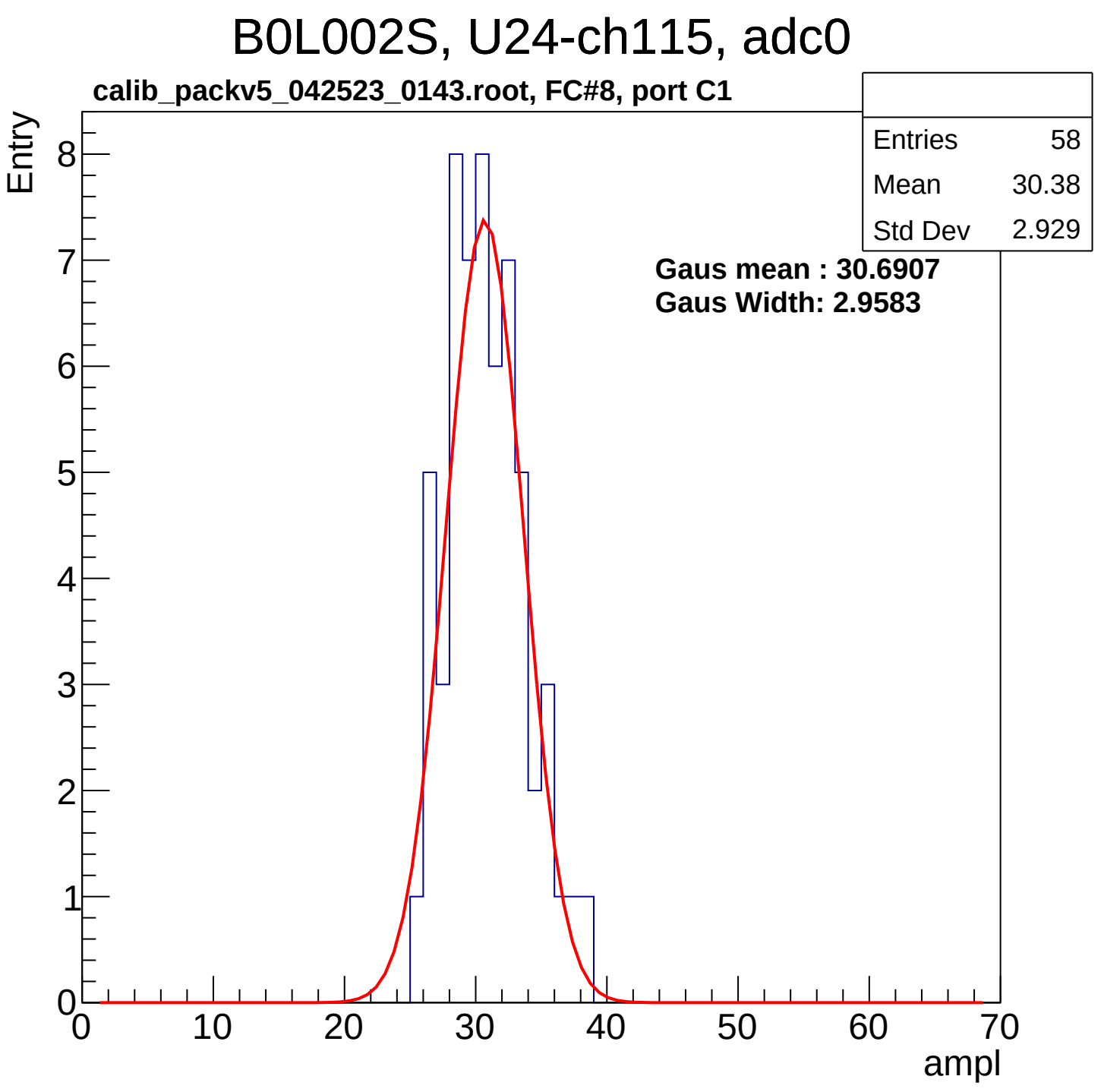
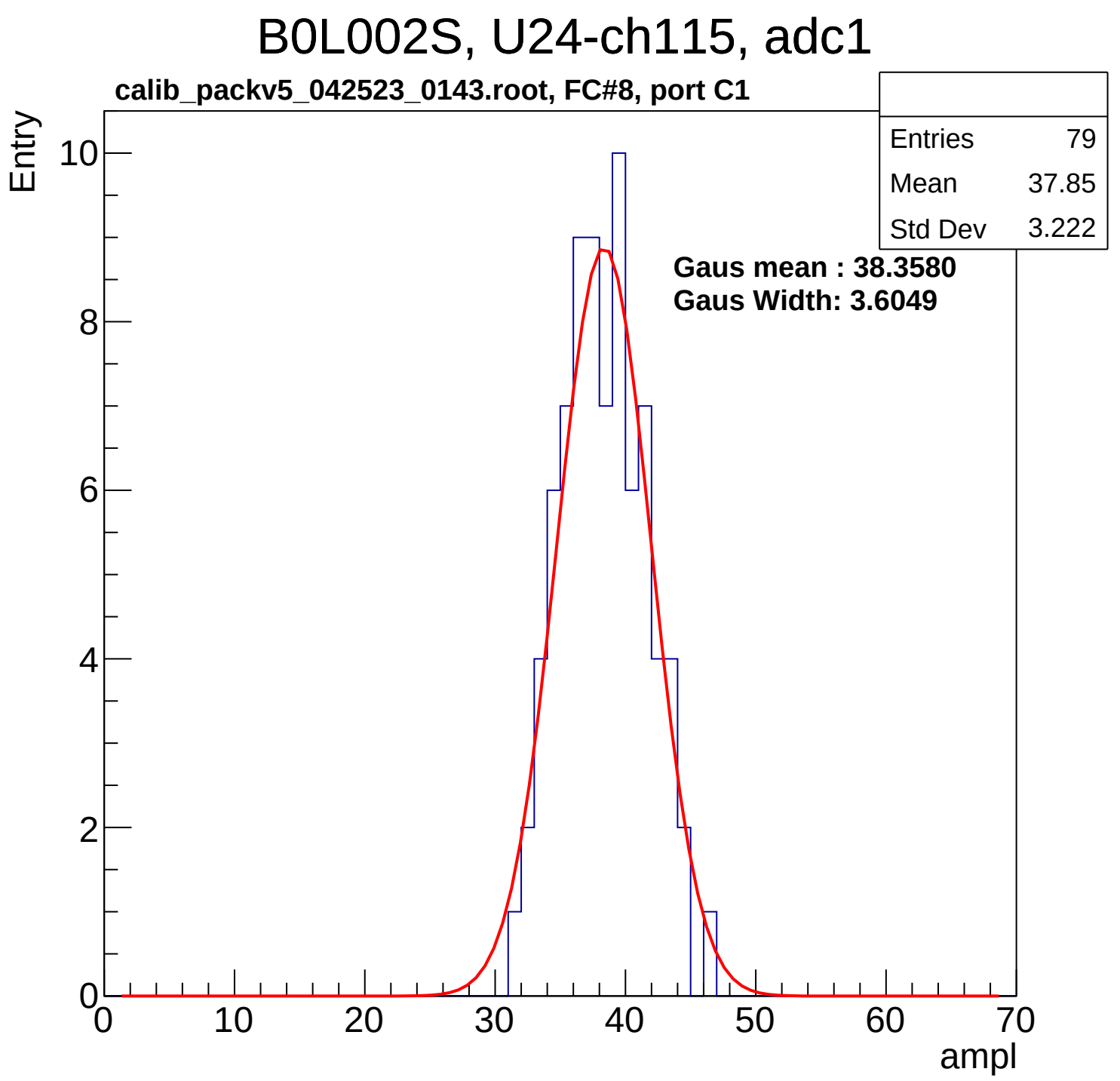
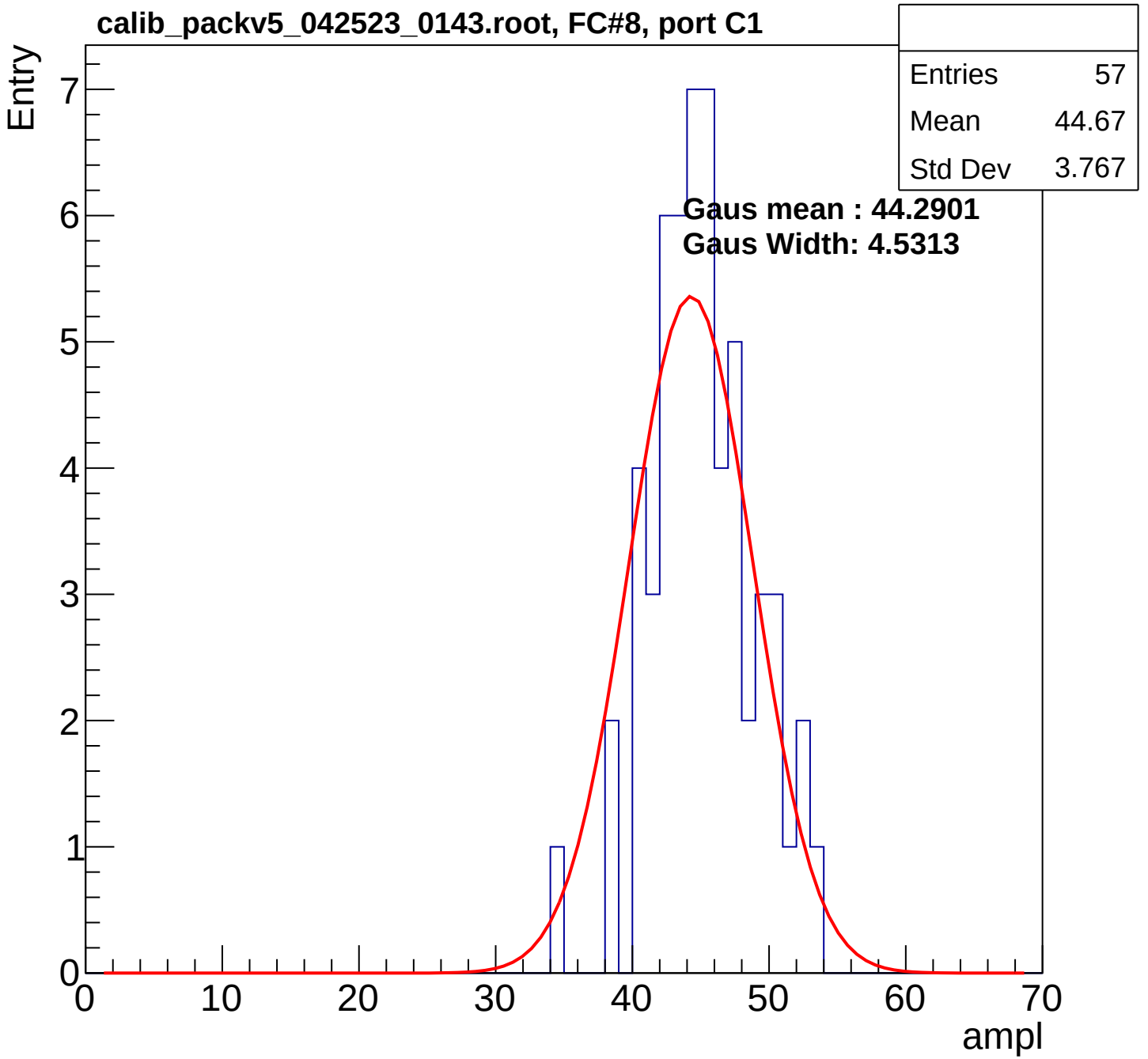
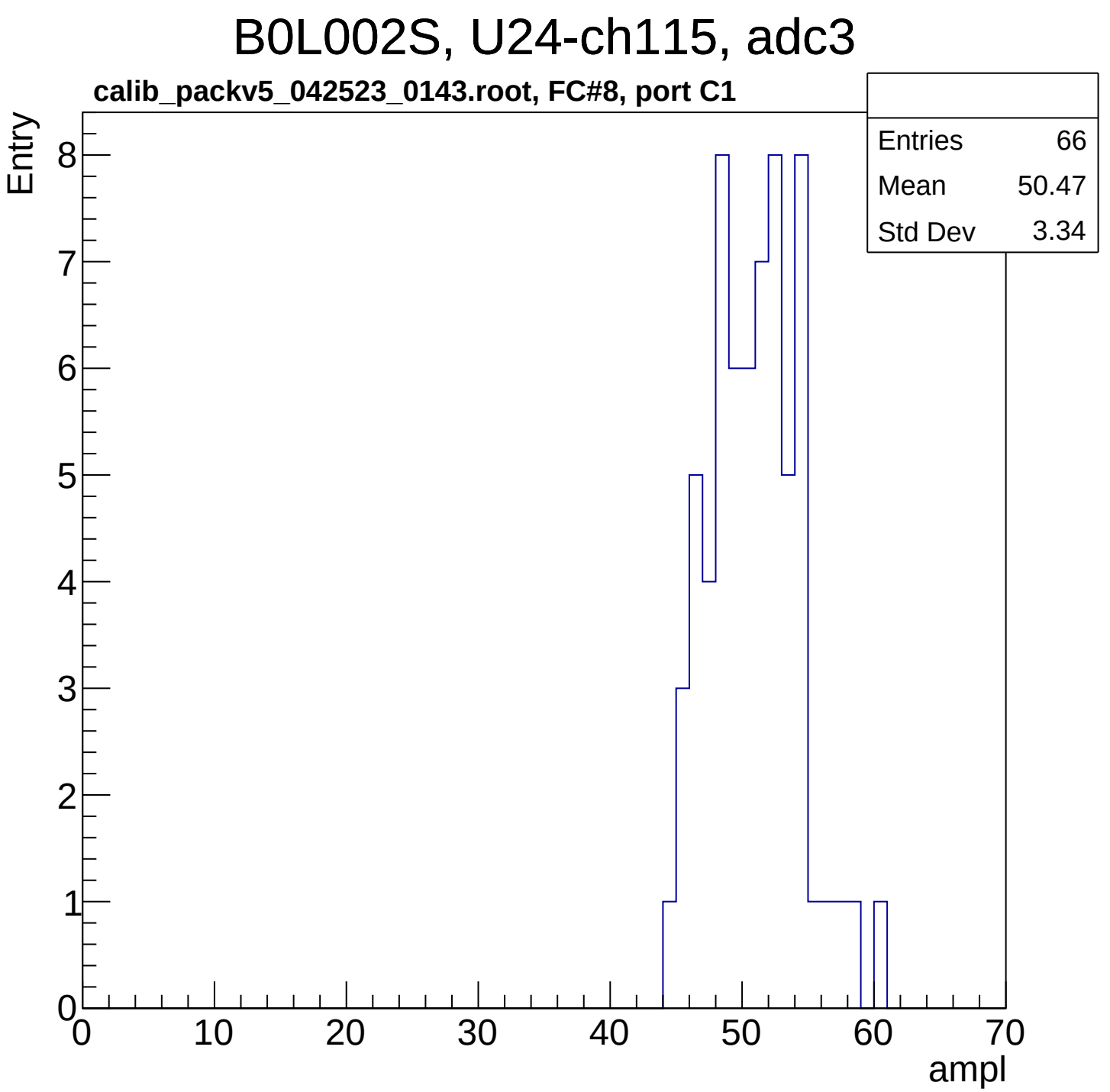
50
60
70
ampl

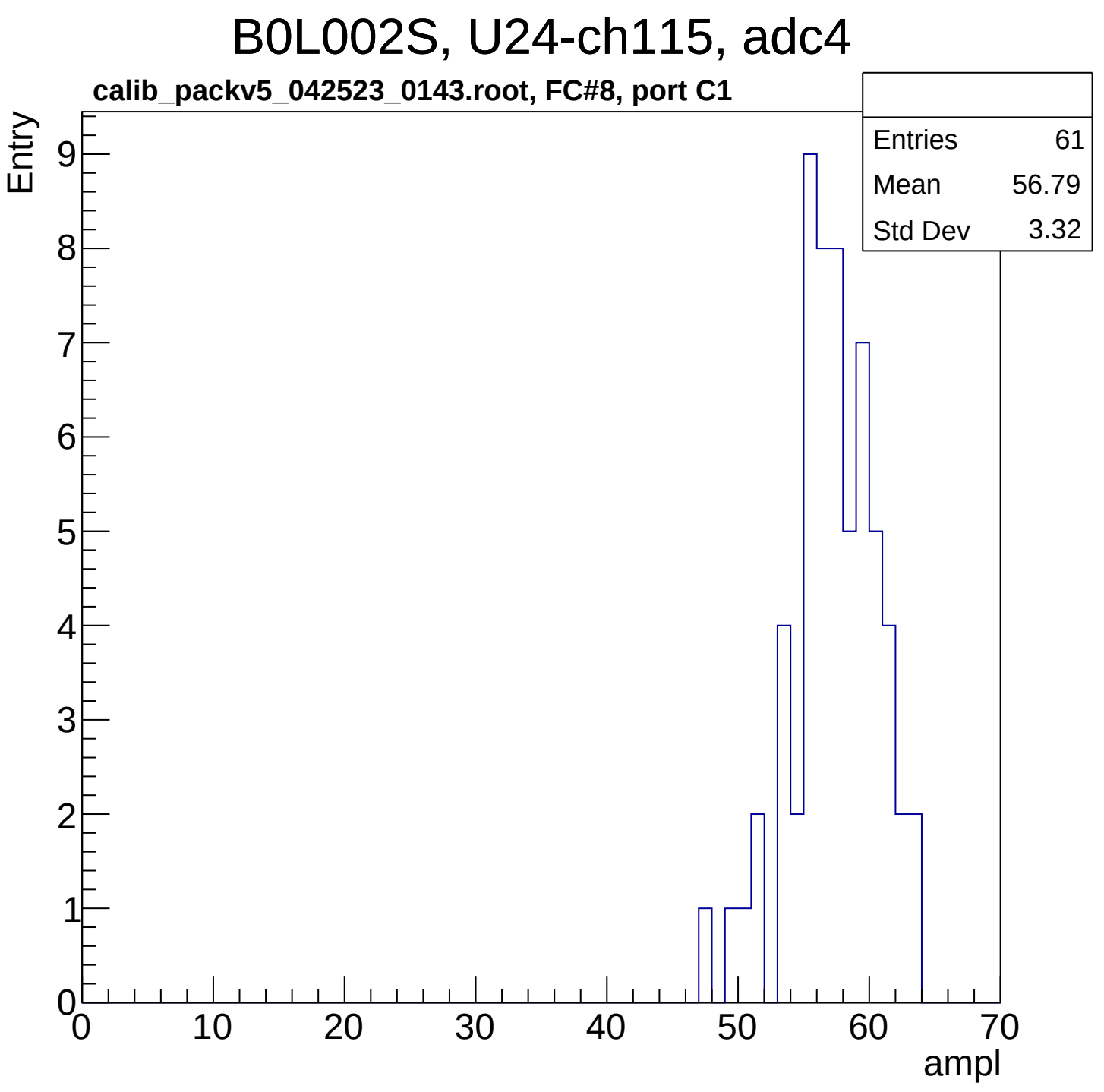

B0L002S, U24-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.38
Std Dev 2.929
Gaus mean : 30.6907
Gaus Width: 2.9583
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 37.85
Std Dev 3.222
Gaus mean : 38.3580
Gaus Width: 3.6049
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 44.67
Std Dev 3.767
Gaus mean : 44.2901
Gaus Width: 4.5313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.47
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.79
Std Dev 3.32
Entry
ampl
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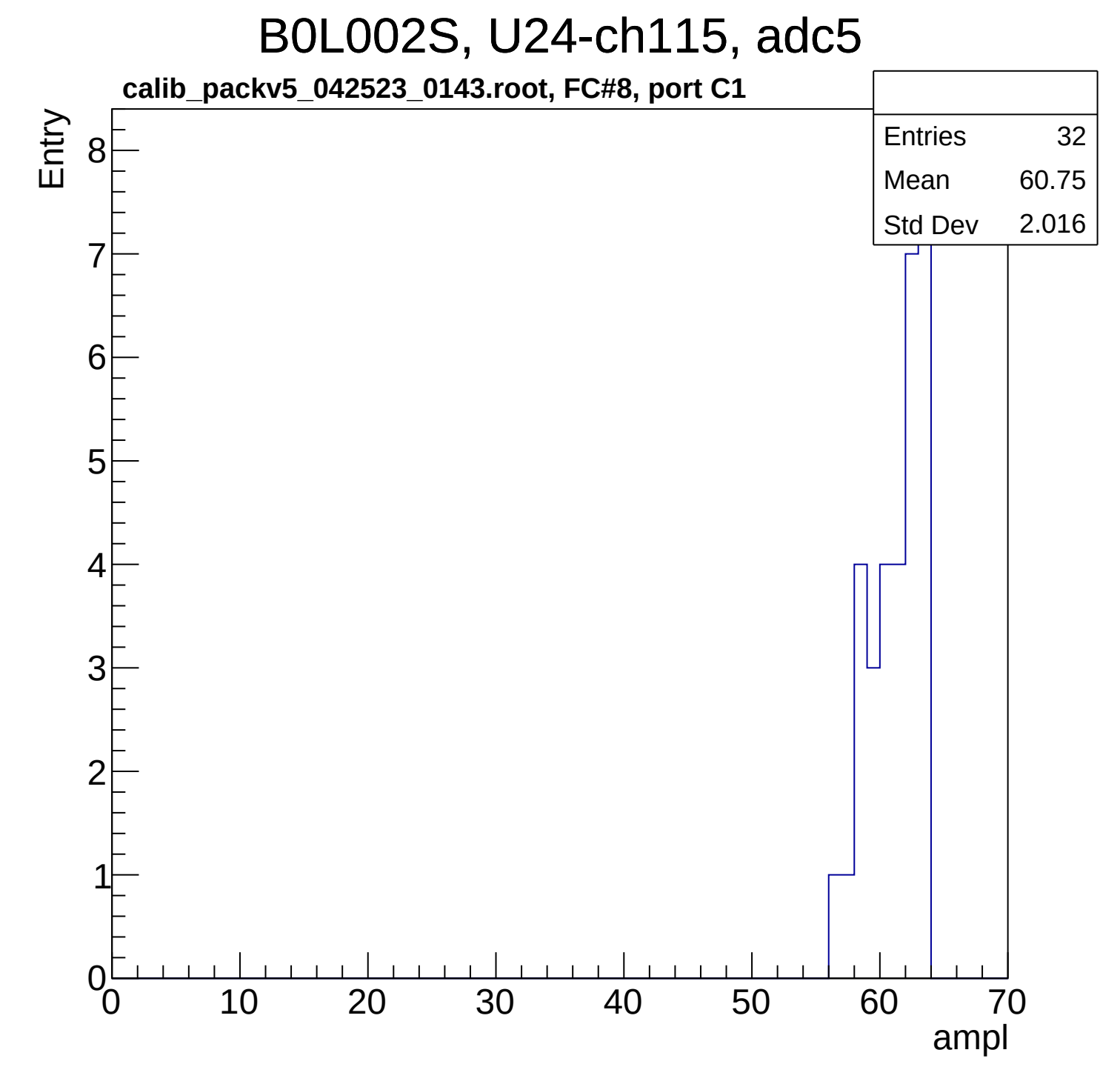
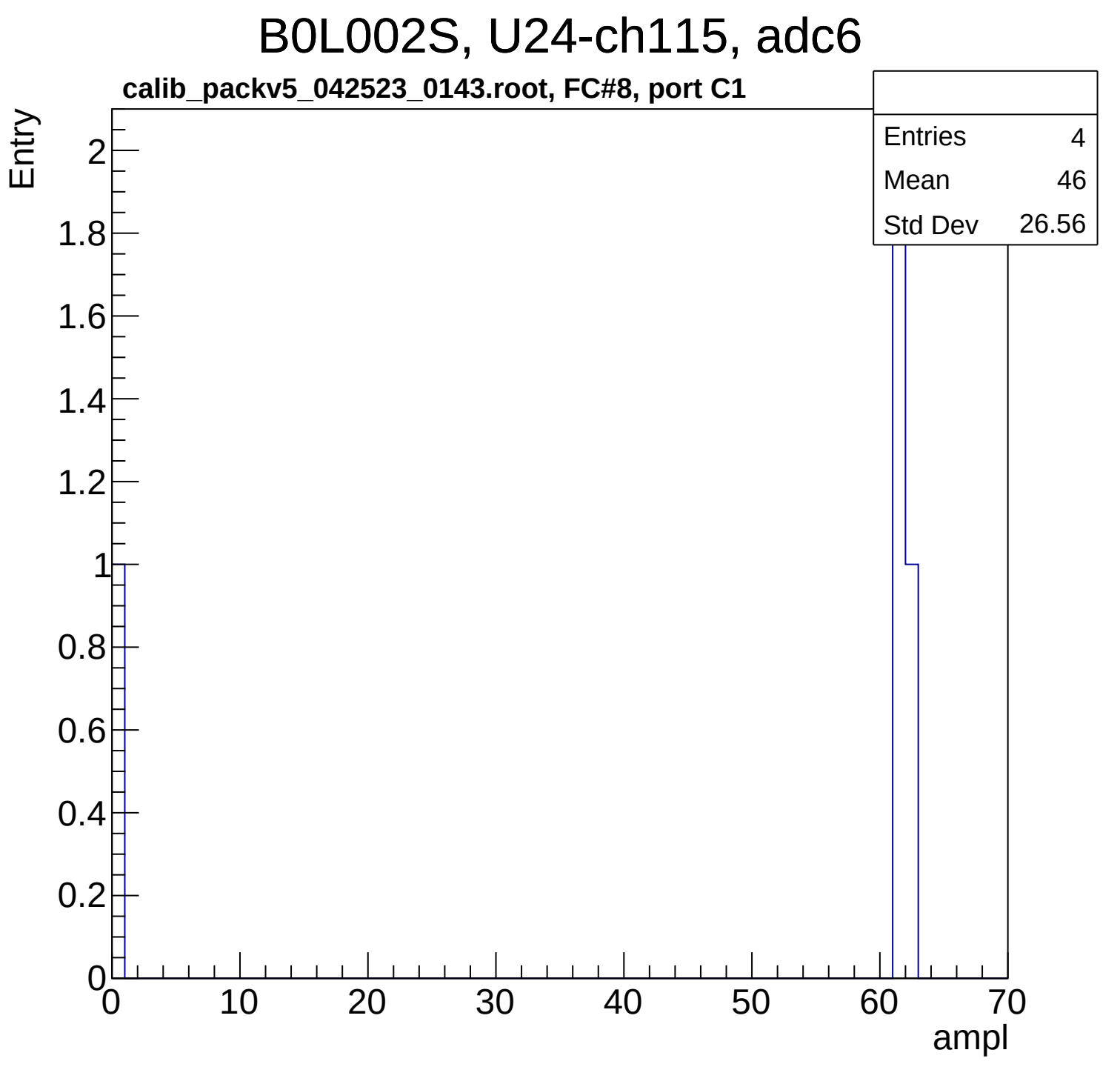
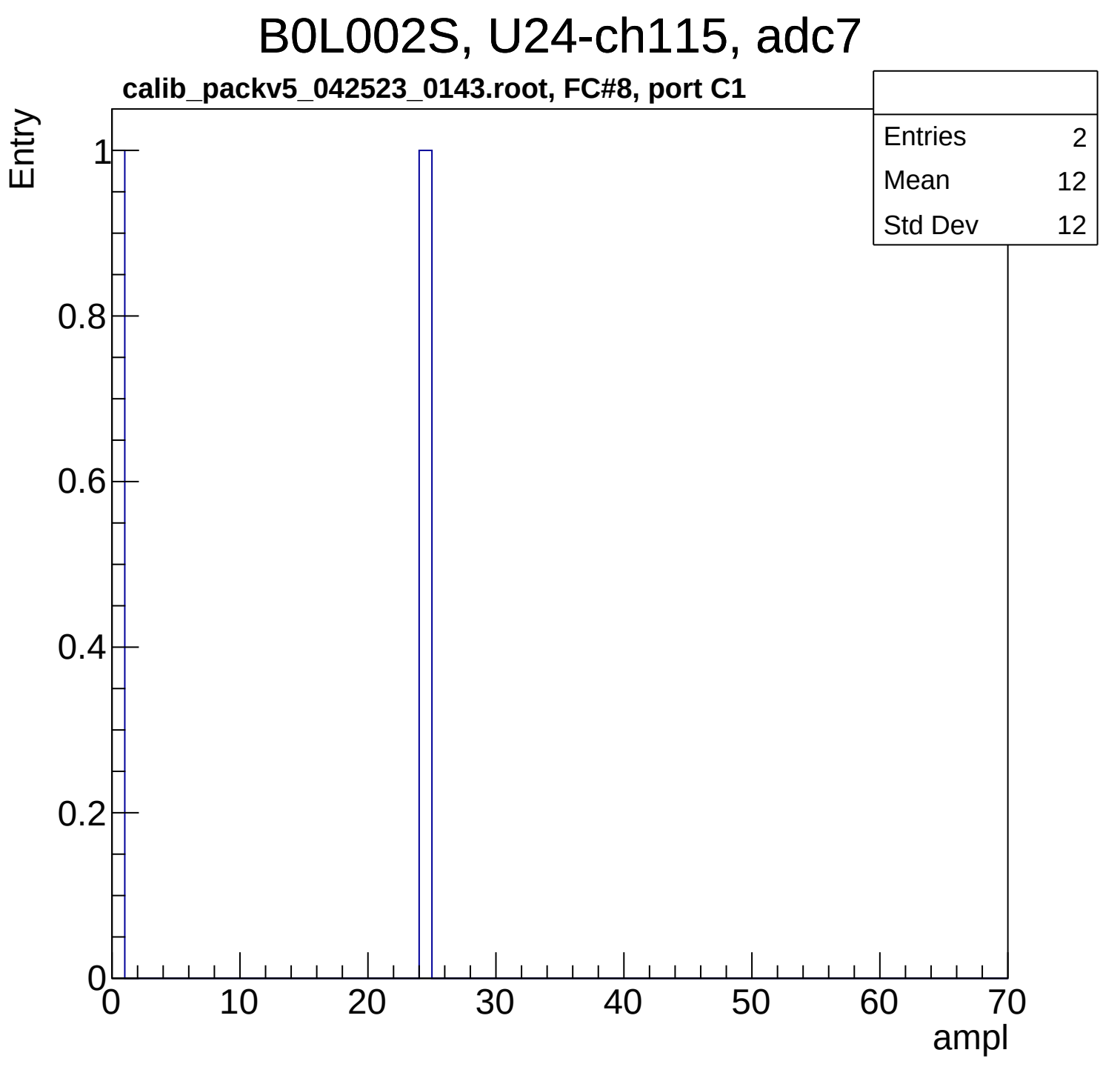
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 60.75
Std Dev 2.016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46
Std Dev 26.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U24-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
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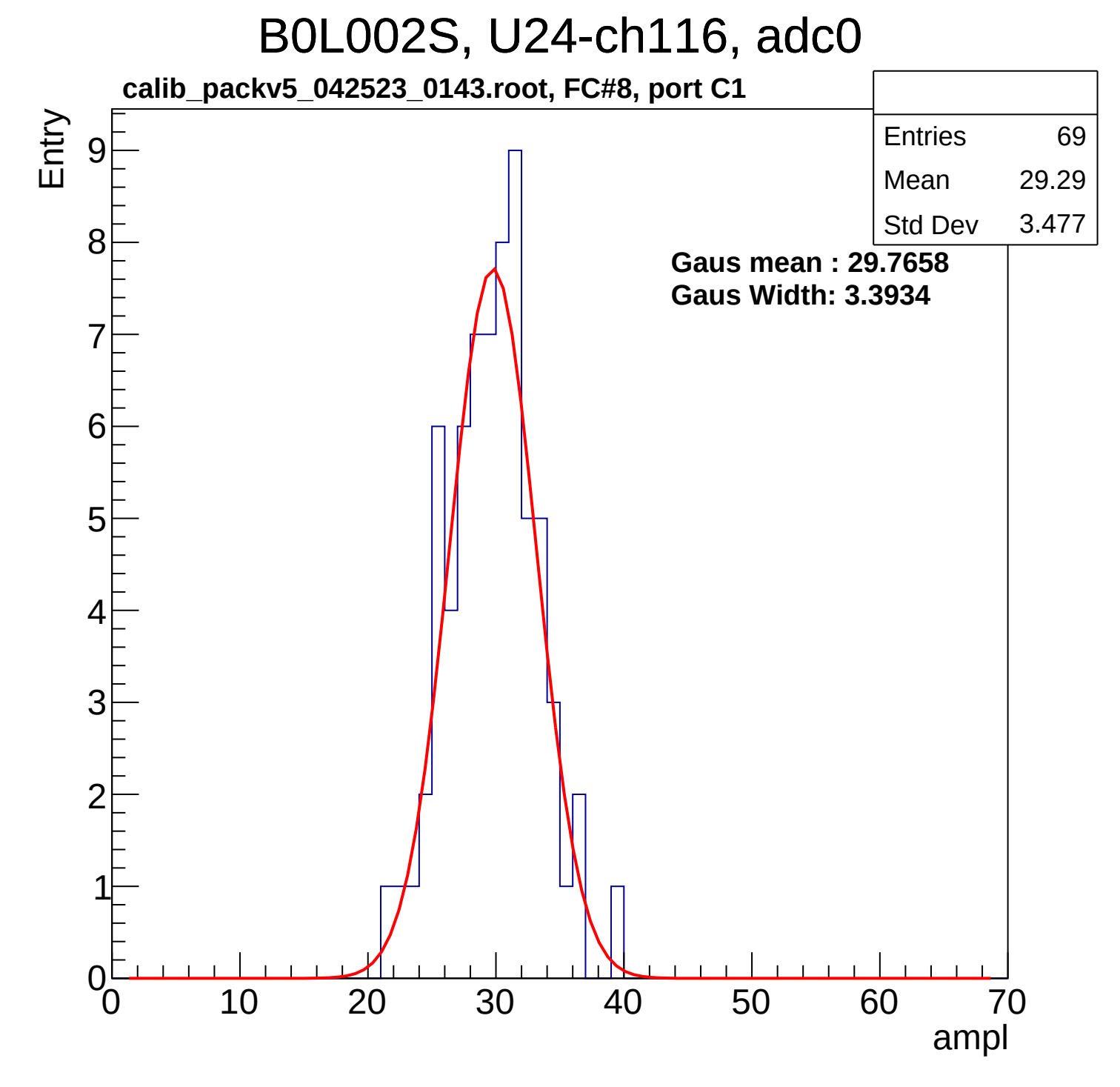
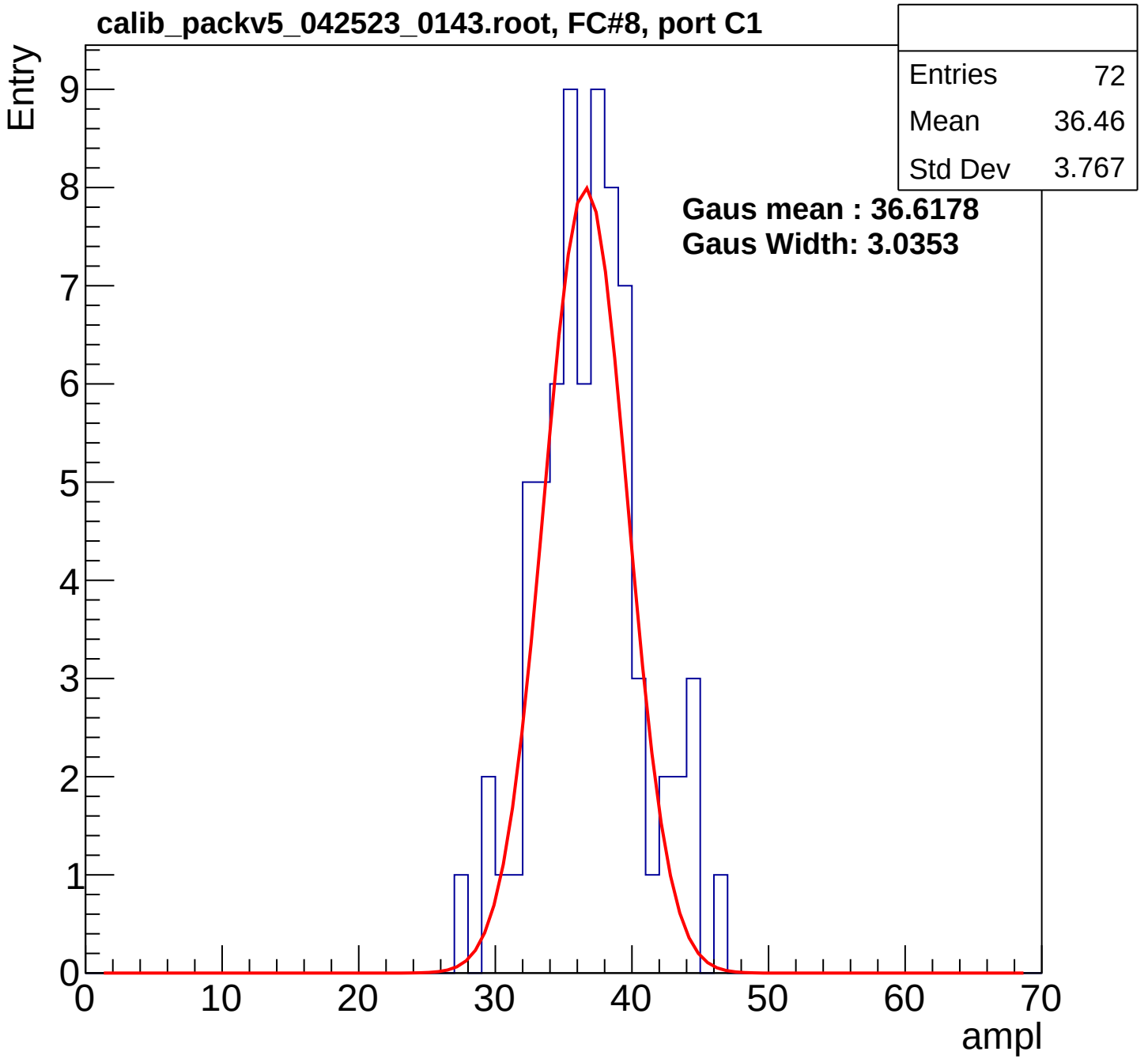
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.29
Std Dev 3.477
Gaus mean : 29.7658
Gaus Width: 3.3934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.46
Std Dev 3.767
Gaus mean : 36.6178
Gaus Width: 3.0353
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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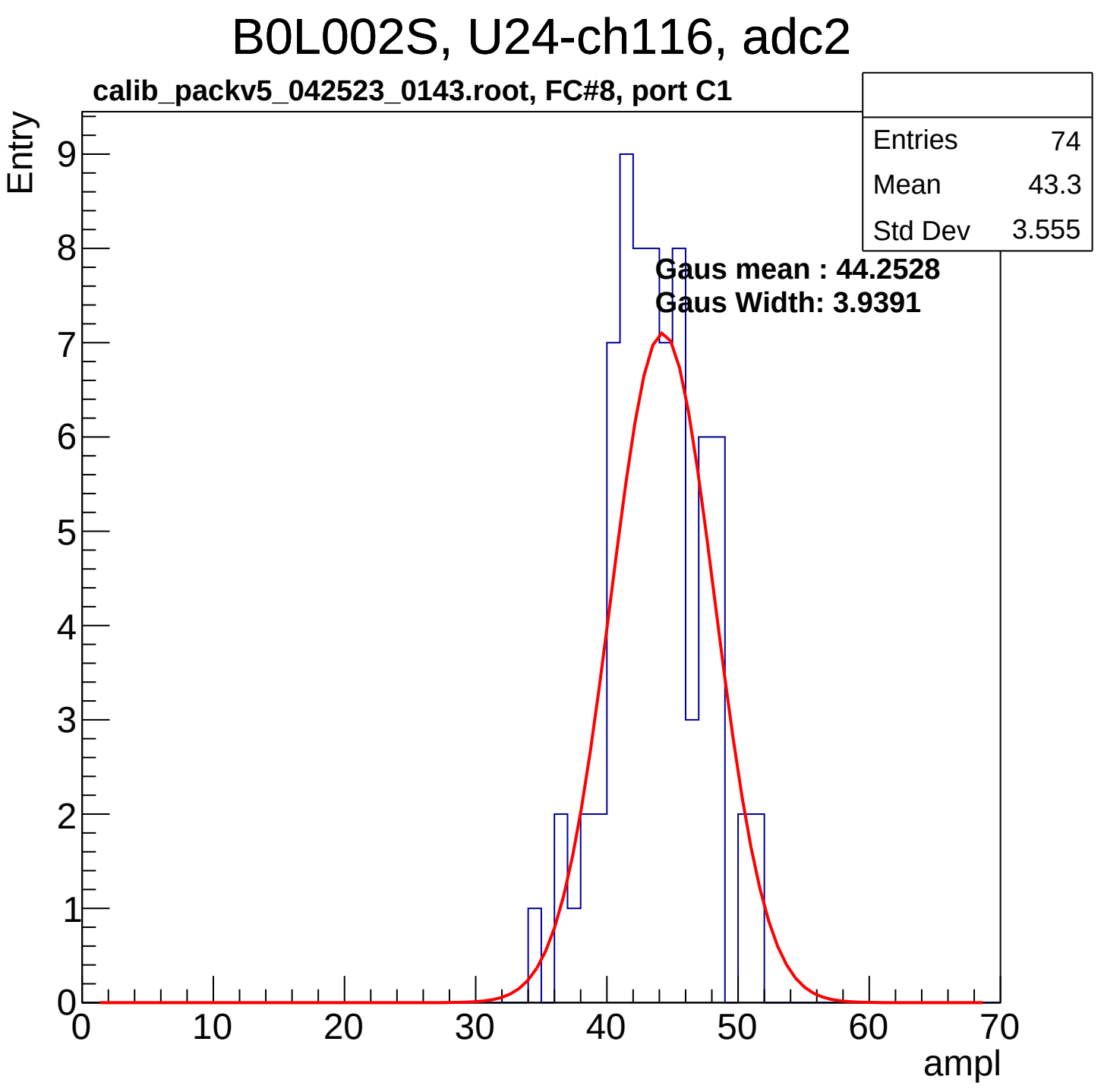
B0L002S, U24-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.3
Std Dev 3.555
Gaus mean : 44.2528
Gaus Width: 3.9391
Entry
ampl
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1
2
3
4
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6
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8
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0
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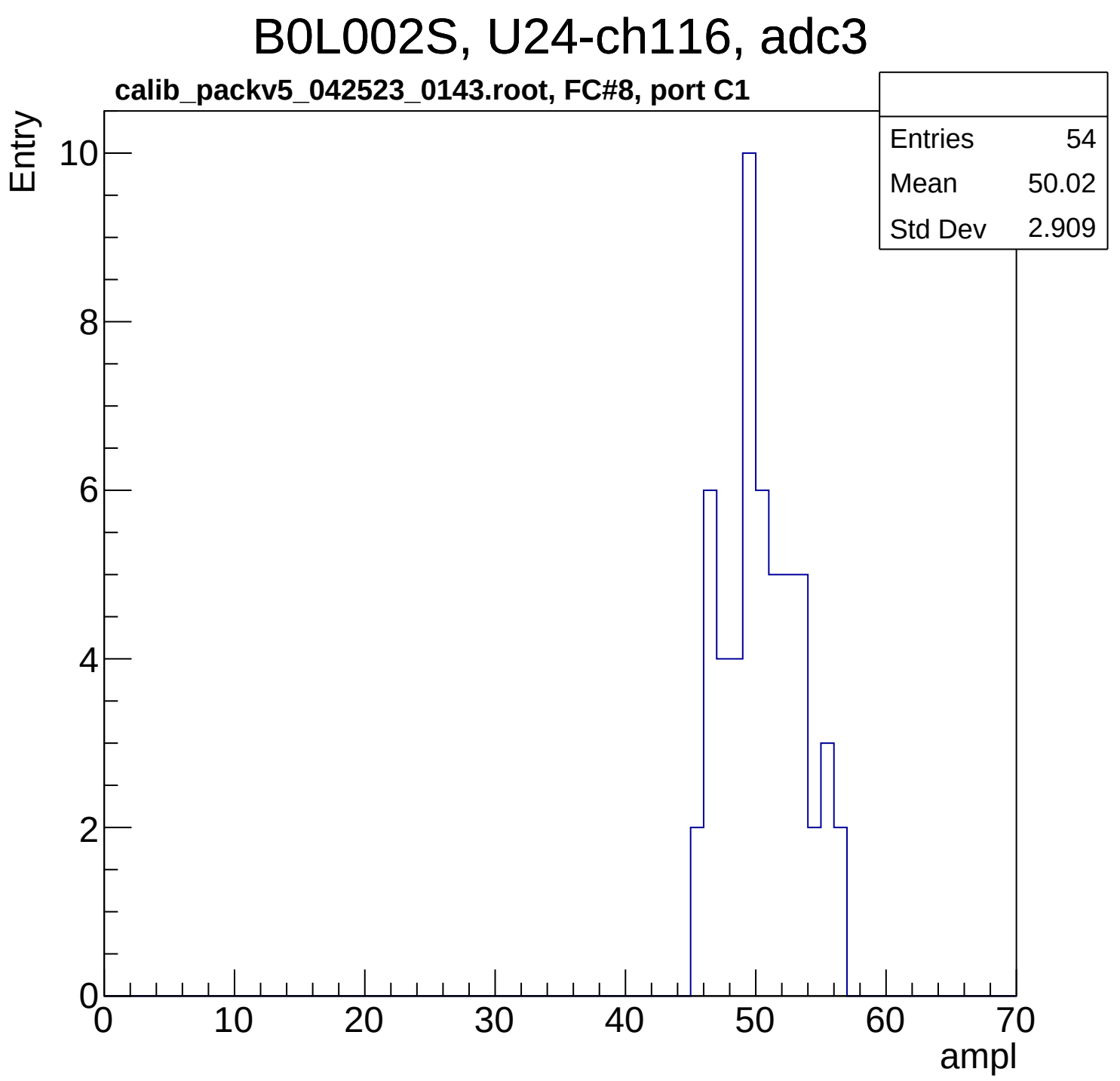
B0L002S, U24-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.02
Std Dev 2.909
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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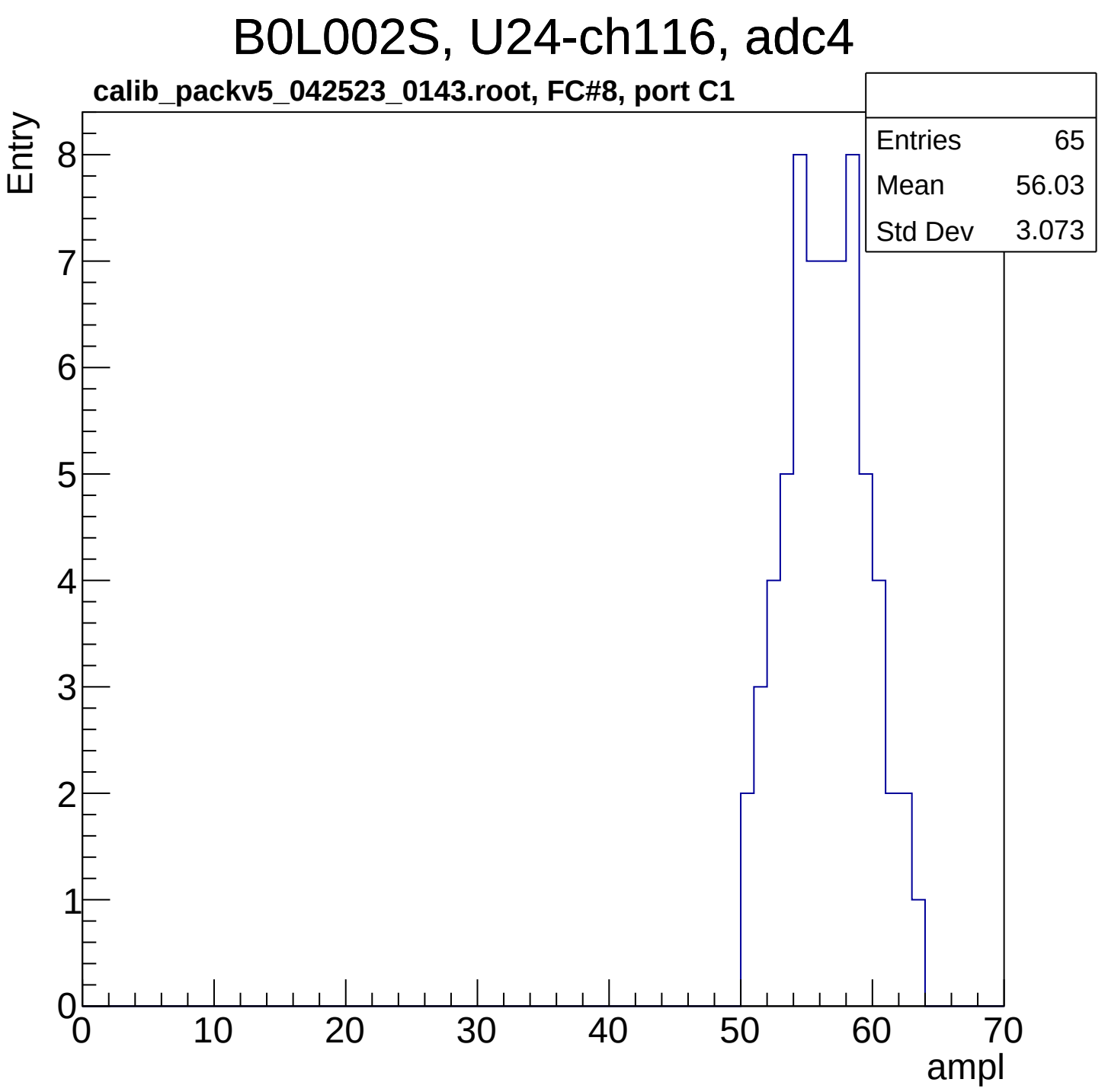
B0L002S, U24-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.03
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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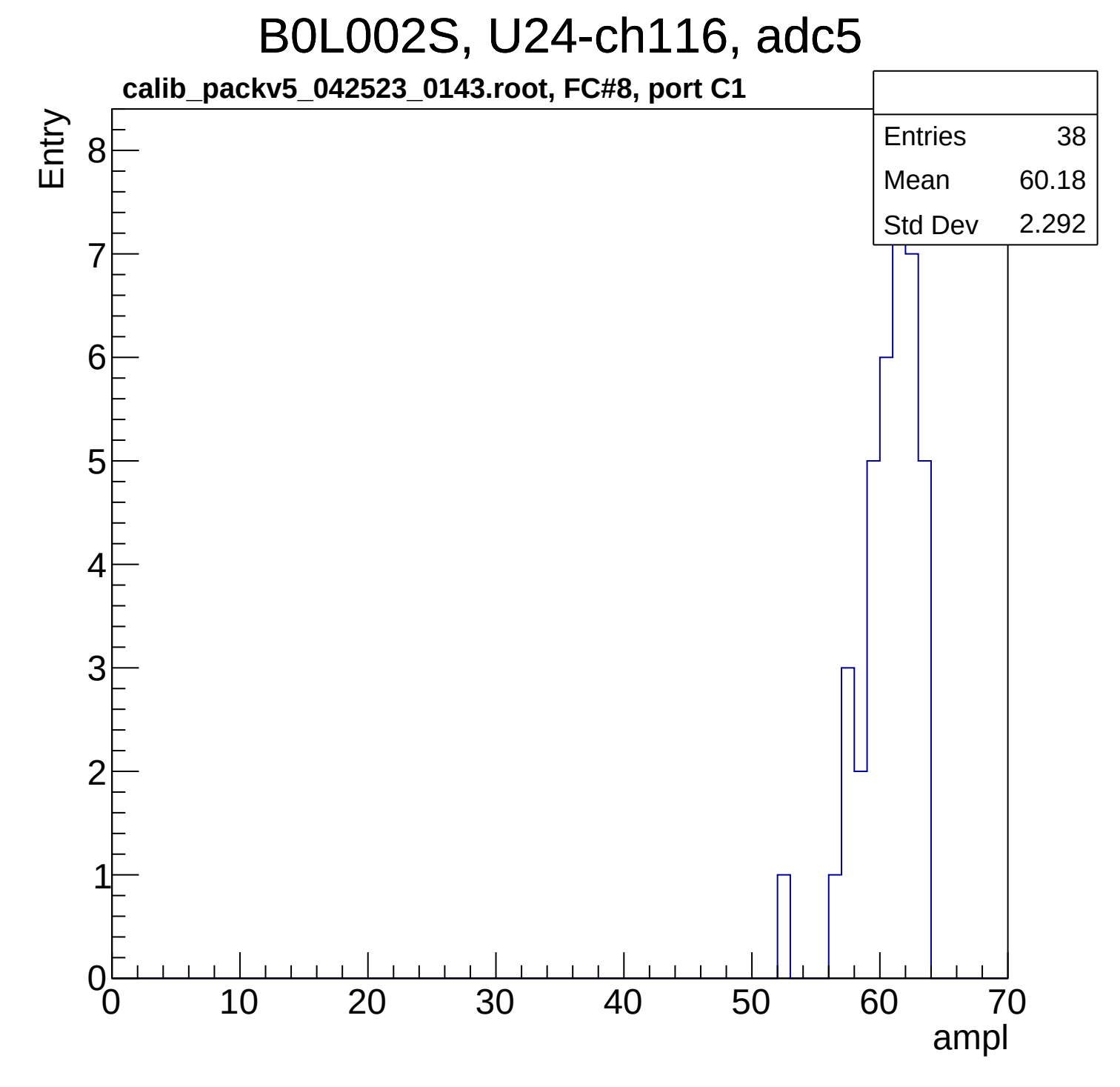
B0L002S, U24-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 60.18
Std Dev 2.292
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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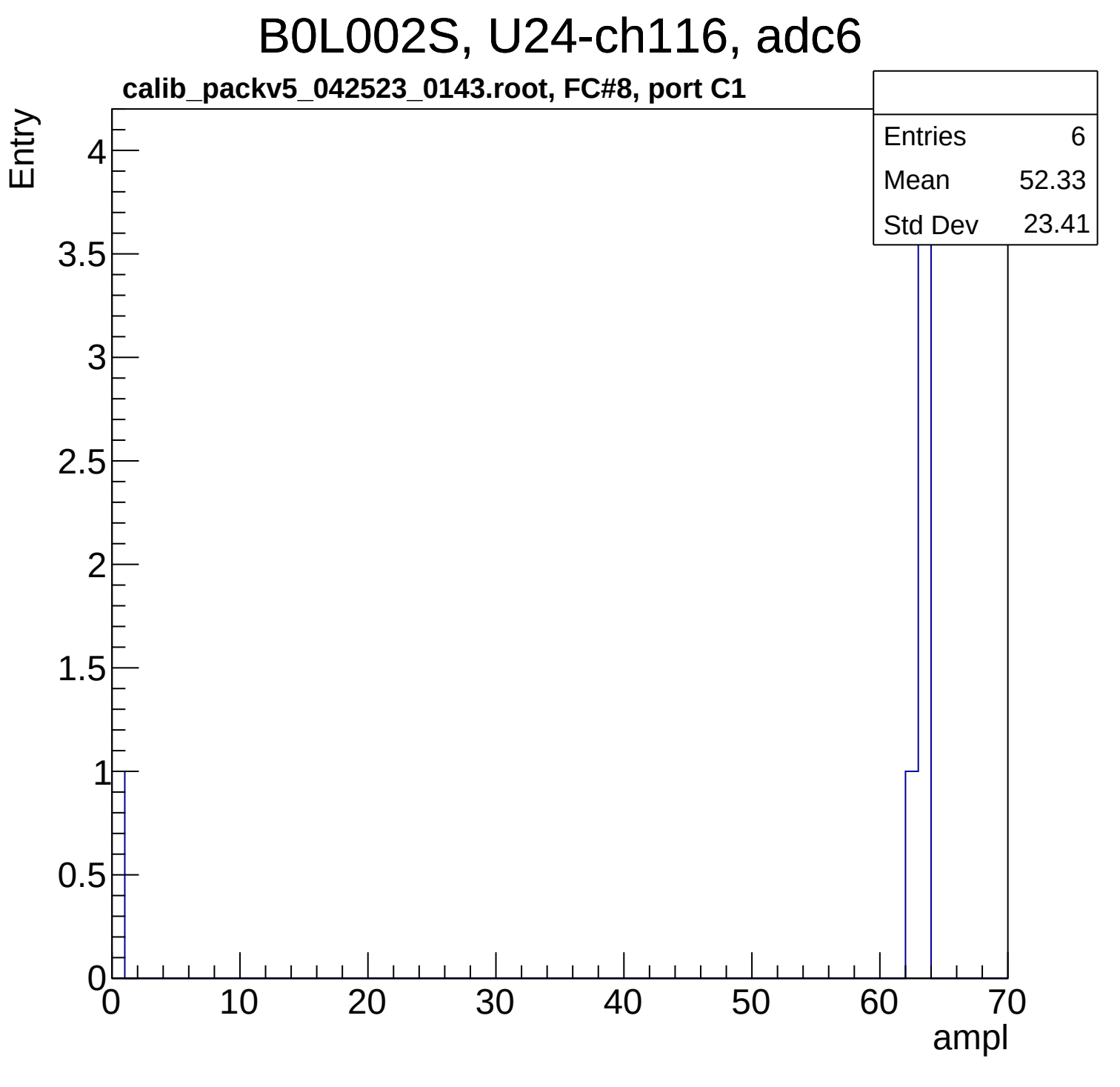
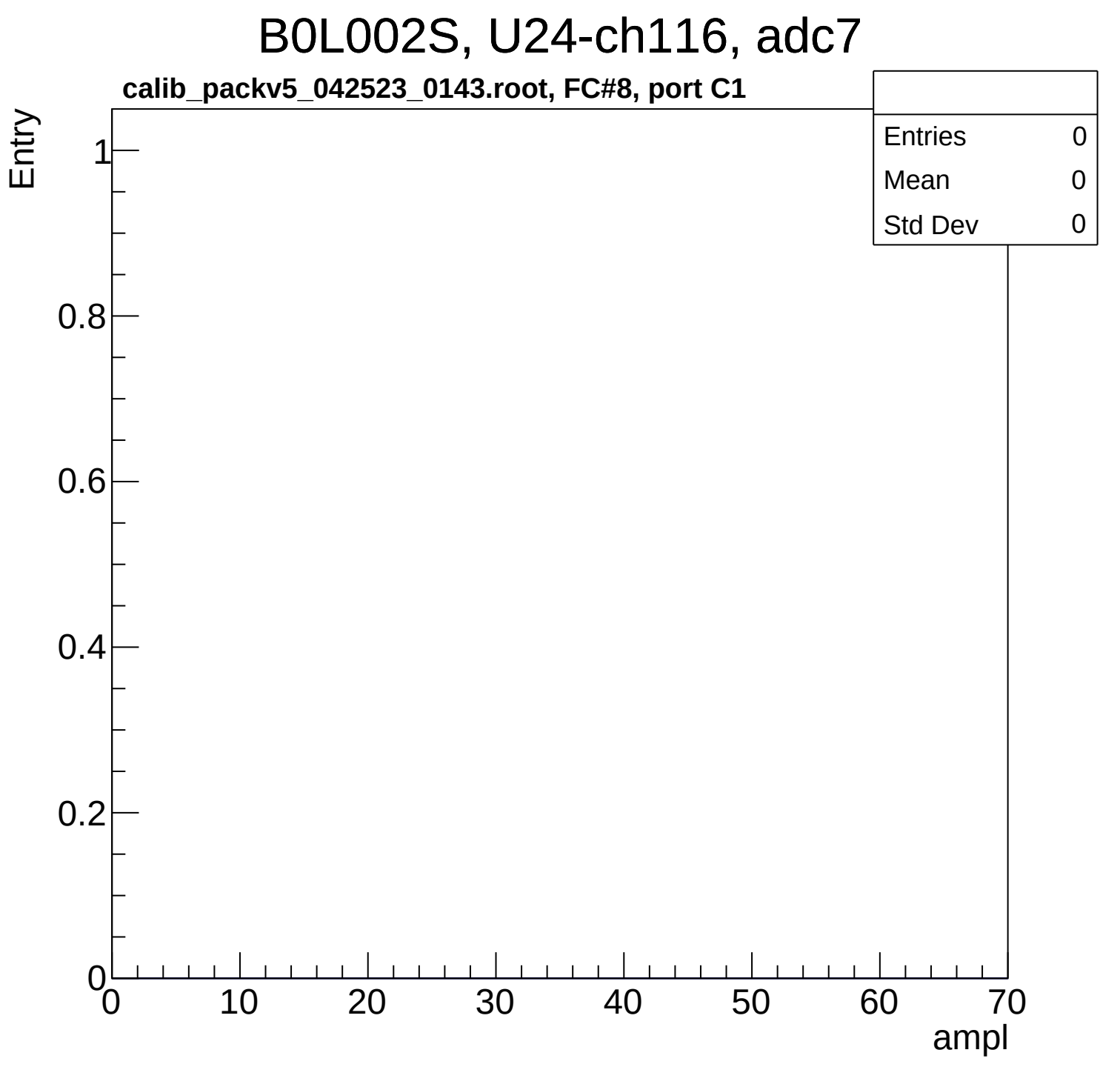
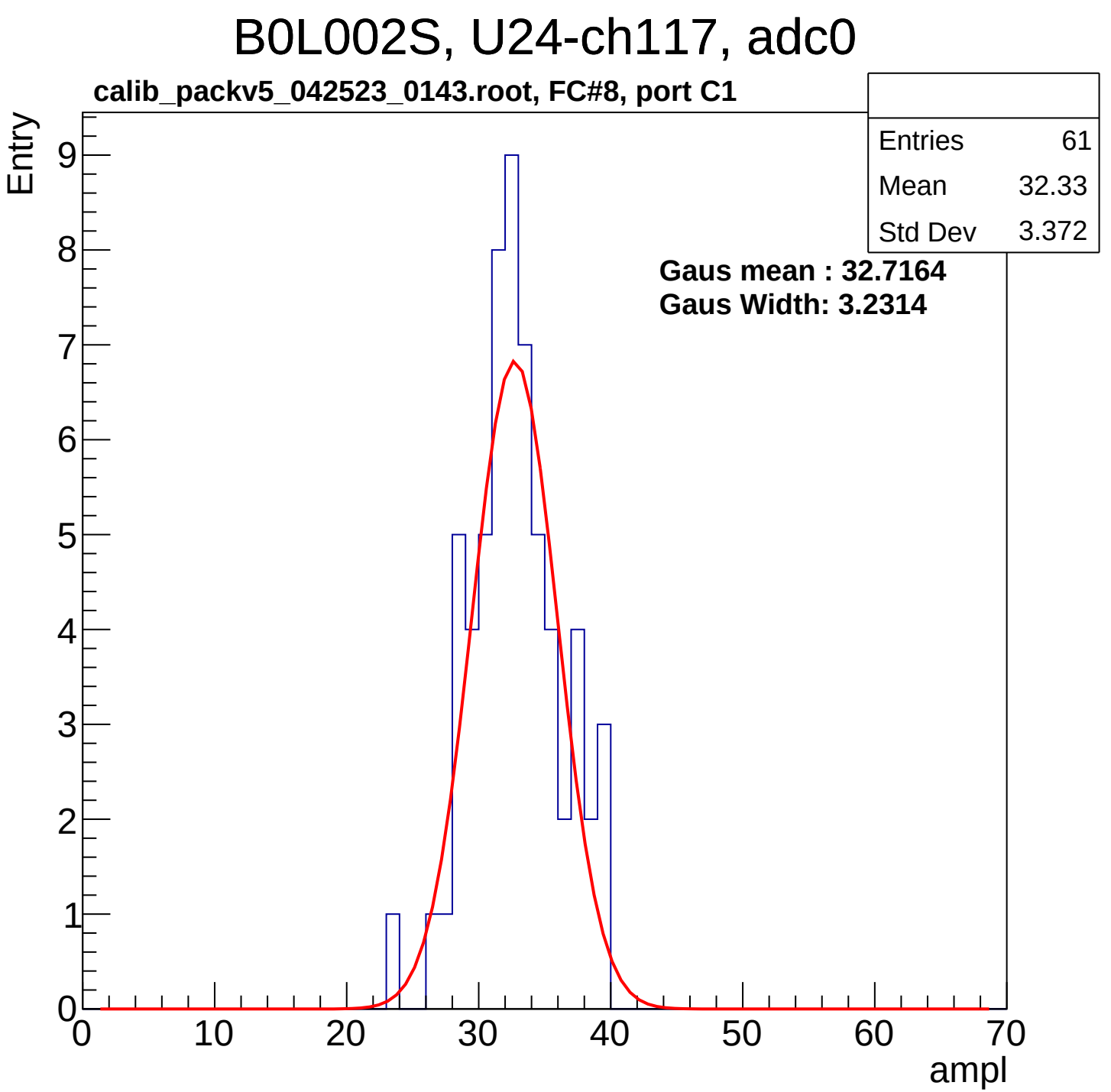

B0L002S, U24-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 52.33
Std Dev 23.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U24-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 32.33
Std Dev 3.372
Gaus mean : 32.7164
Gaus Width: 3.2314
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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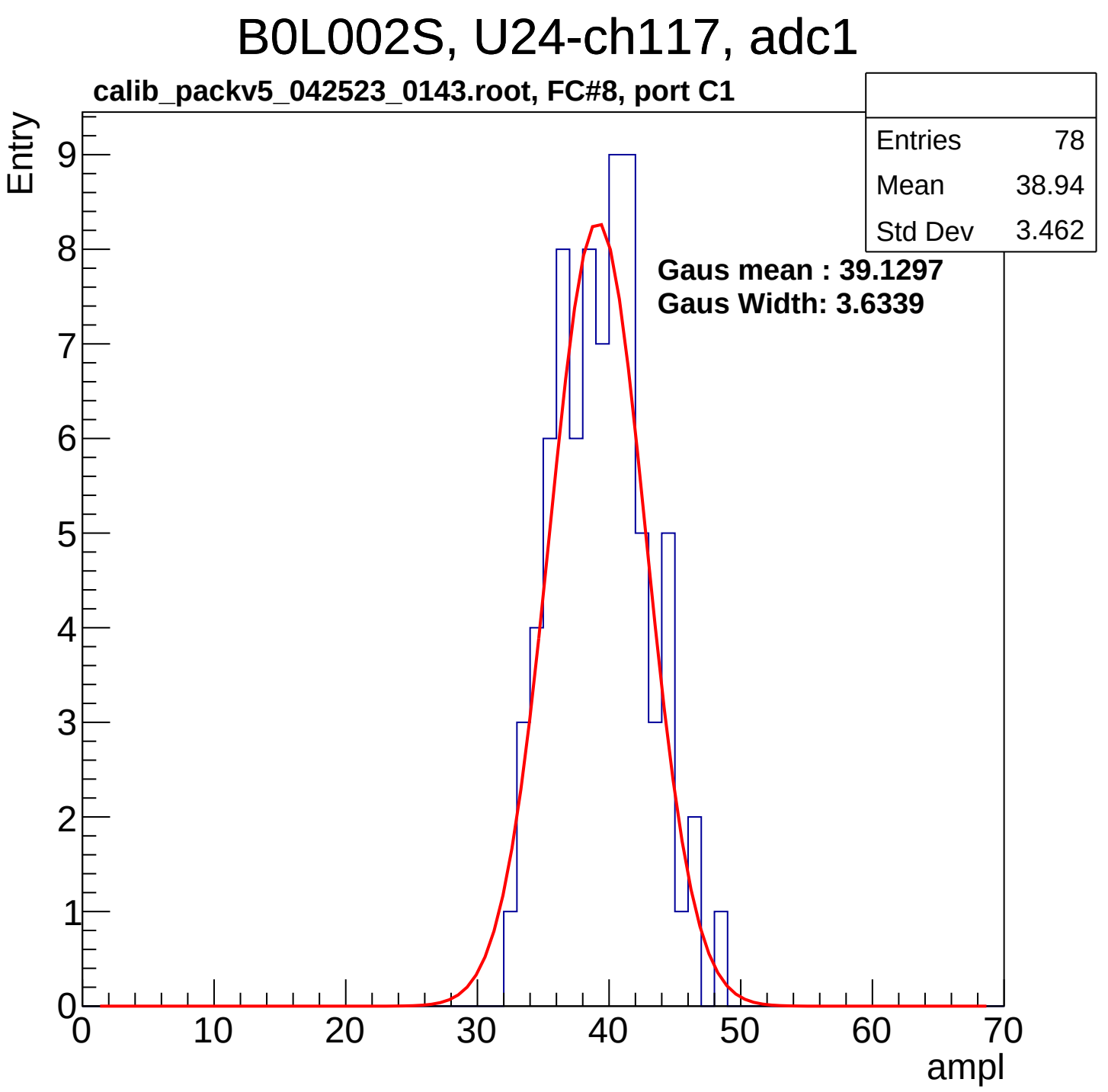
B0L002S, U24-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.94
Std Dev 3.462
Gaus mean : 39.1297
Gaus Width: 3.6339
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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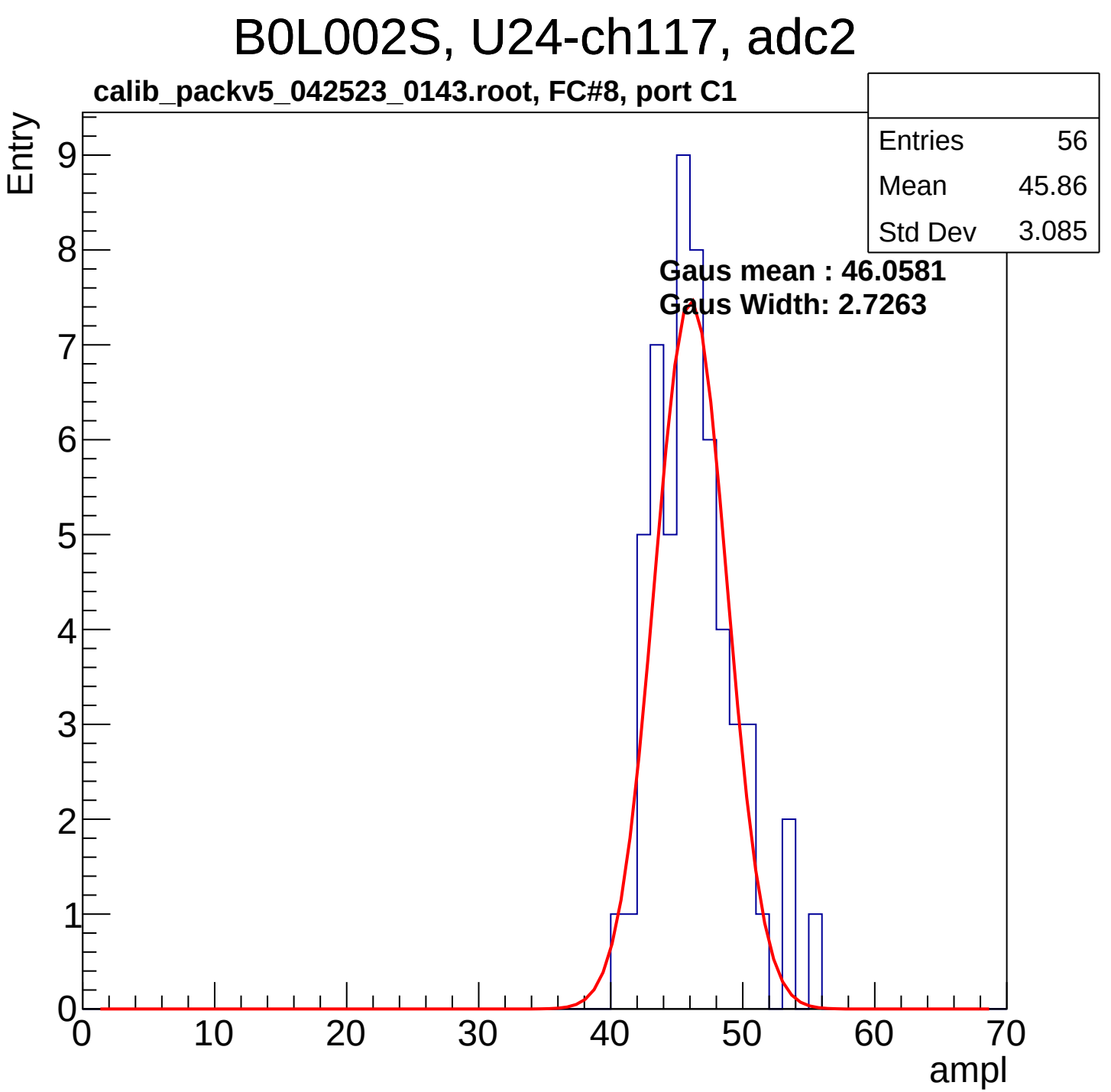
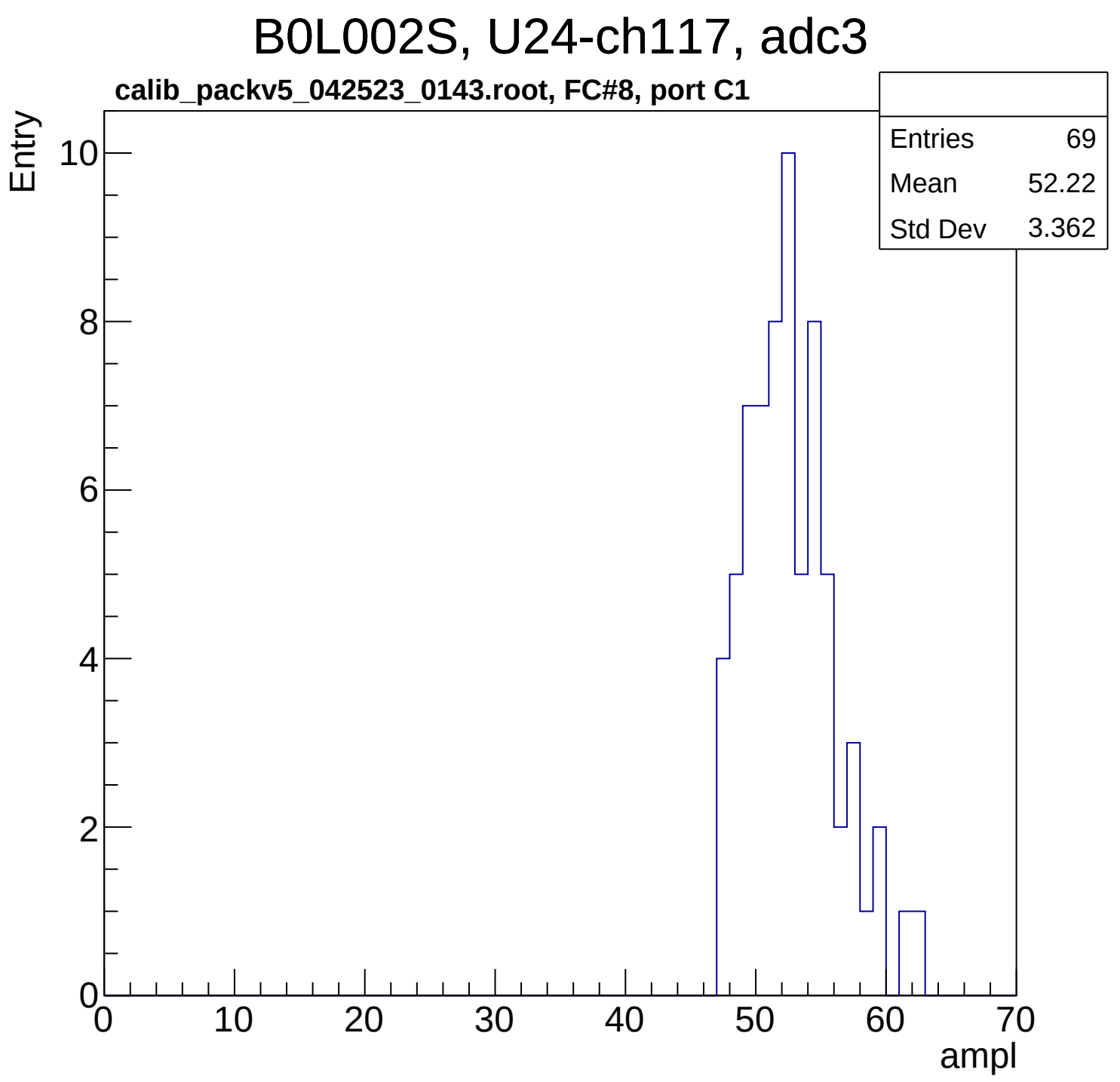
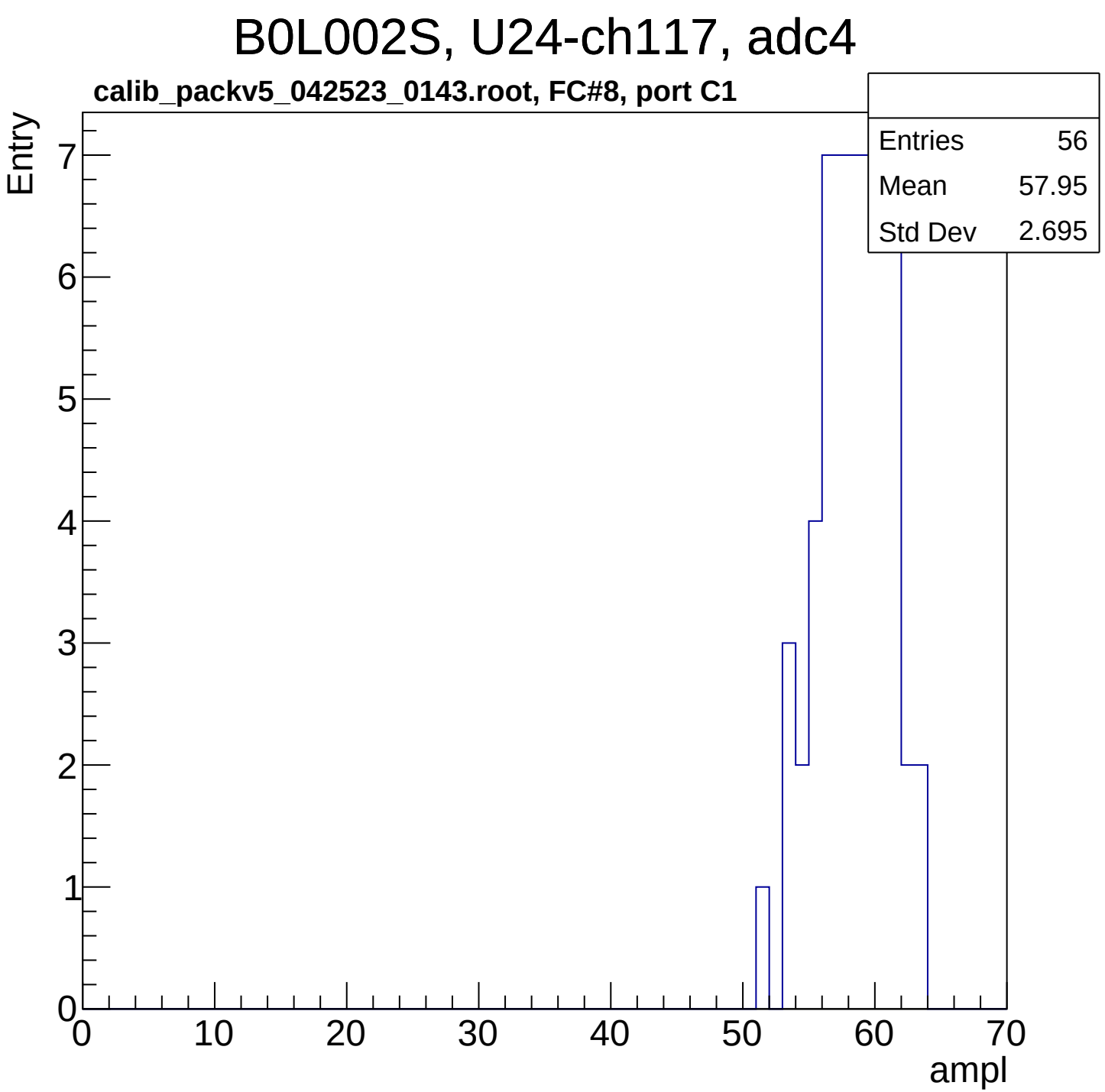
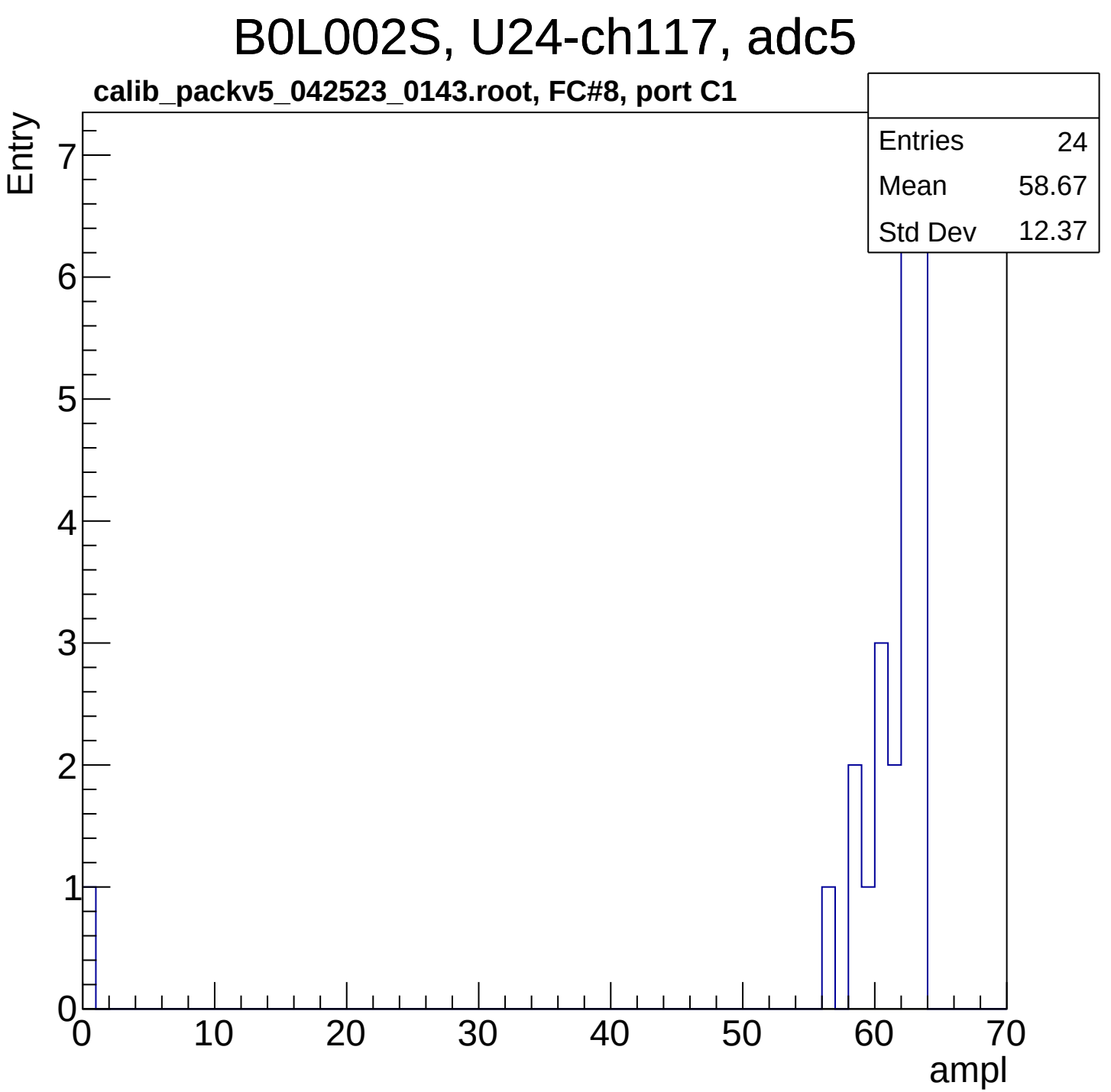
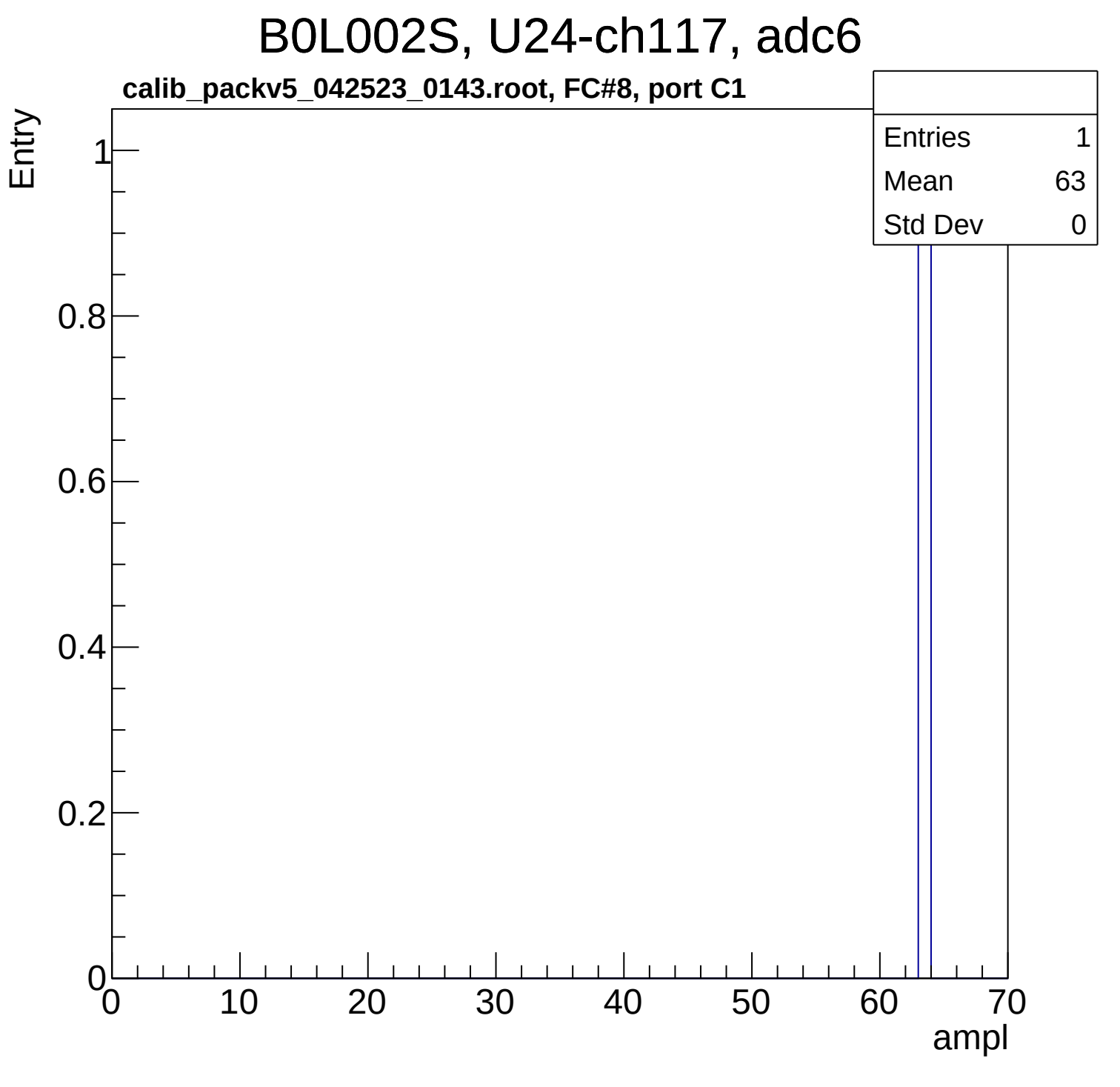
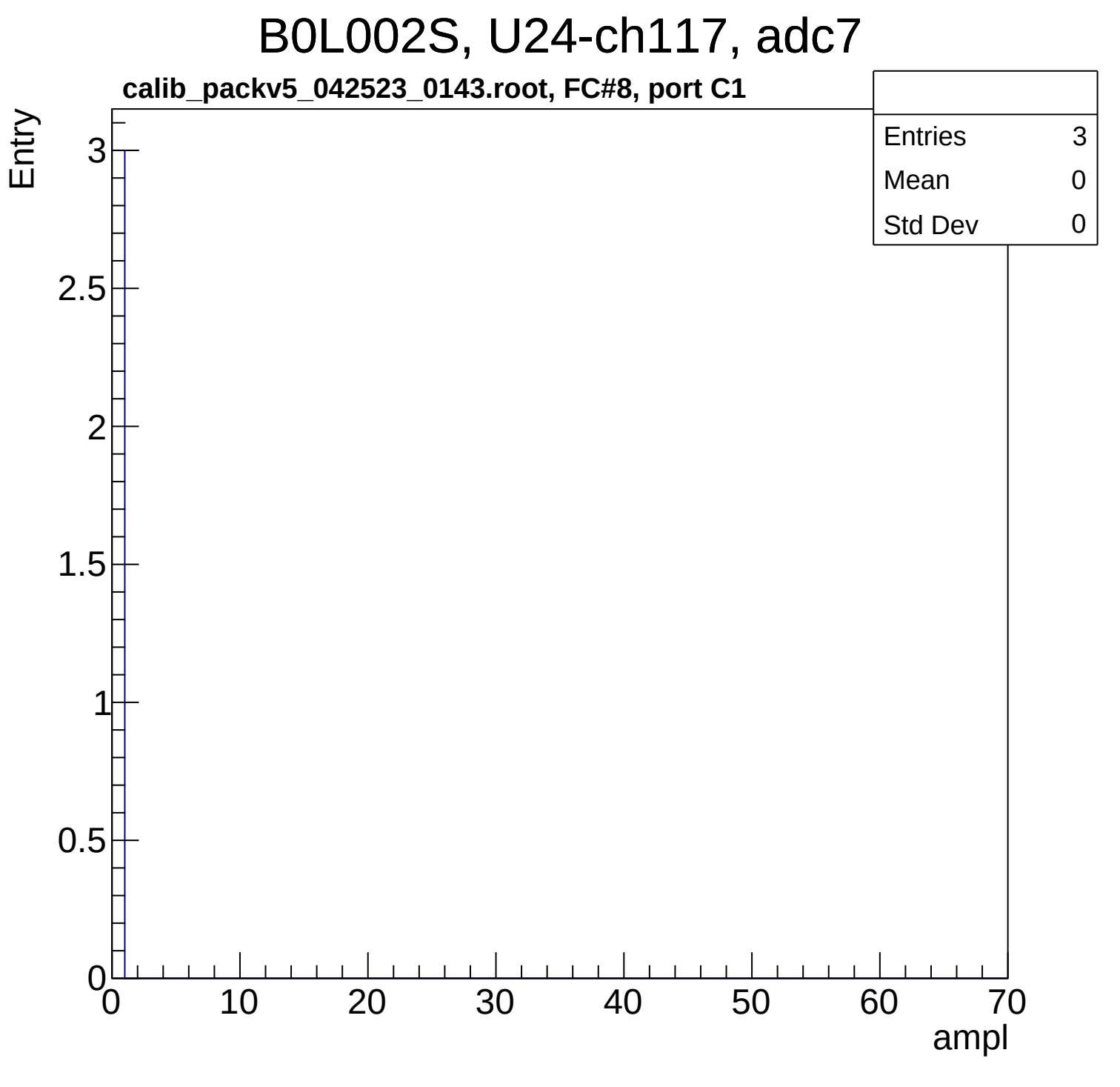

B0L002S, U24-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.86
Std Dev 3.085
Gaus mean : 46.0581
Gaus Width: 2.7263
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

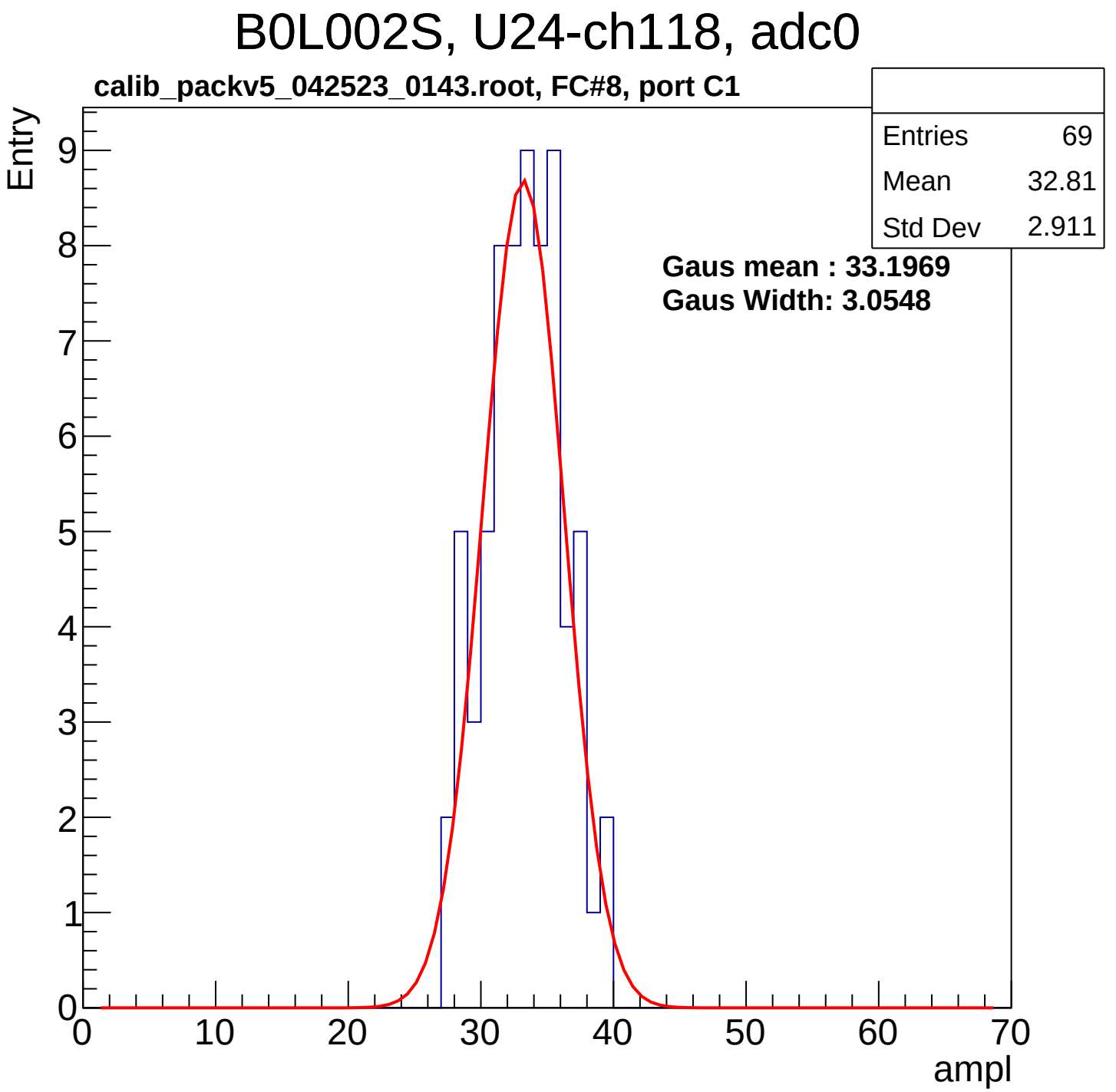

B0L002S, U24-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.22
Std Dev 3.362
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

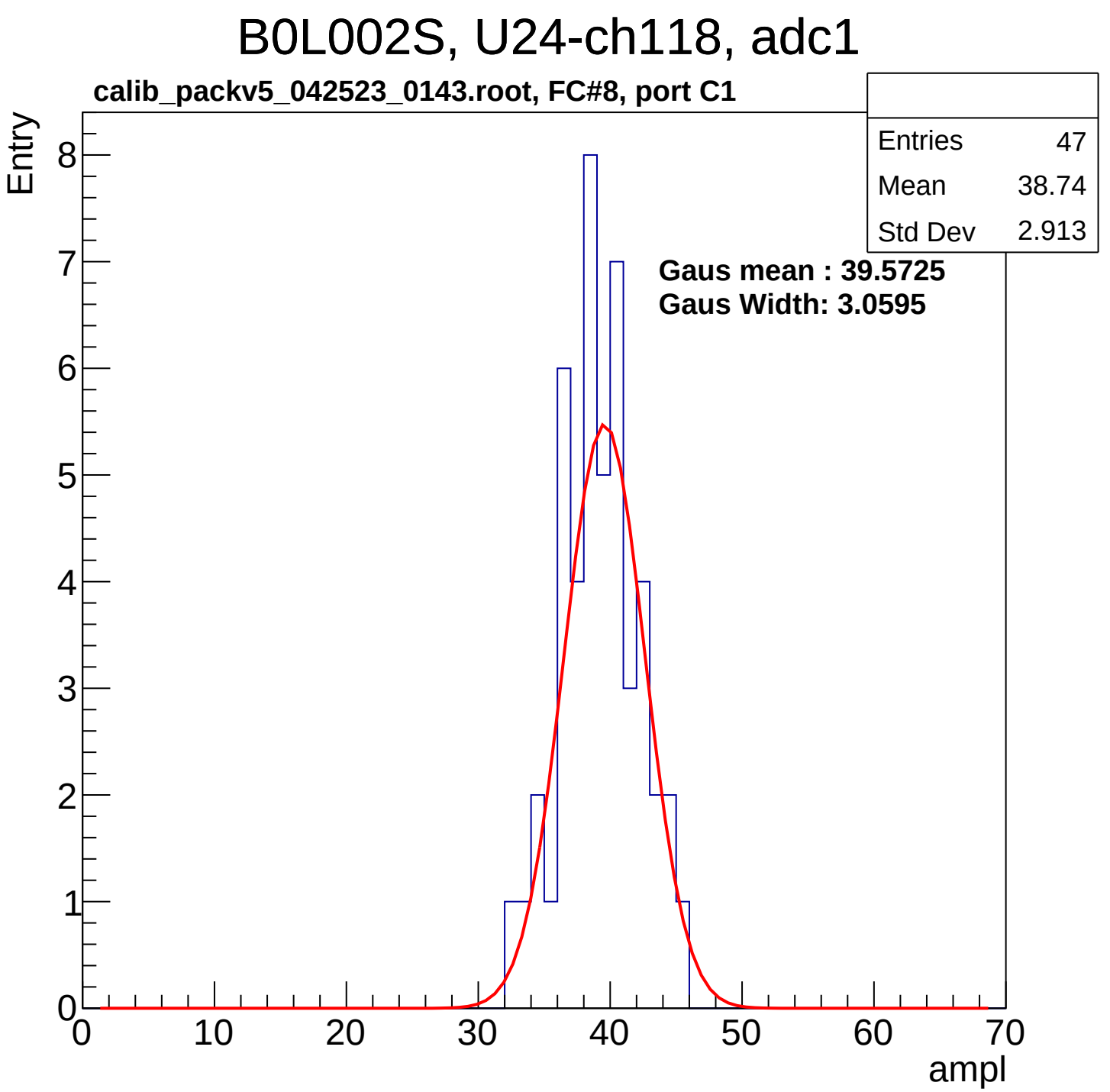

B0L002S, U24-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.95
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

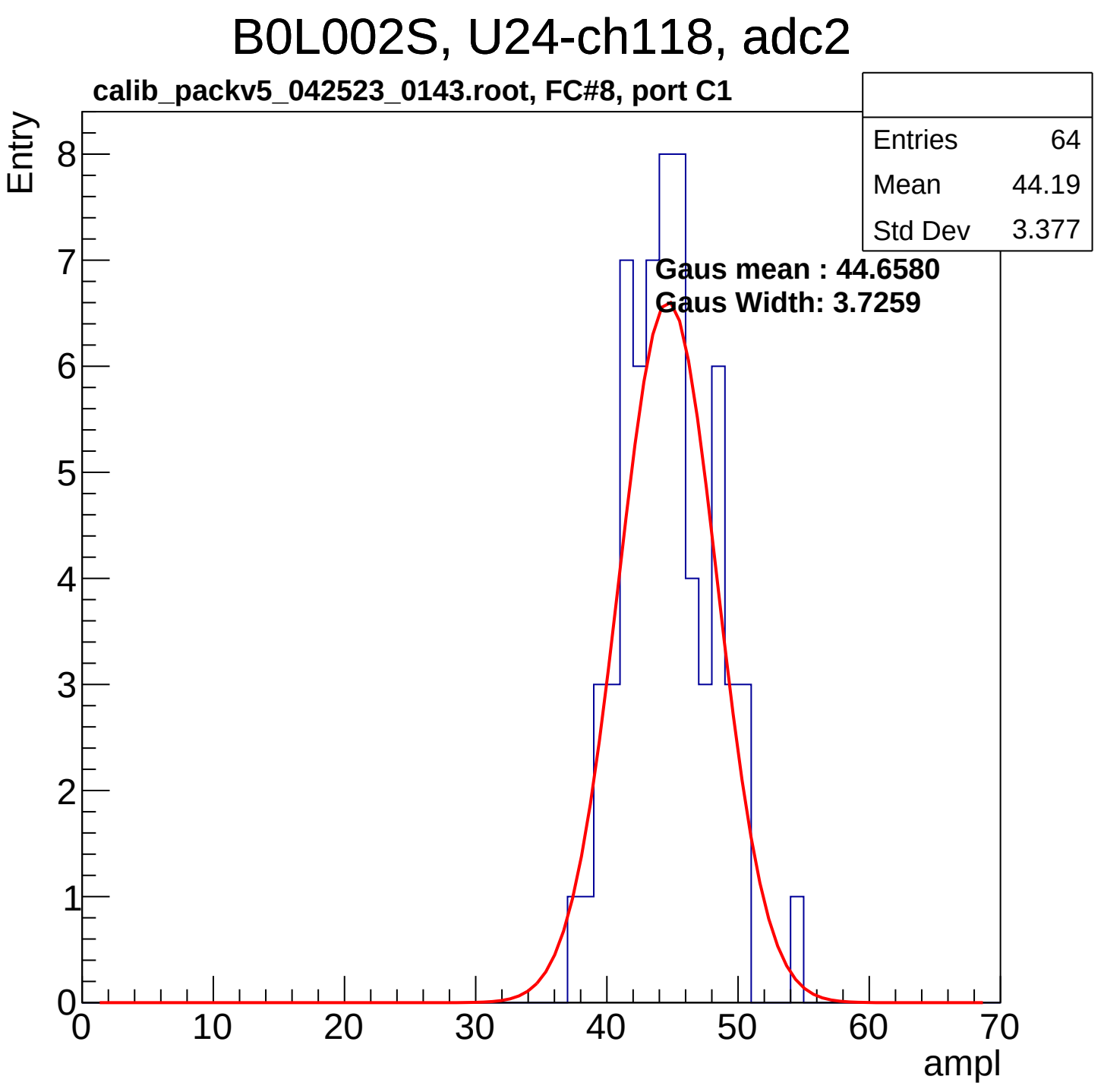

B0L002S, U24-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.67
Std Dev 12.37
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

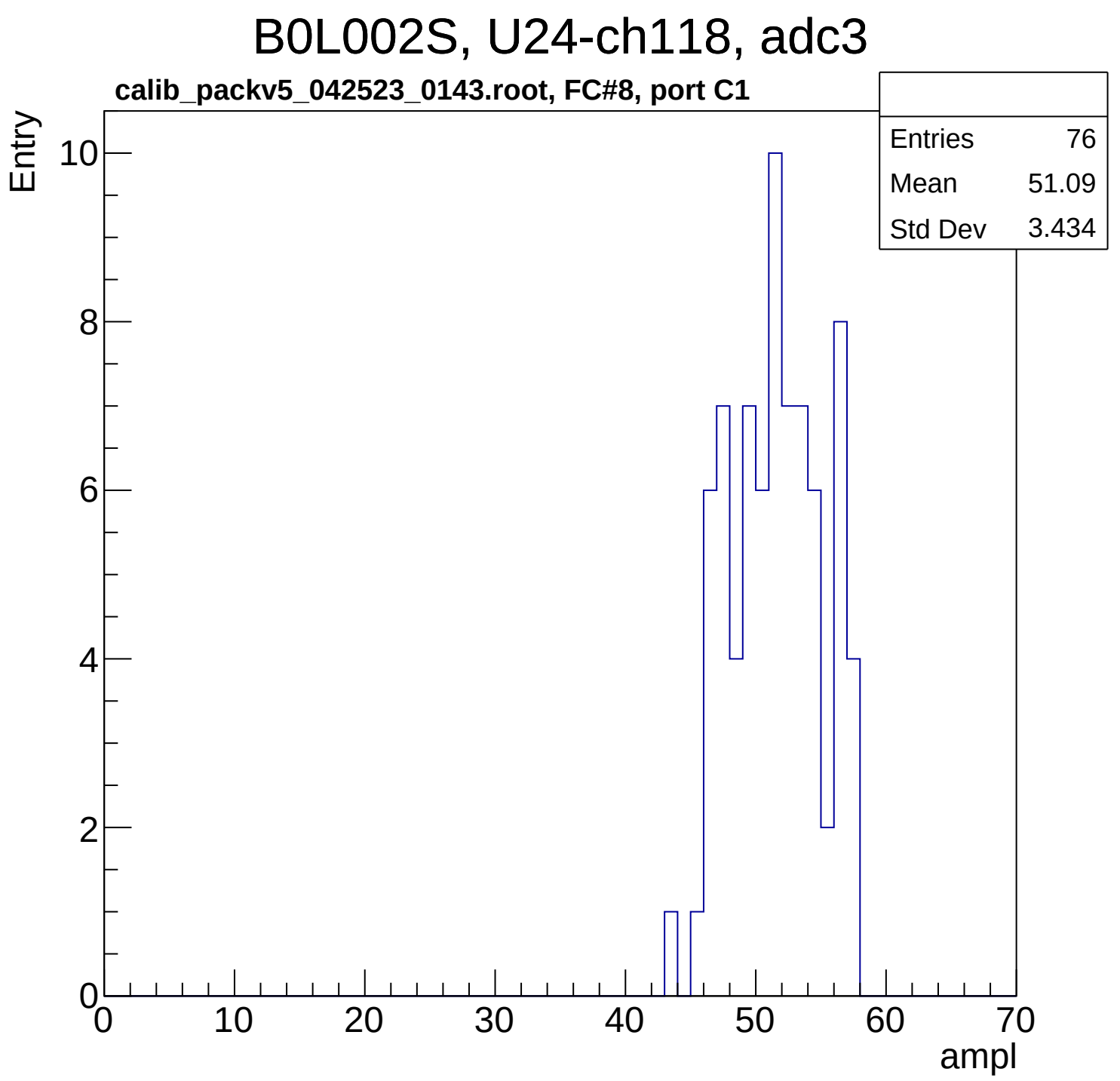

B0L002S, U24-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

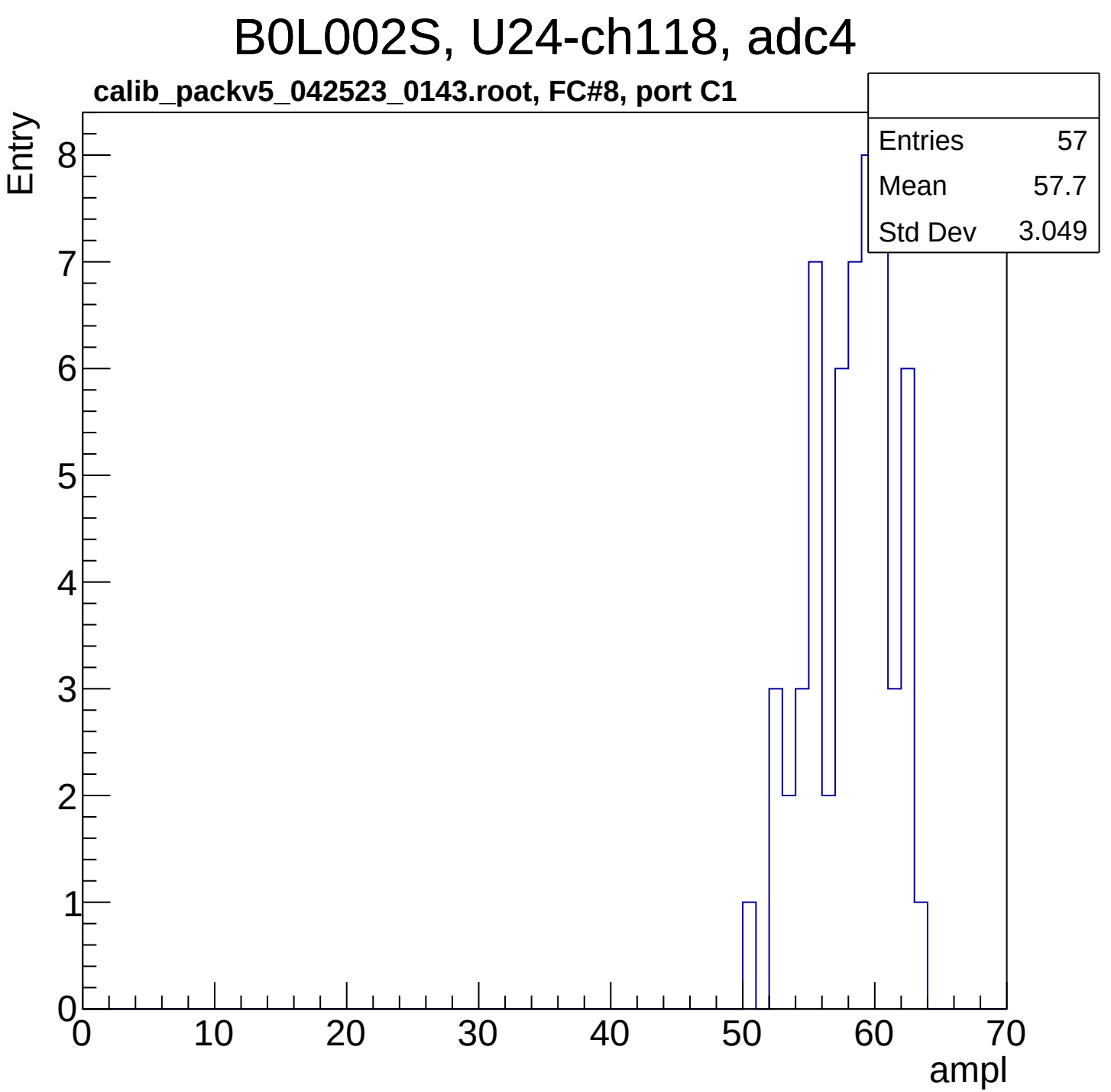

B0L002S, U24-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

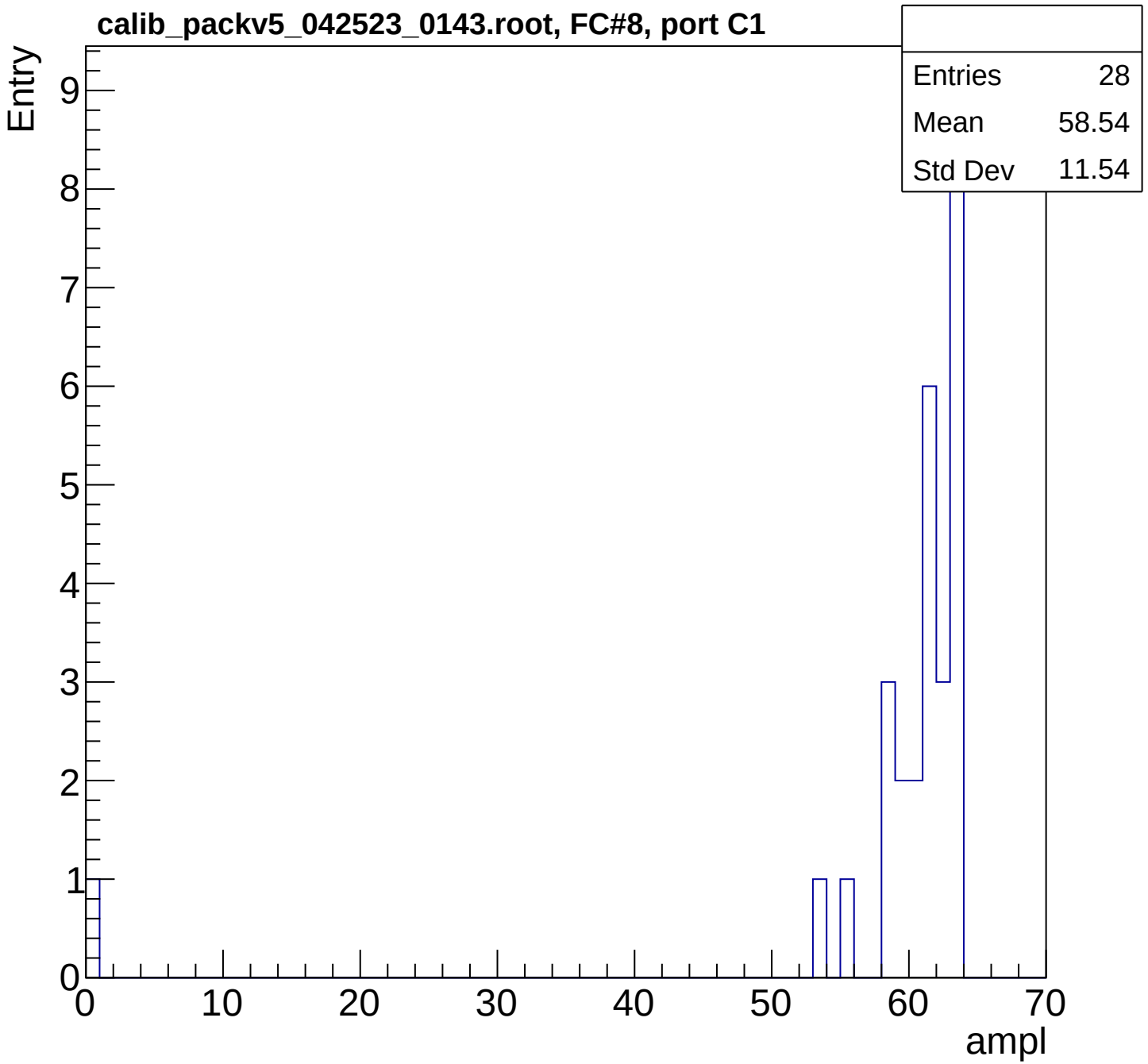

B0L002S, U24-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 32.81
Std Dev 2.911
Gaus mean : 33.1969
Gaus Width: 3.0548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 38.74
Std Dev 2.913
Gaus mean : 39.5725
Gaus Width: 3.0595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.19
Std Dev 3.377
Gaus mean : 44.6580
Gaus Width: 3.7259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.09
Std Dev 3.434
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.7
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.54
Std Dev 11.54
Entry
0
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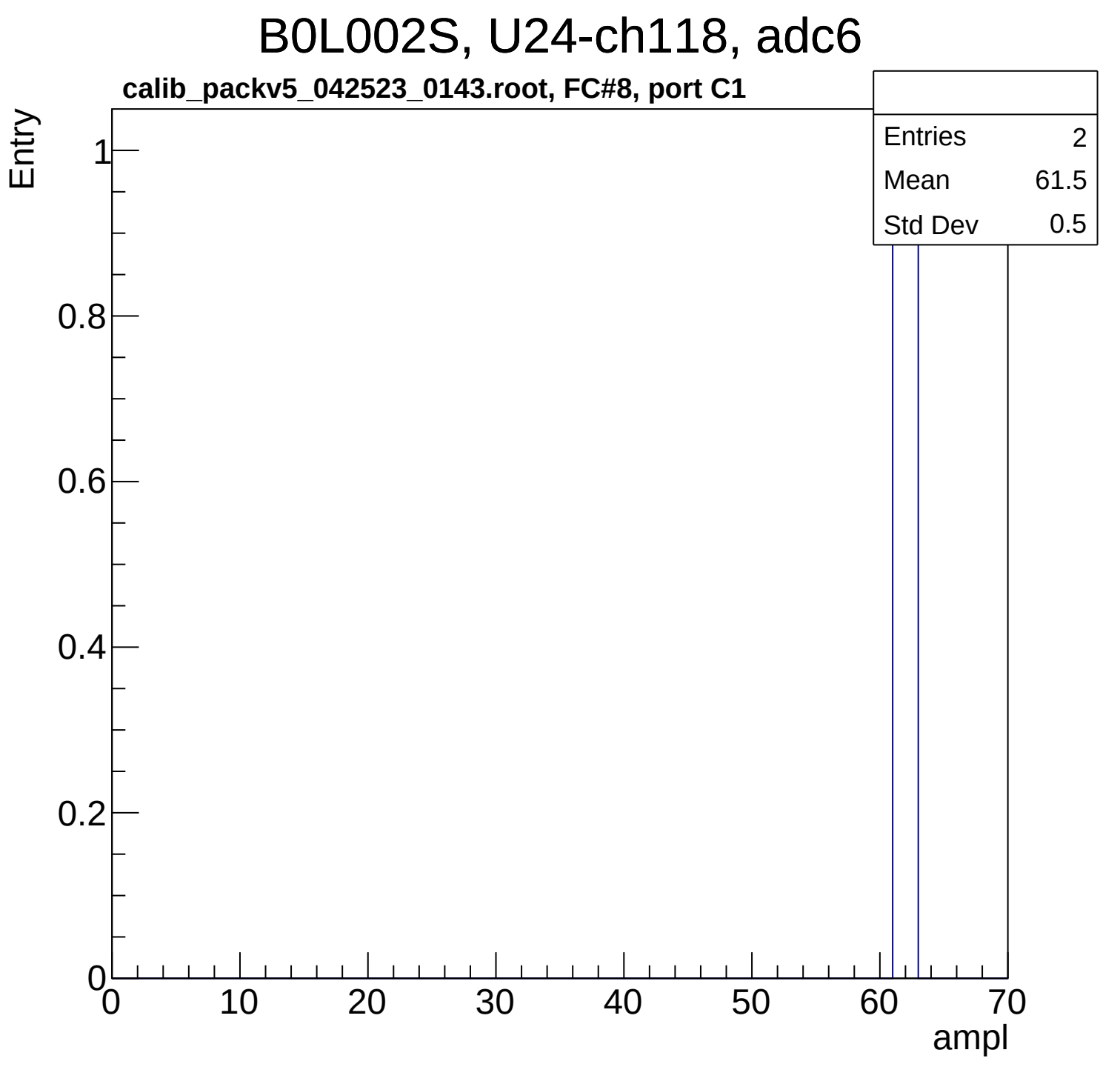
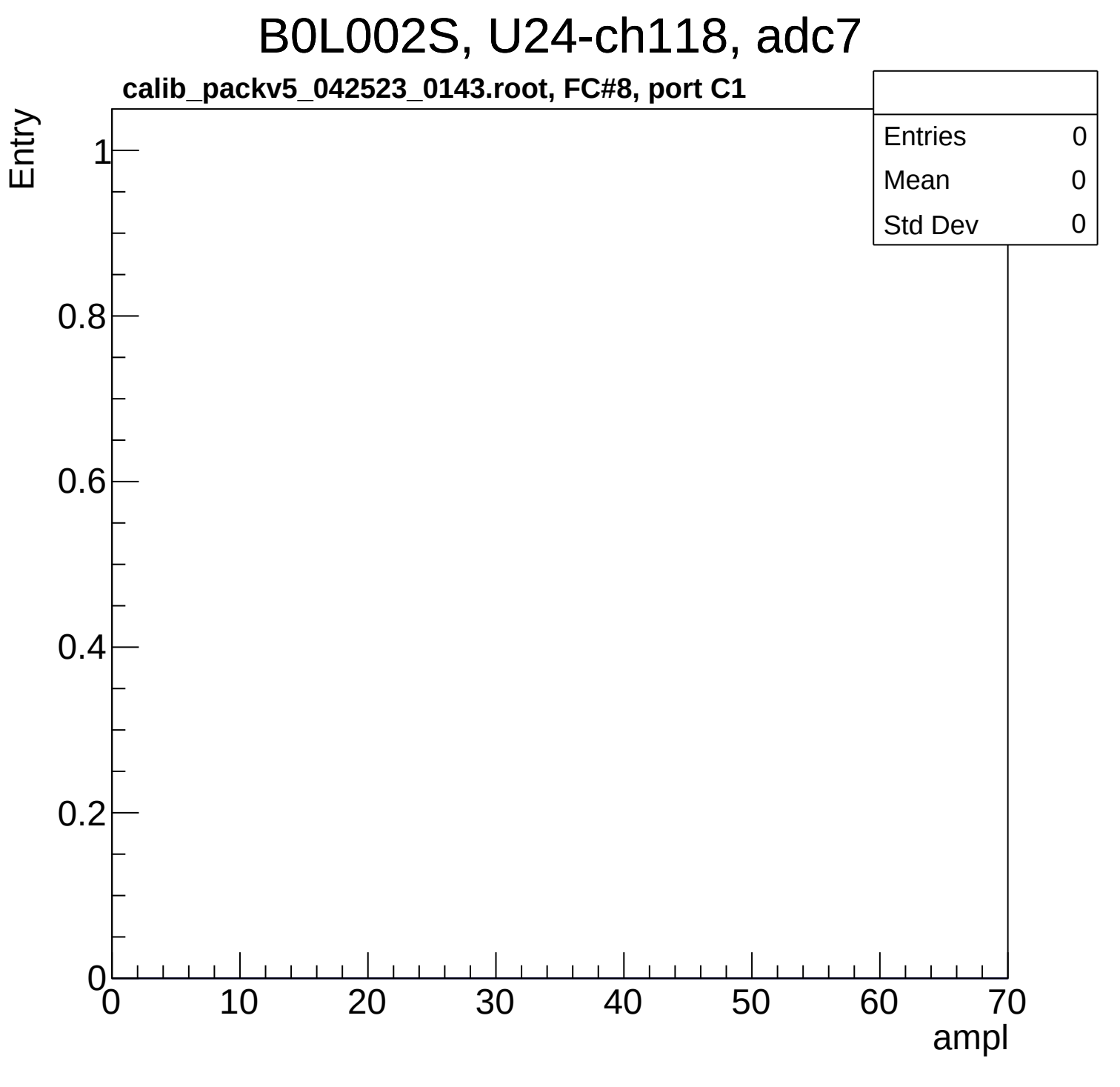
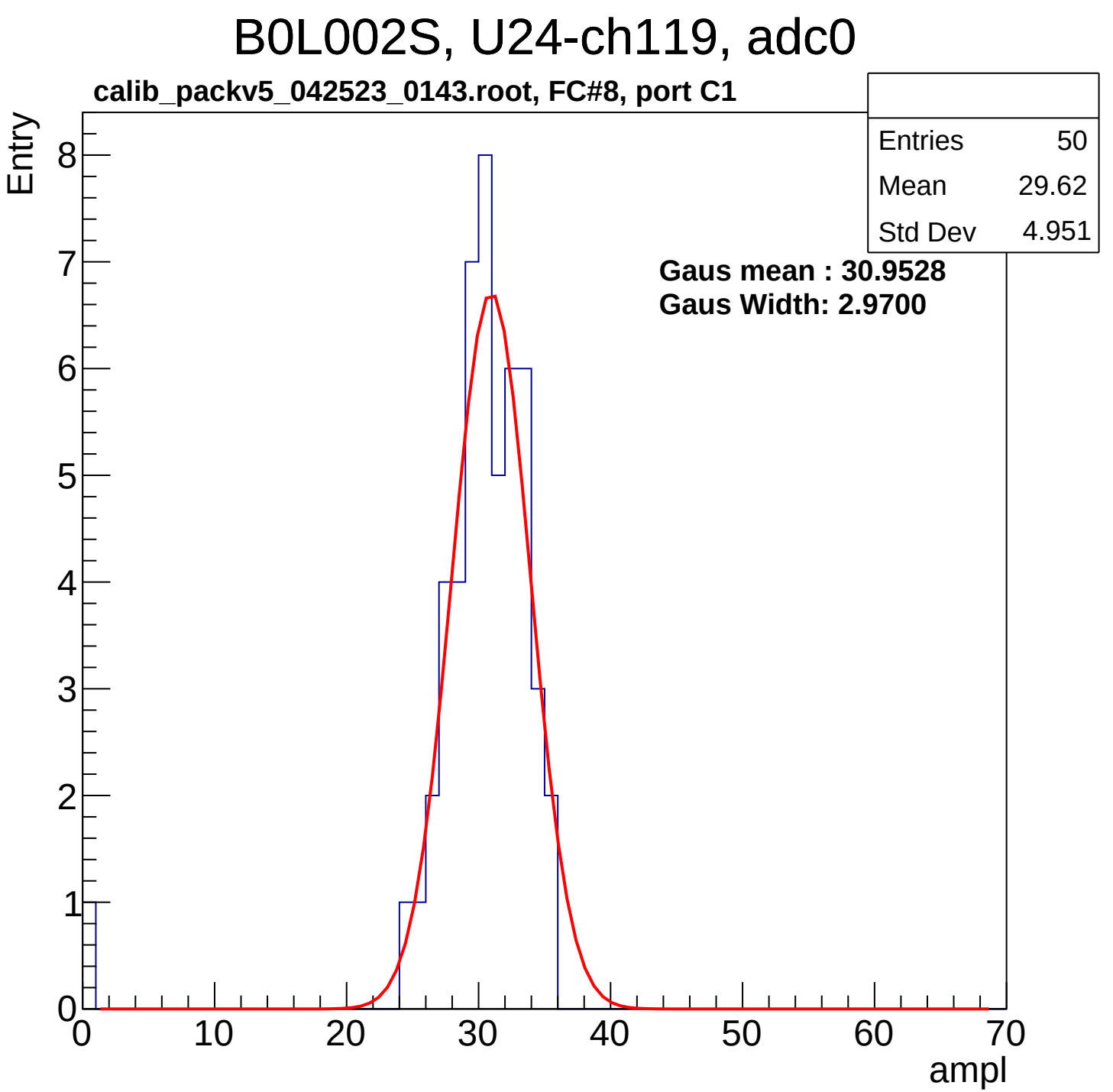
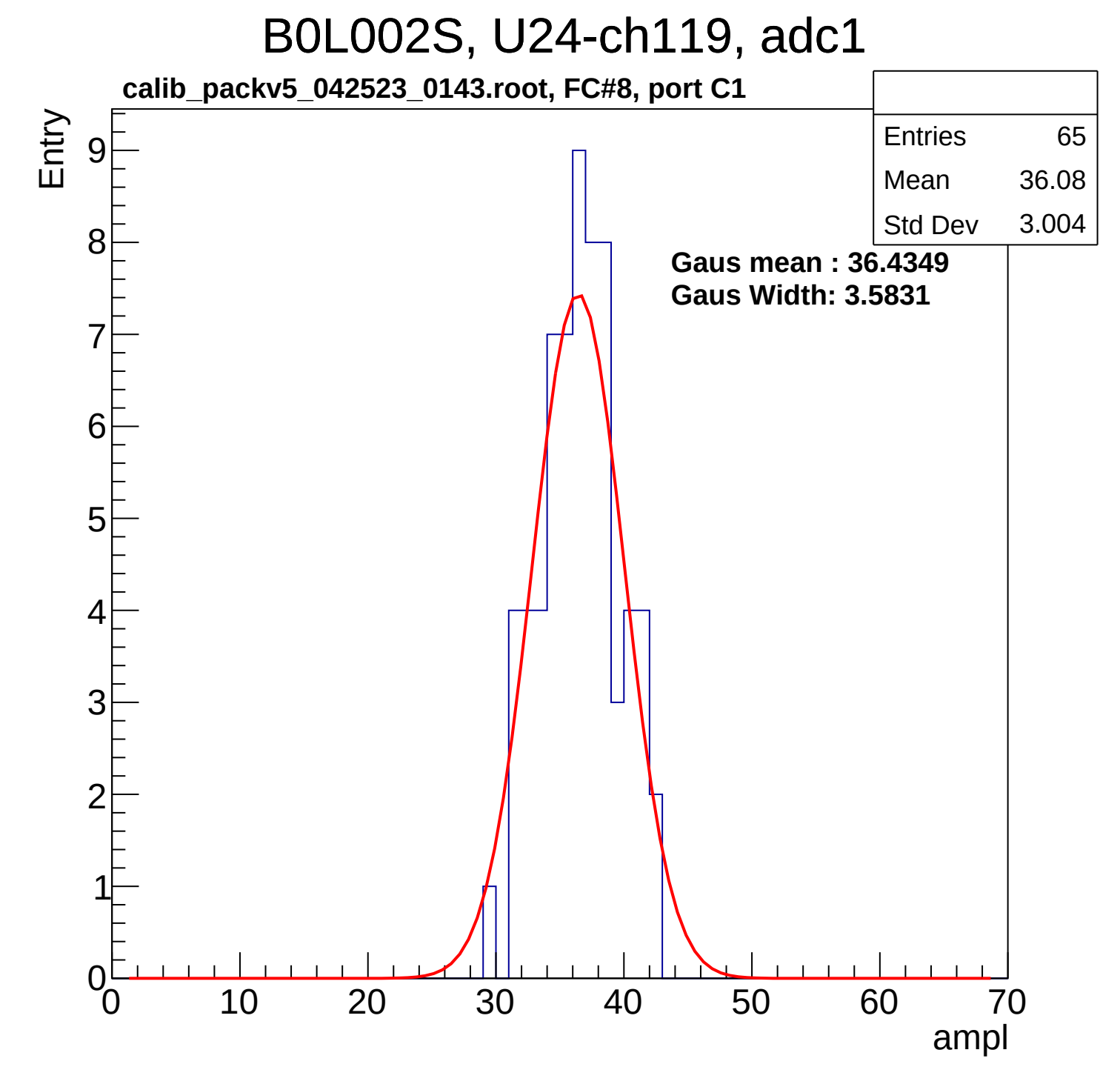
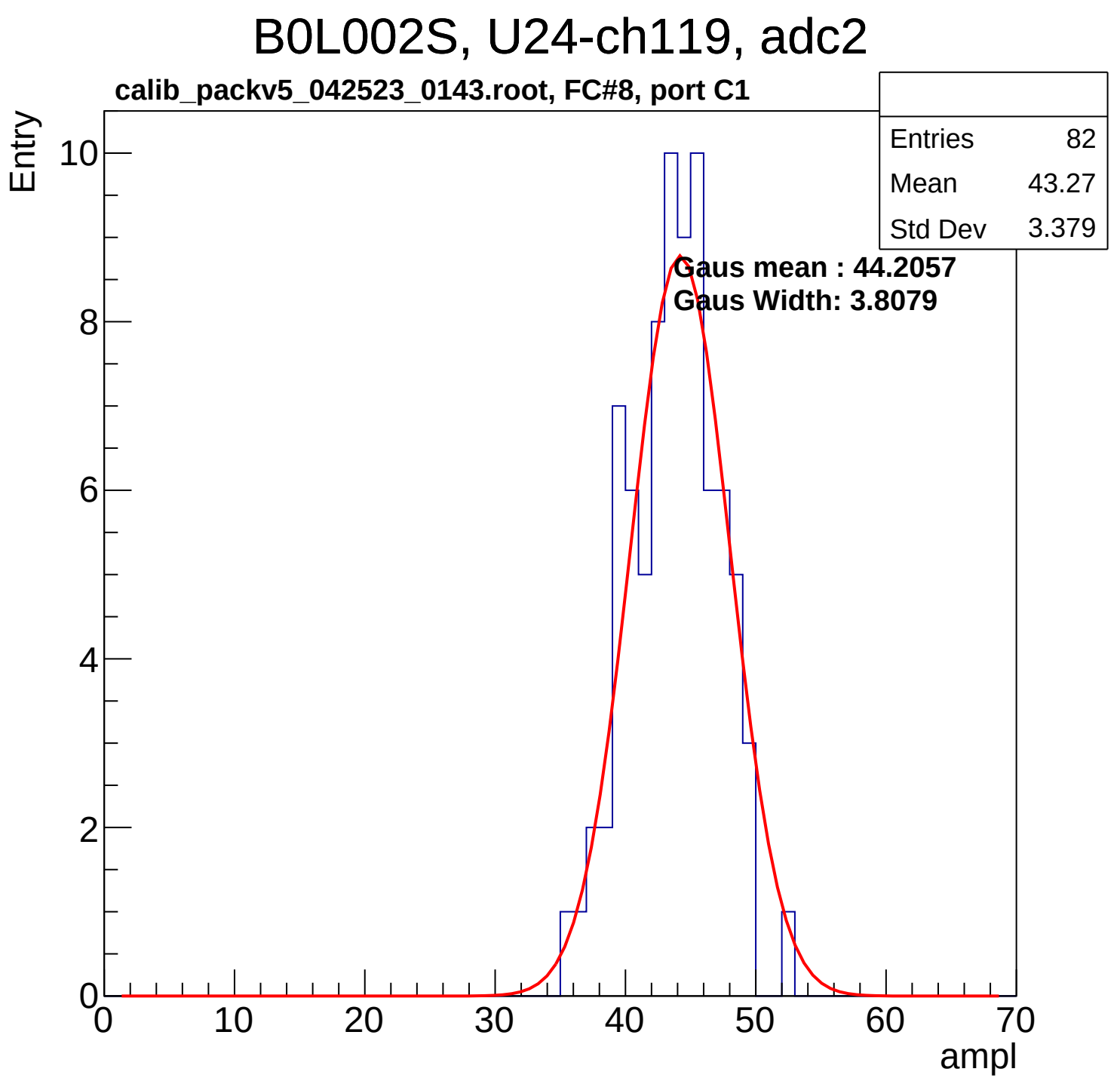
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

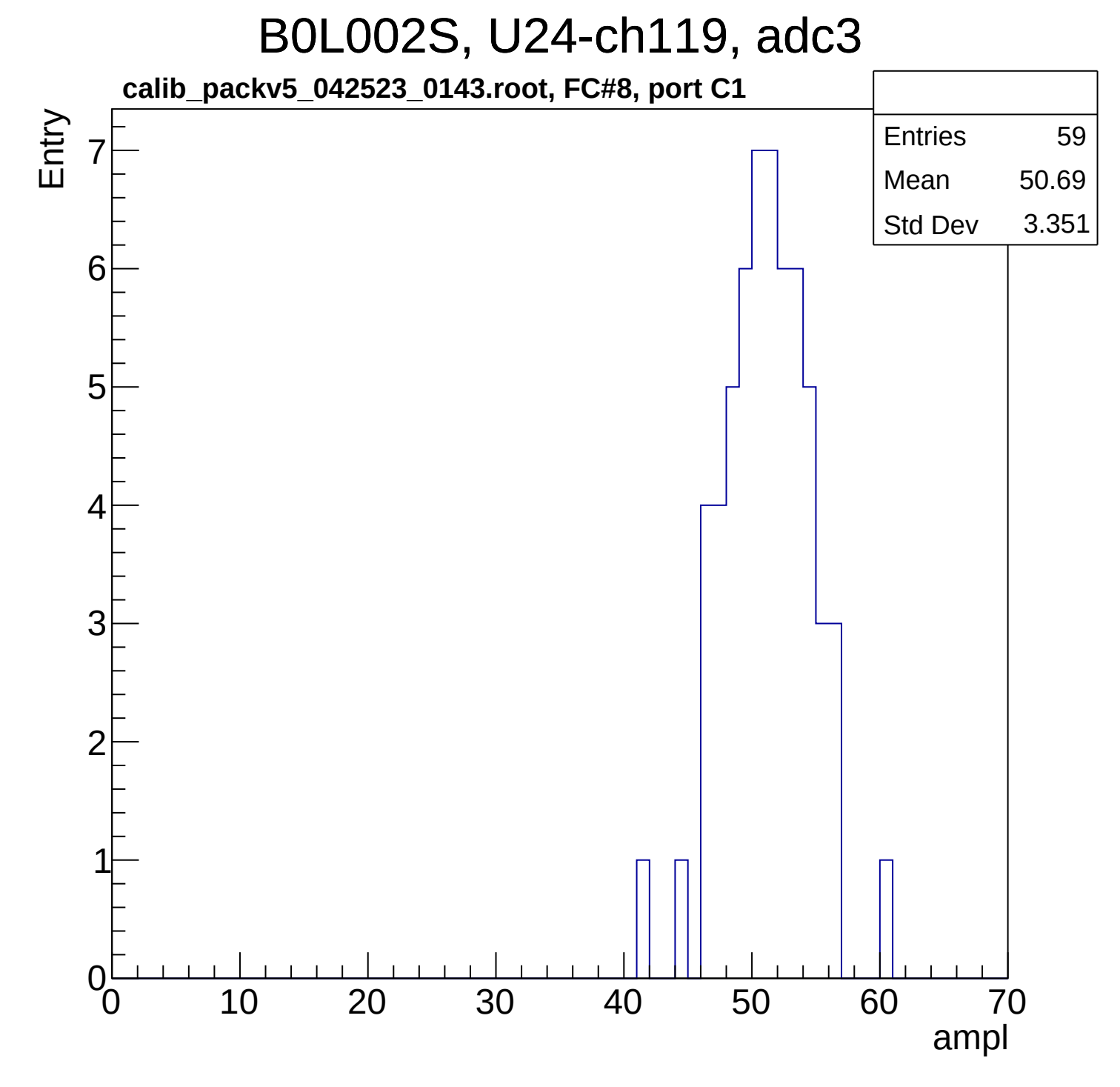

B0L002S, U24-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

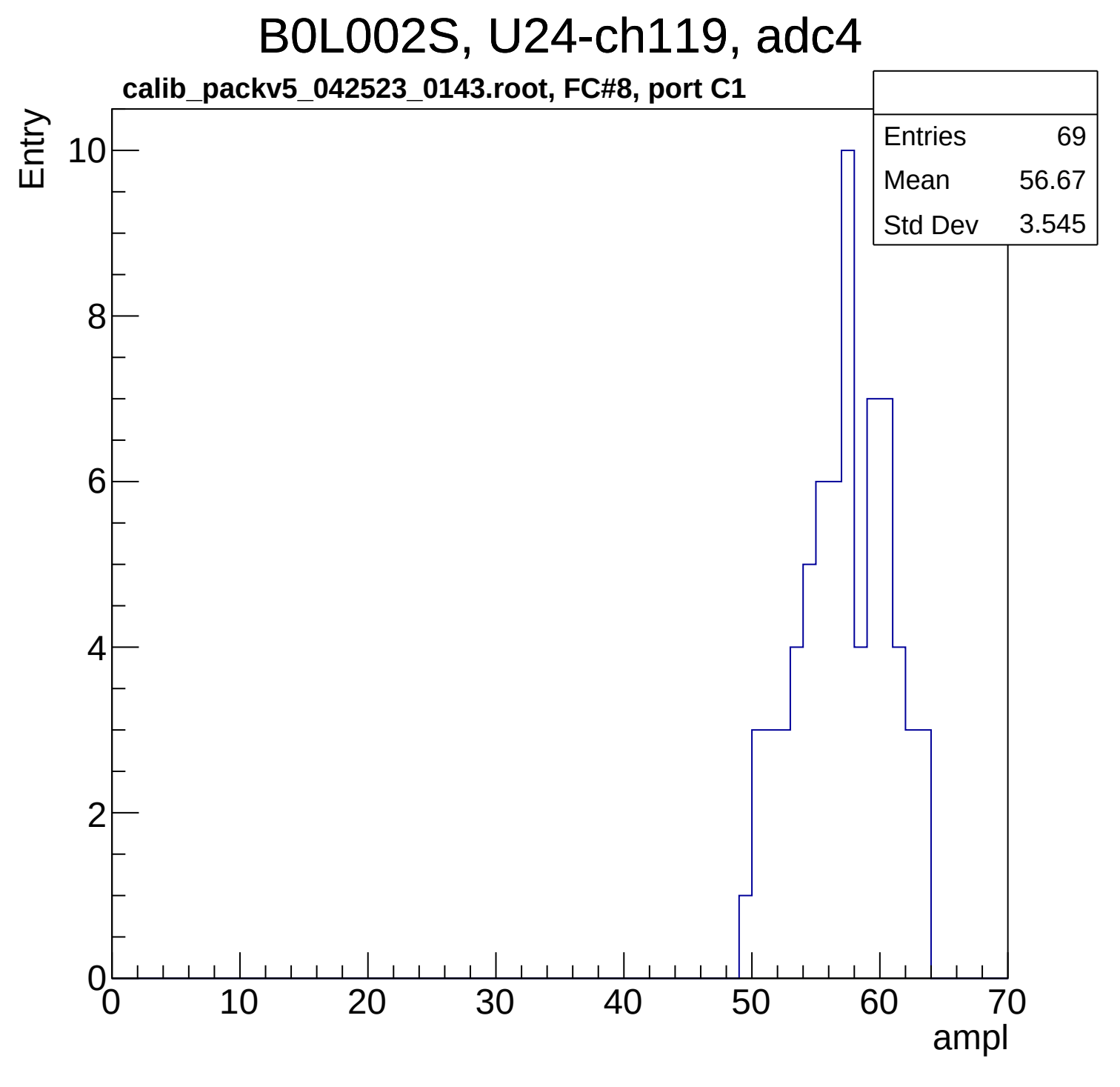

B0L002S, U24-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

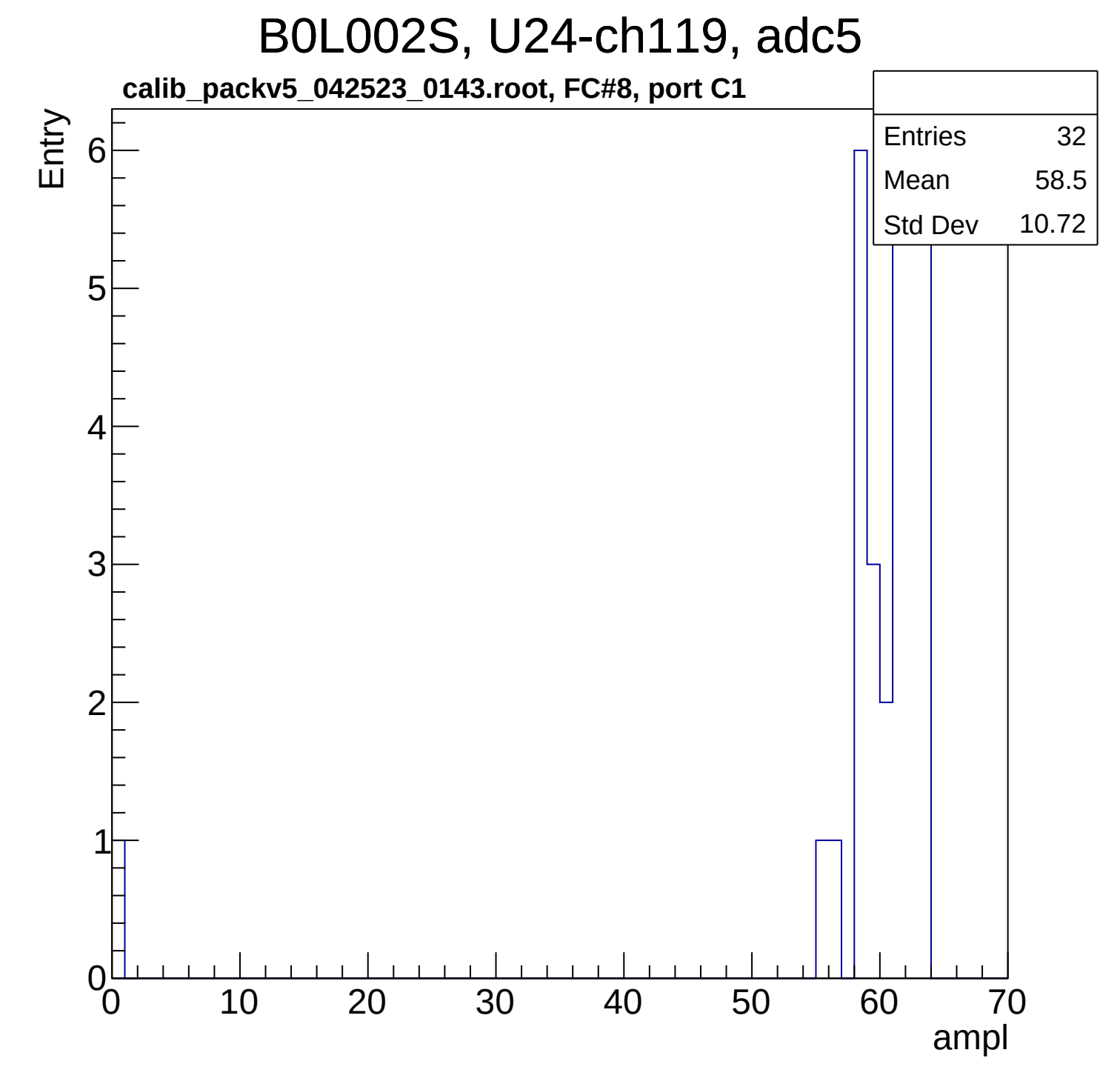

B0L002S, U24-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 29.62
Std Dev 4.951
Gaus mean : 30.9528
Gaus Width: 2.9700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

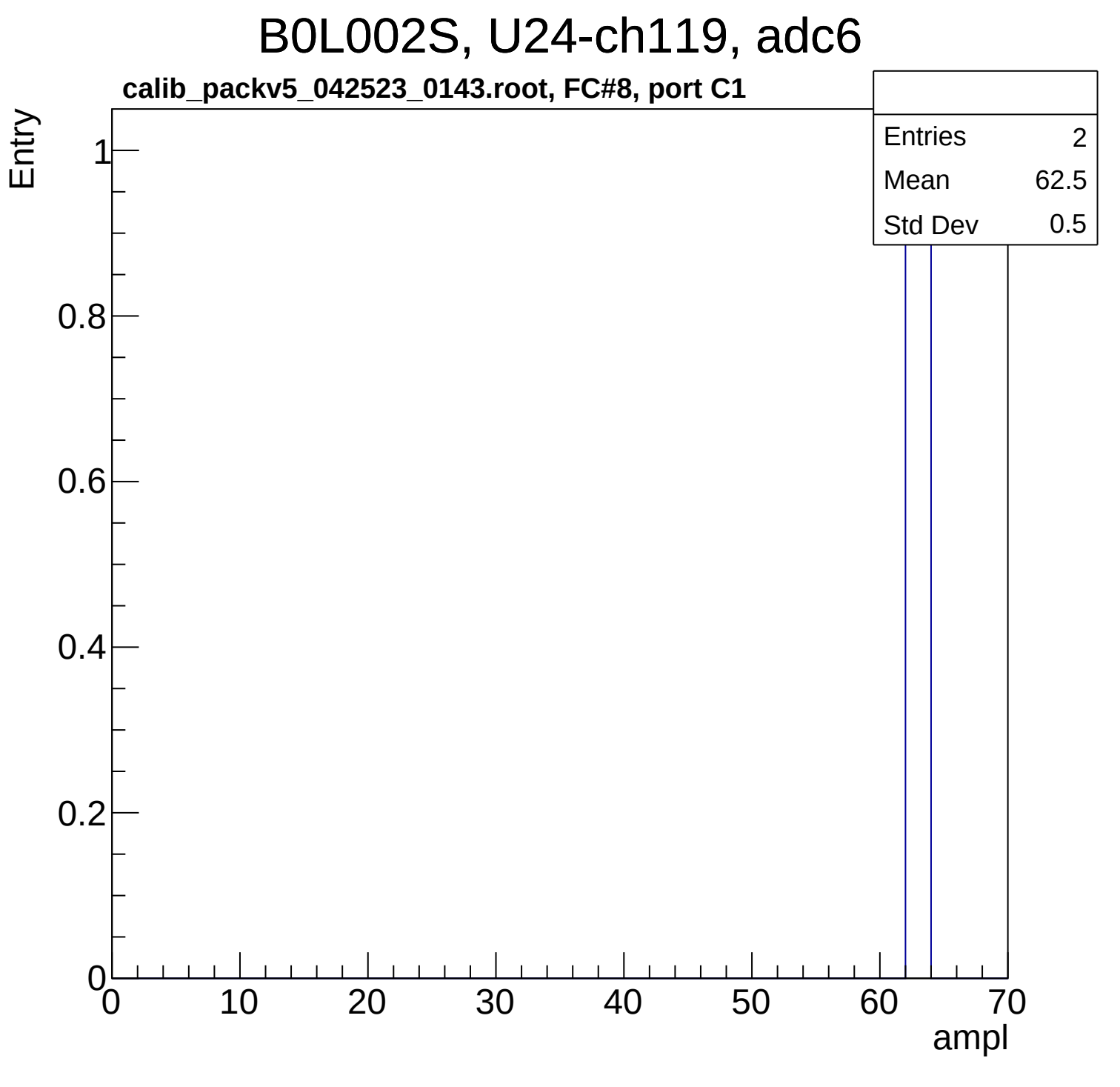

B0L002S, U24-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.08
Std Dev 3.004
Gaus mean : 36.4349
Gaus Width: 3.5831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 43.27
Std Dev 3.379
Gaus mean : 44.2057
Gaus Width: 3.8079
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.69
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U24-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 56.67
Std Dev 3.545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.5
Std Dev 10.72
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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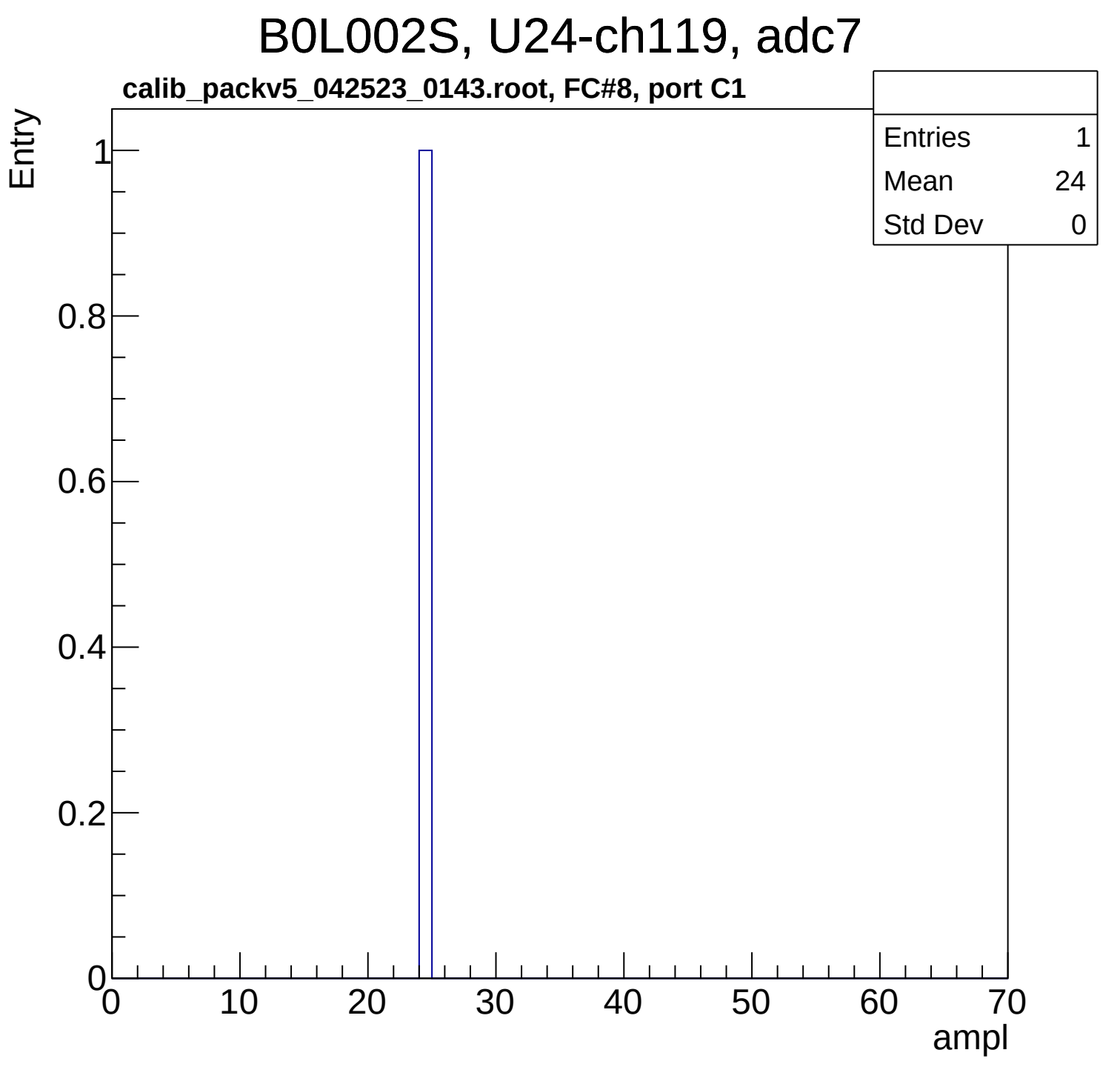
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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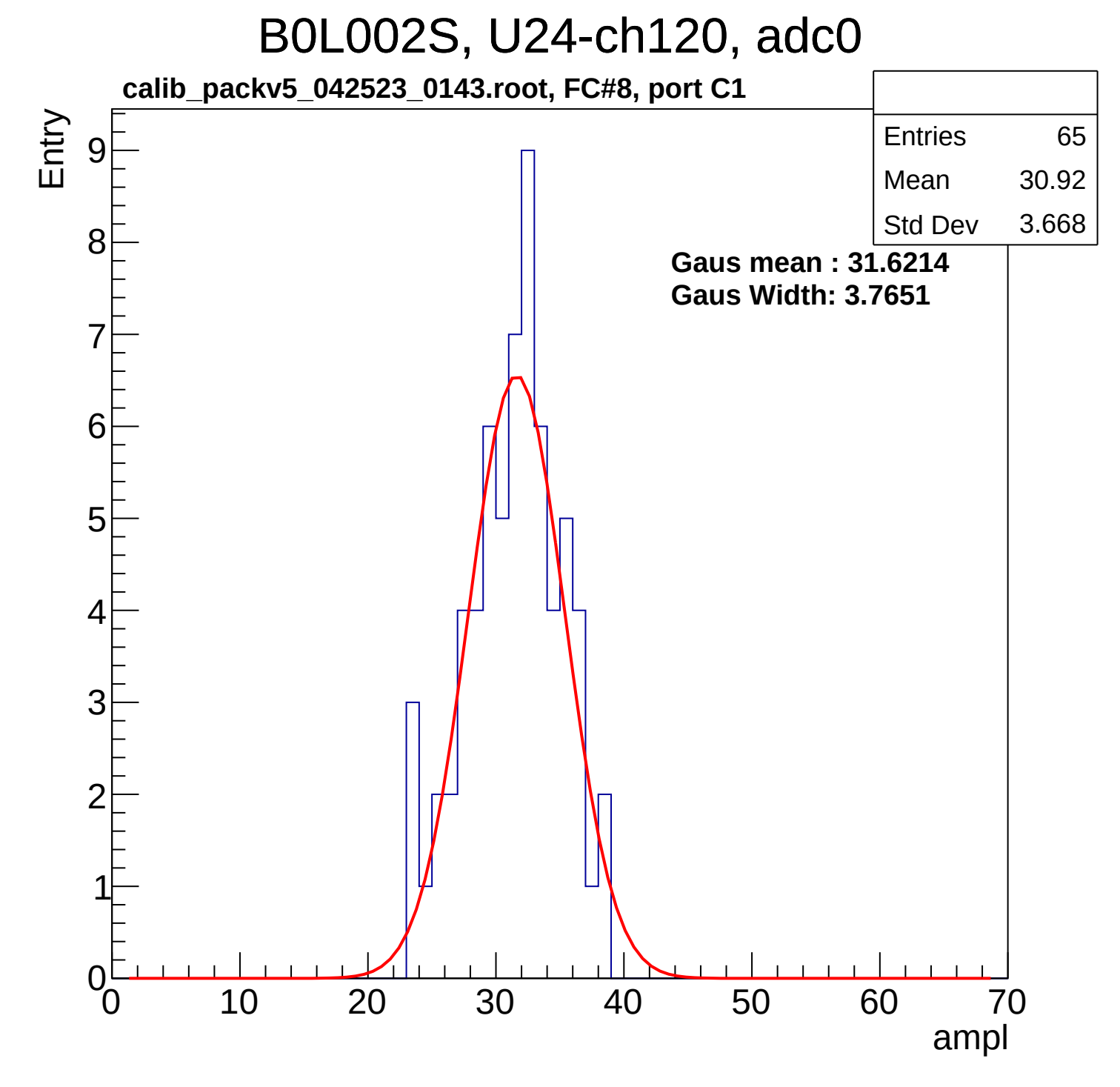
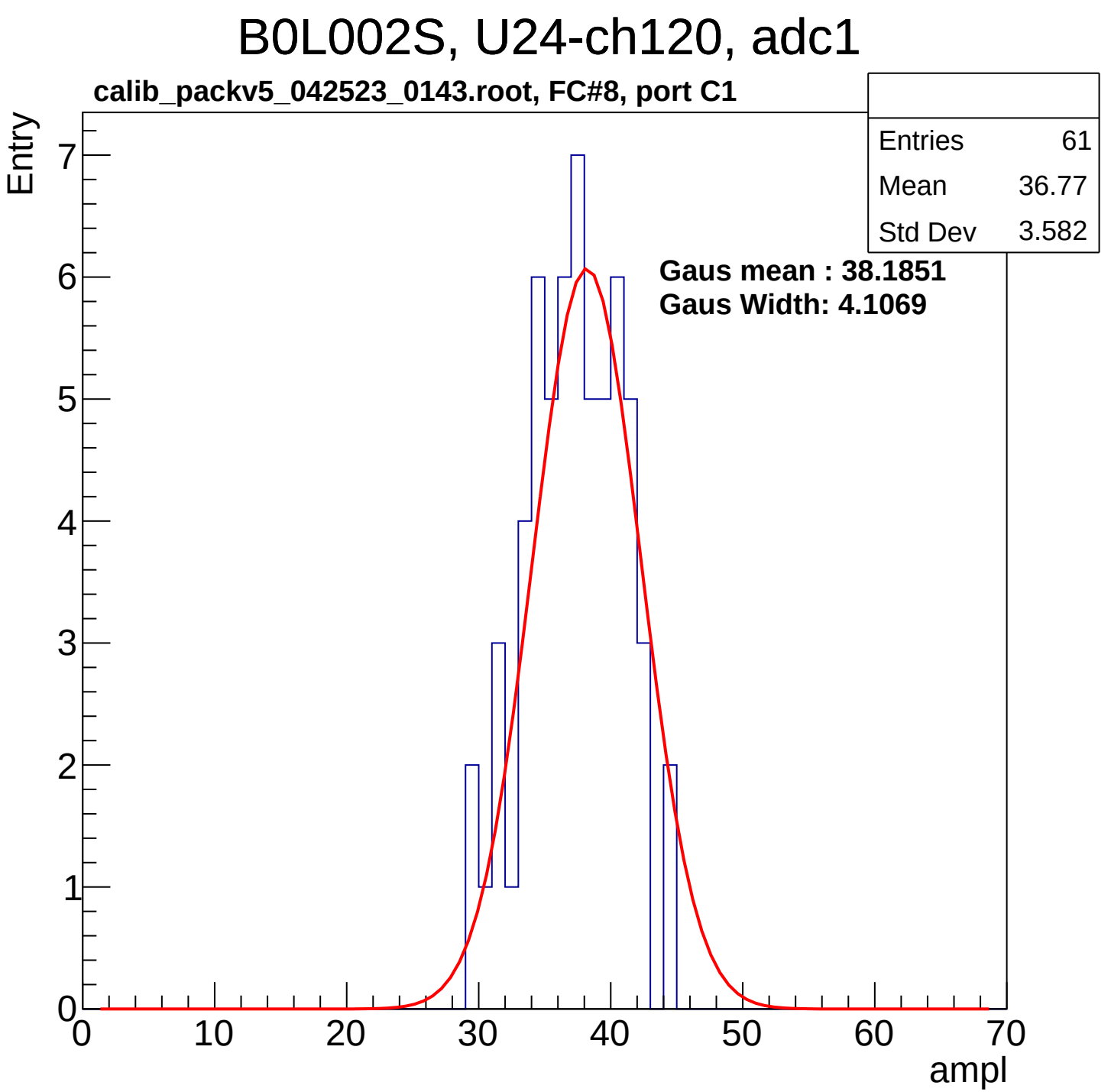
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.92
Std Dev 3.668
Gaus mean : 31.6214
Gaus Width: 3.7651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 36.77
Std Dev 3.582
Gaus mean : 38.1851
Gaus Width: 4.1069
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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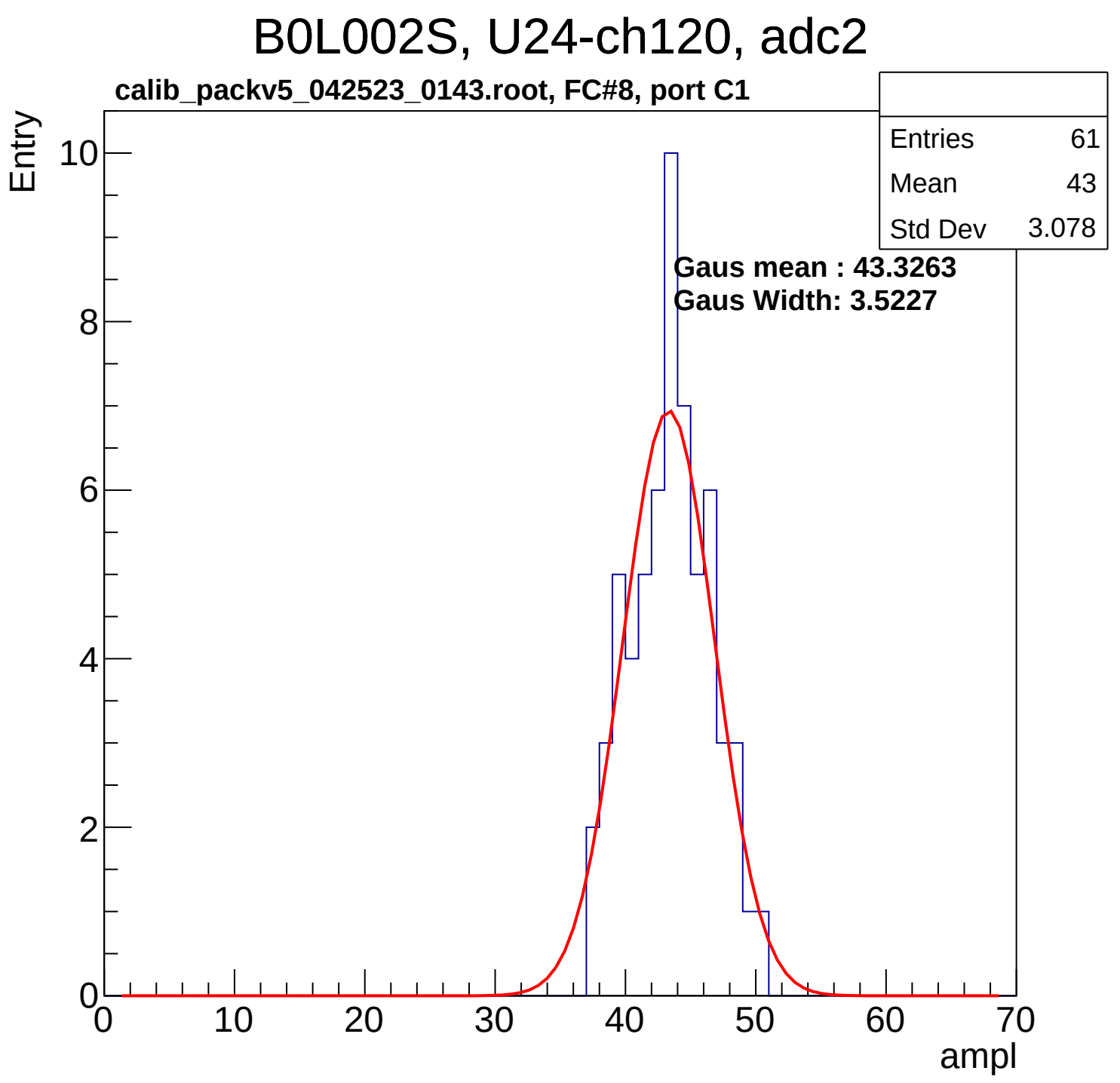
B0L002S, U24-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43
Std Dev 3.078
Gaus mean : 43.3263
Gaus Width: 3.5227
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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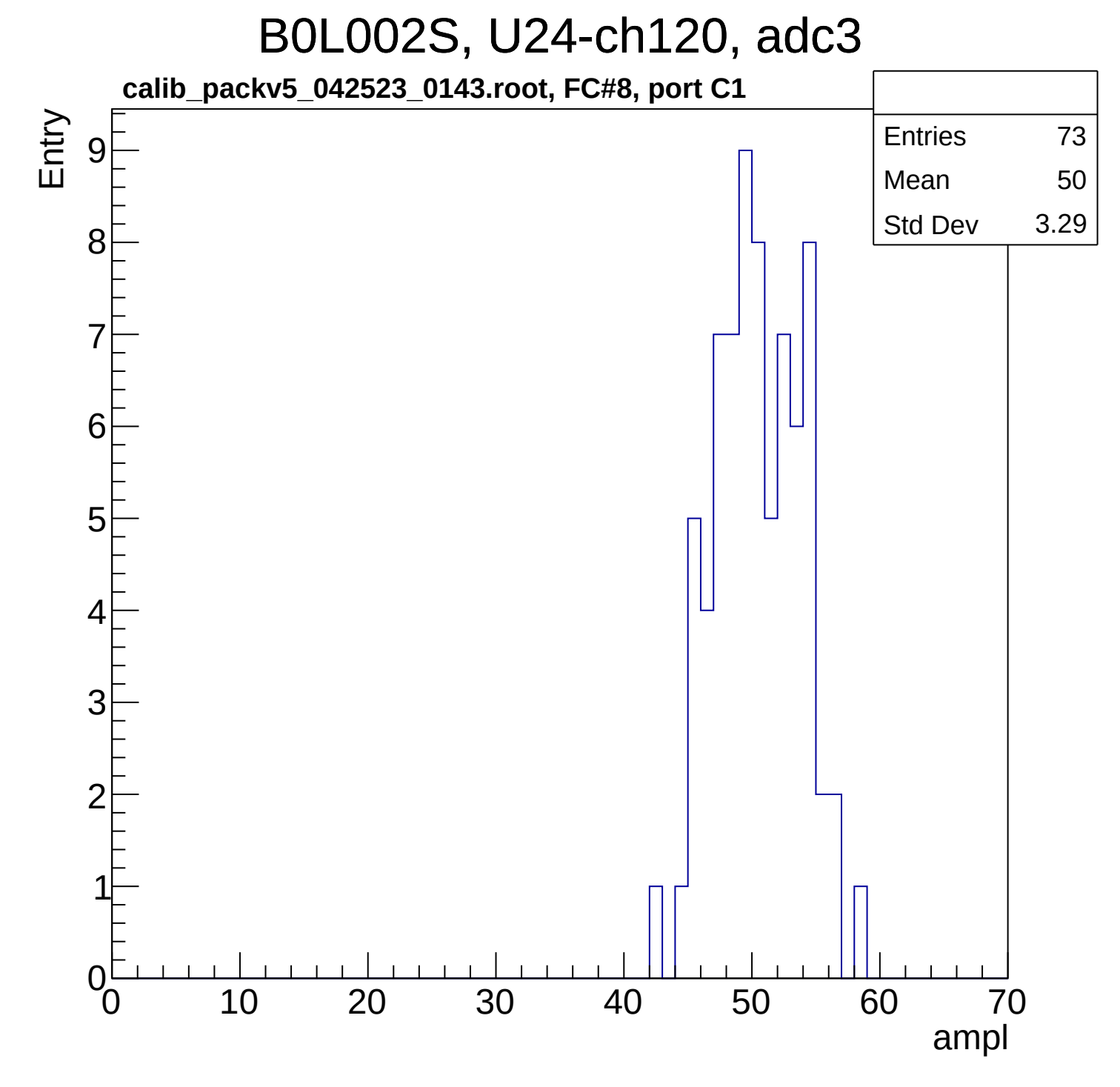
B0L002S, U24-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50
Std Dev 3.29
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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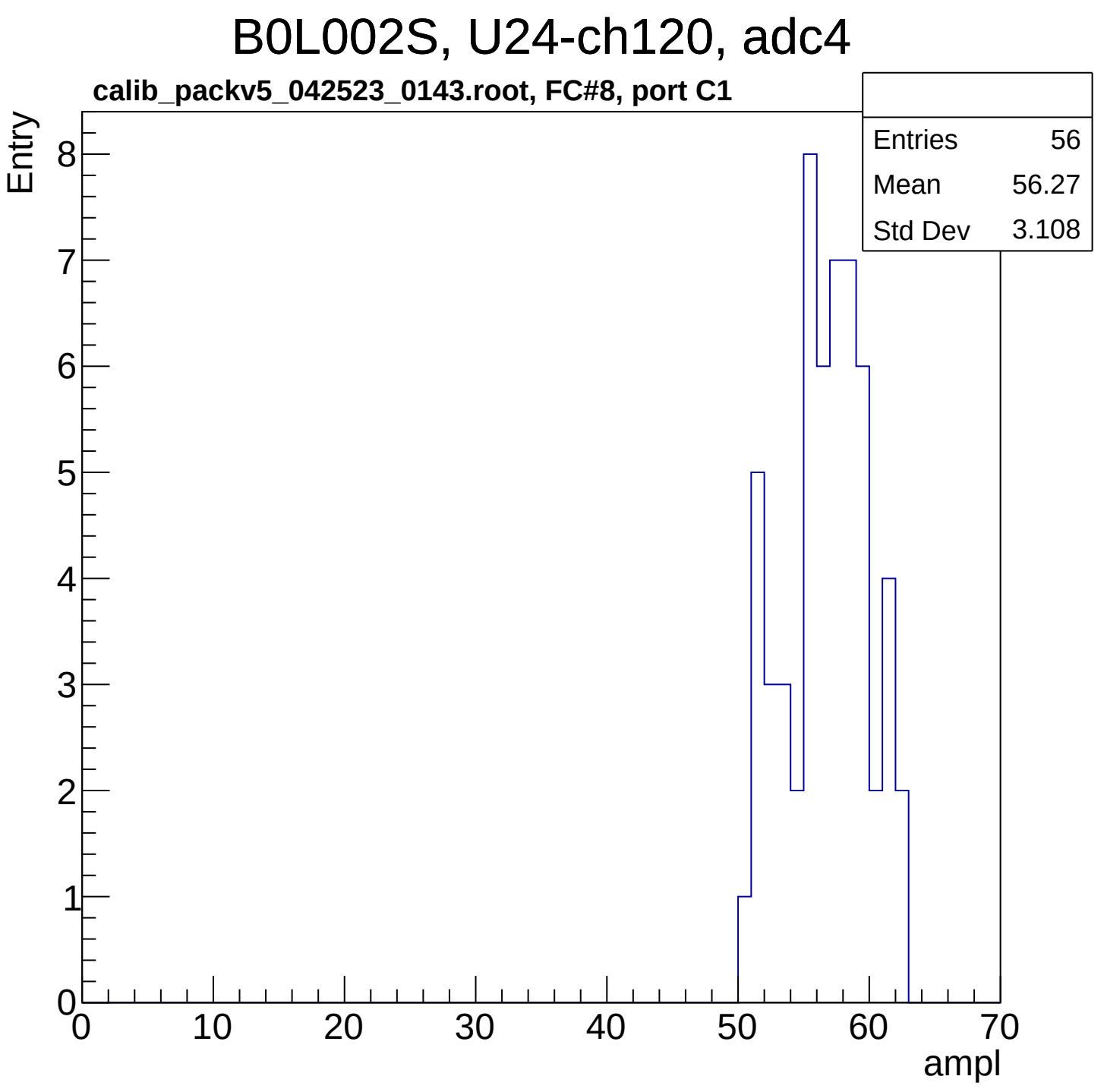
B0L002S, U24-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.27
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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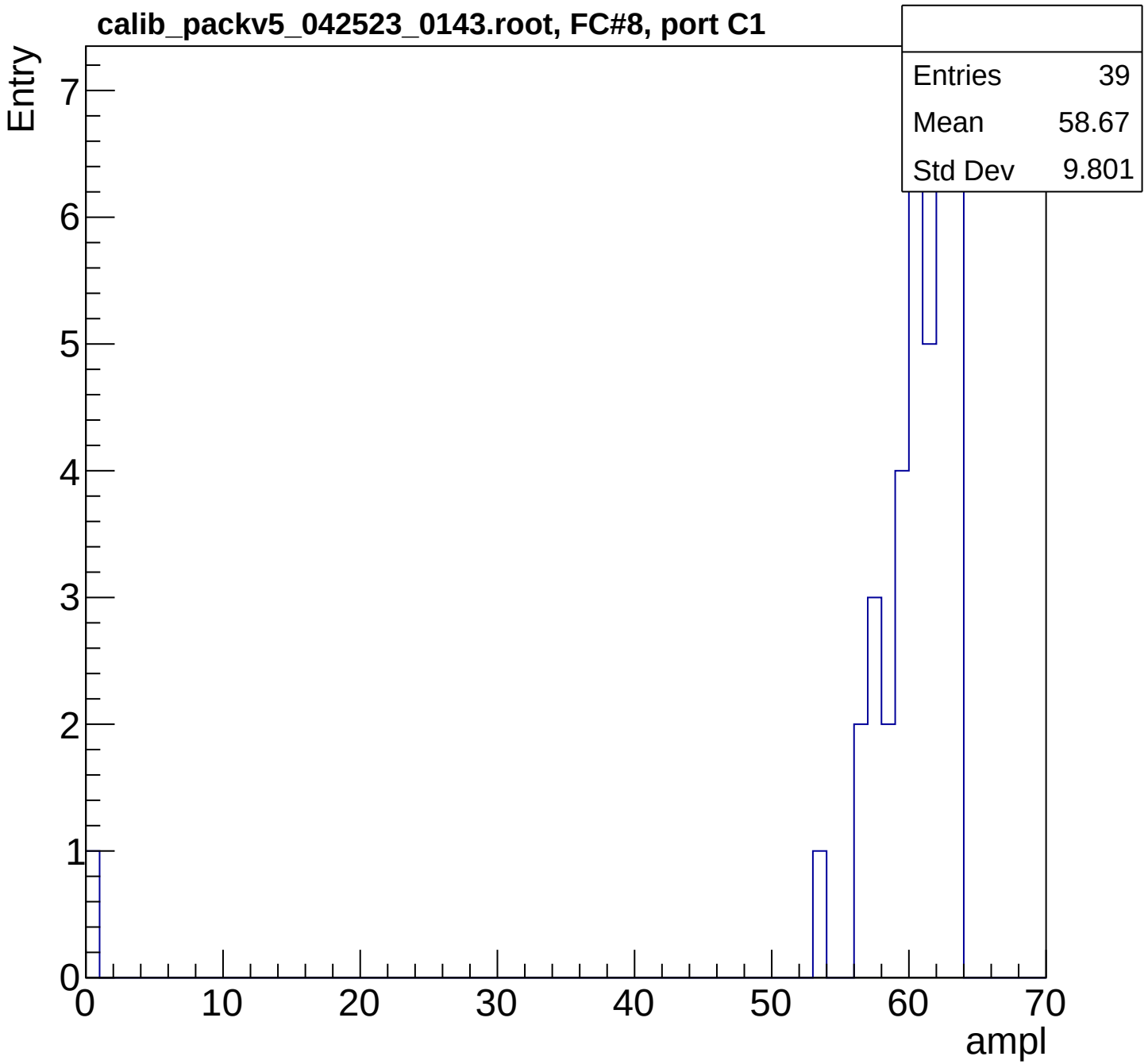
B0L002S, U24-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.67
Std Dev 9.801
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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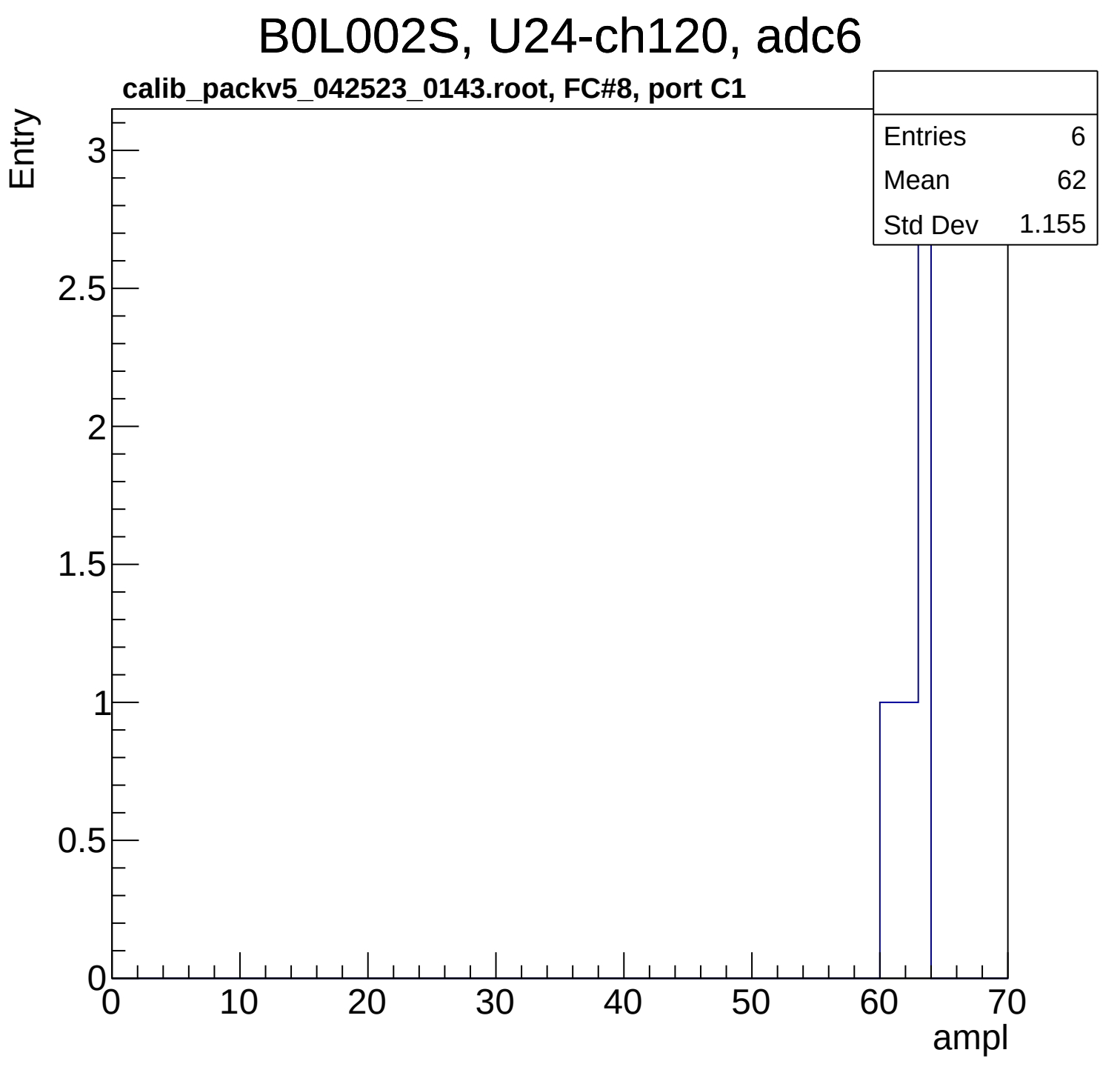
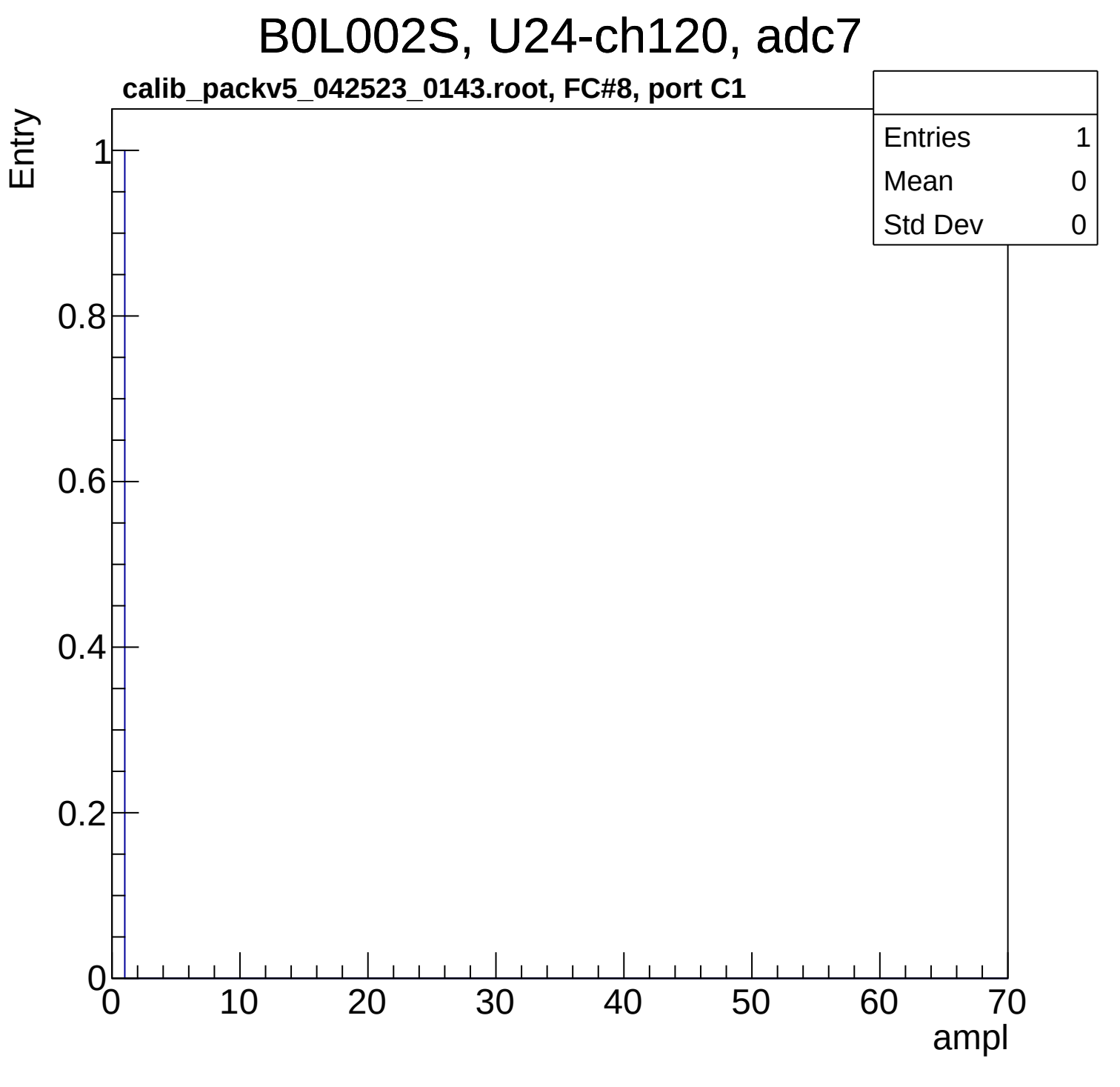
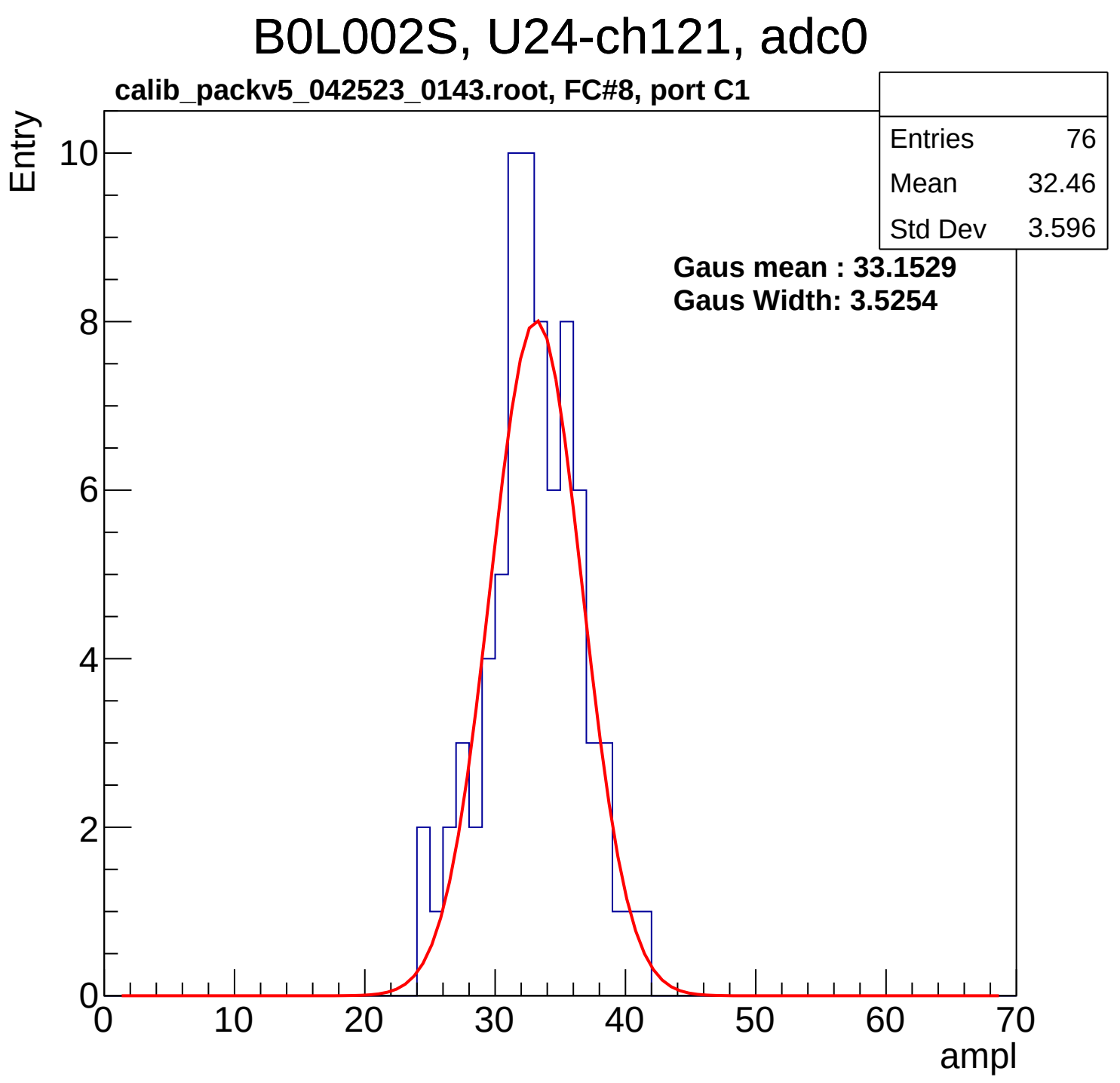
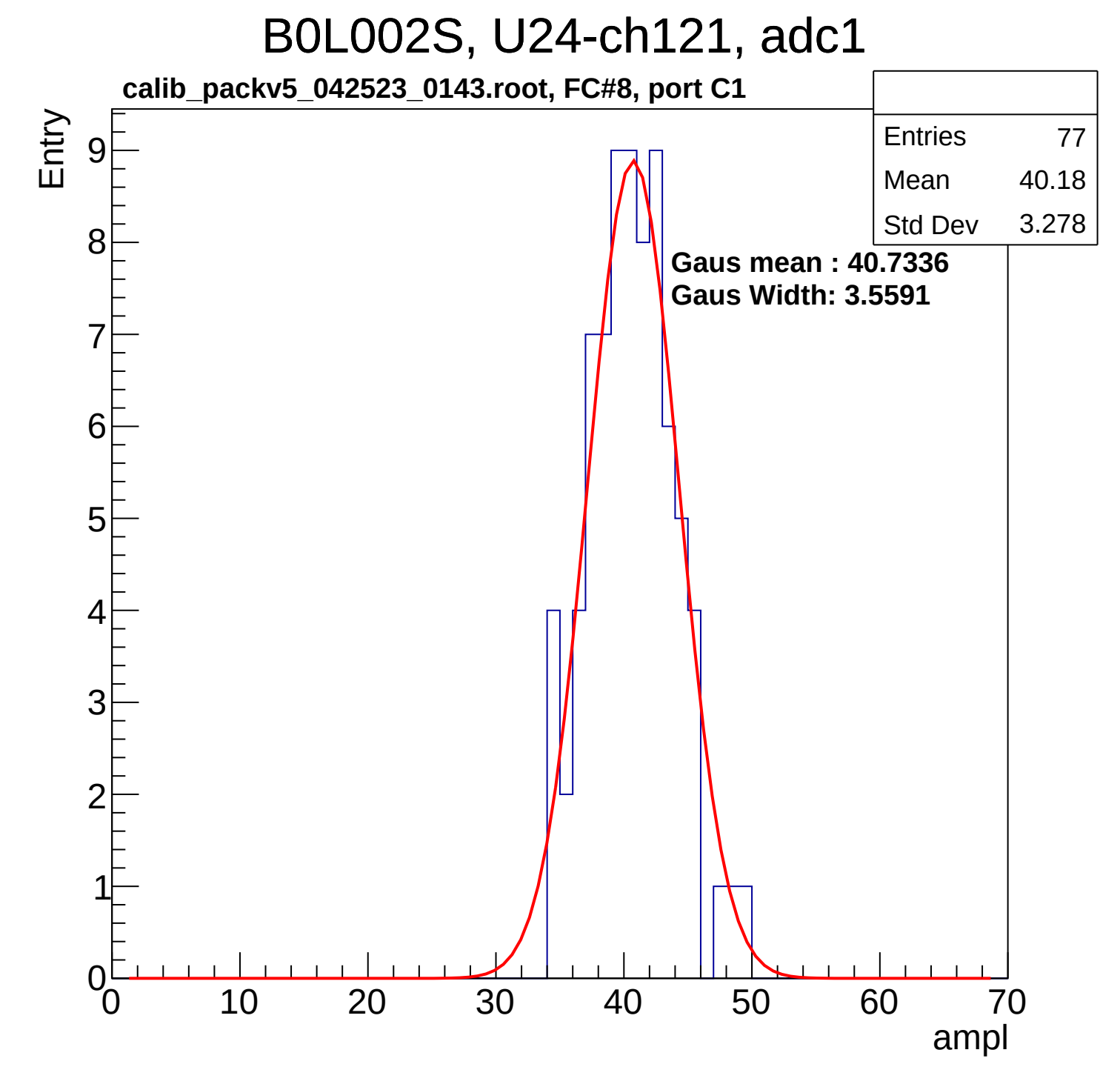
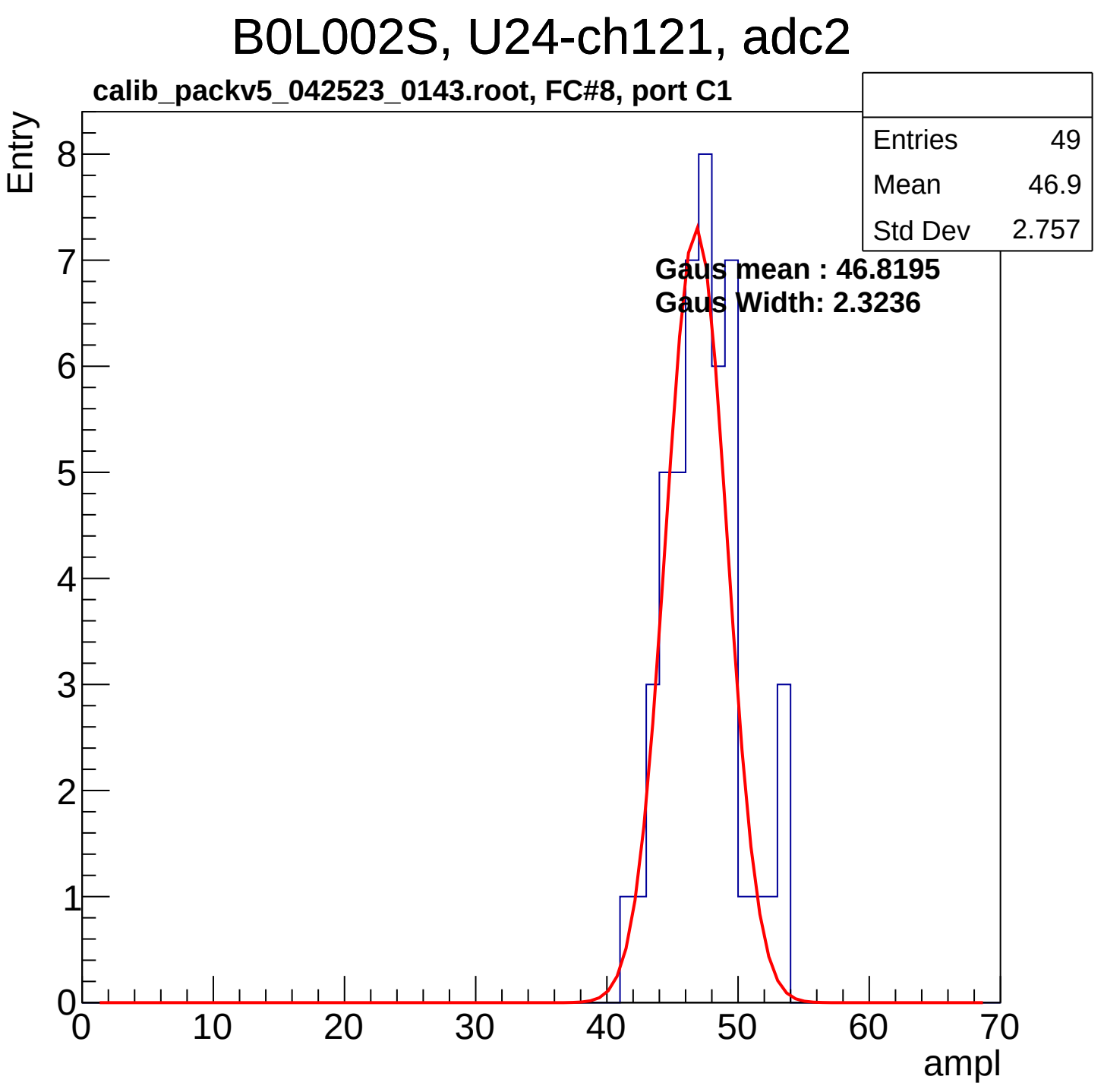
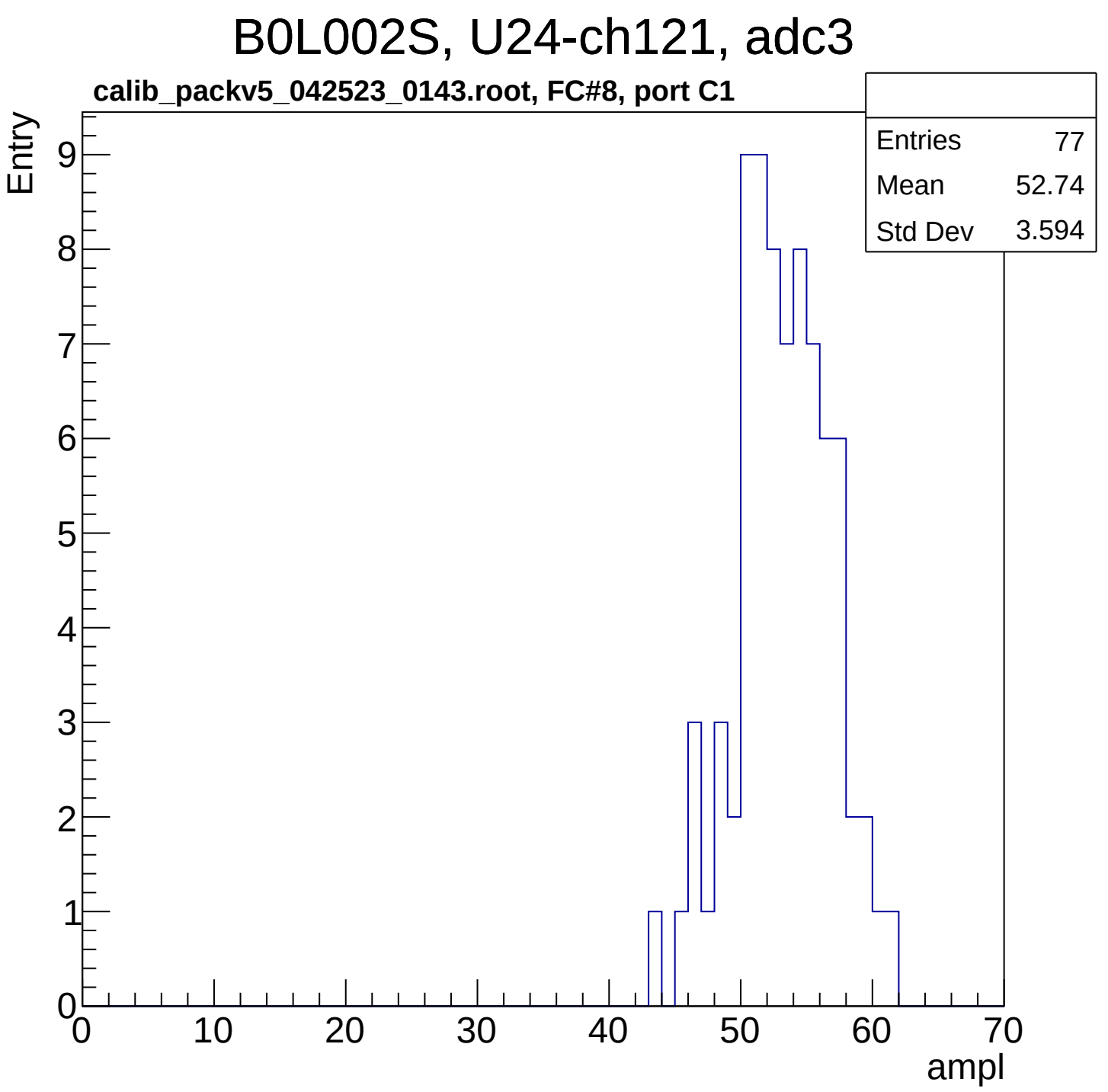

B0L002S, U24-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 32.46
Std Dev 3.596
Gaus mean : 33.1529
Gaus Width: 3.5254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 40.18
Std Dev 3.278
Gaus mean : 40.7336
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 46.9
Std Dev 2.757
Gaus mean : 46.8195
Gaus Width: 2.3236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 52.74
Std Dev 3.594
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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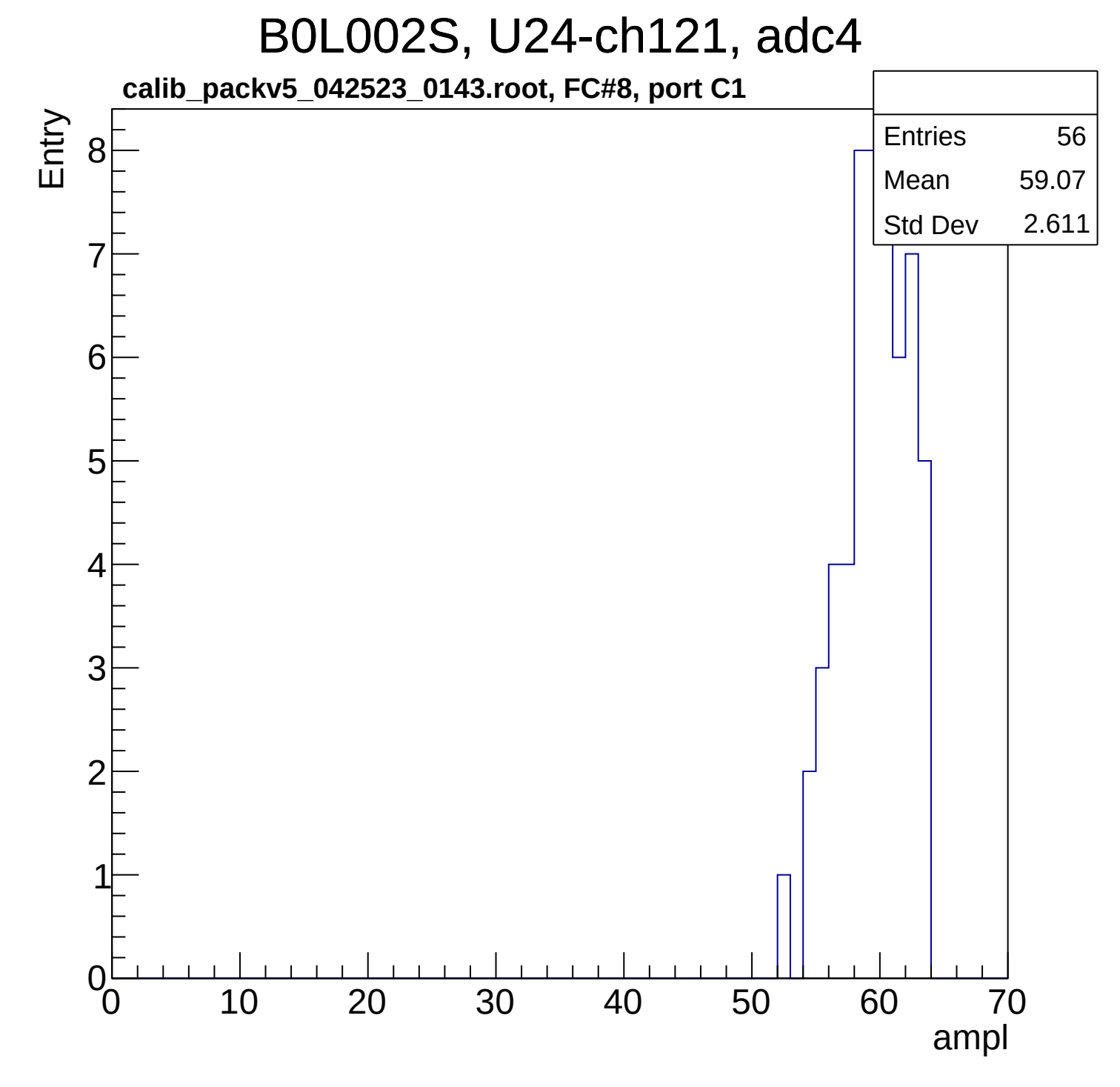
B0L002S, U24-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 59.07
Std Dev 2.611
Entry
ampl
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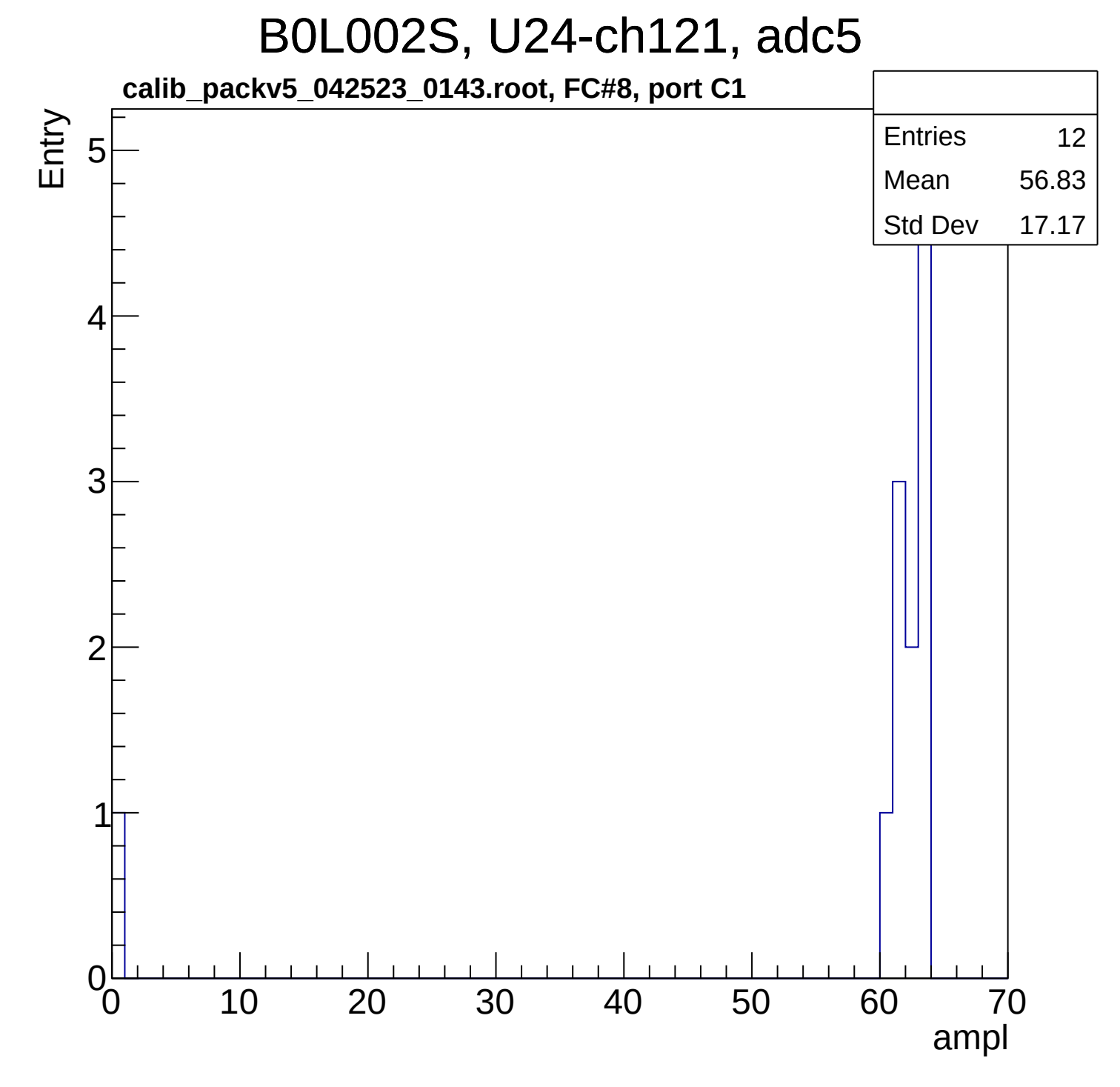
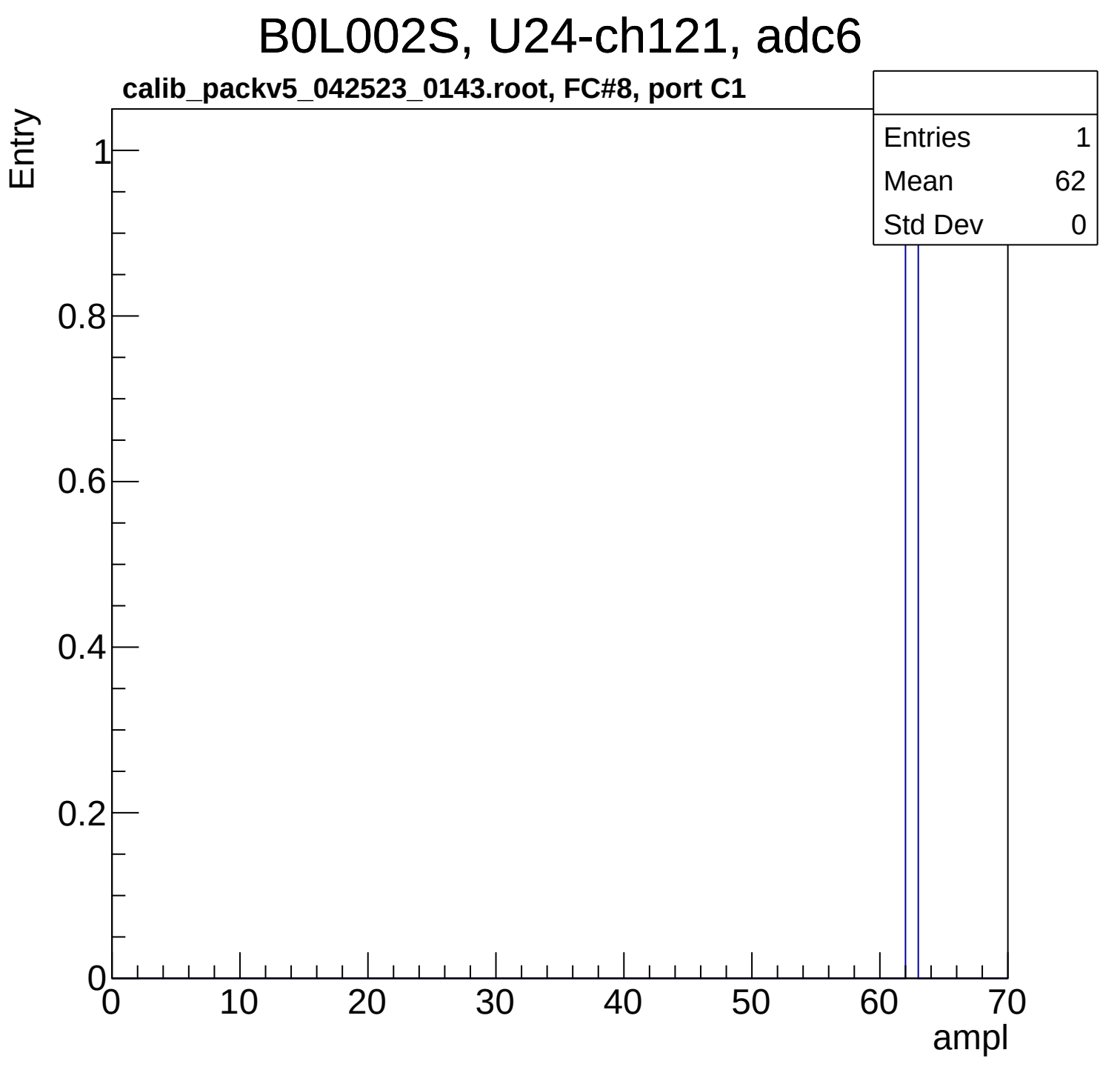
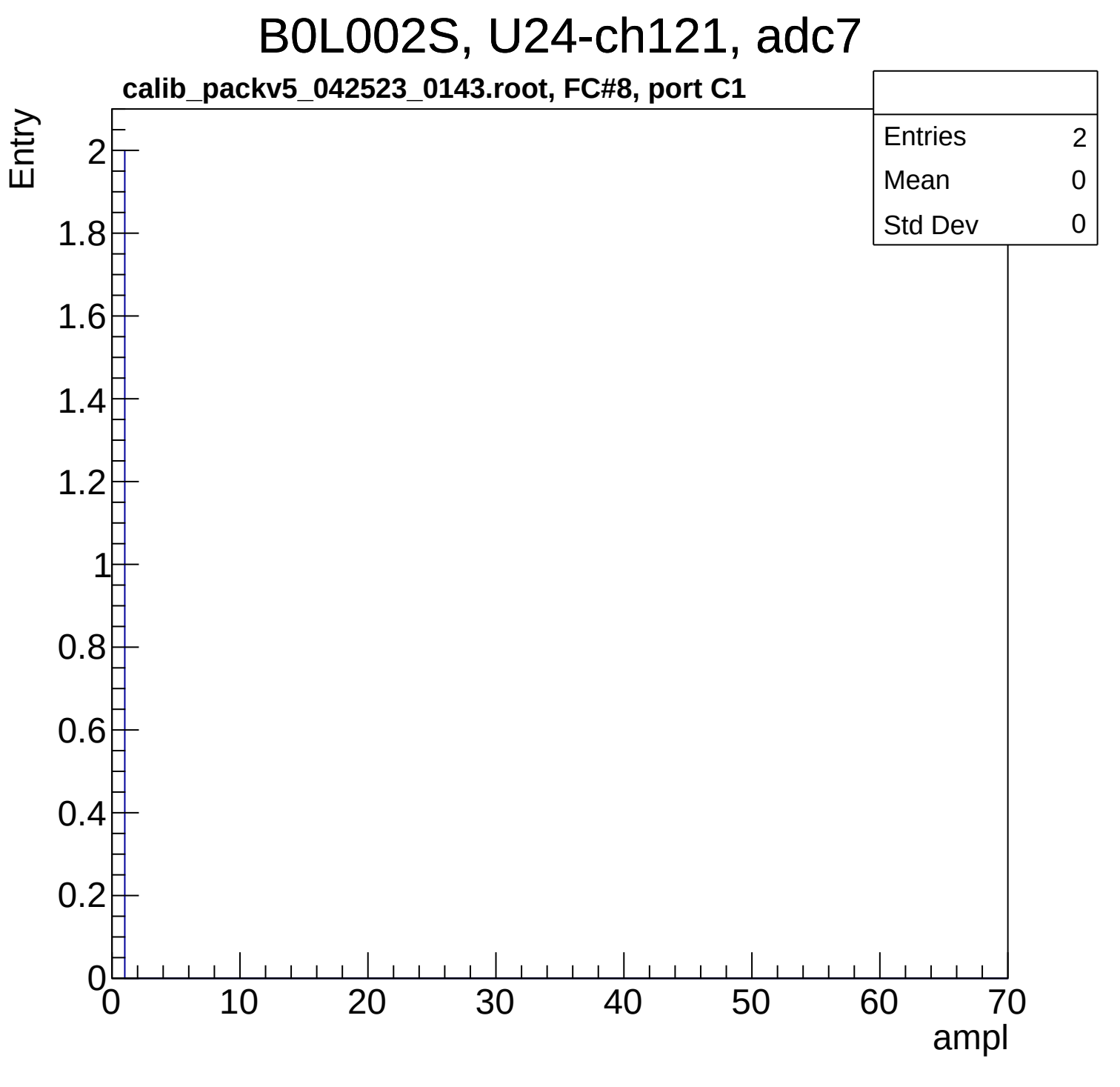
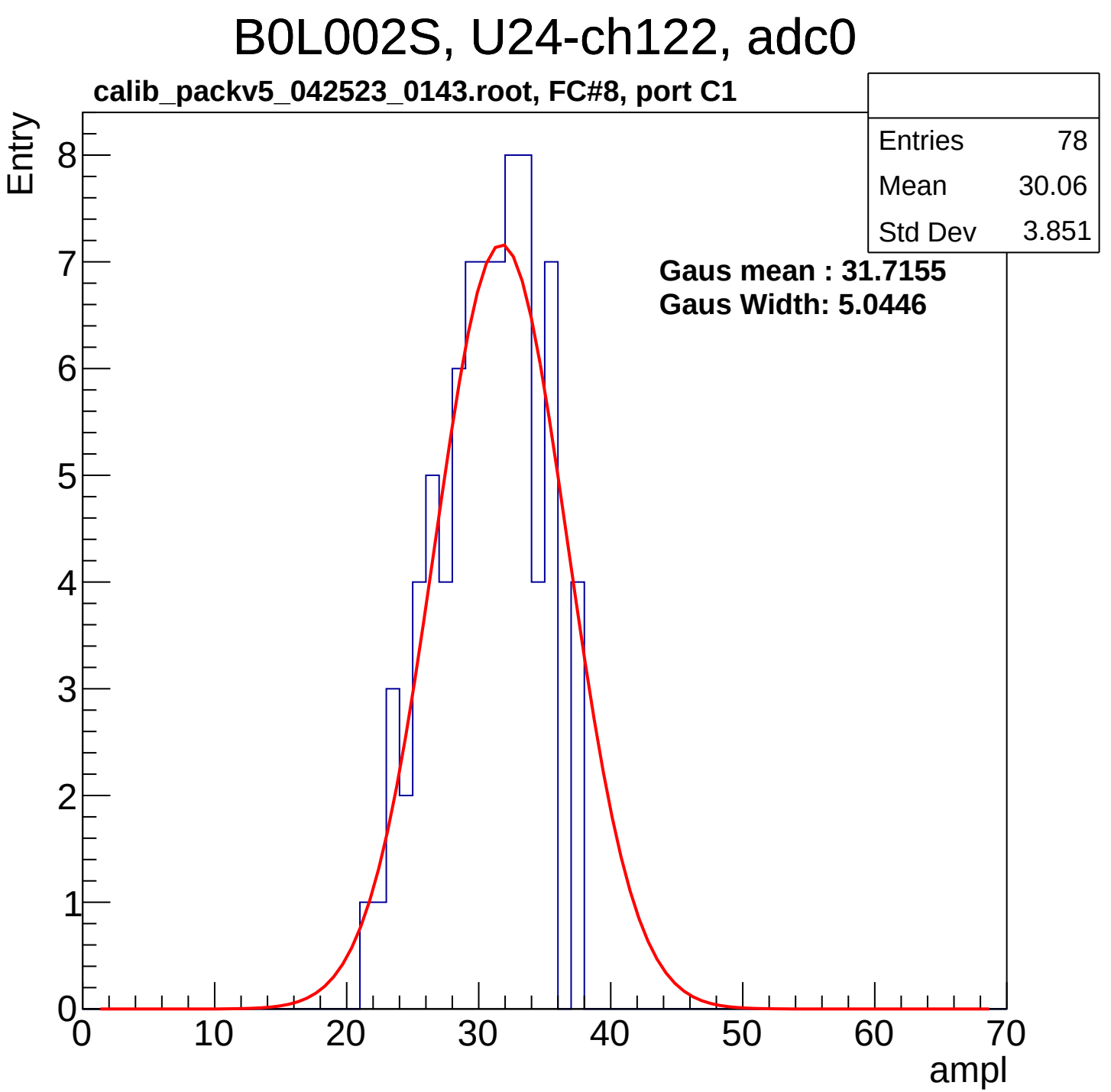
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 30.06
Std Dev 3.851
Gaus mean : 31.7155
Gaus Width: 5.0446
Entry
ampl
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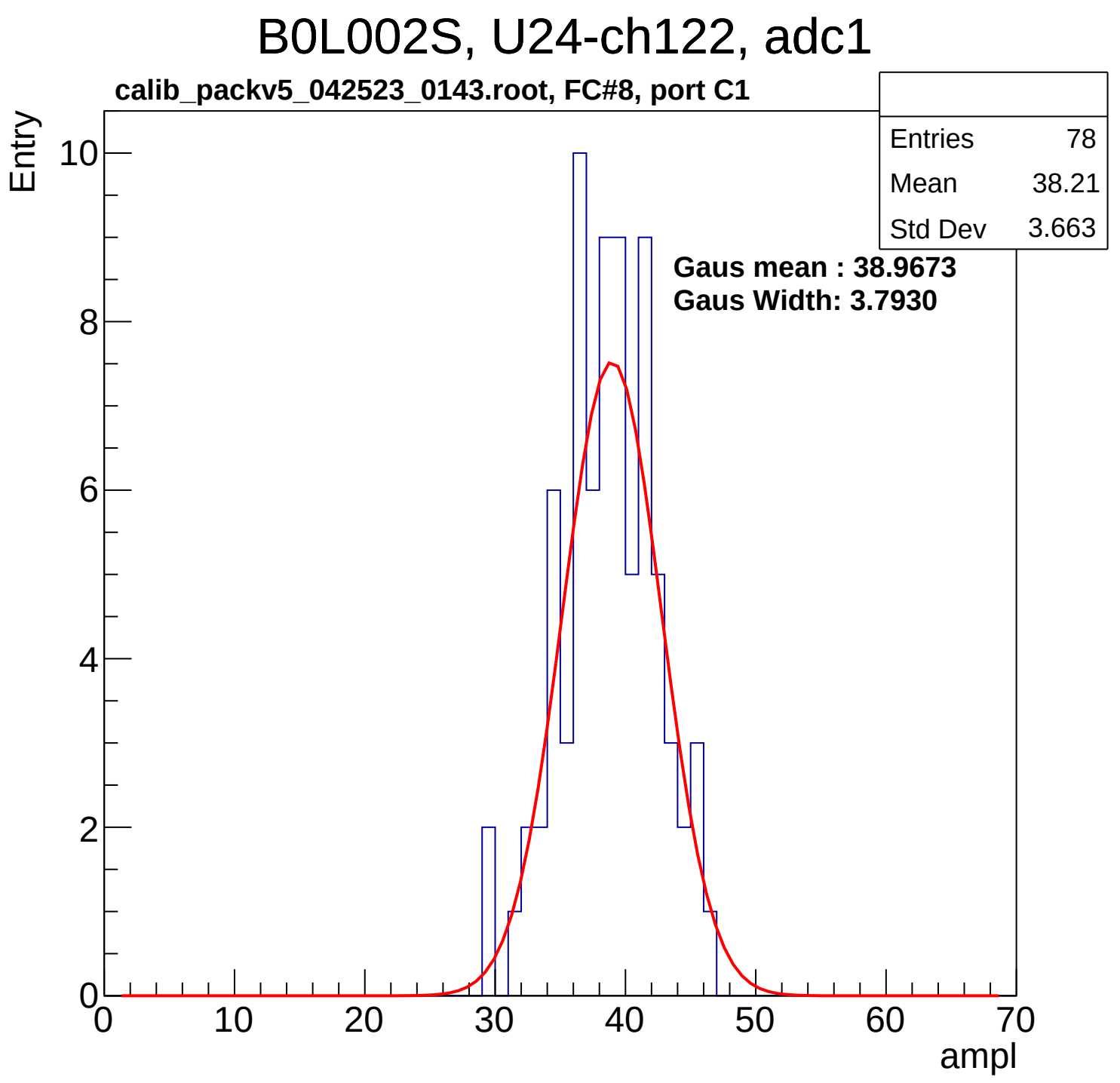
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.21
Std Dev 3.663
Gaus mean : 38.9673
Gaus Width: 3.7930
Entry
ampl
0
2
4
6
8
10
0
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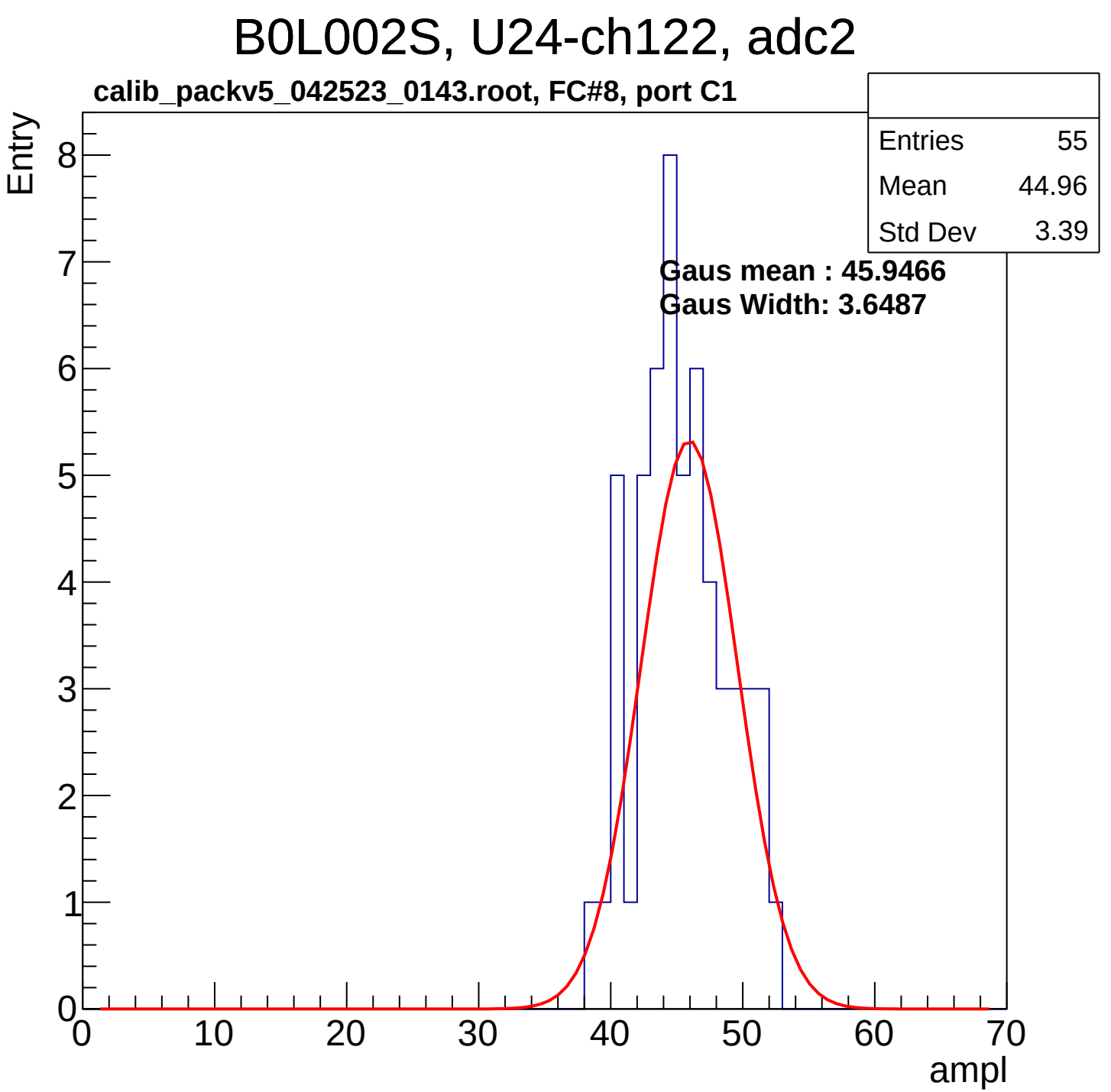
20
30
40
50
60
70


B0L002S, U24-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.96
Std Dev 3.39
Gaus mean : 45.9466
Gaus Width: 3.6487
Entry
ampl
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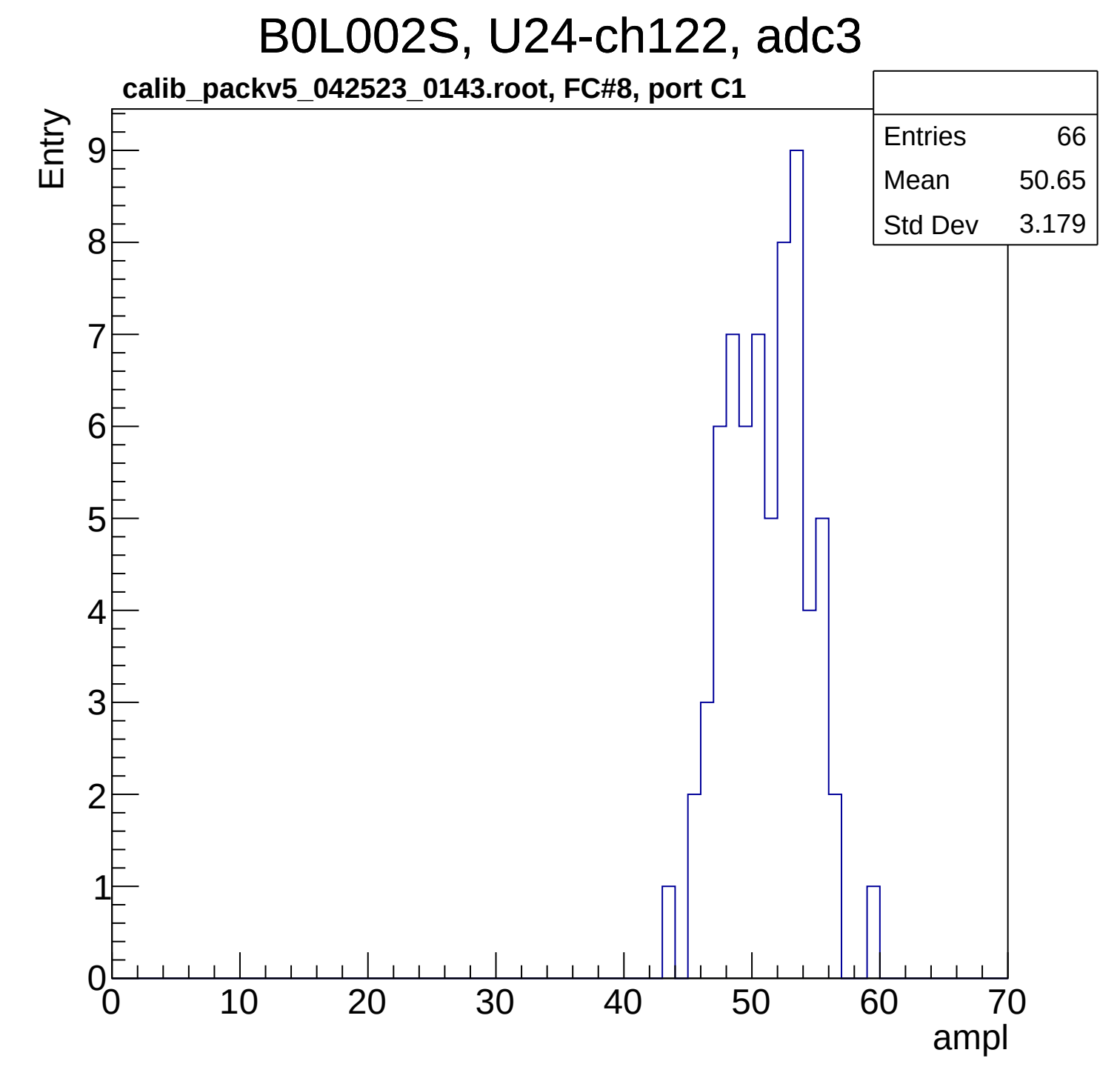
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.65
Std Dev 3.179
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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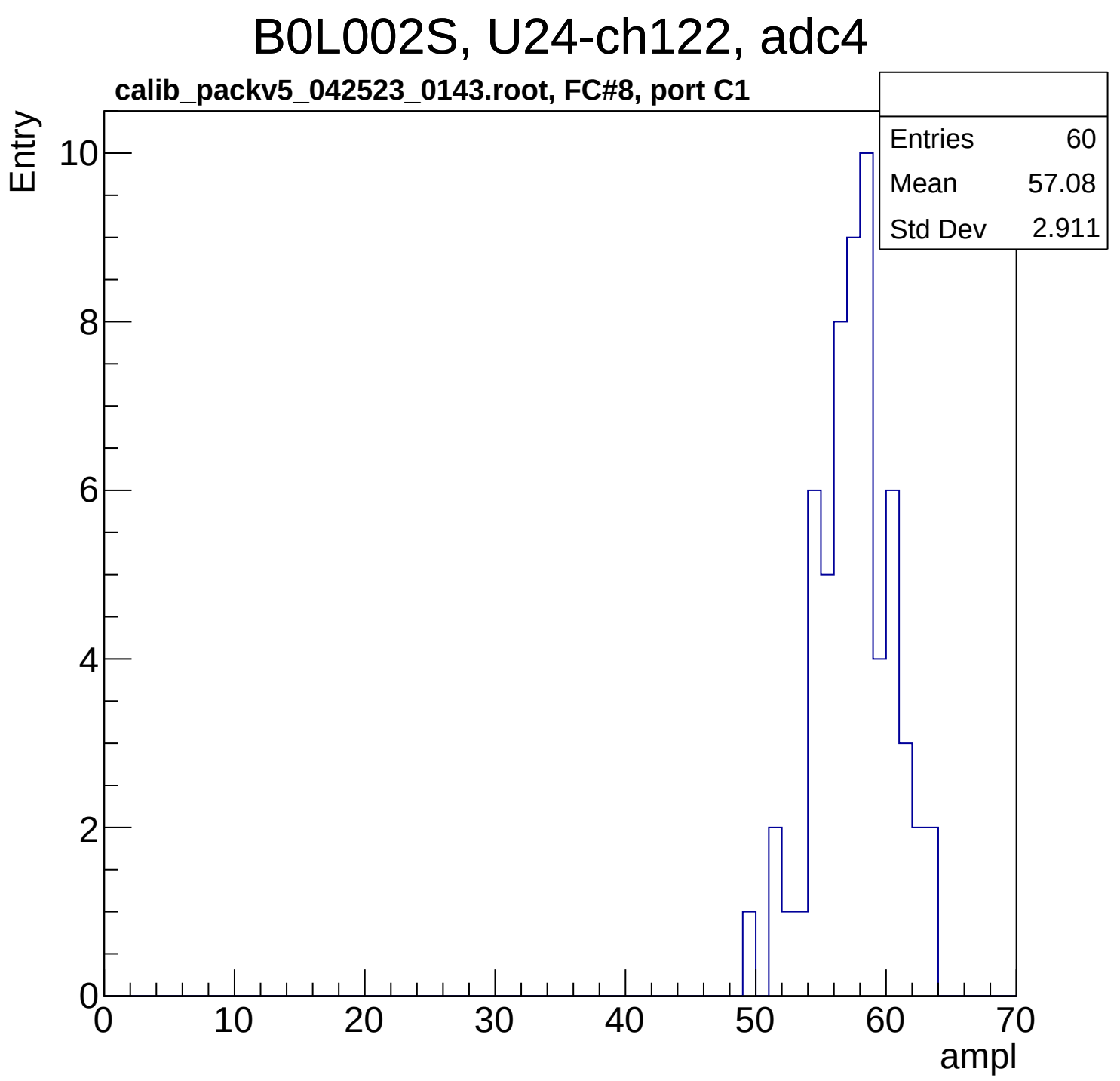
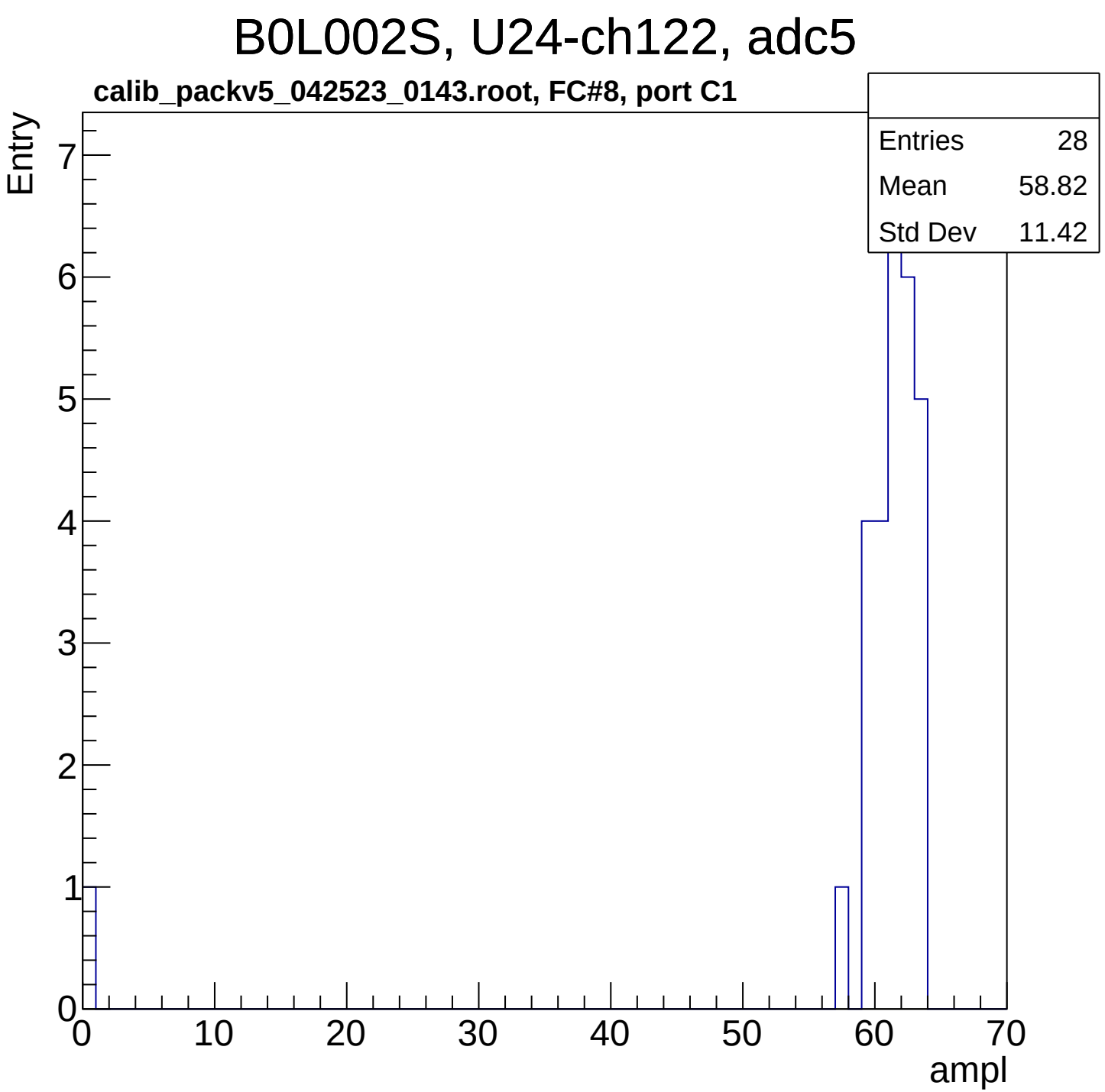
60
70


B0L002S, U24-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.08
Std Dev 2.911
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.82
Std Dev 11.42
Entry
ampl
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3
4
5
6
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0
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60
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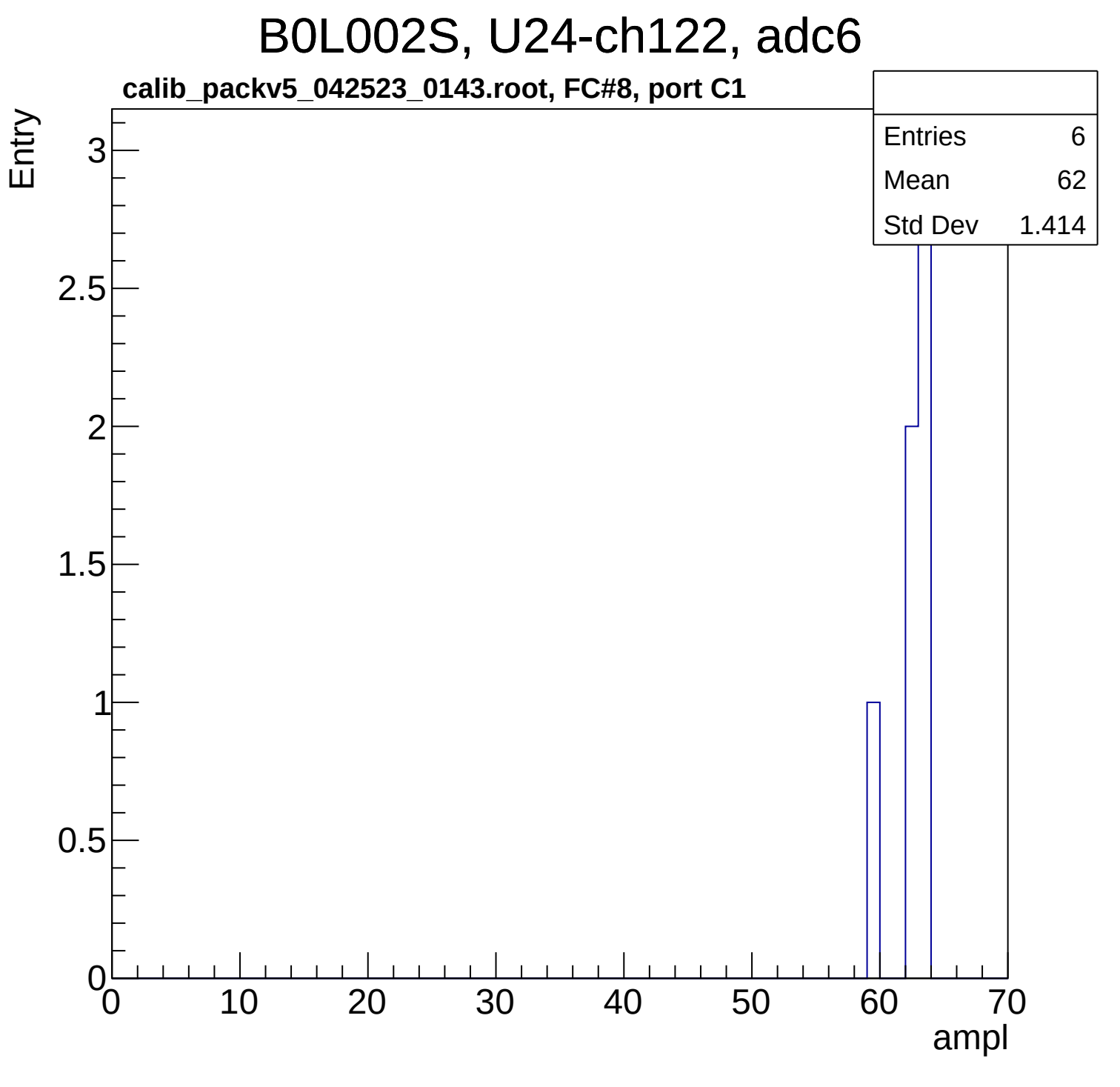
B0L002S, U24-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62
Std Dev 1.414
Entry
ampl
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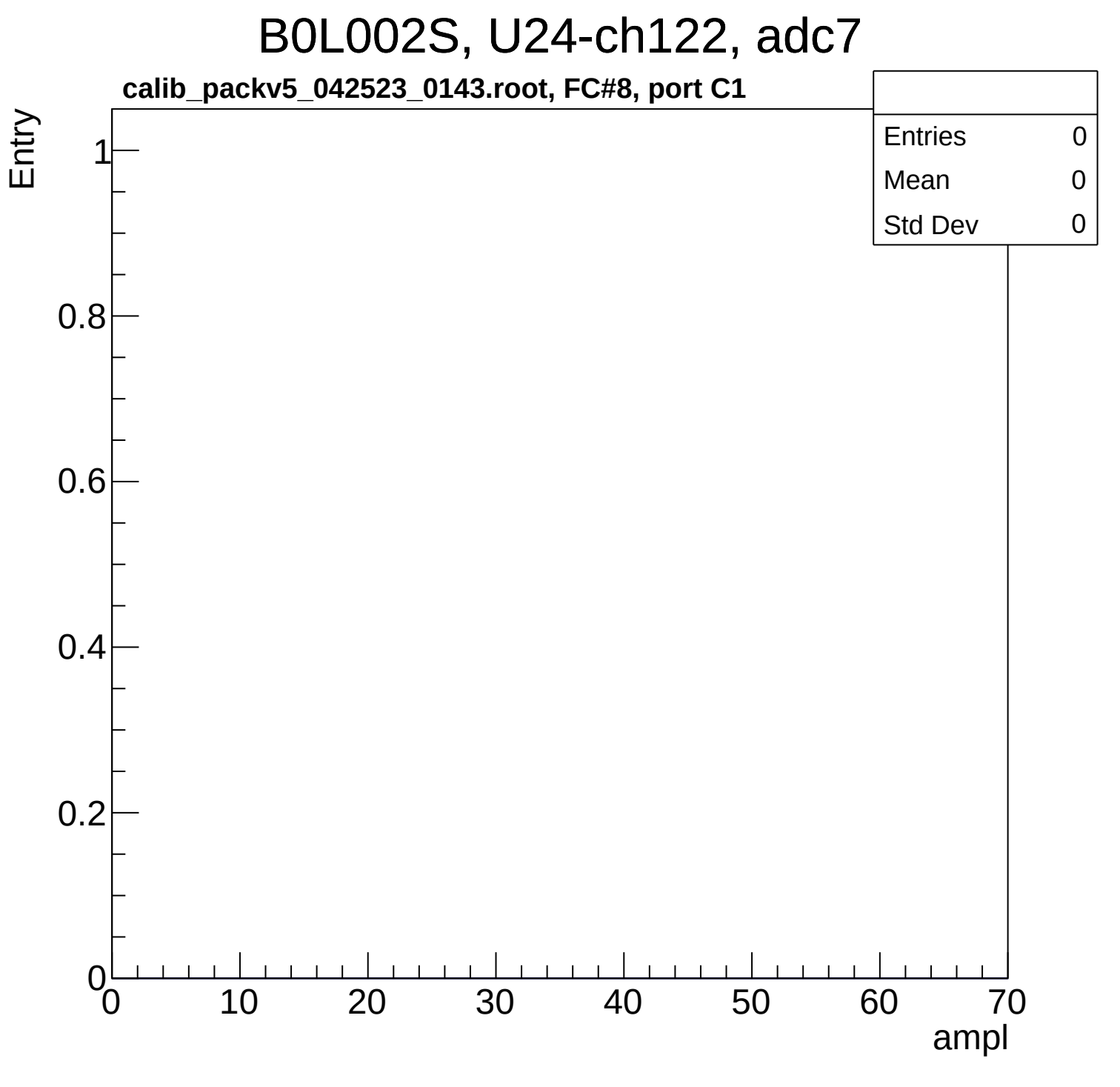
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U24-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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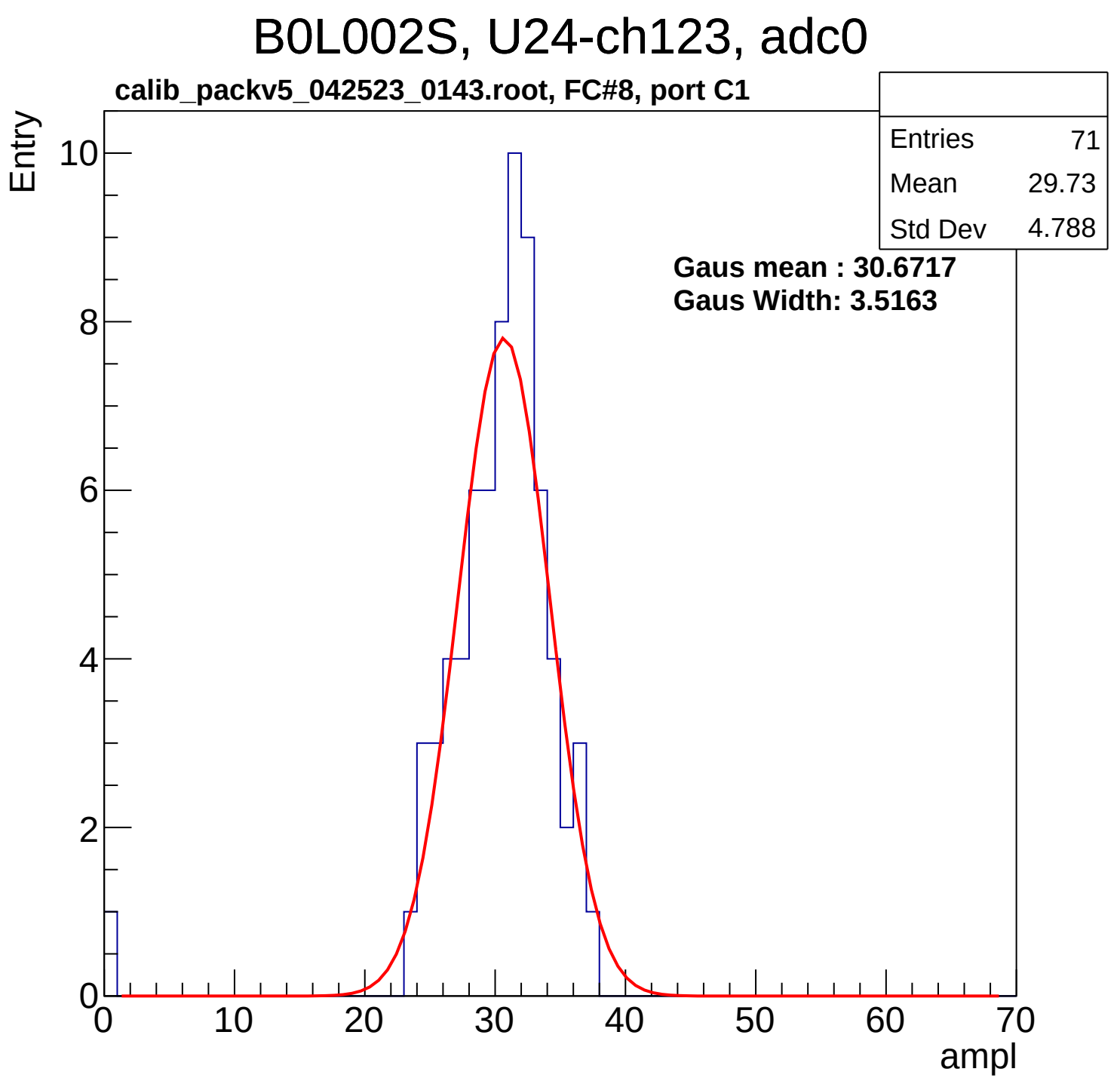
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.73
Std Dev 4.788
Gaus mean : 30.6717
Gaus Width: 3.5163
Entry
ampl
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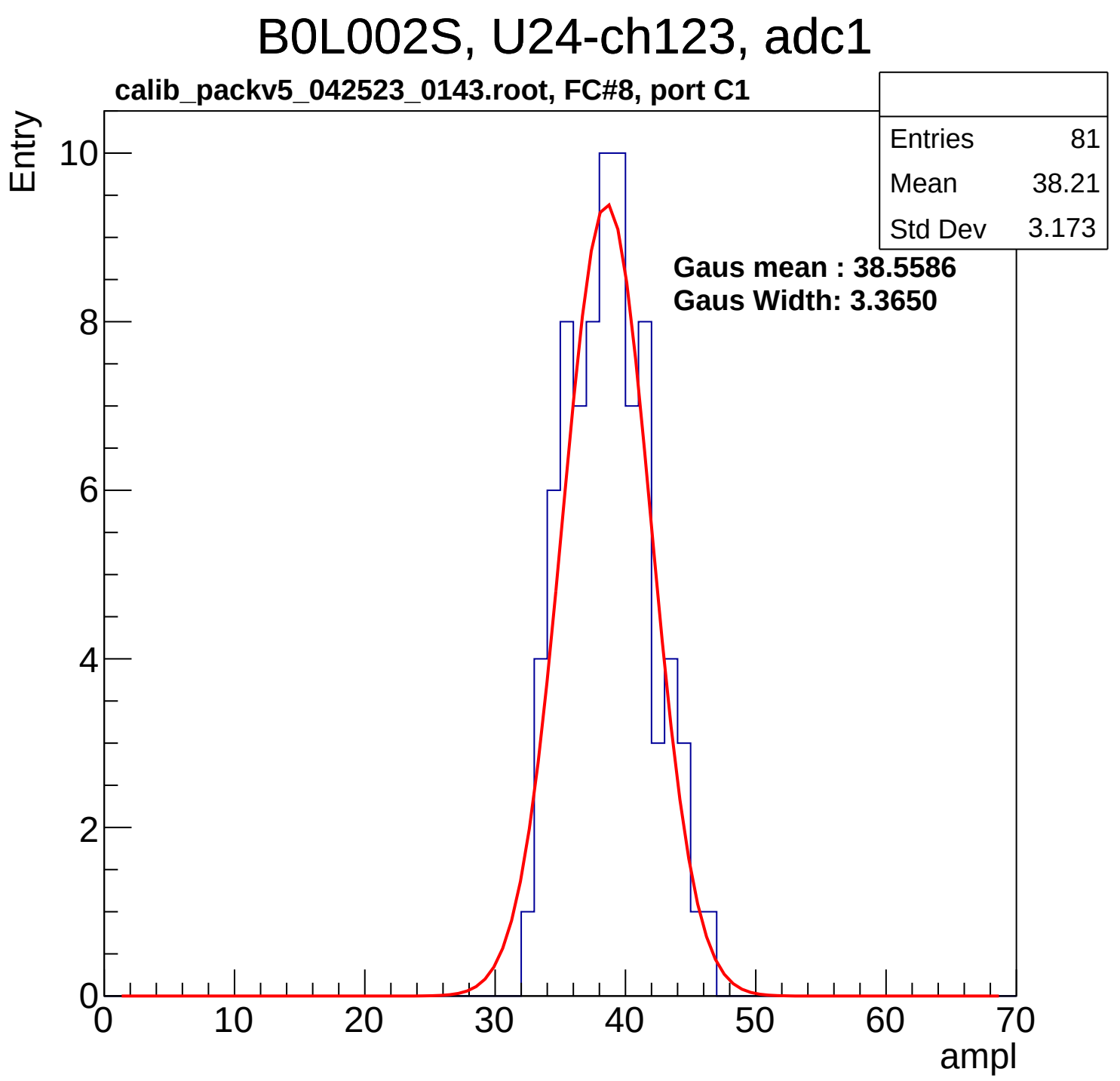
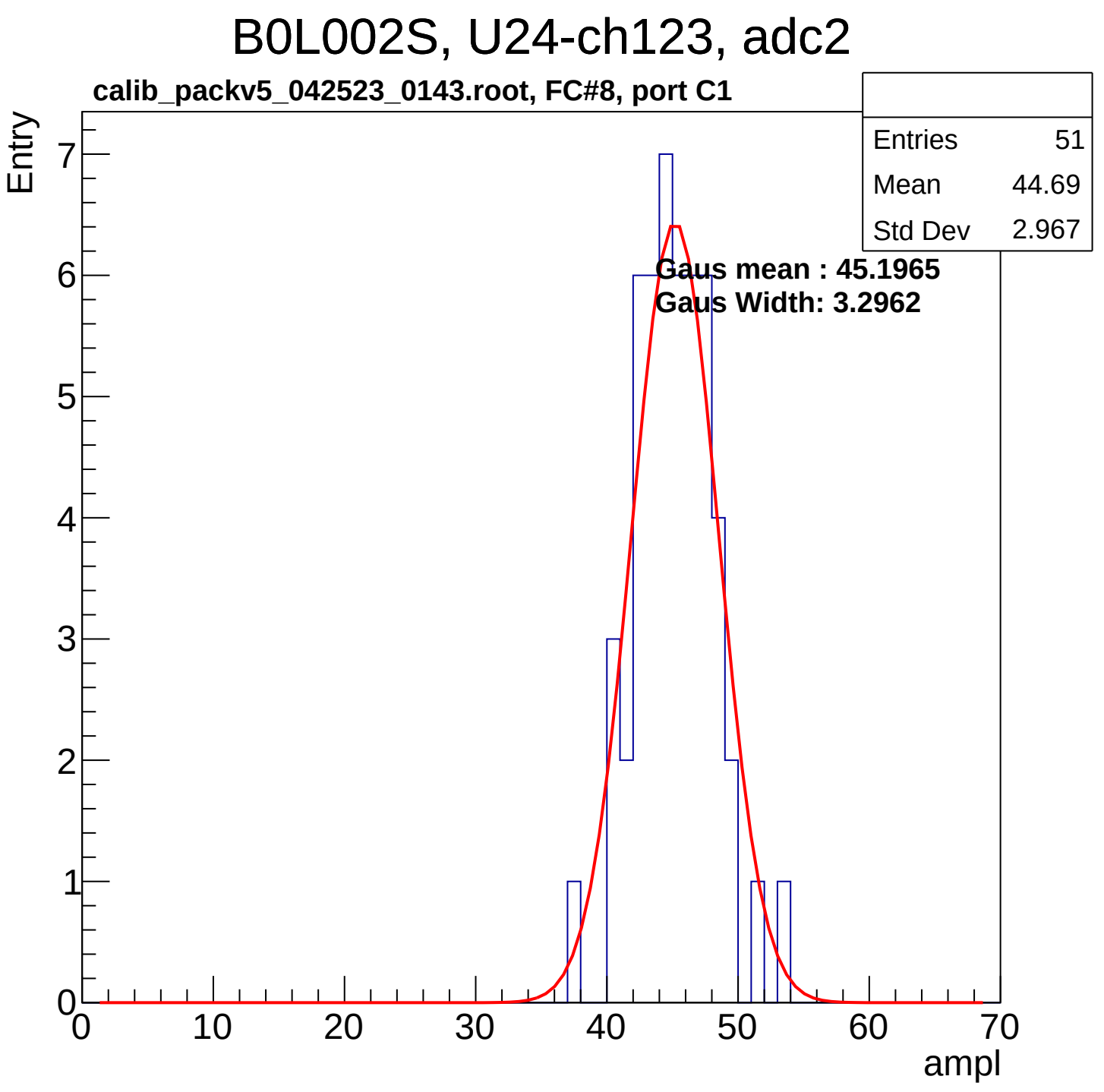
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 38.21
Std Dev 3.173
Gaus mean : 38.5586
Gaus Width: 3.3650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 44.69
Std Dev 2.967
Gaus mean : 45.1965
Gaus Width: 3.2962
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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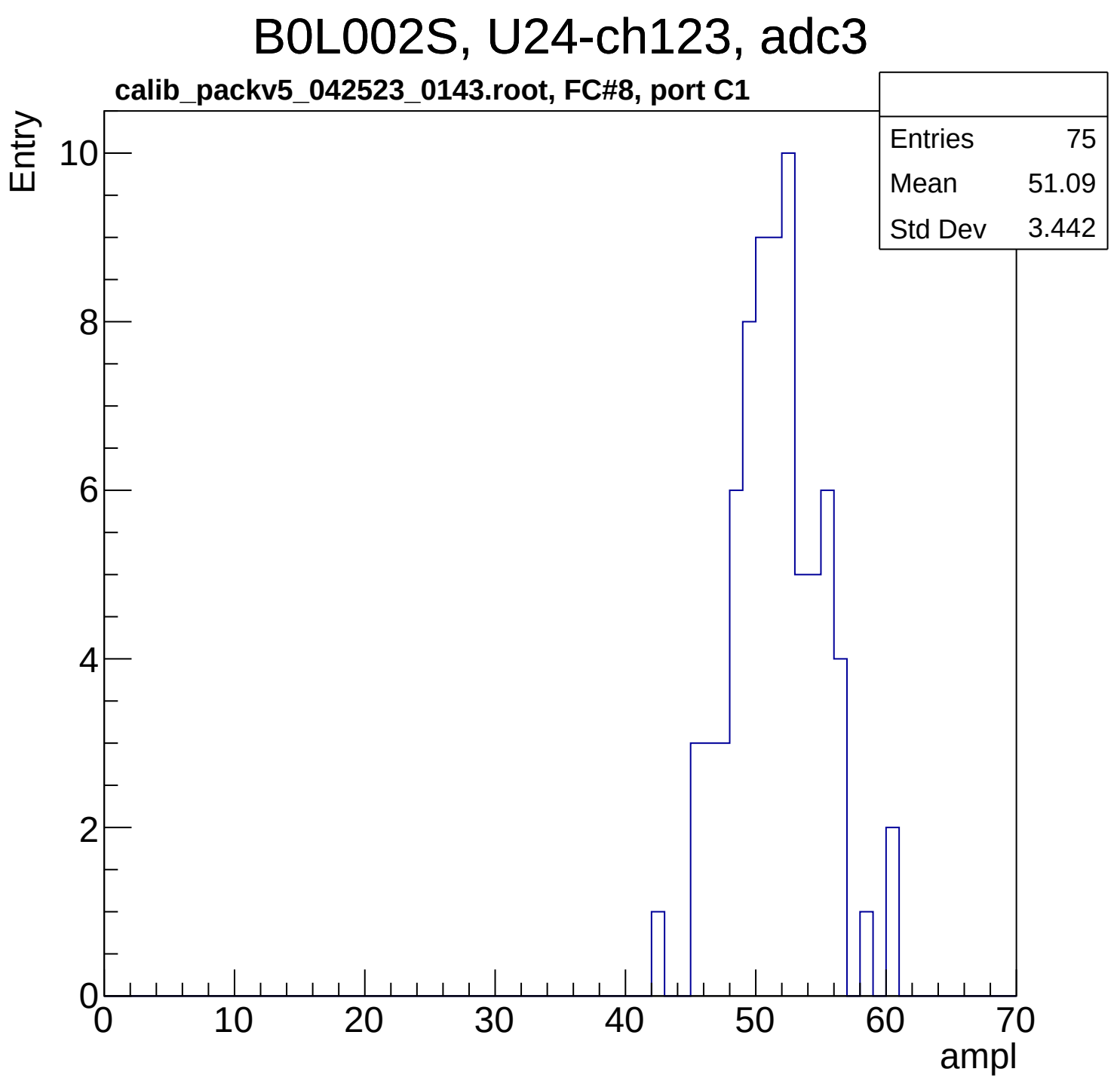
B0L002S, U24-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.09
Std Dev 3.442
Entry
ampl
0
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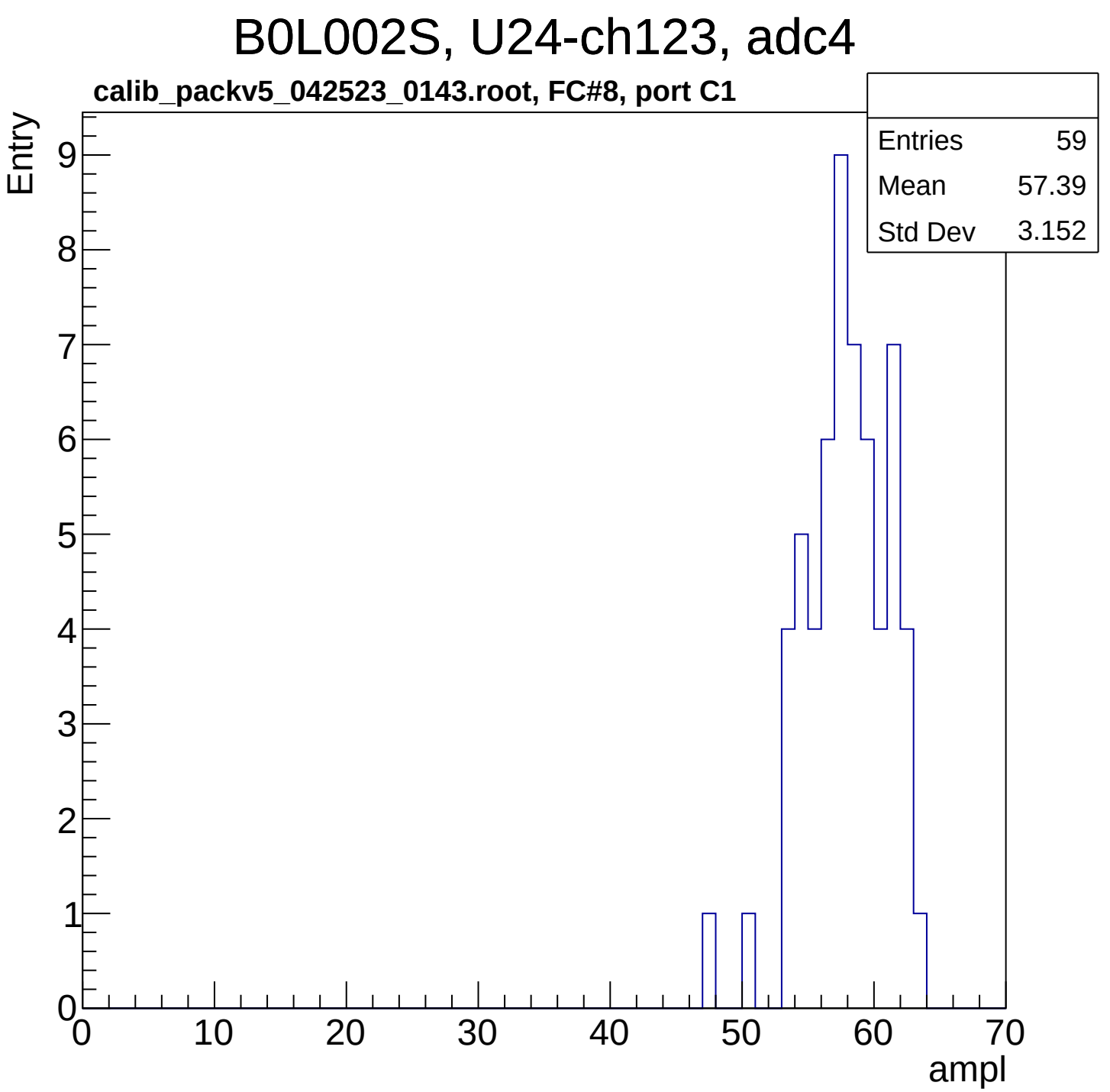
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.39
Std Dev 3.152
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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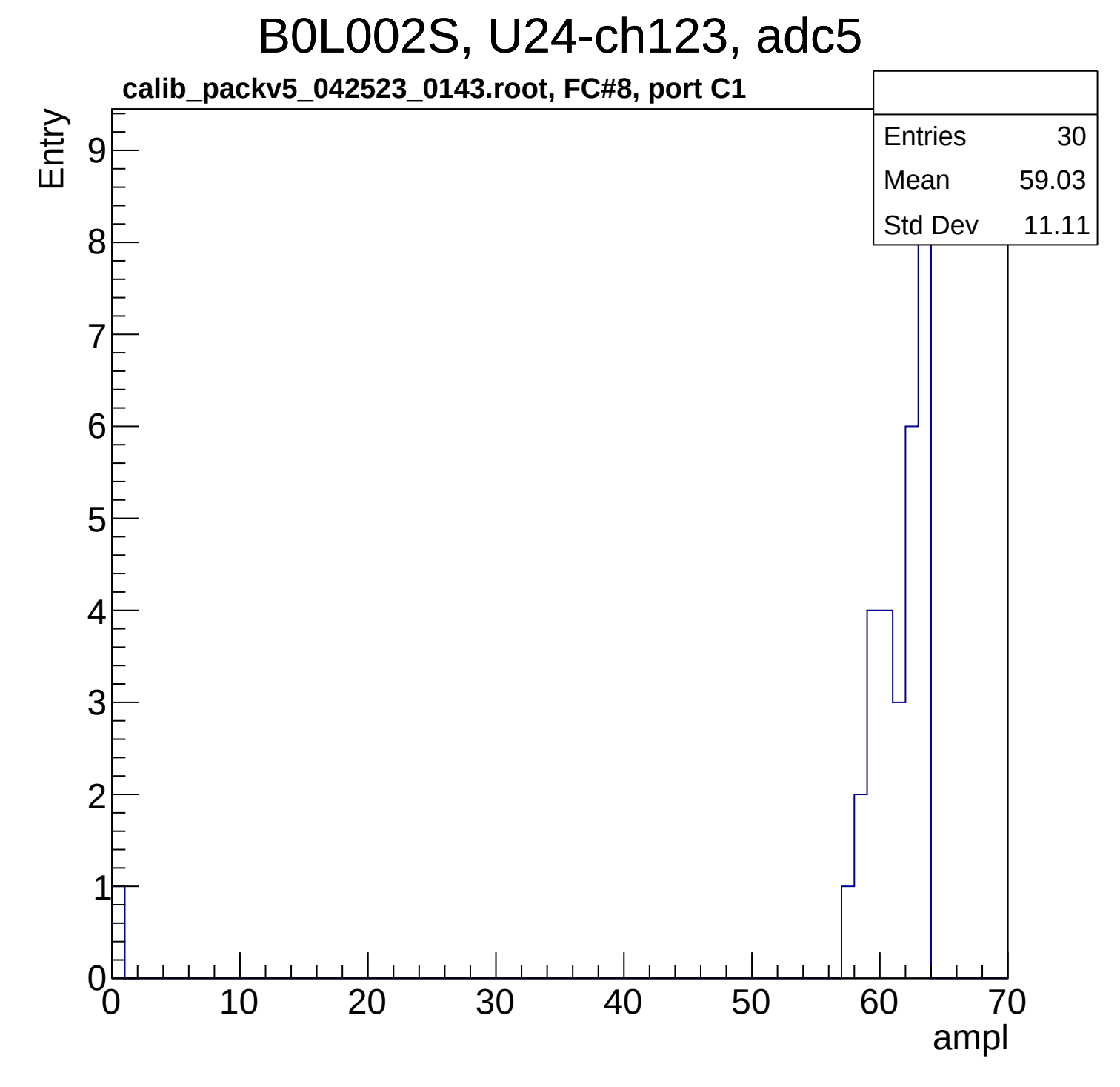
B0L002S, U24-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.03
Std Dev 11.11
Entry
ampl
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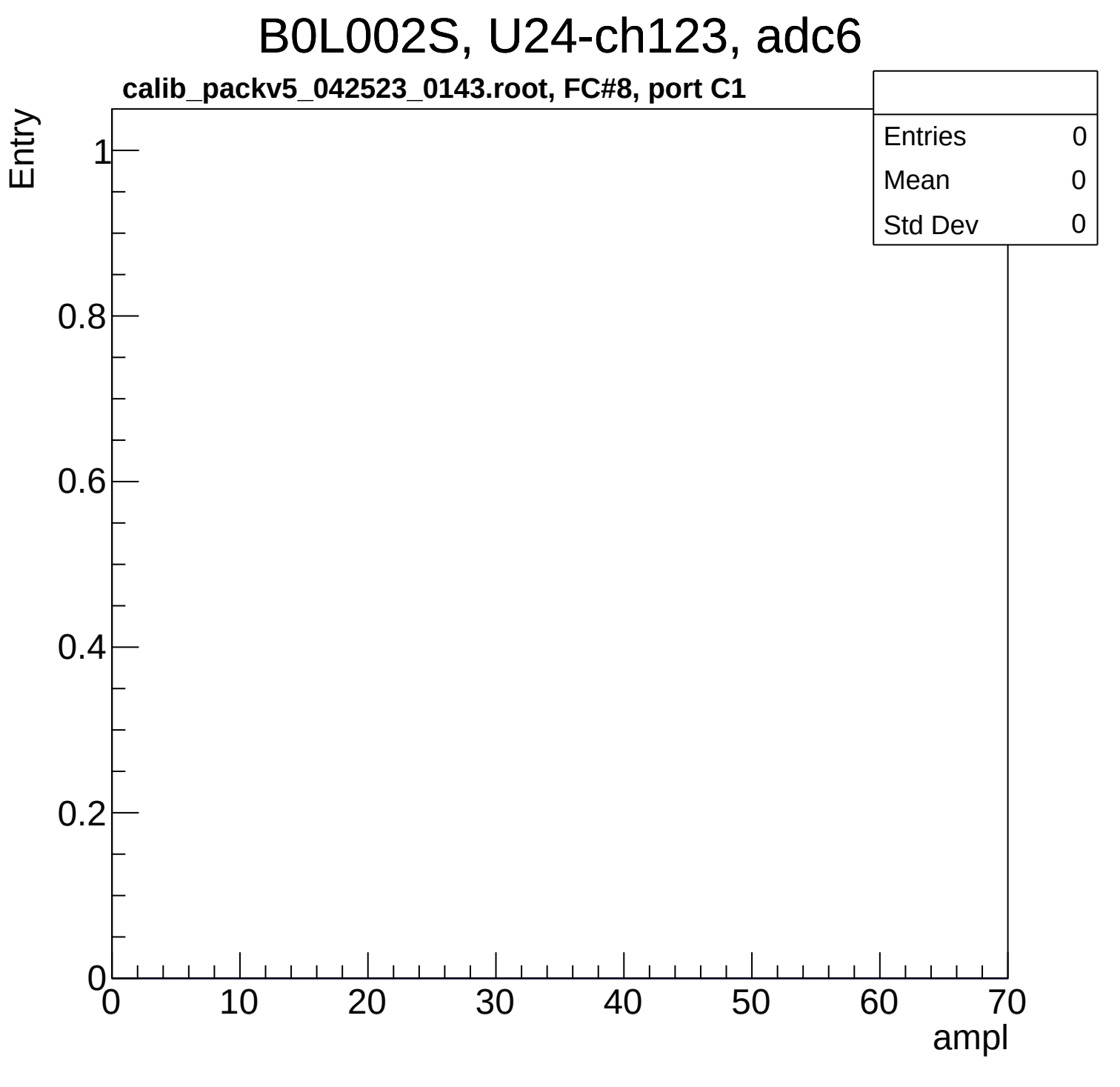
B0L002S, U24-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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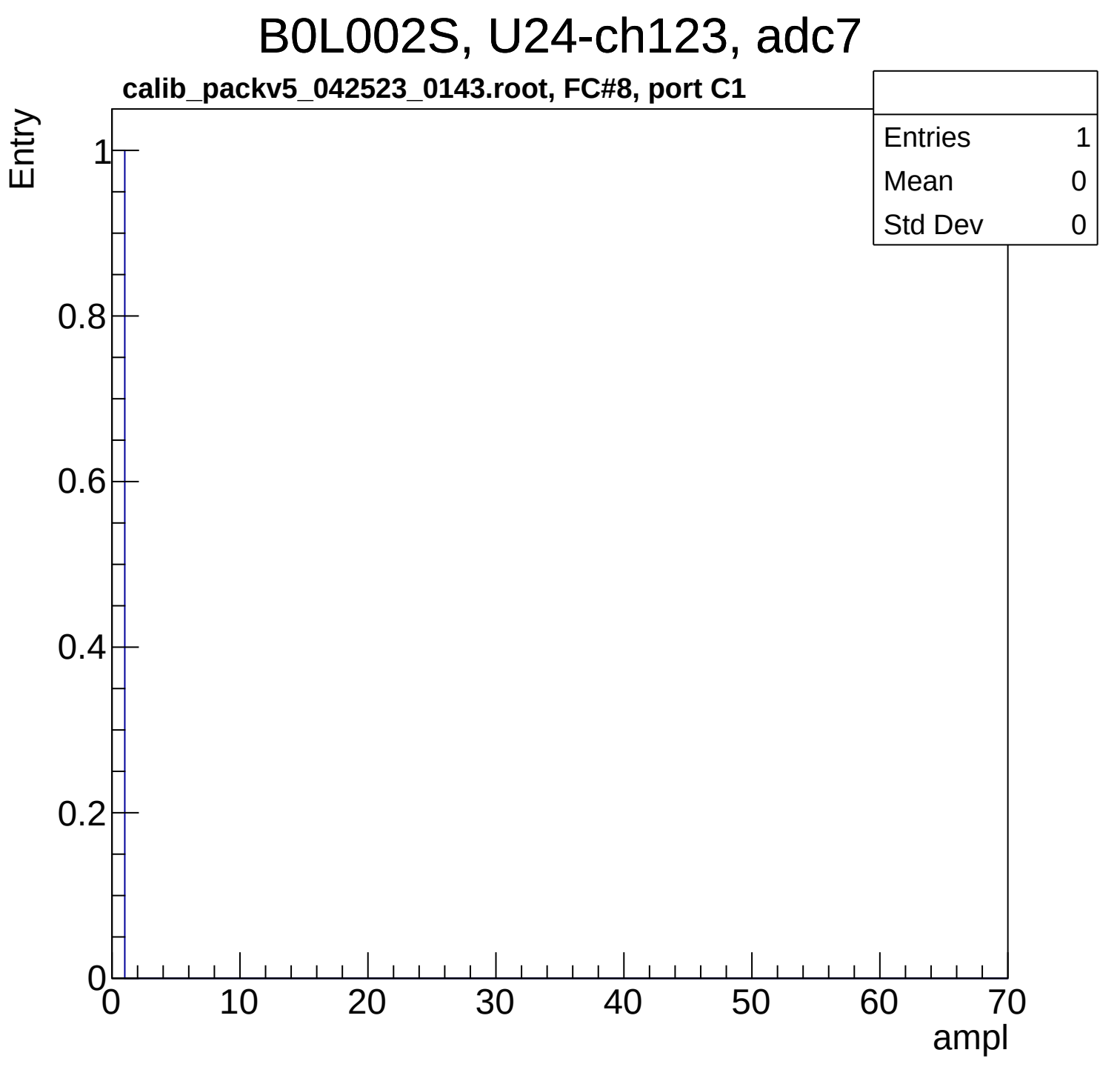
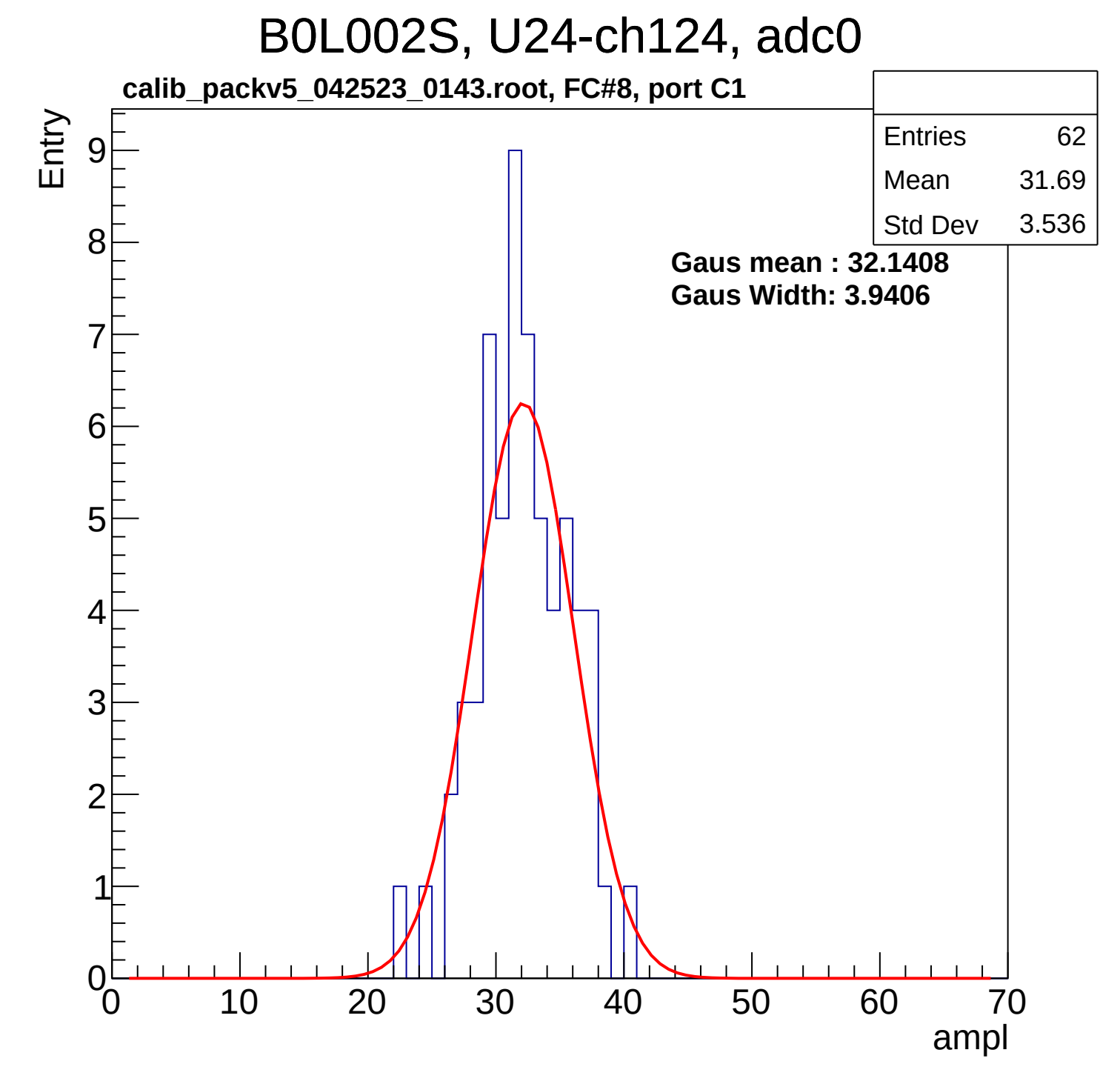
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 31.69
Std Dev 3.536
Gaus mean : 32.1408
Gaus Width: 3.9406
Entry
ampl
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10
20
30
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1
2
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4
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6
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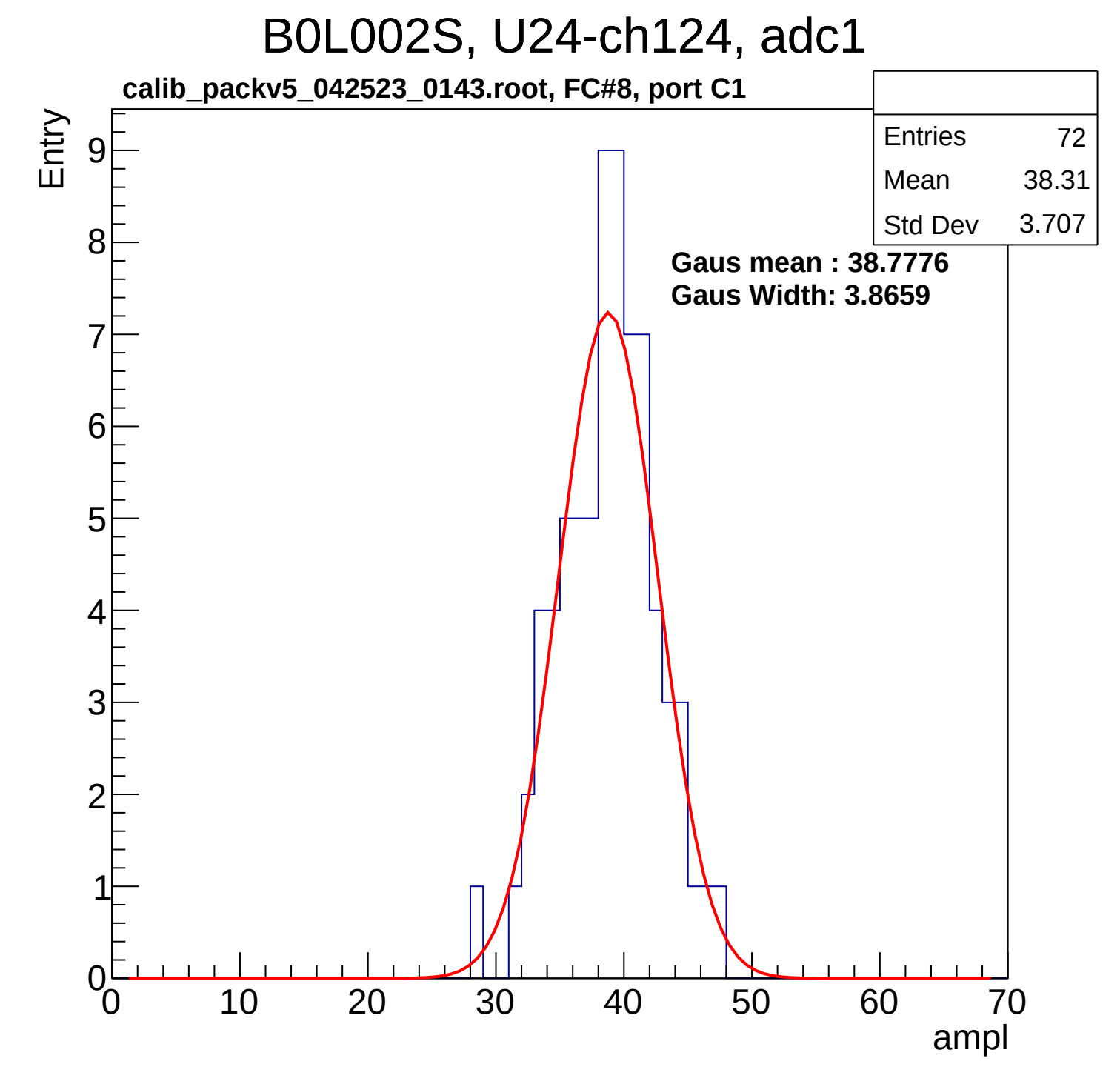
8
9


B0L002S, U24-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 38.31
Std Dev 3.707
Gaus mean : 38.7776
Gaus Width: 3.8659
Entry
ampl
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3
4
5
6
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0
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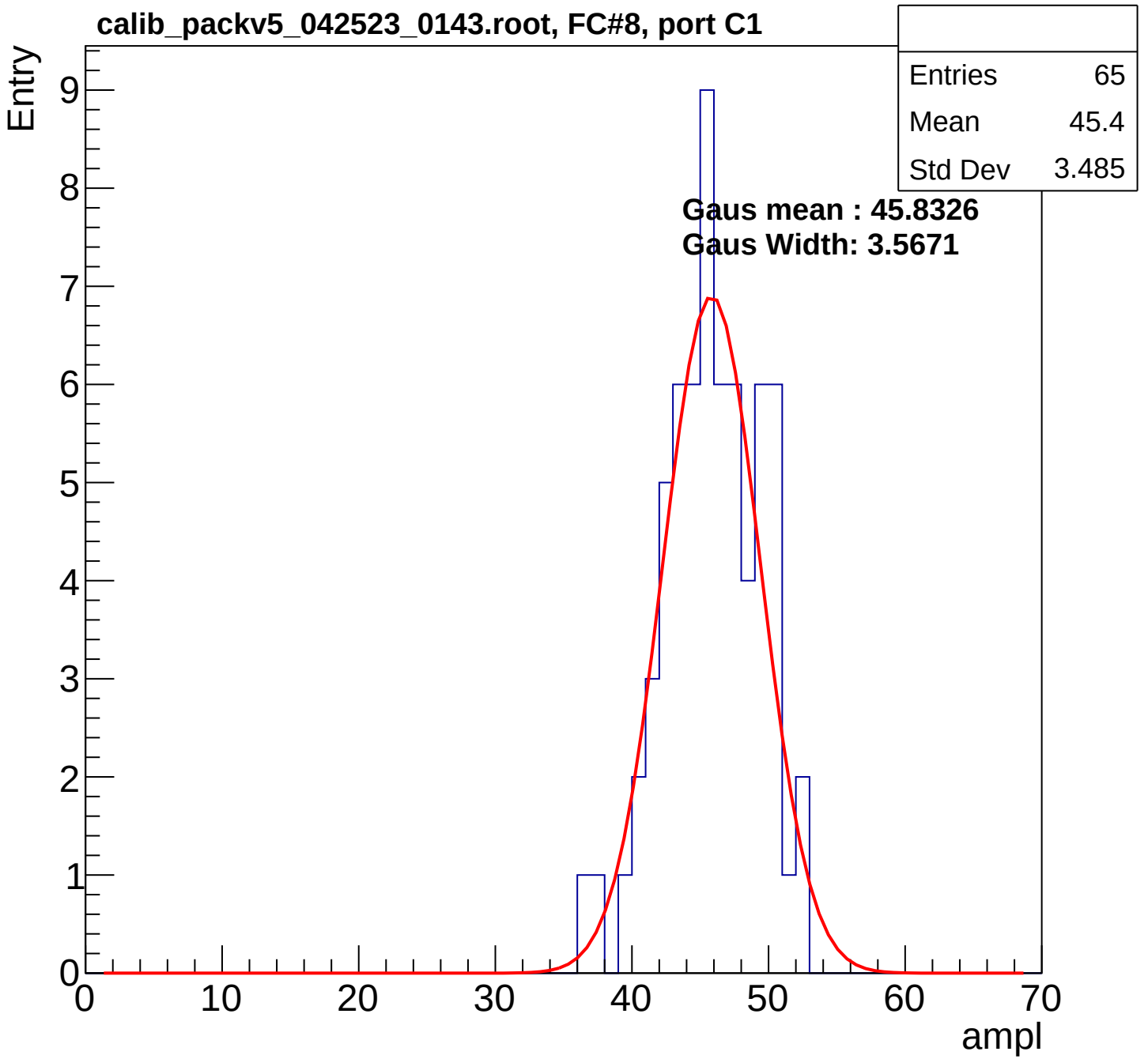
B0L002S, U24-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.4
Std Dev 3.485
Gaus mean : 45.8326
Gaus Width: 3.5671
Entry
ampl
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3
4
5
6
7
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0
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60
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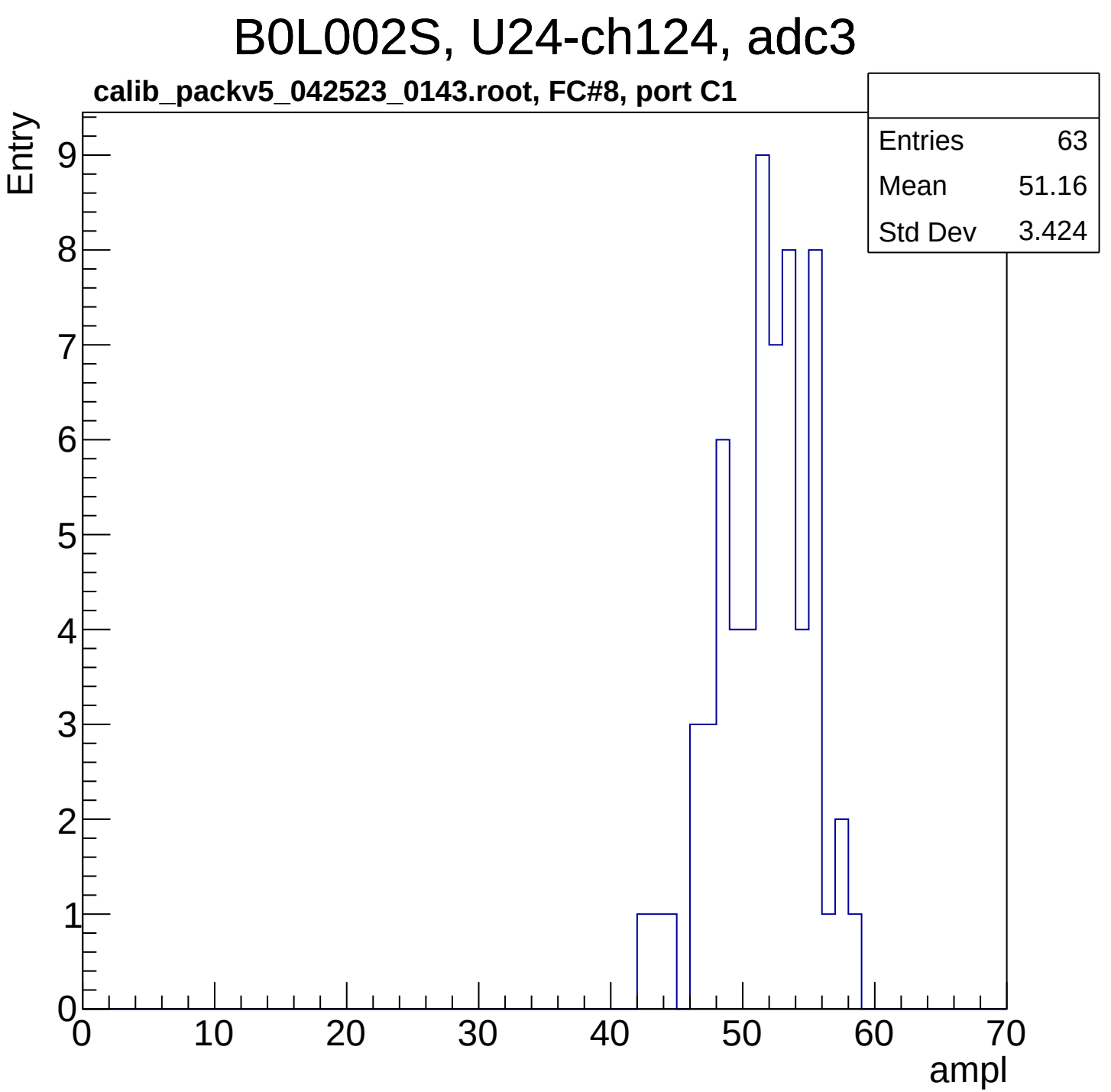
B0L002S, U24-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.16
Std Dev 3.424
Entry
ampl
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2
3
4
5
6
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0
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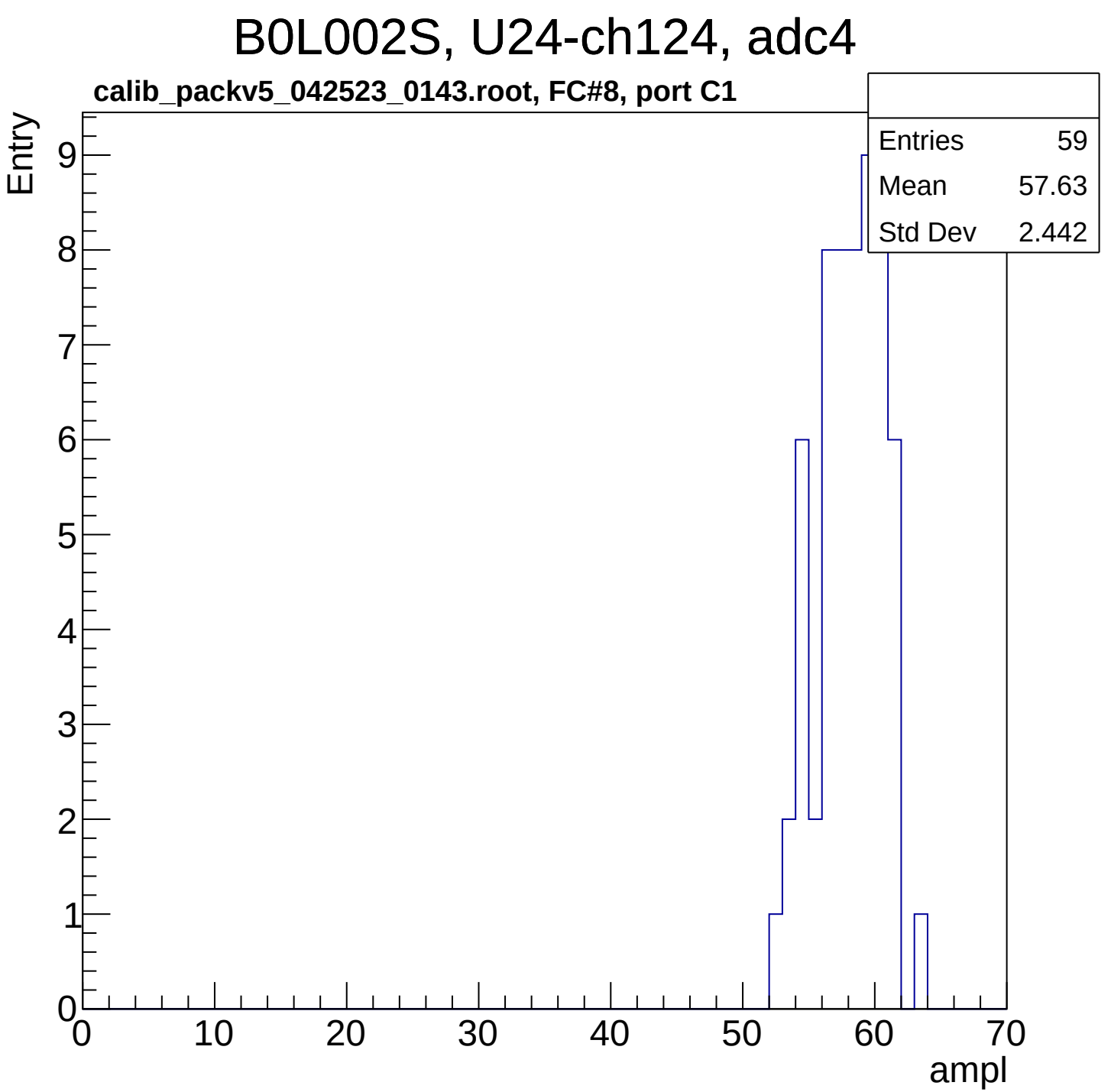
B0L002S, U24-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.63
Std Dev 2.442
Entry
ampl
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3
4
5
6
7
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9
0
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60
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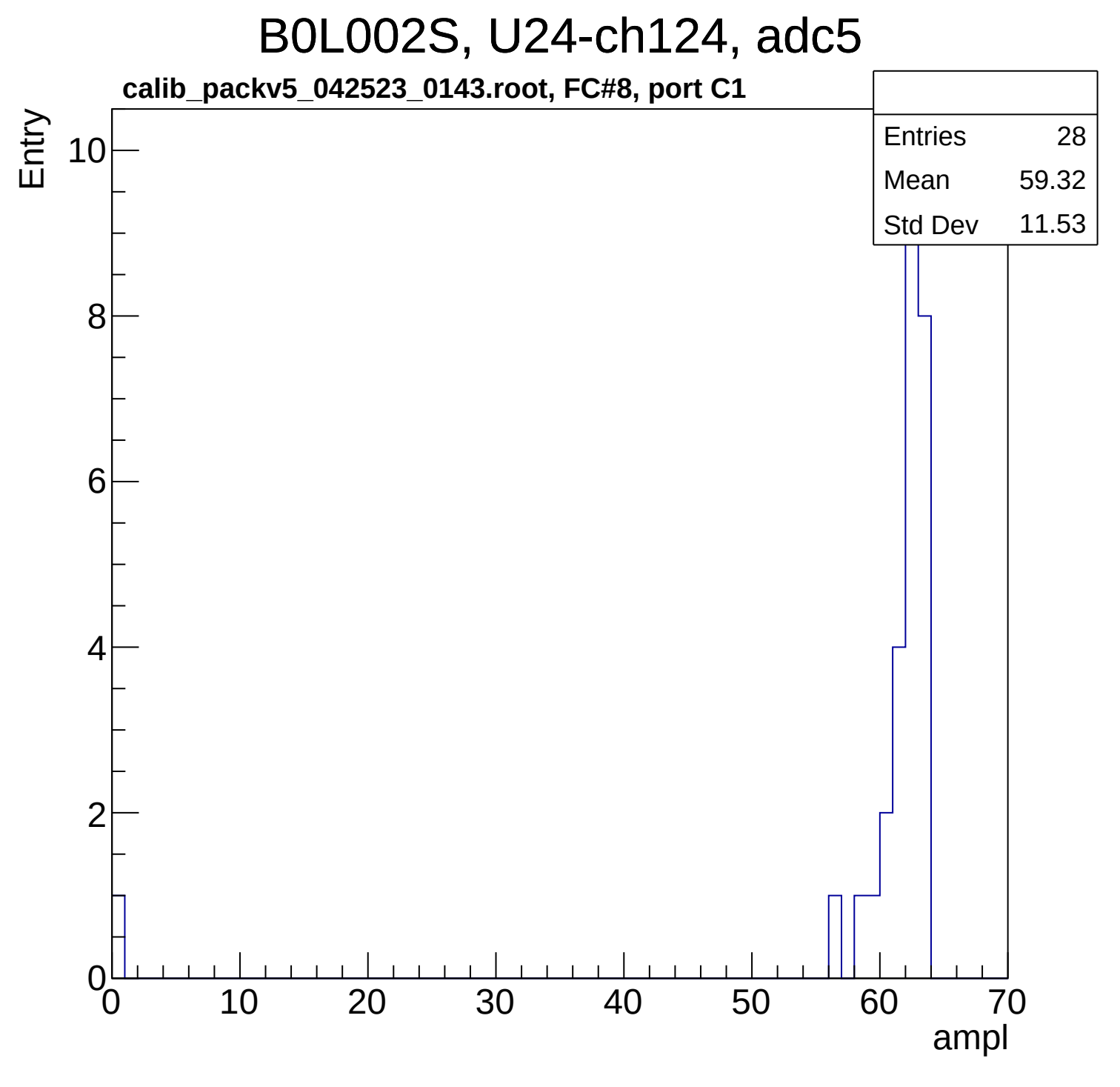
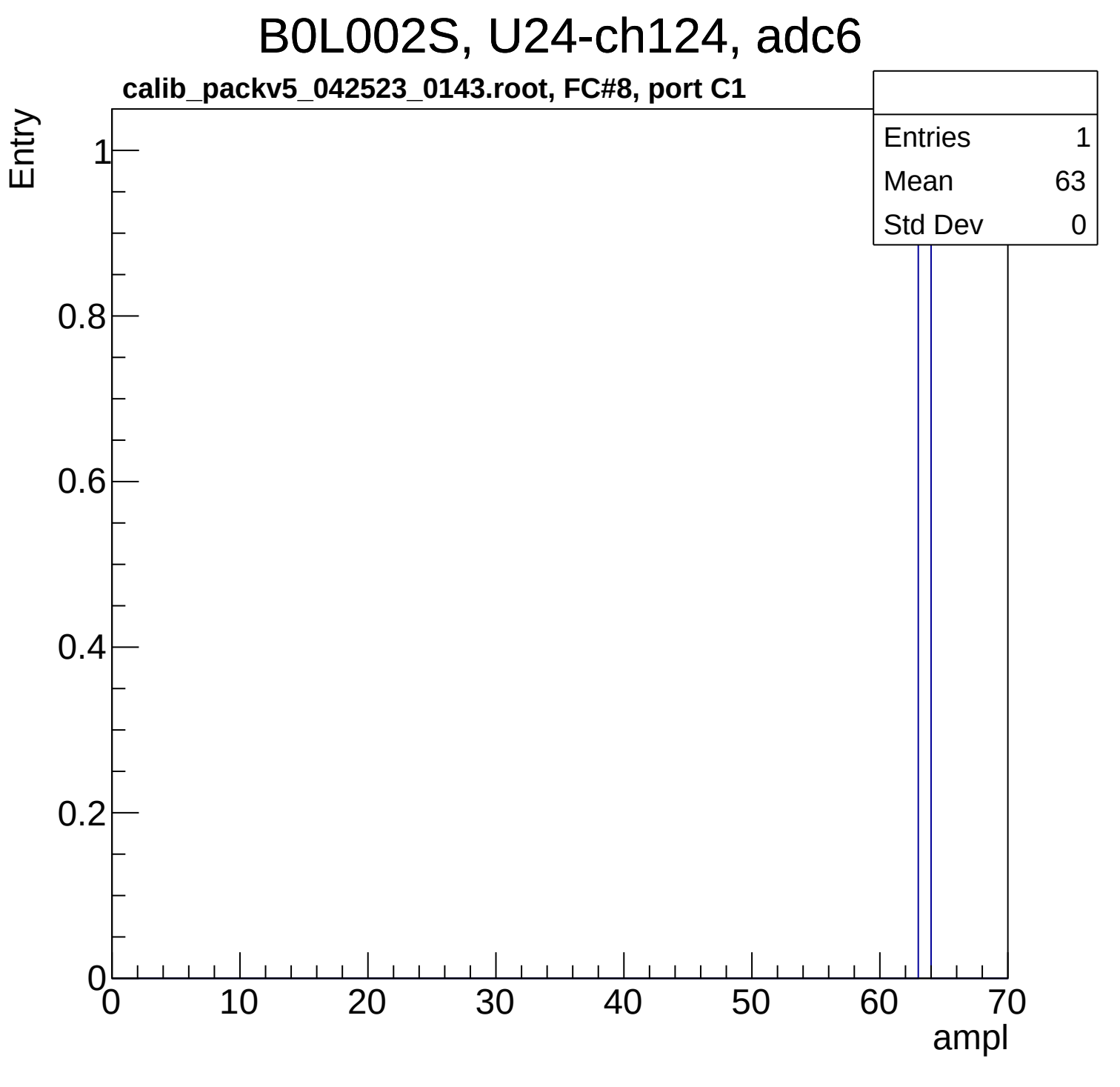
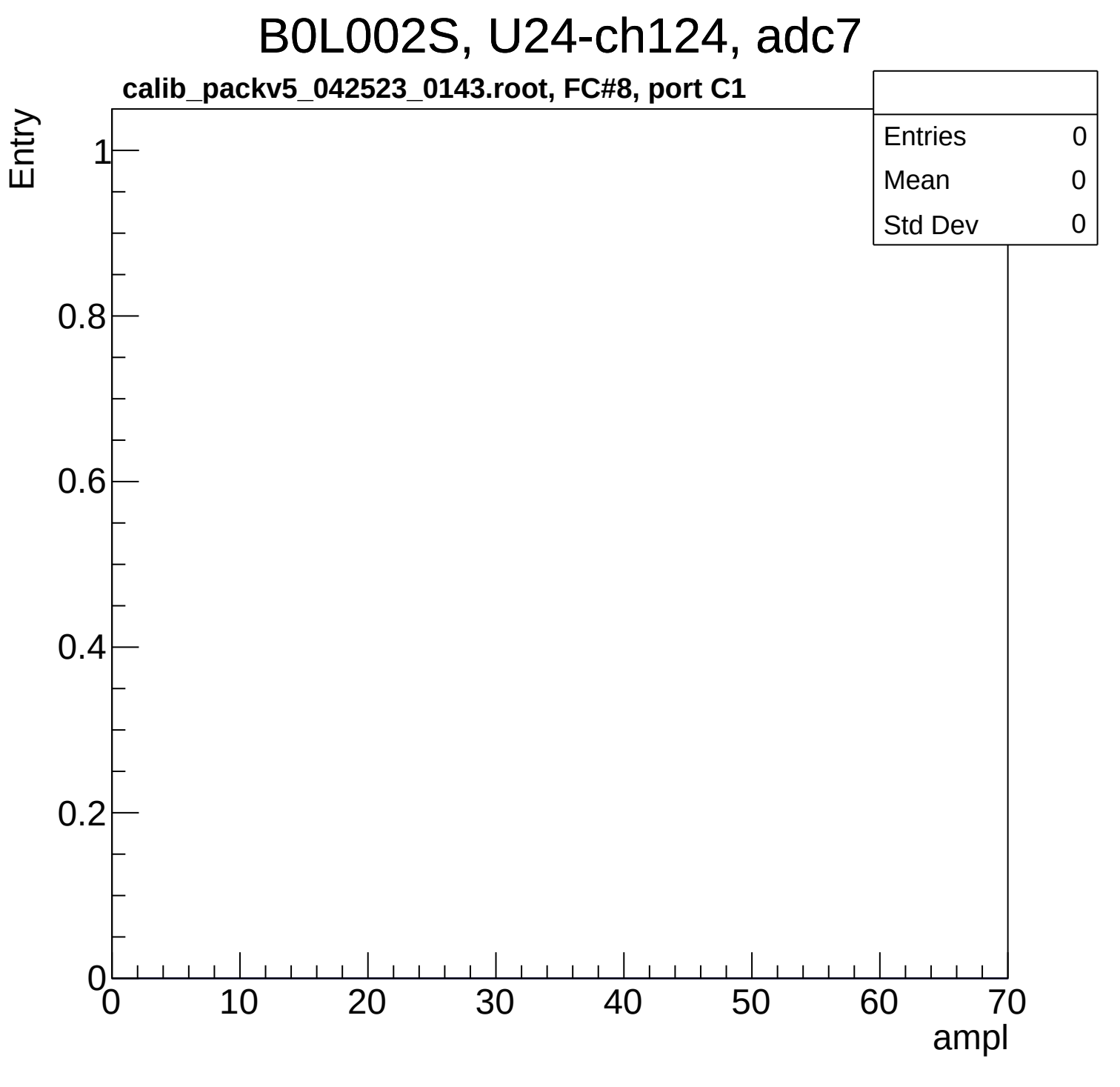
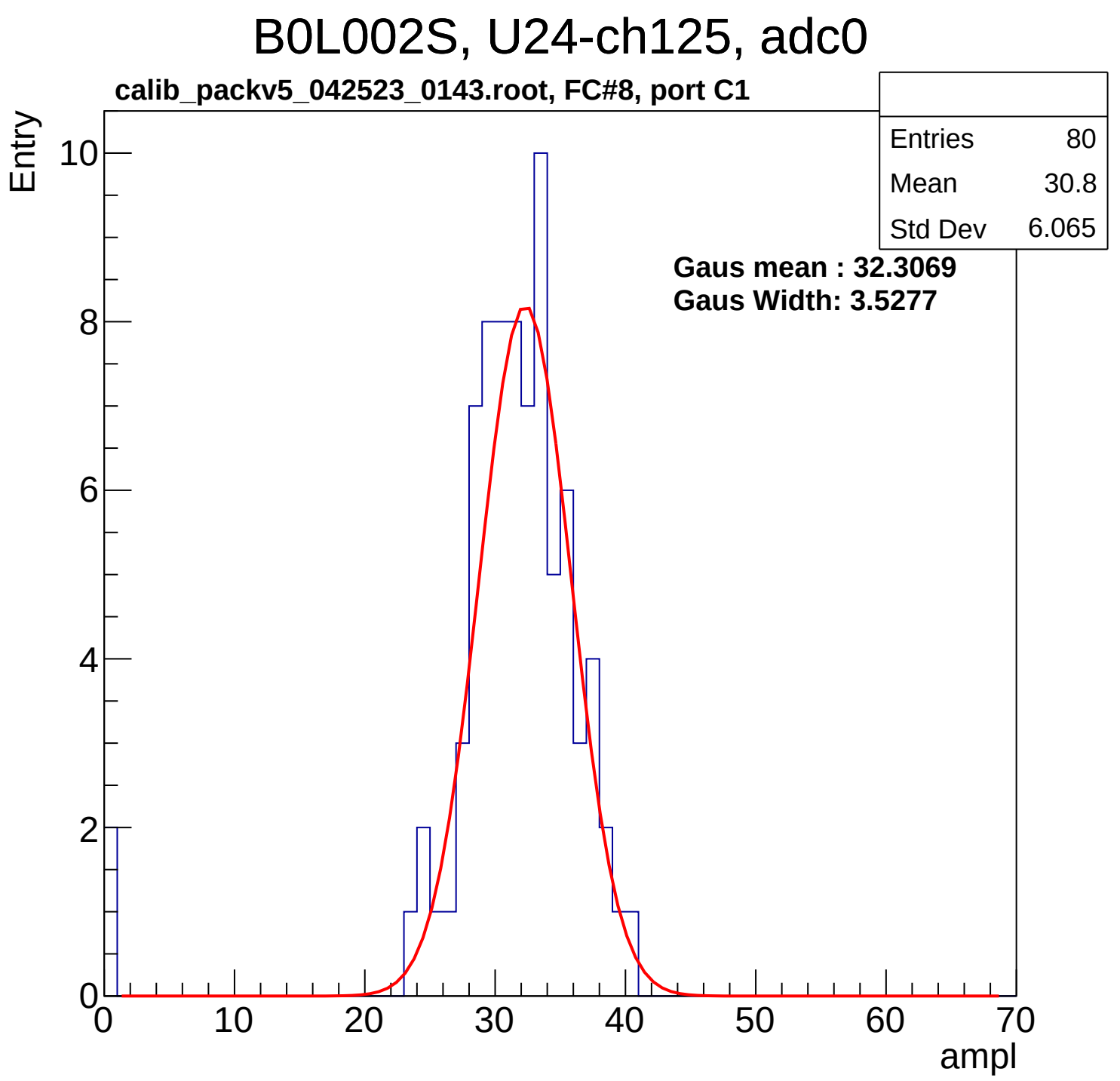
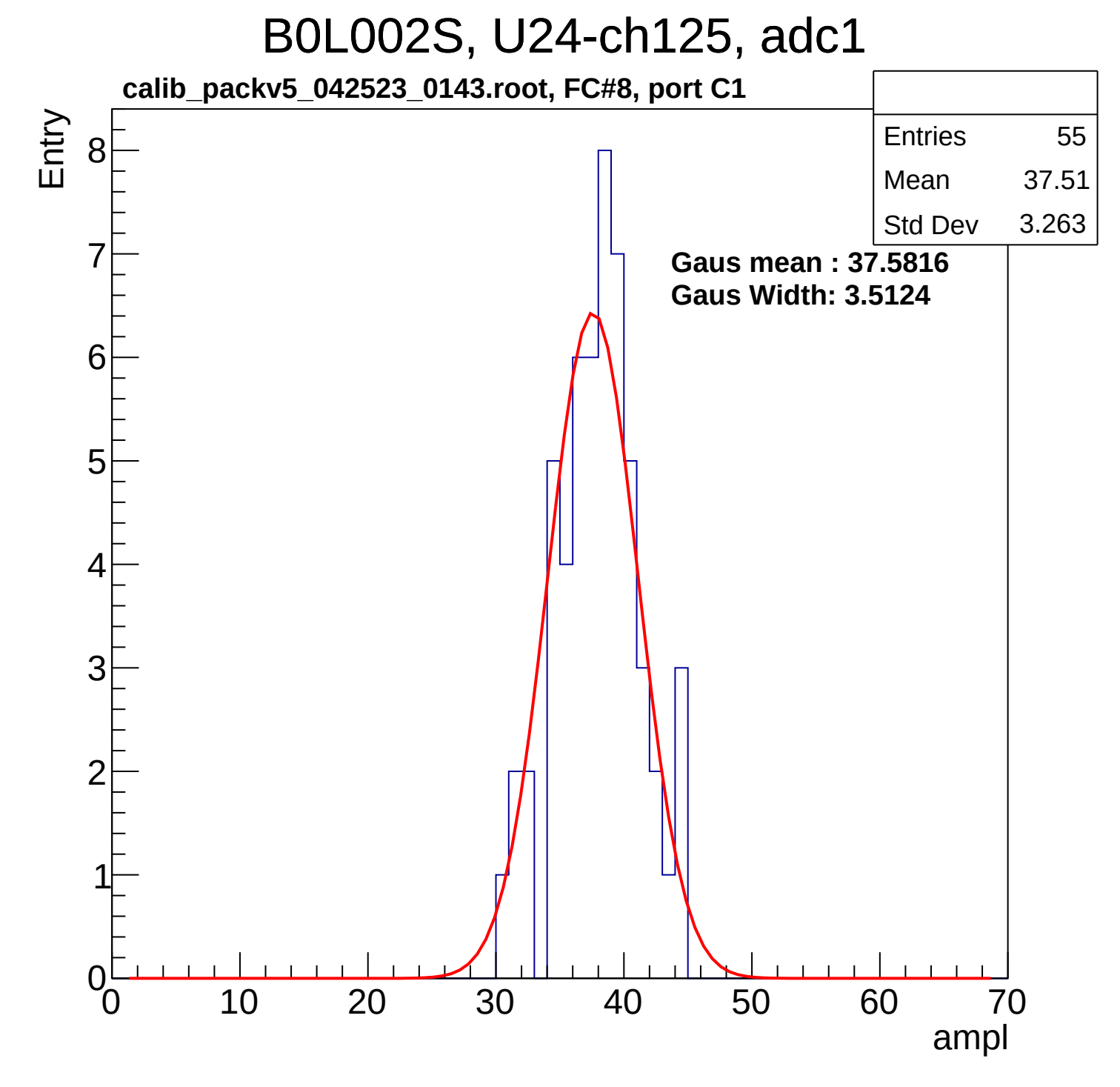

B0L002S, U24-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 59.32
Std Dev 11.53
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.8
Std Dev 6.065
Gaus mean : 32.3069
Gaus Width: 3.5277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 37.51
Std Dev 3.263
Gaus mean : 37.5816
Gaus Width: 3.5124
Entry
ampl
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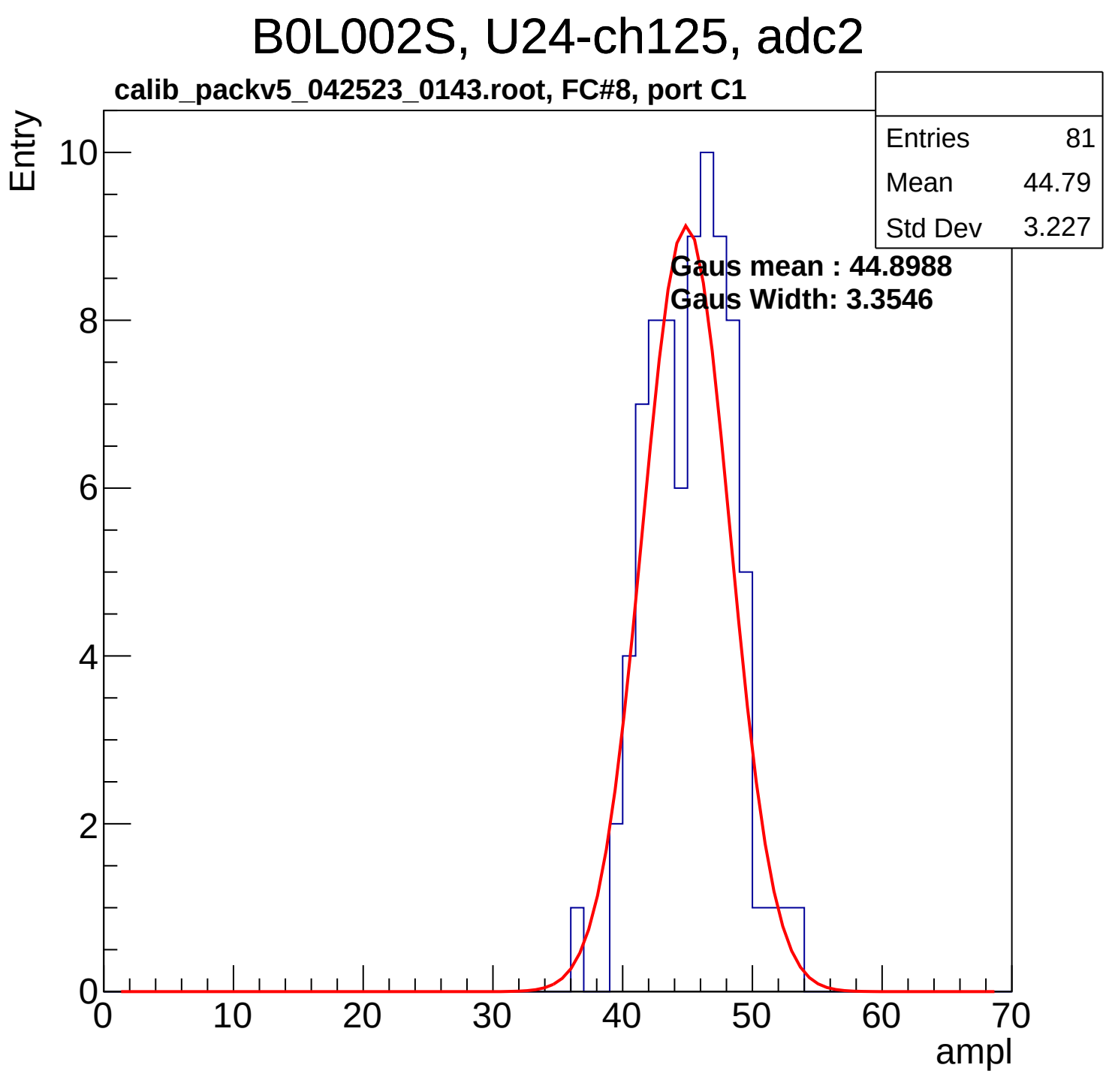
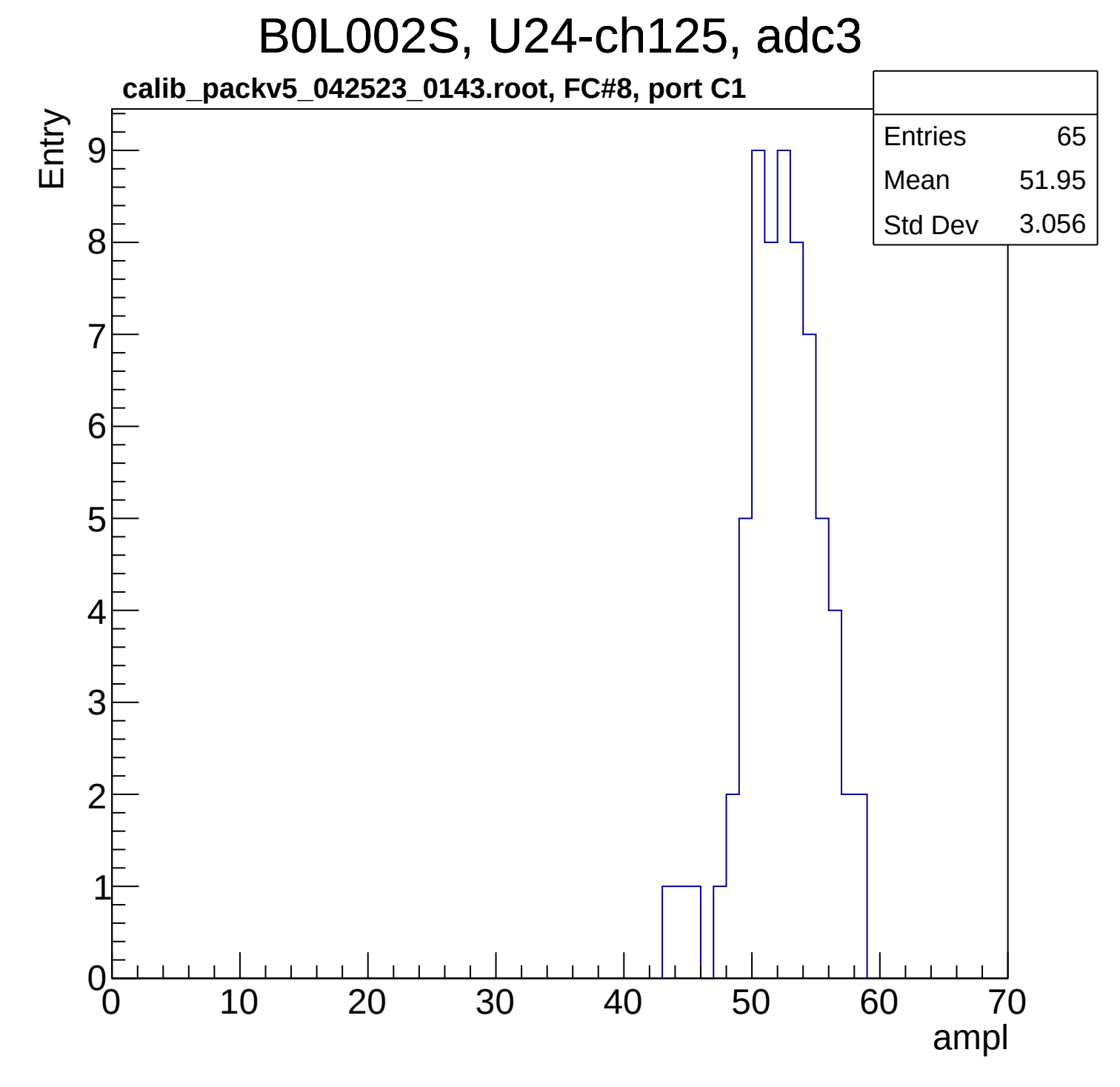
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U24-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 44.79
Std Dev 3.227
Gaus mean : 44.8988
Gaus Width: 3.3546
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.95
Std Dev 3.056
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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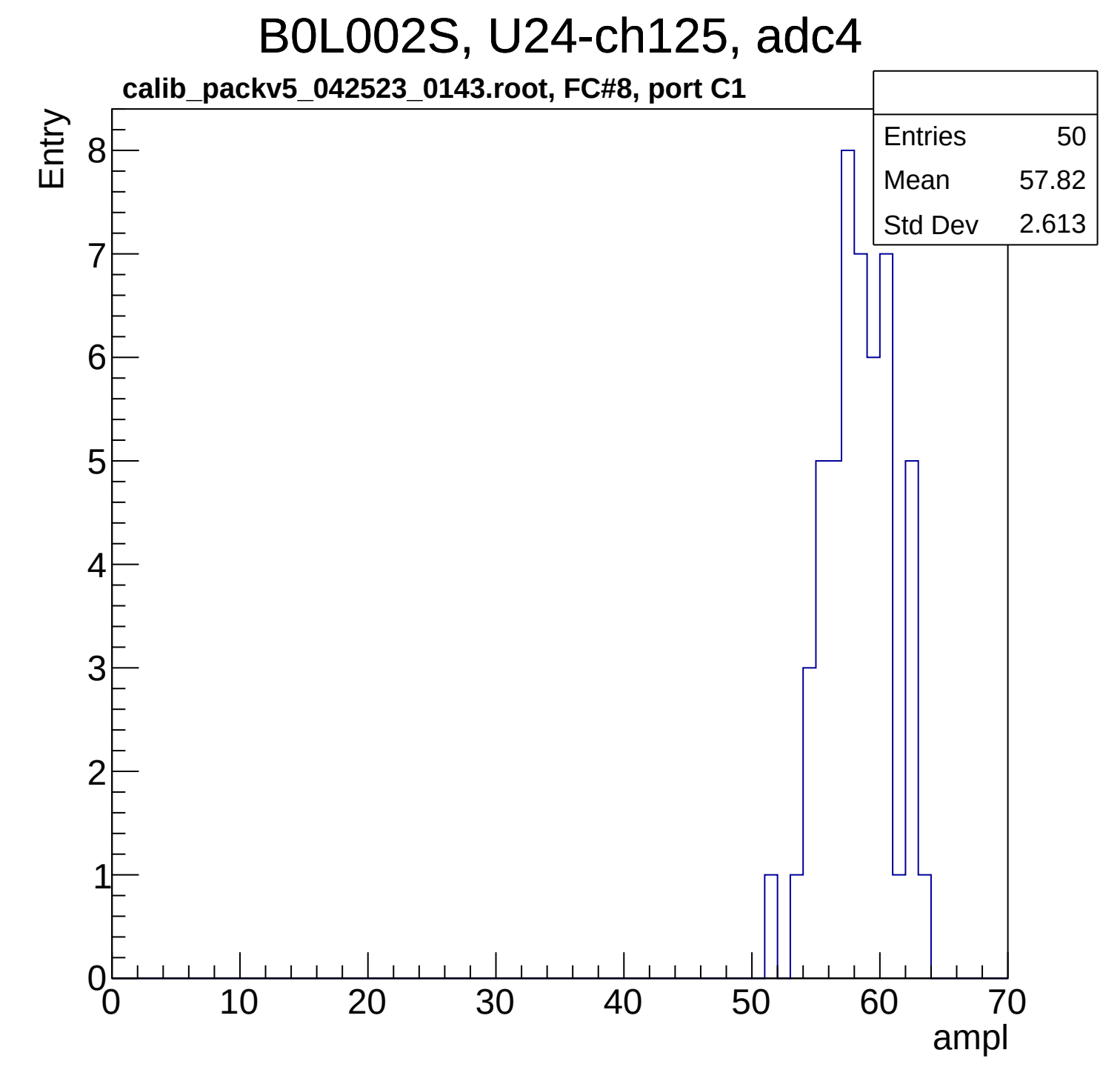
B0L002S, U24-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.82
Std Dev 2.613
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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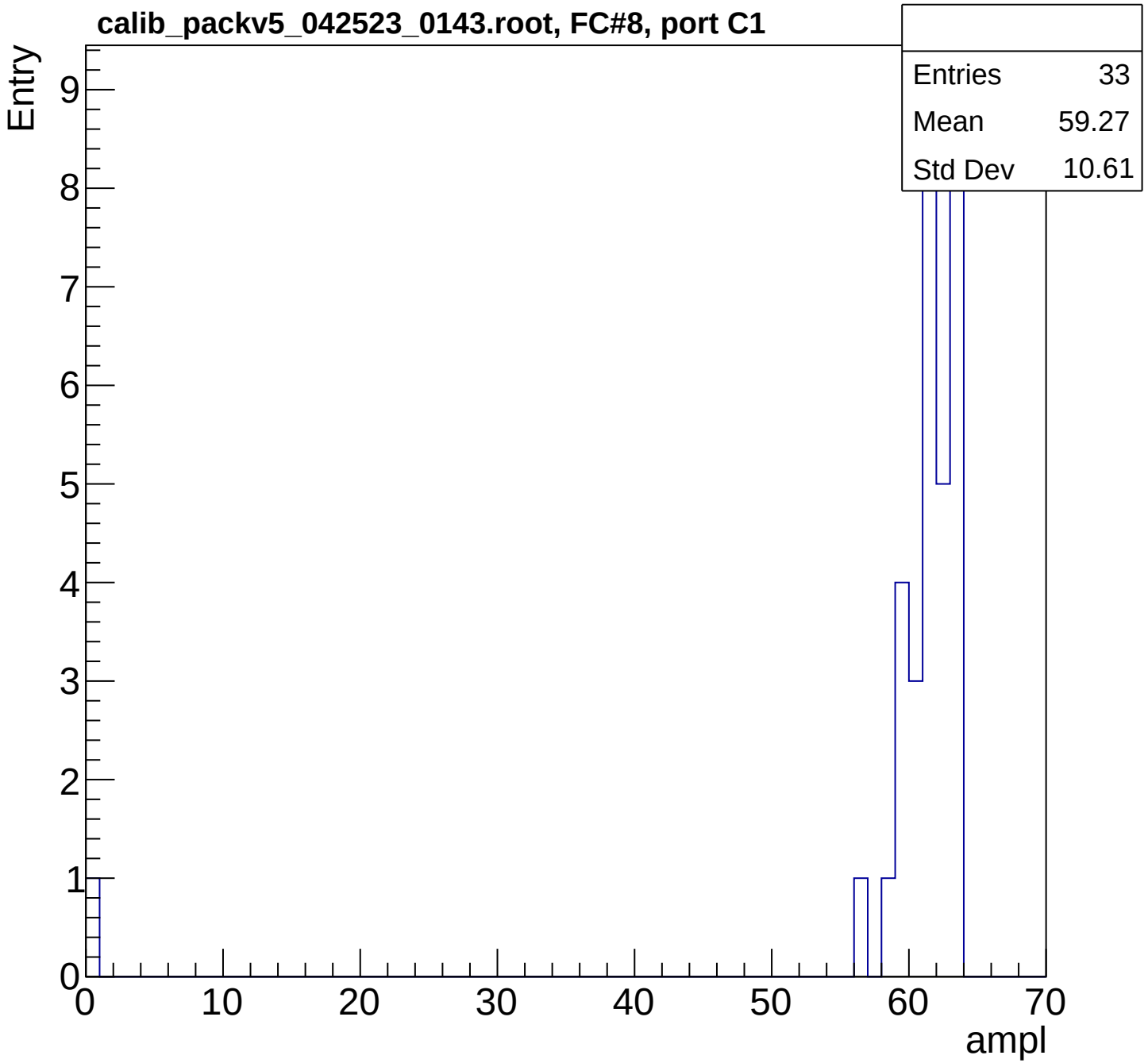
B0L002S, U24-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.27
Std Dev 10.61
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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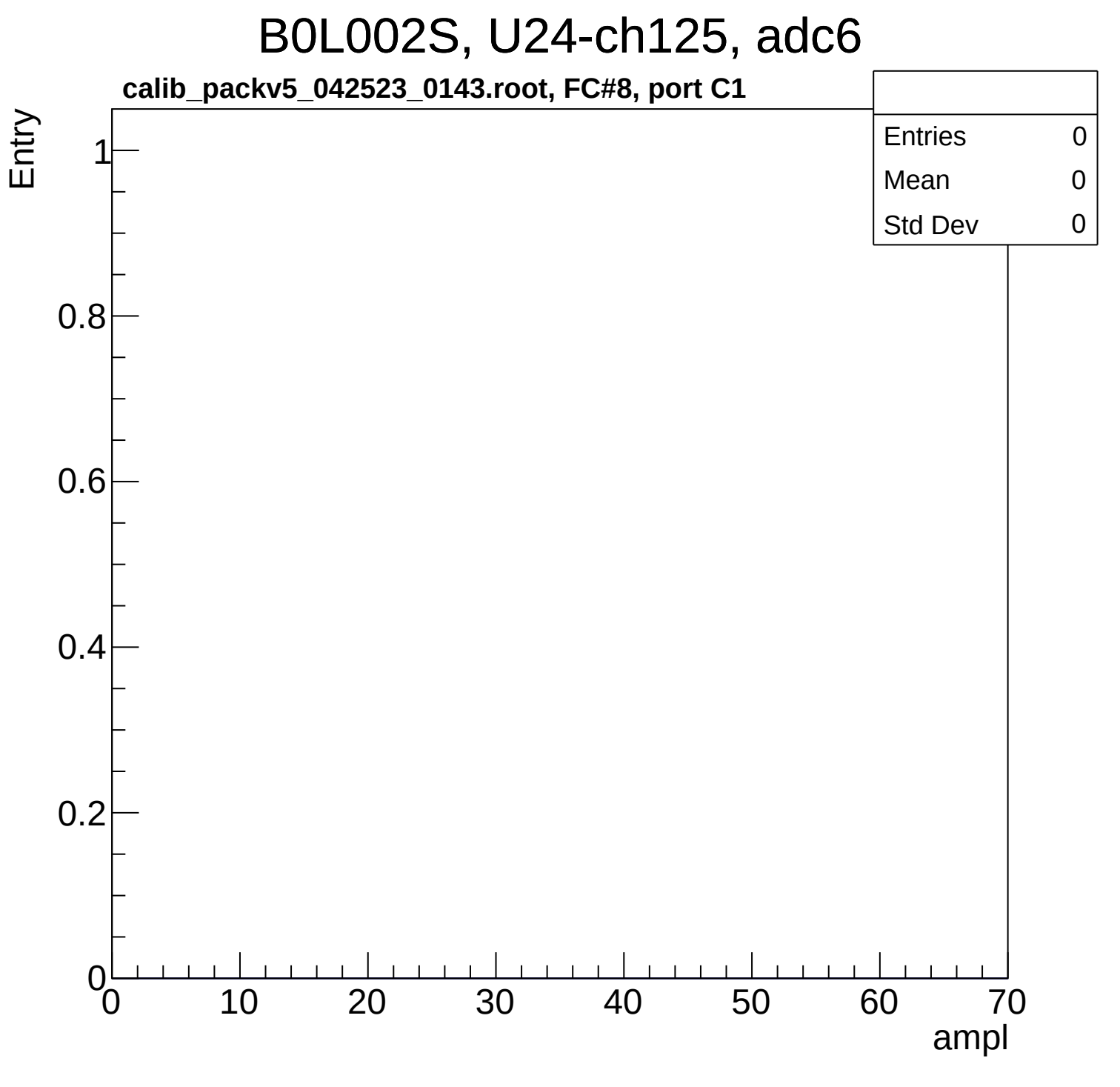
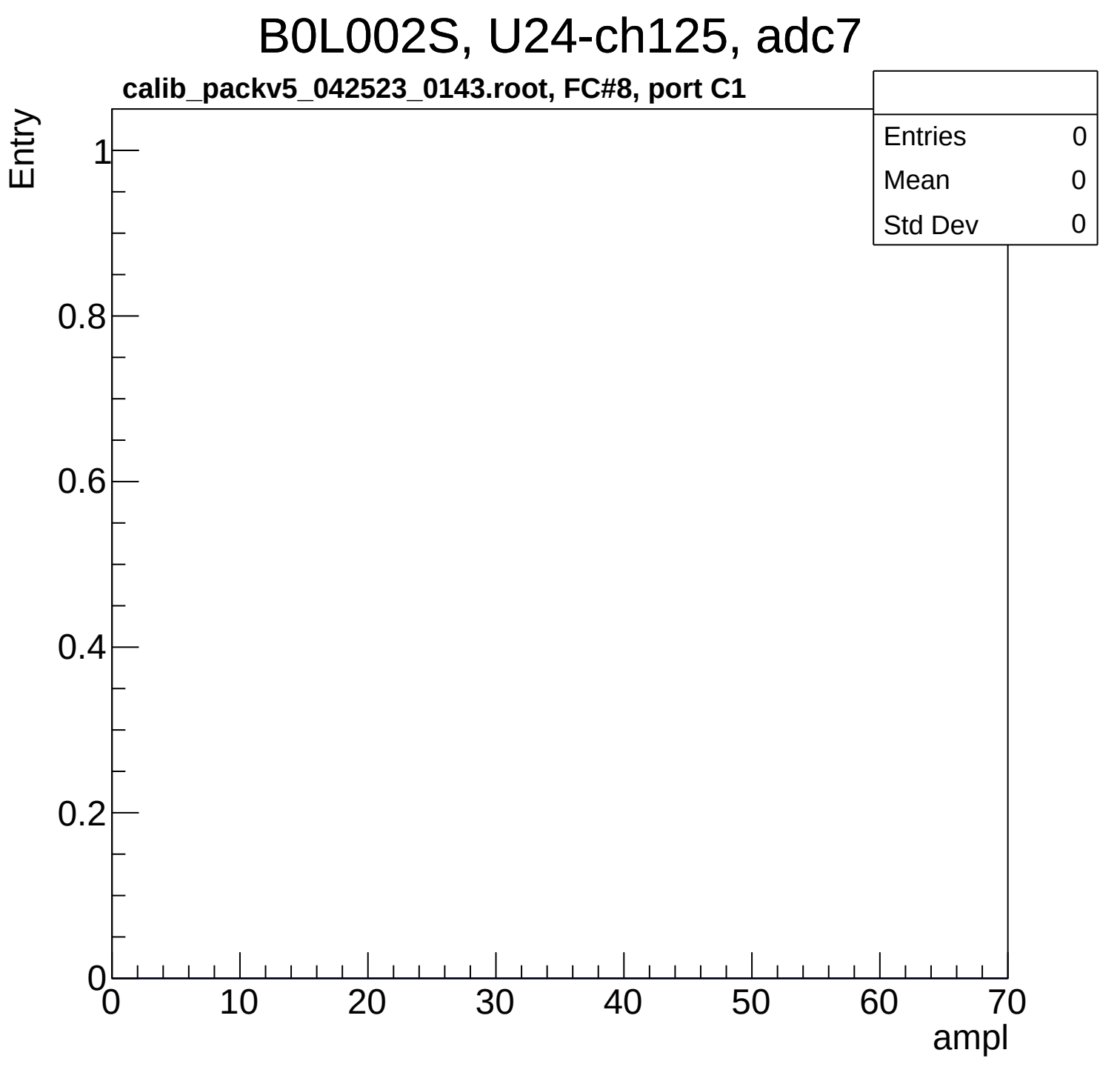
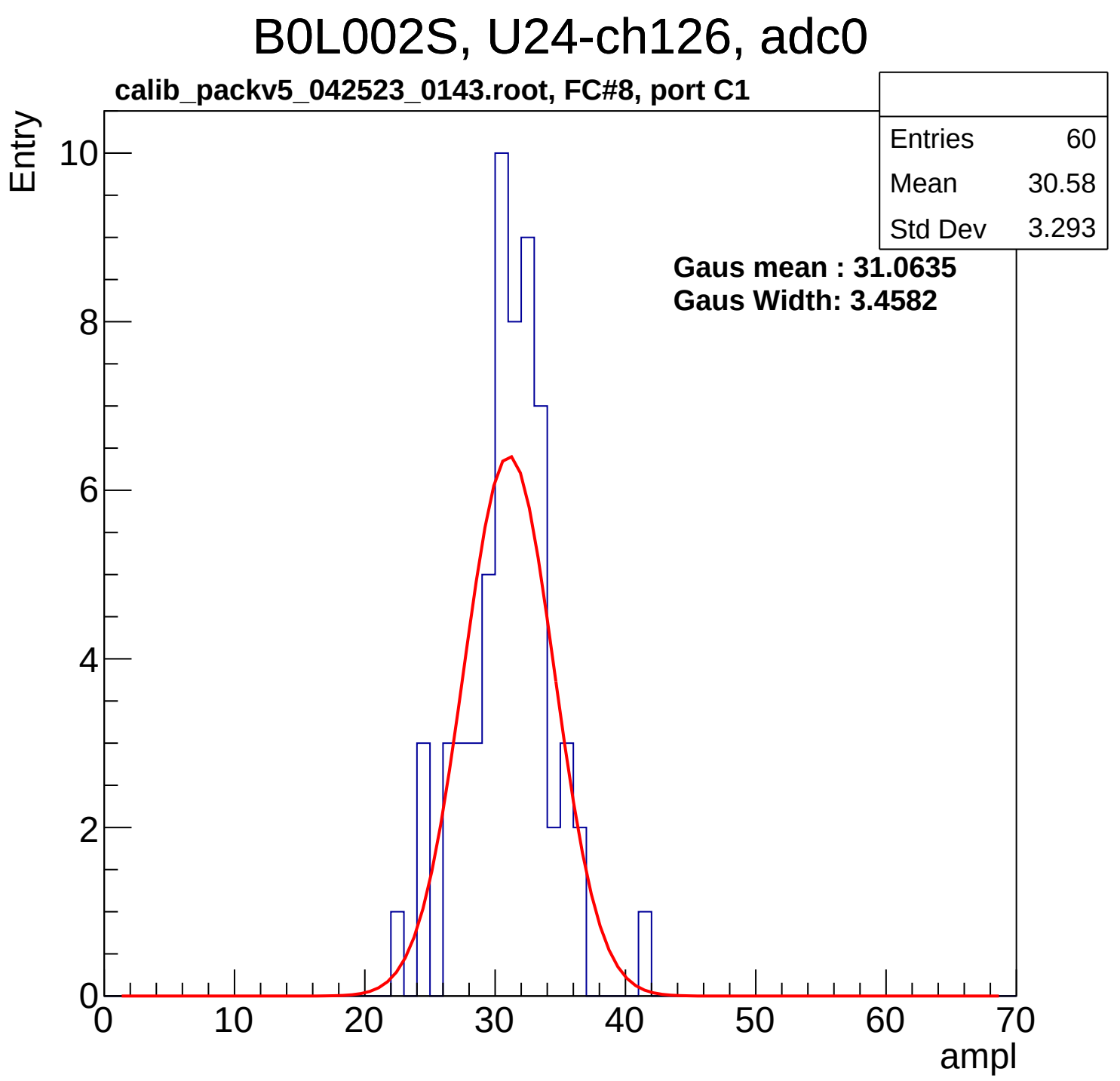
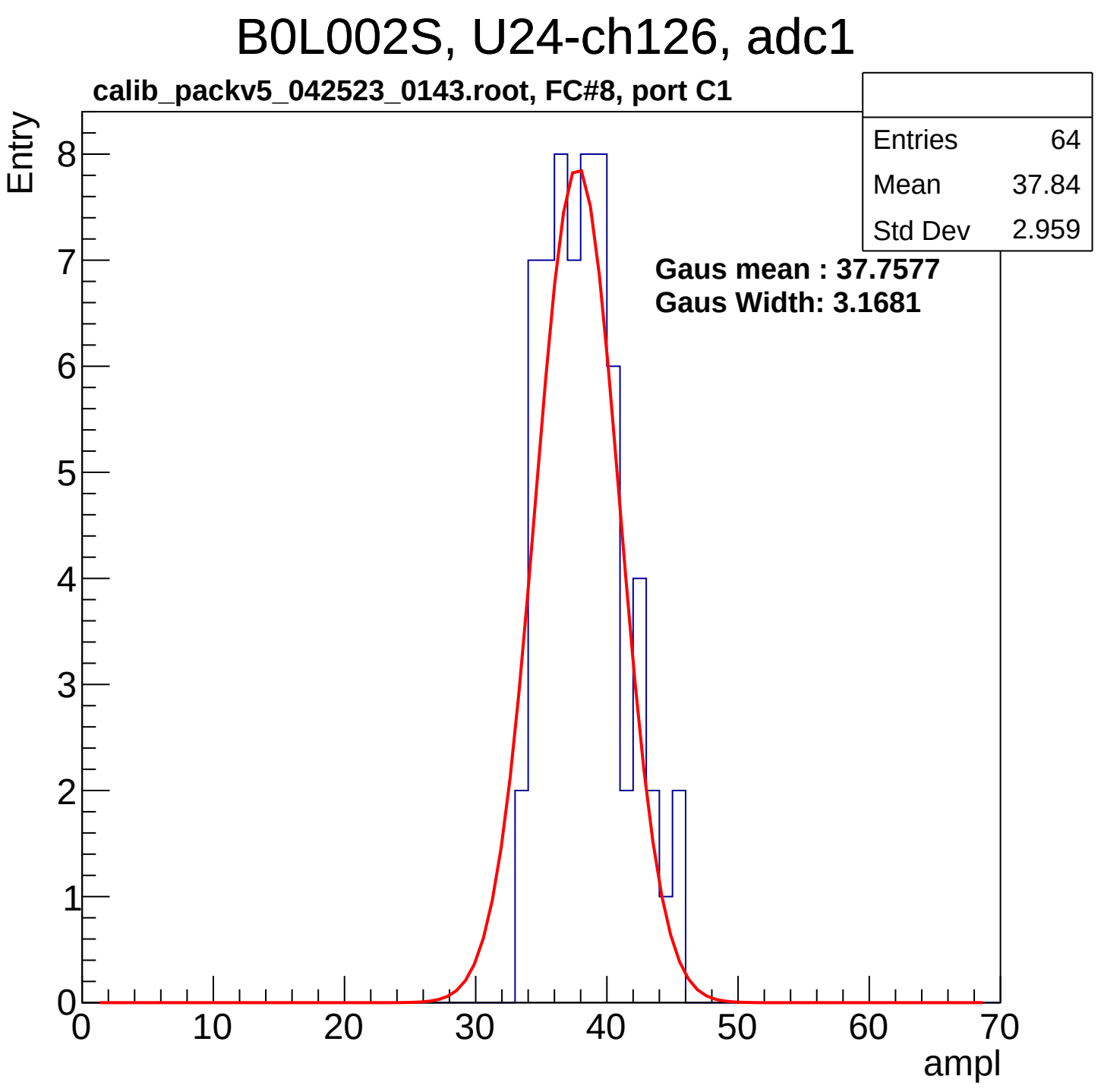

B0L002S, U24-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 30.58
Std Dev 3.293
Gaus mean : 31.0635
Gaus Width: 3.4582
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U24-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 37.84
Std Dev 2.959
Gaus mean : 37.7577
Gaus Width: 3.1681
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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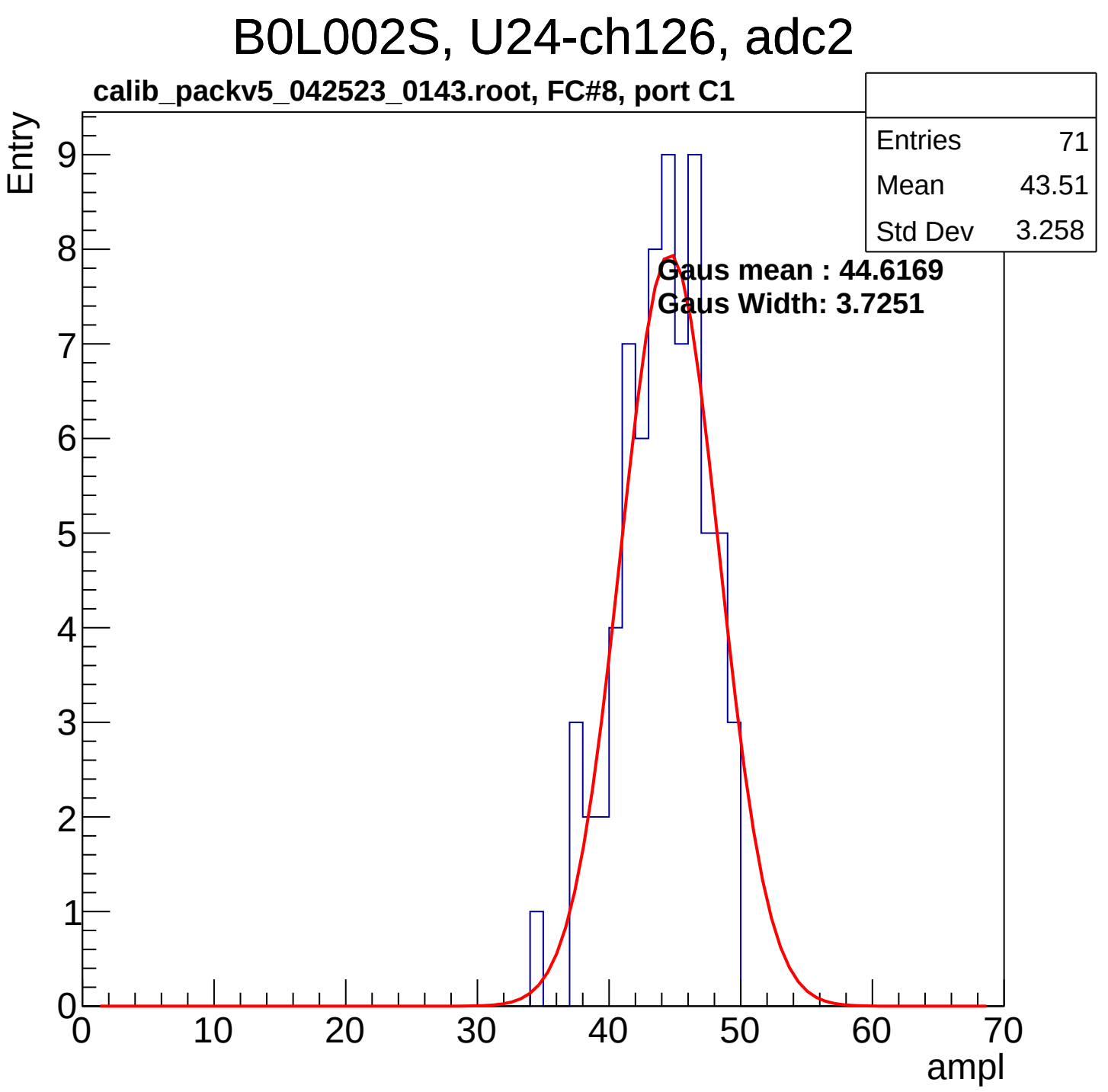
40
50
60
70


B0L002S, U24-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.51
Std Dev 3.258
Gaus mean : 44.6169
Gaus Width: 3.7251
Entry
ampl
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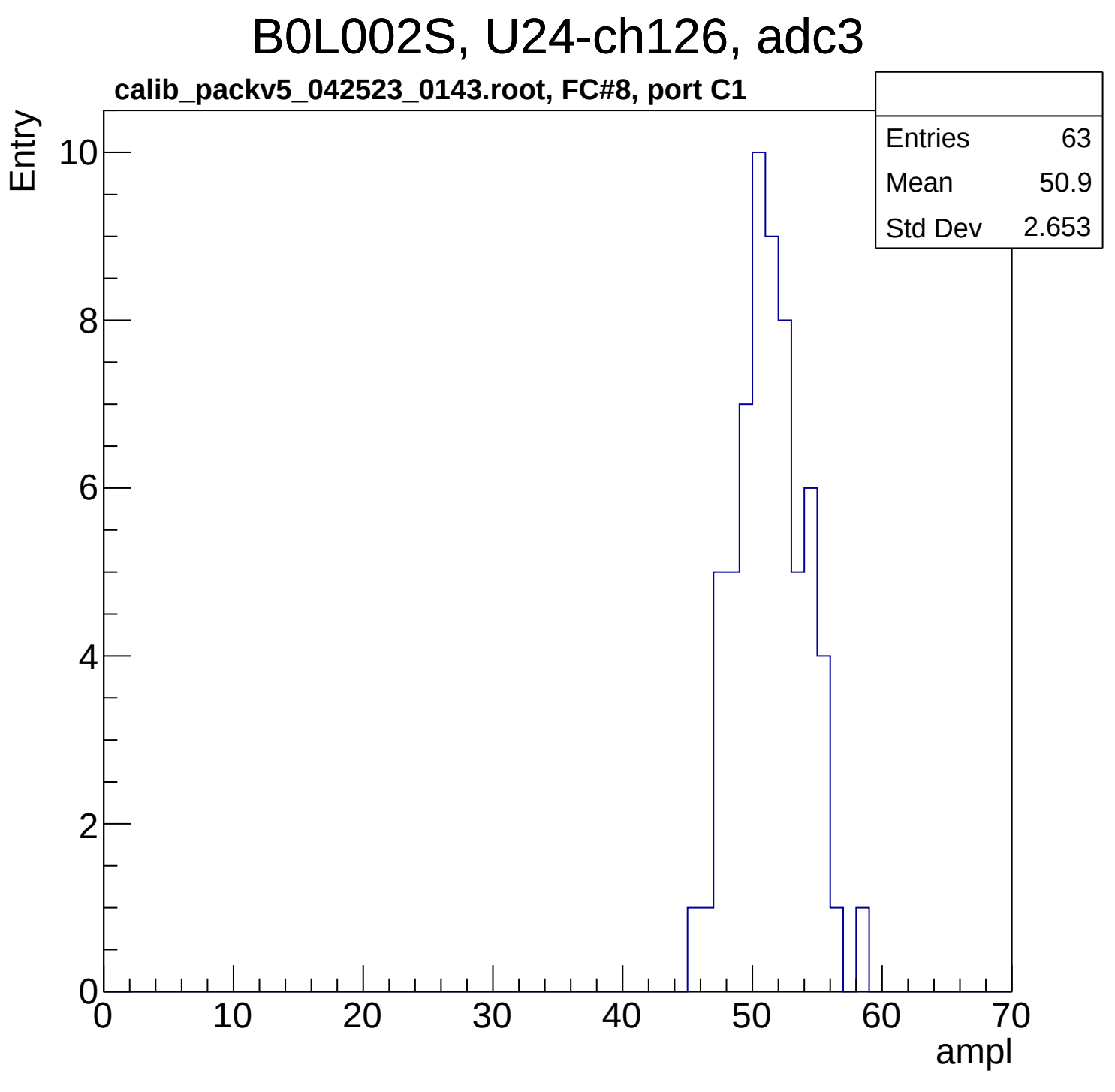
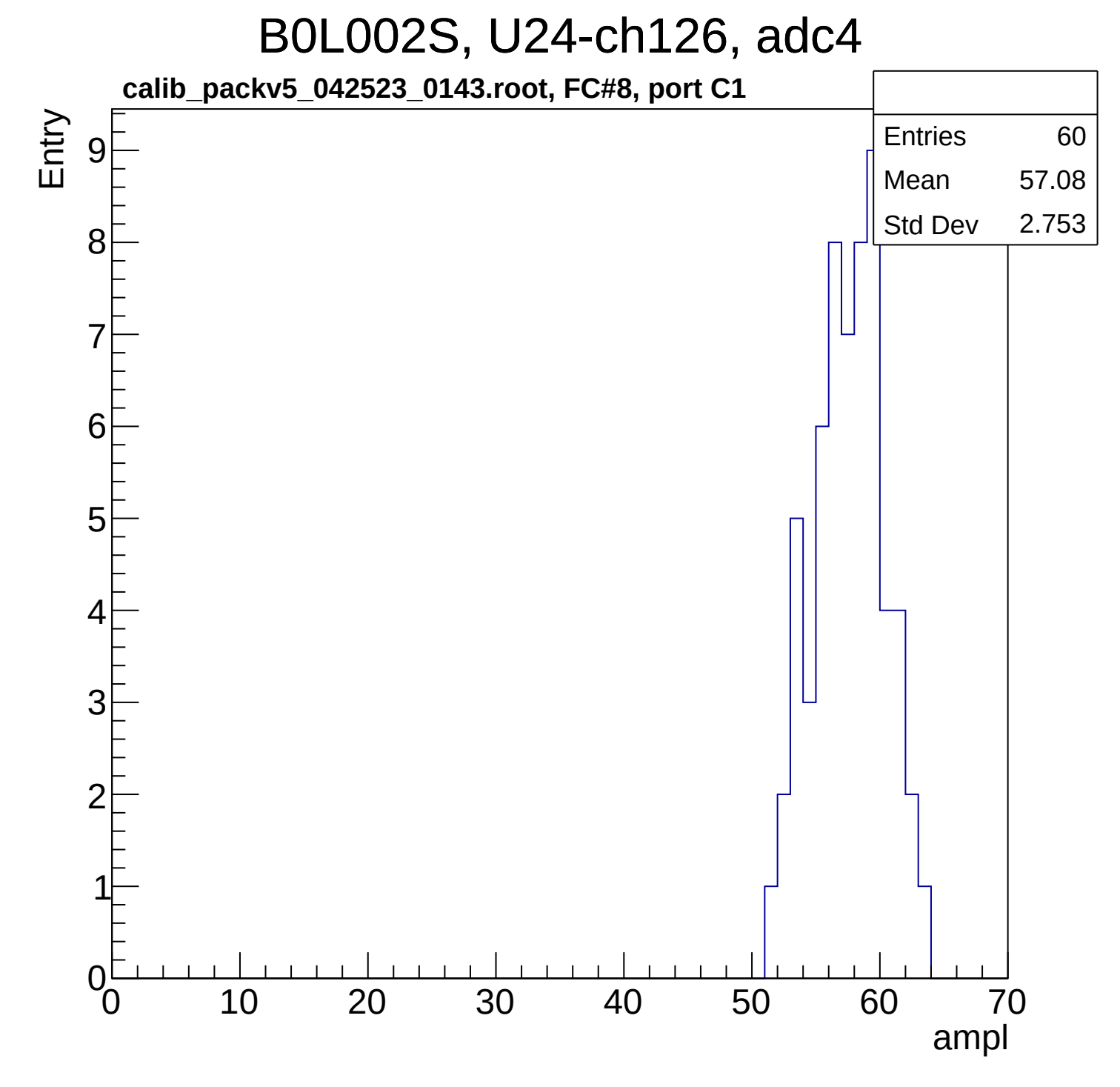
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U24-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.9
Std Dev 2.653
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.08
Std Dev 2.753
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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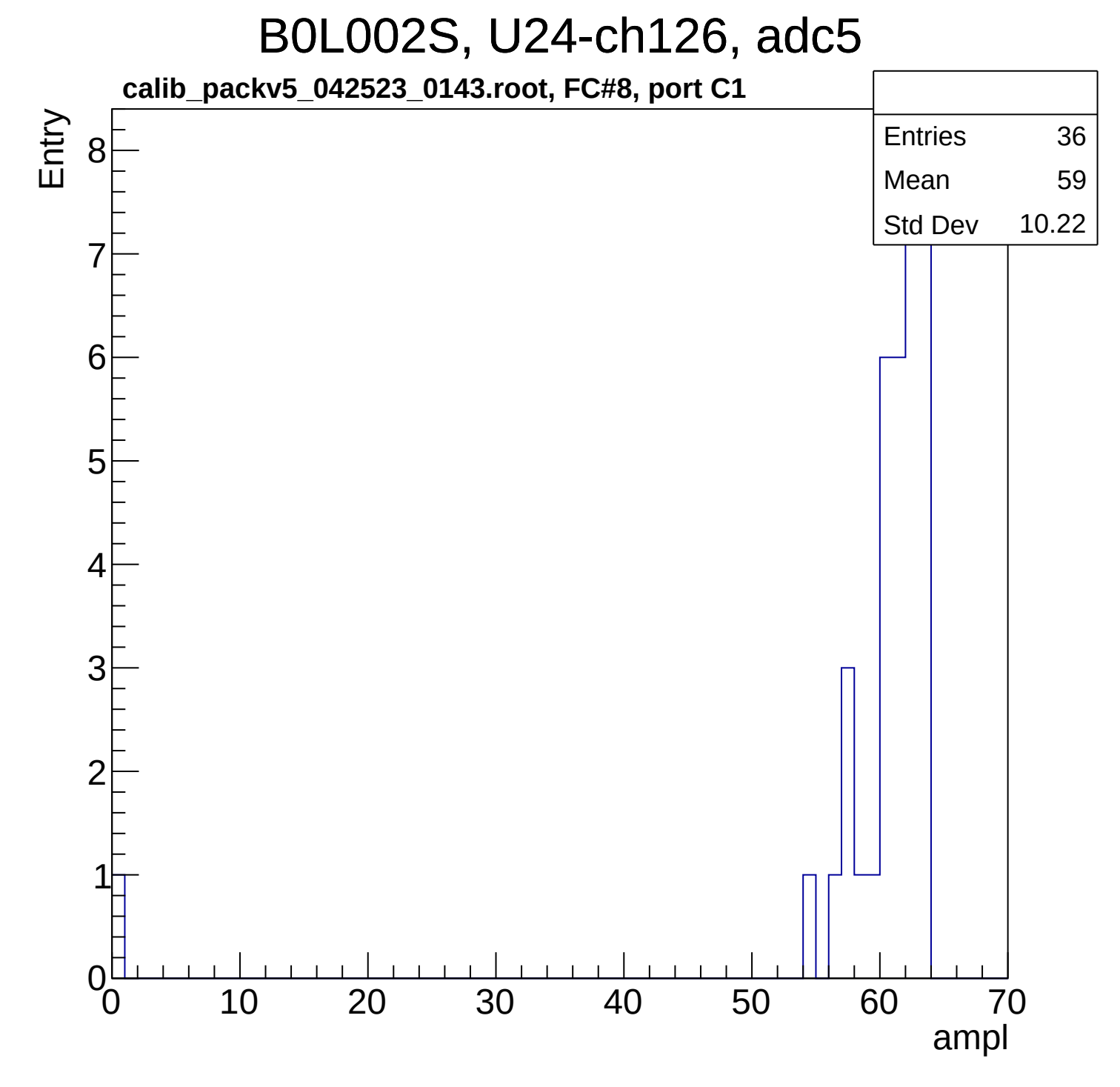
B0L002S, U24-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59
Std Dev 10.22
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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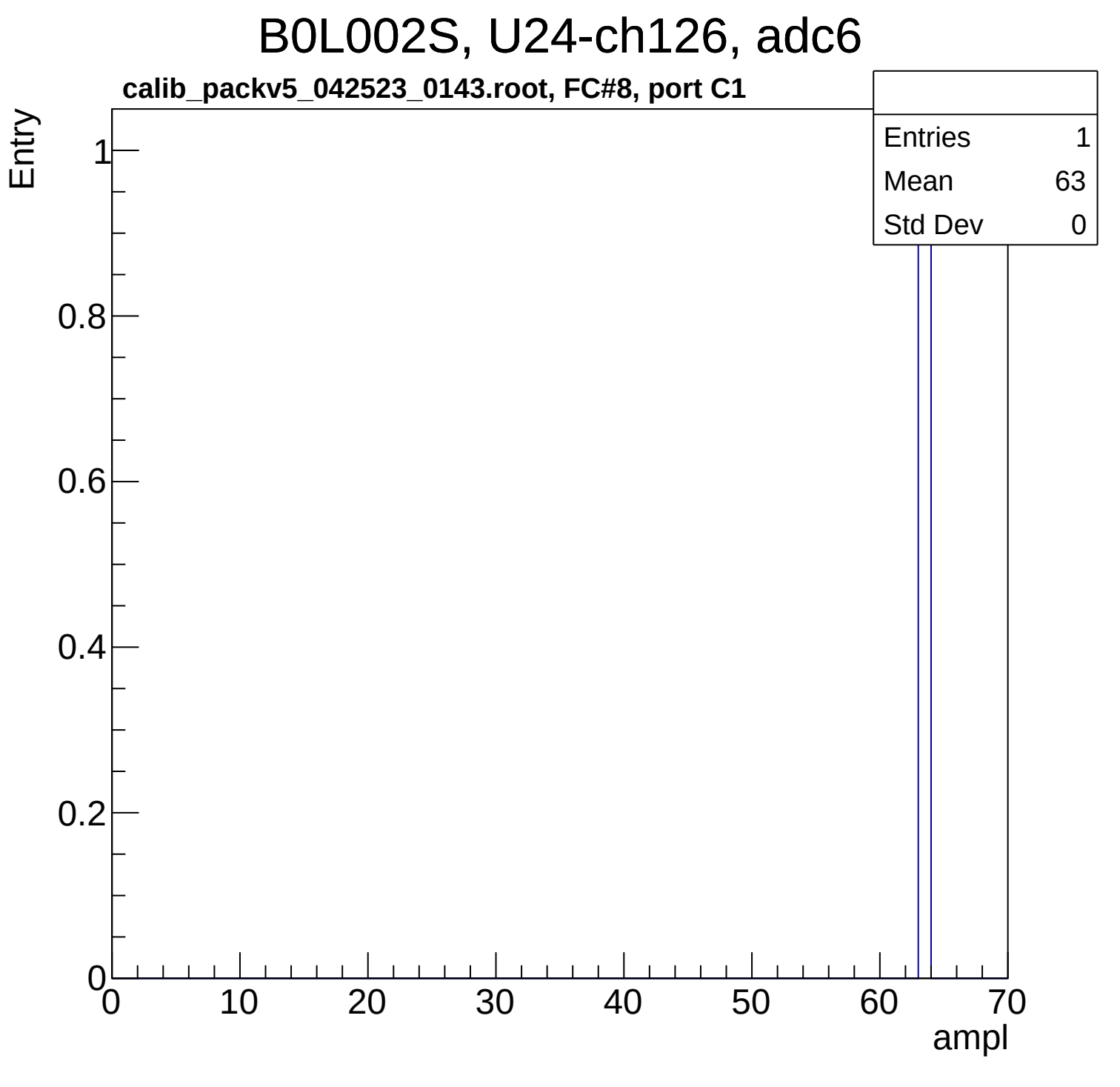
B0L002S, U24-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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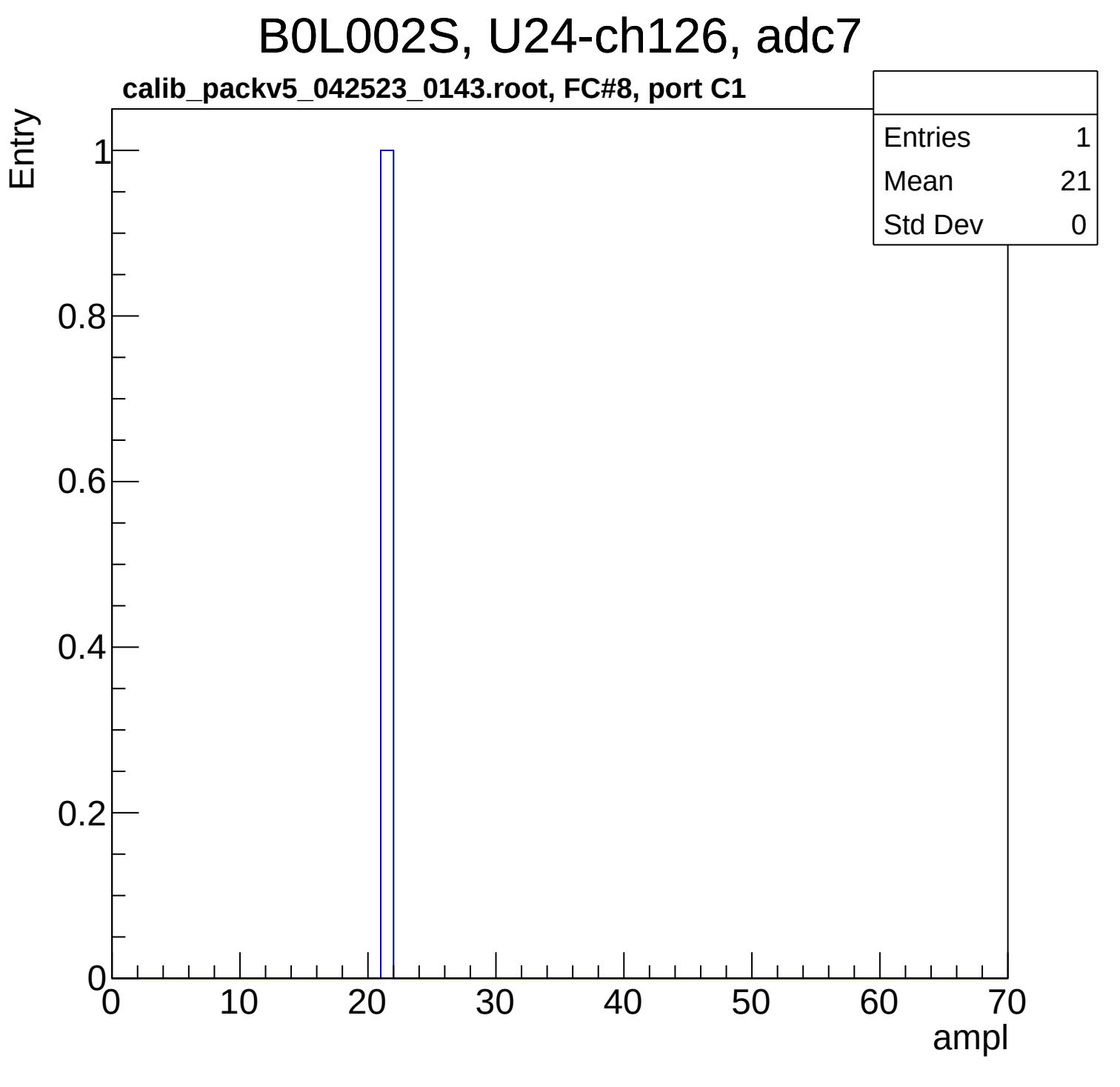
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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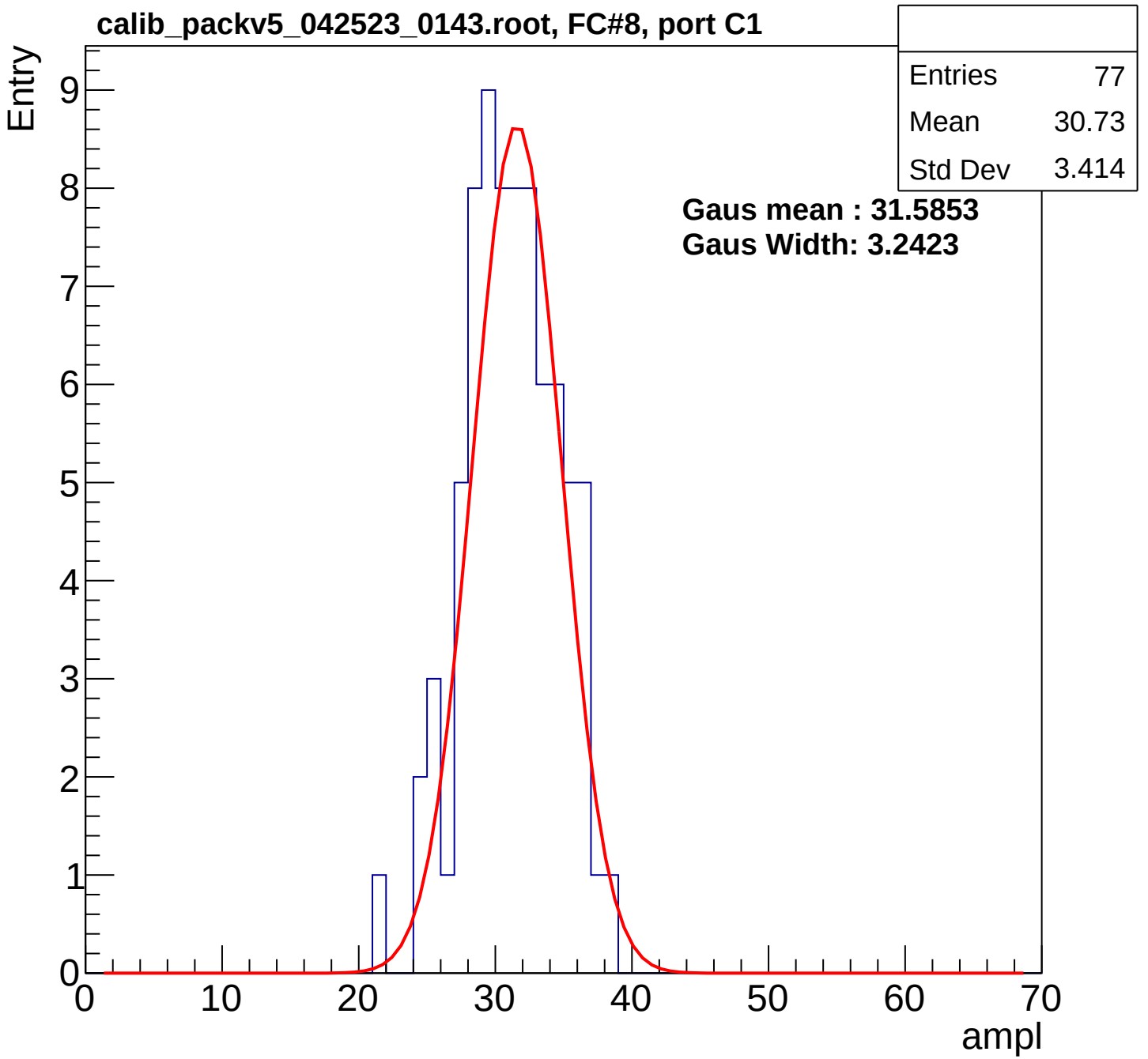
50
60
70
ampl


B0L002S, U24-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.73
Std Dev 3.414
Gaus mean : 31.5853
Gaus Width: 3.2423
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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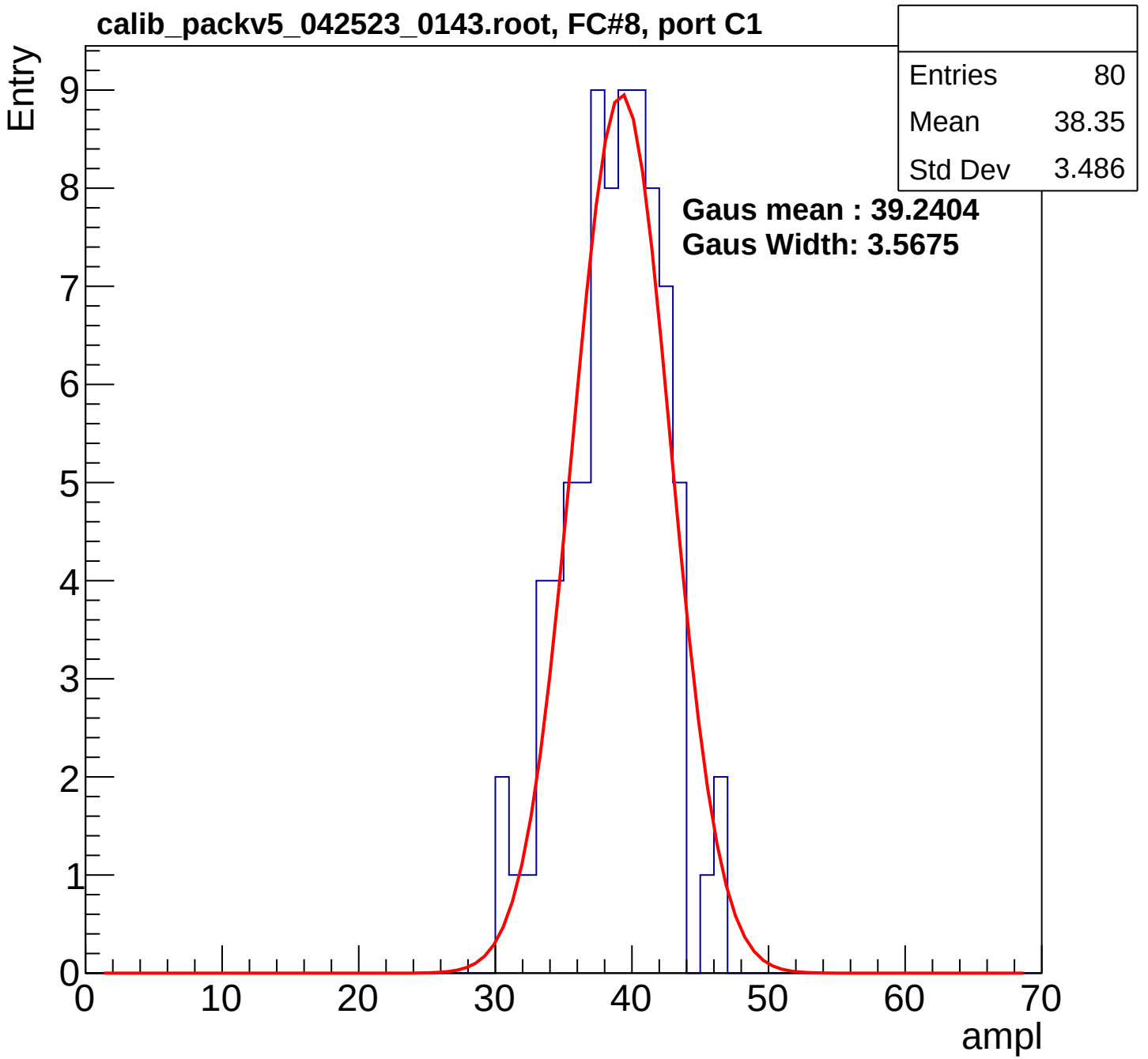
B0L002S, U24-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 38.35
Std Dev 3.486
Gaus mean : 39.2404
Gaus Width: 3.5675
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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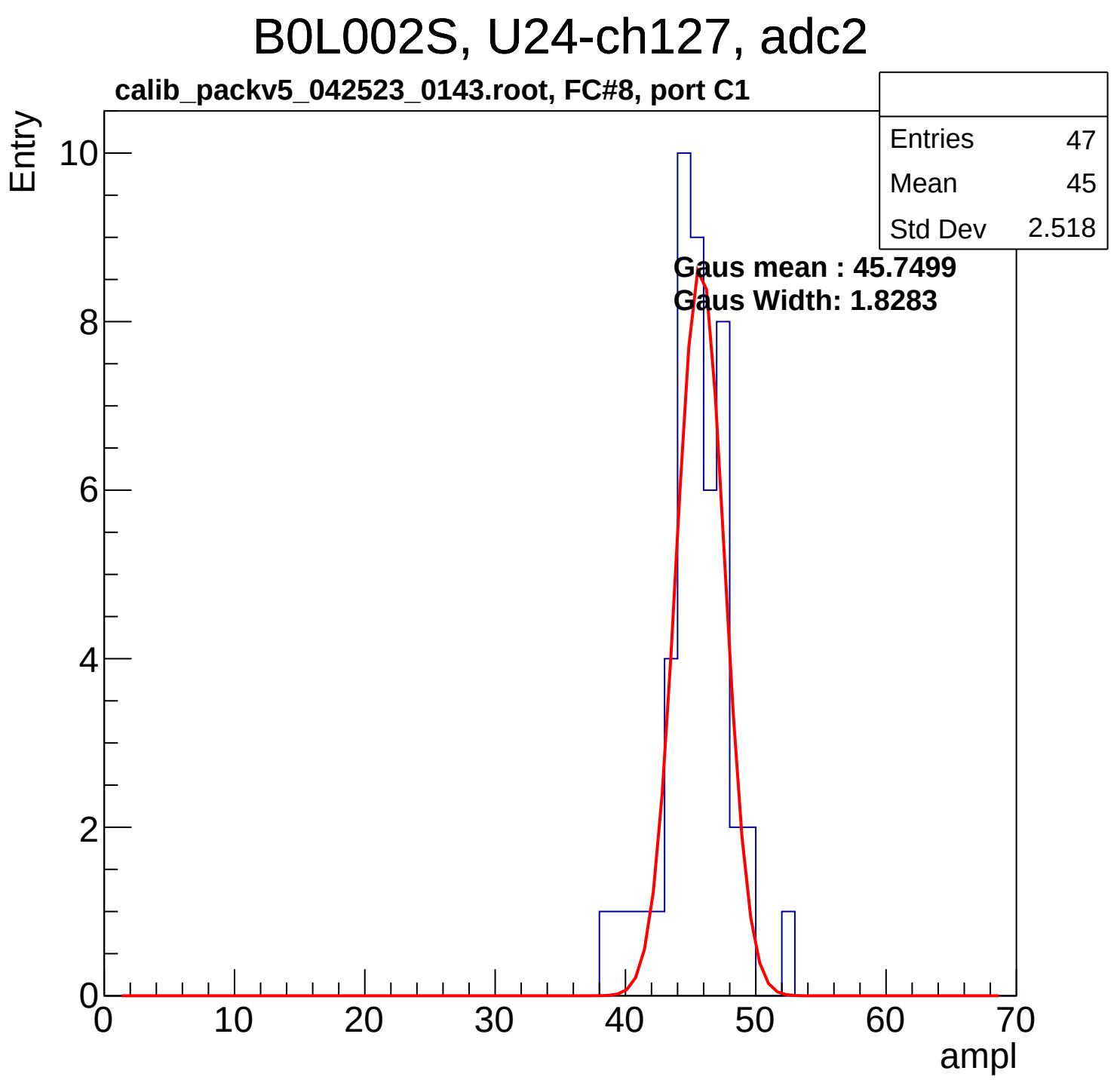
B0L002S, U24-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 45
Std Dev 2.518
Gaus mean : 45.7499
Gaus Width: 1.8283
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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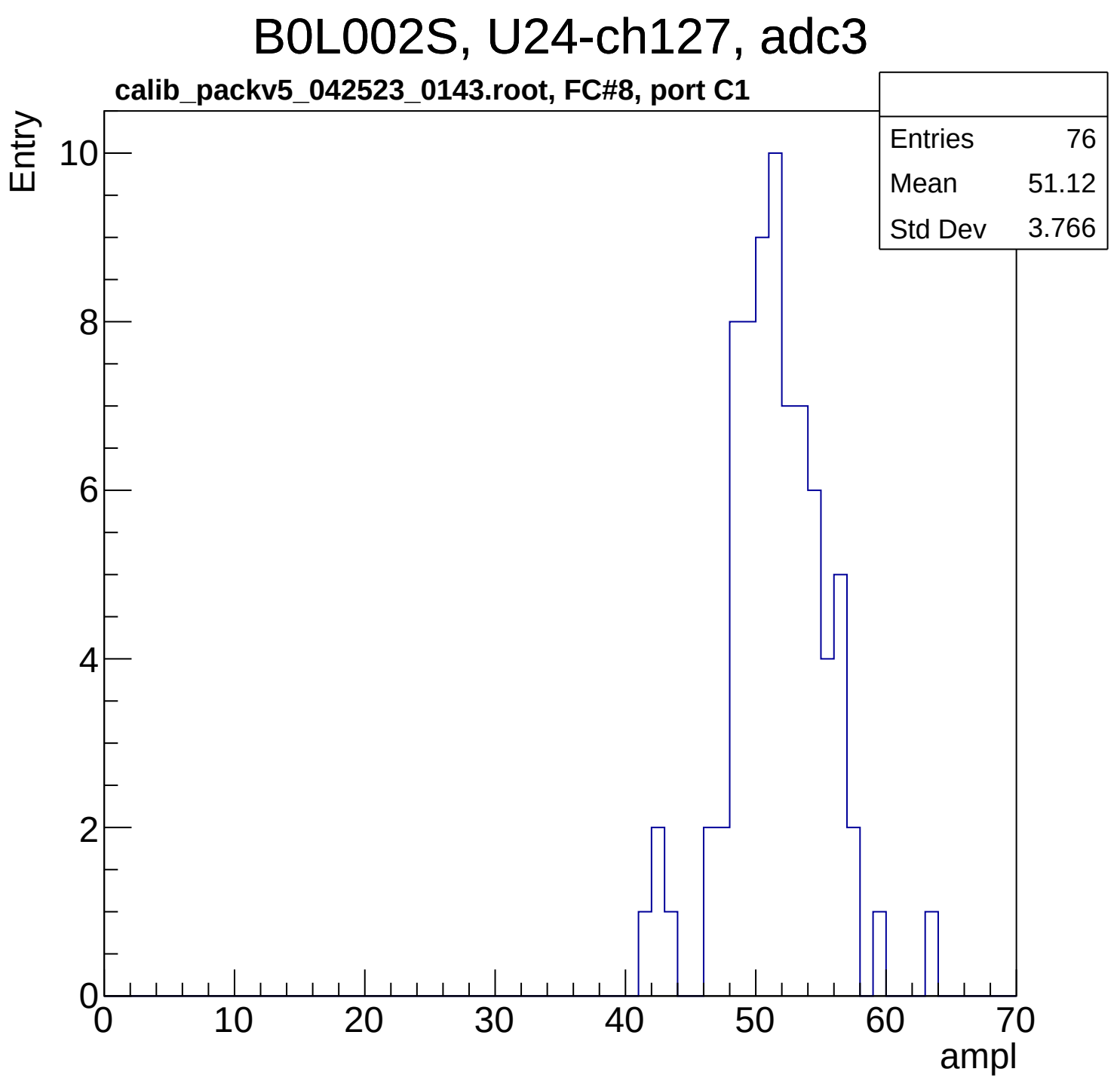
B0L002S, U24-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.12
Std Dev 3.766
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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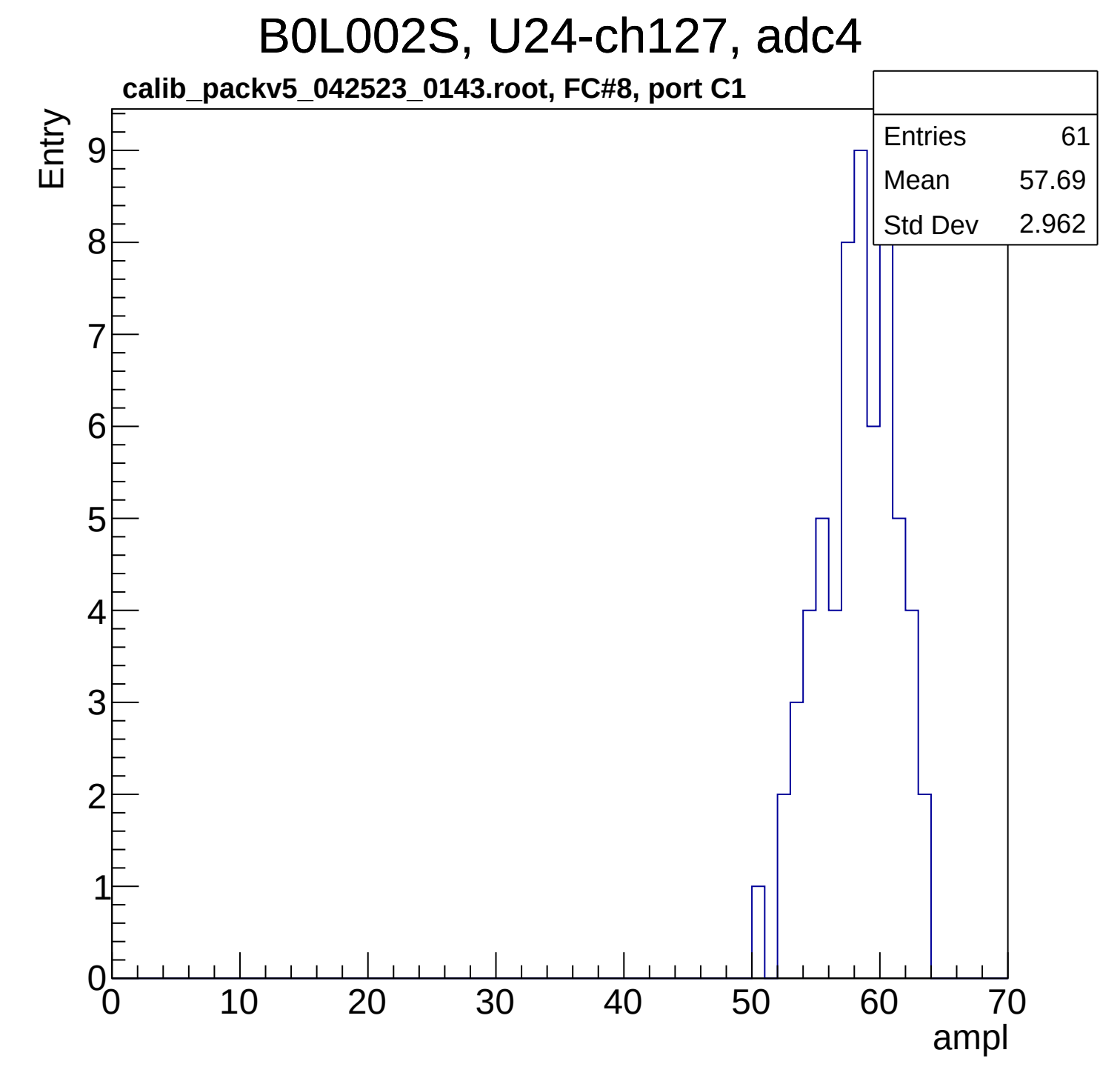
B0L002S, U24-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.69
Std Dev 2.962
Entry
ampl
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3
4
5
6
7
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0
10
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60
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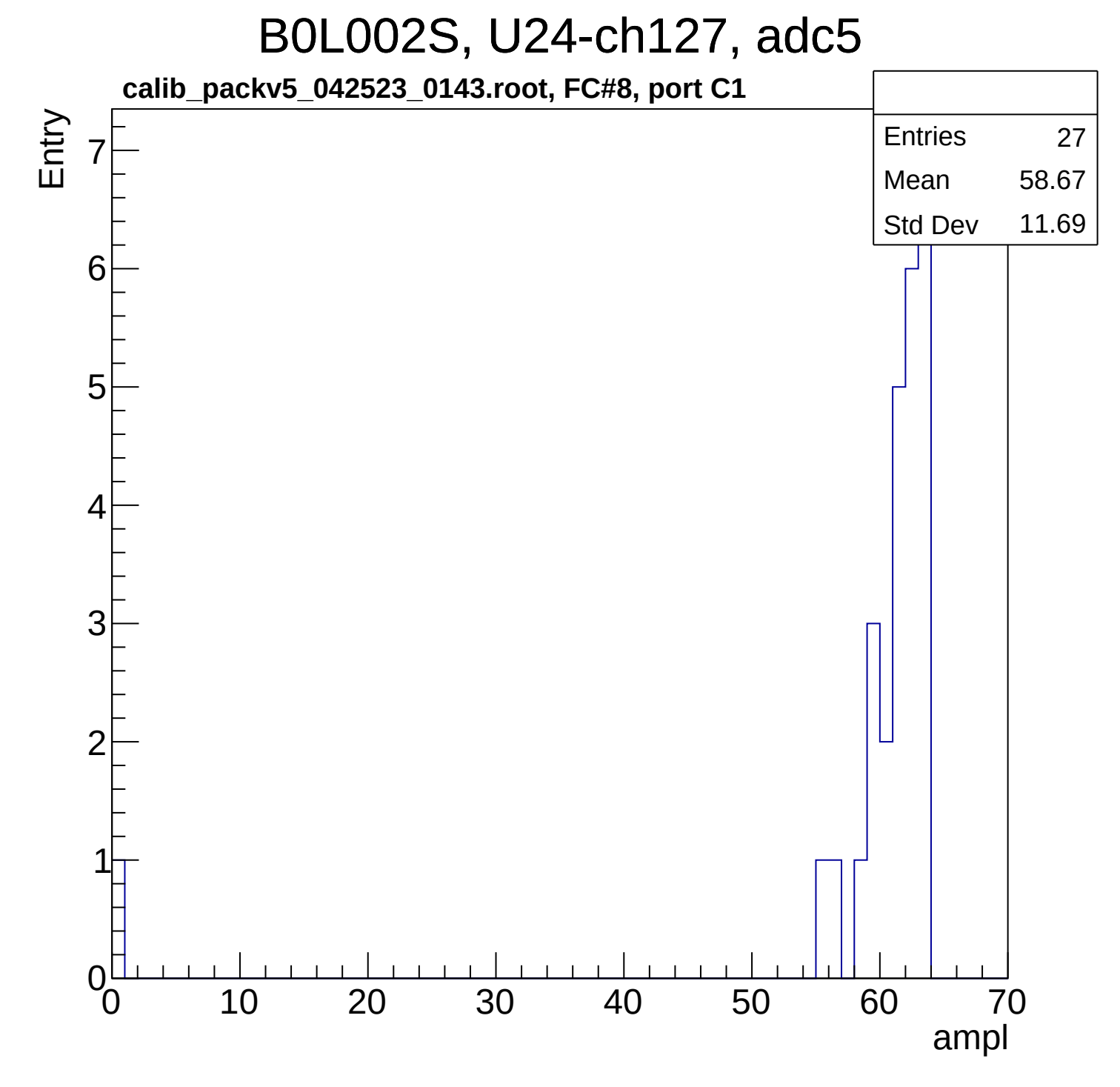
B0L002S, U24-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.67
Std Dev 11.69
Entry
ampl
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3
4
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7
0
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30
40
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60
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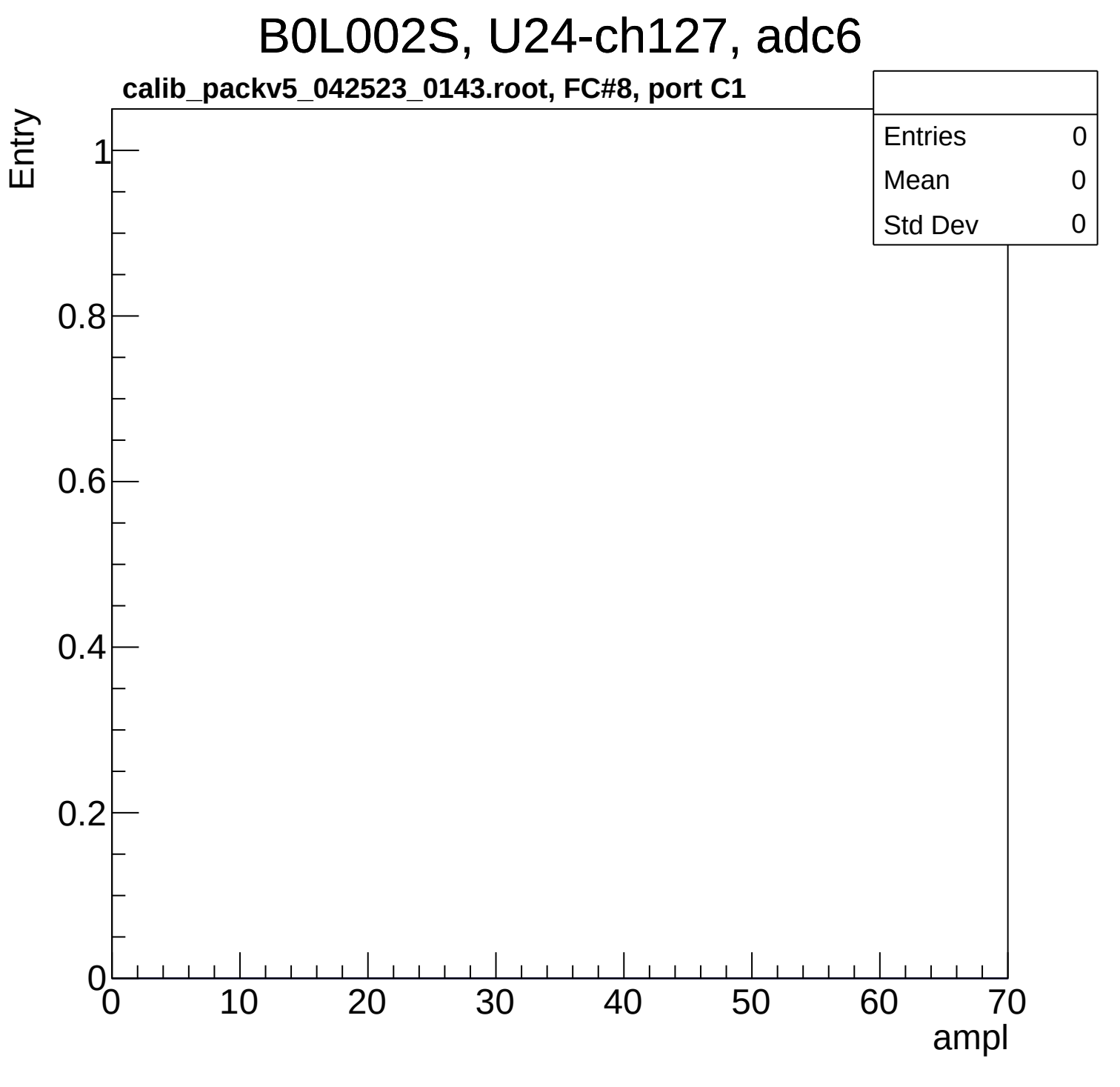
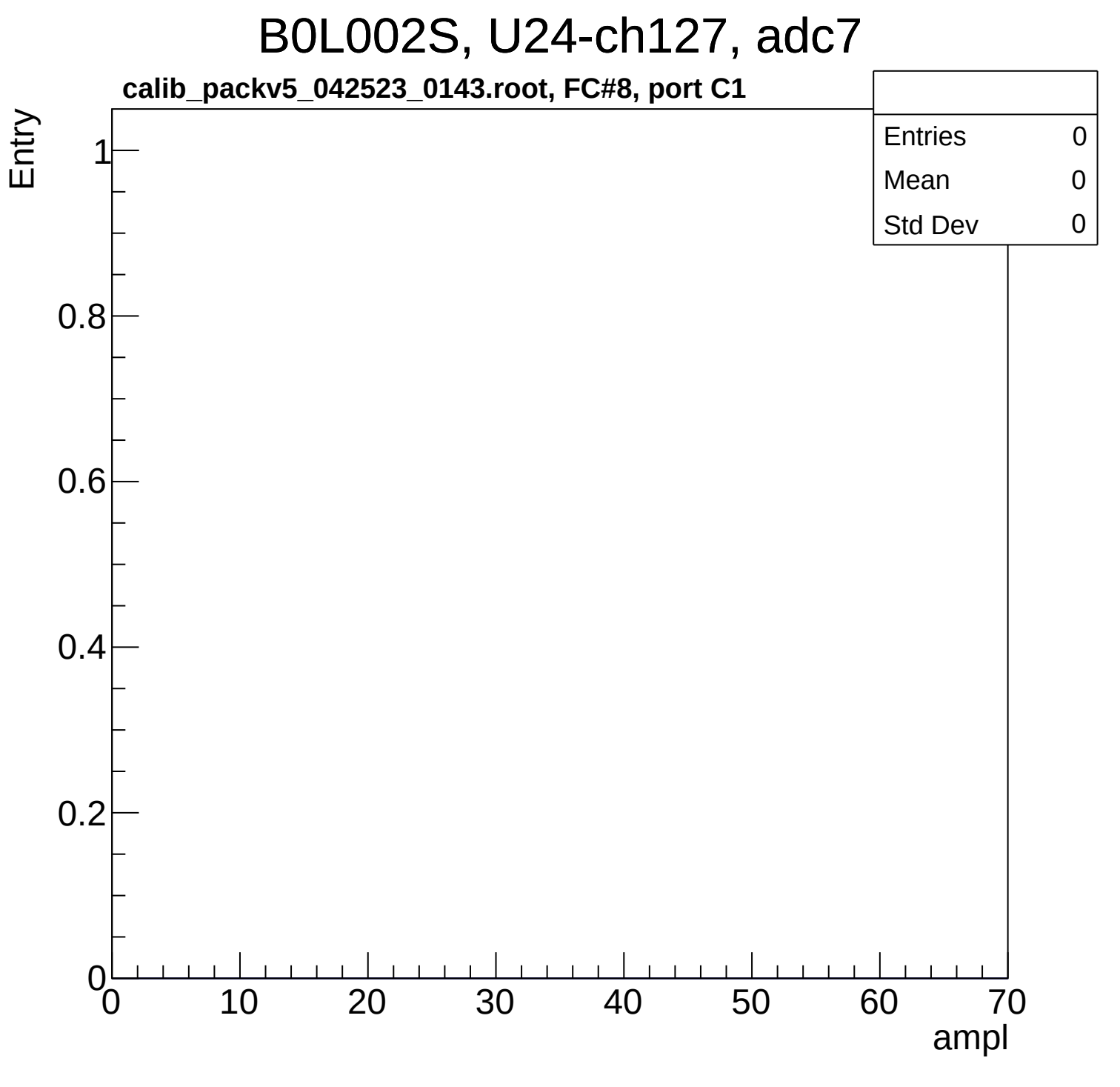
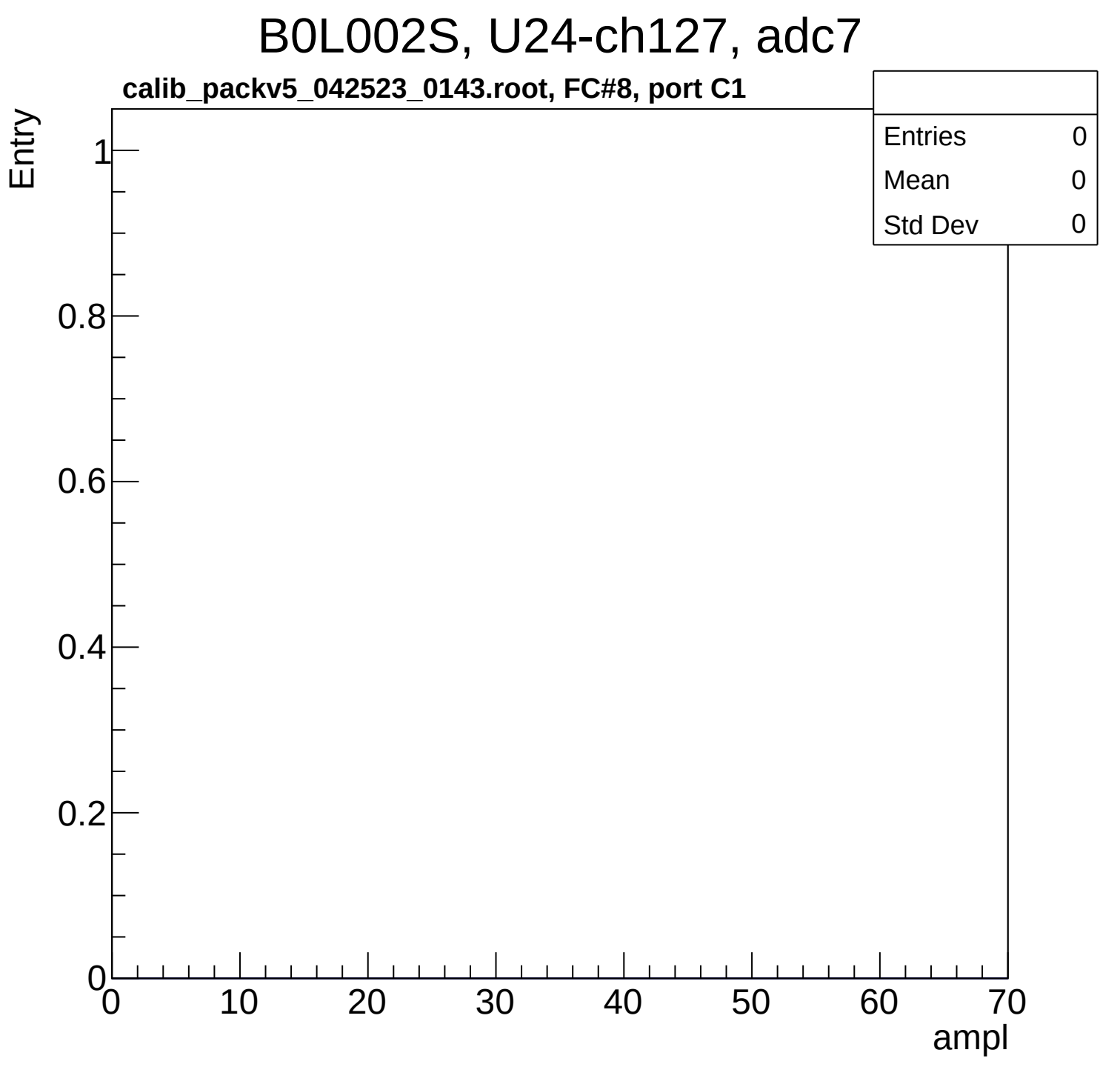

B0L002S, U24-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl